

XRT Timeline to be uploaded on 2016/04/26

Period: 2016/04/26 10:27:00 - 2016/04/30 10:39:00

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Normal mode

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XOB #1AC7: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 512x512 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (3ms)

Term	Pointing (x, y)	Comment
04/26 11:09:36 - 04/26 17:20:00	Track (820.6, 82.1) @ 04/26 10:37:00	# OP start + 10min, AR 12532 observations.
04/26 18:13:00 - 04/26 23:53:30	Track (853.7, 77.0) @ 04/26 18:10:00	# AR cont.
04/27 03:03:00 - 04/27 05:45:54	Track (167.8, -2.9) @ 04/27 03:00:00	# AR 12533 sunspot.
04/27 05:59:00 - 04/27 09:34:00	Track (896.0, 68.7) @ 04/27 05:56:00	# AR 12532 cont.
04/27 15:12:38 - 04/27 17:45:24	Fixed (912.0, 79.0)	# AR cont.
04/27 18:35:38 - 04/27 22:50:30	Fixed (912.0, 79.0)	# AR cont.
04/28 06:13:00 - 04/28 10:11:00	Track (406.1, -10.6) @ 04/28 06:10:00	# AR 12533 sunspot.

PROG= 10 Inf.-time(s)

Subr= 2	1-time(s)	2.0sec										
Seqn= 56	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 42	4-time(s)	2.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 66	20-time(s)	45.0sec										
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1B14: Synoptic Q95 2x2 - Al/mesh(24/256/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(45/512/4096) + TI

Term	Pointing (x, y)	Comment
04/26 18:04:26 - 04/26 18:09:54	Fixed (0.0, 0.0)	synoptic
04/27 05:49:00 - 04/27 05:55:54	Fixed (0.0, 0.0)	synoptic, shifted -14.0 min
04/27 17:48:30 - 04/27 17:55:30	Fixed (0.0, 0.0)	synoptic, shifted -14.5 min
04/28 06:03:00 - 04/28 06:09:54	Fixed (0.0, 0.0)	synoptic

PROG= 15 1-time(s)

Subr= 1	1-time(s)	2.0sec										
Seqn= 5	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 1	1-time(s)	2.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 99	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 67	1-time(s)	2.0sec										
thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1B28: CH monitoring - Al-Poly (4s) Thin-Be (8s) - 2x2 -1024FOV with G band(3ms/3ms VLS=CLS)

Term	Pointing (x, y)	Comment
04/27 00:14:38 - 04/27 02:59:54	Track (-258.7, 308.2) @ 04/27 00:00:00	# EIS coronal hole observations.
04/27 23:23:38 - 04/28 01:59:54	Track (-57.6, 309.5) @ 04/27 23:00:00	# EIS coronal hole observations.

PROG= 06 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec										
Seqn= 16	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	1024x1024 (1064, 1048)	DPCM	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Dark	8.00s	Obs	2x2	1024x1024 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2	90-time(s)	120.0sec										
Seqn= 34	1-time(s)	2.0sec										
Al-poly/Open	med-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	1024x1024 (1064, 1048)	Q=98	0	0	2.0sec

thin-Be/Open	med-Be/Open	close	Safe	Norm	8.00s	Obs	2x2	1024x1024 (1064, 1048)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1AD5: CME watch - 4x4 - AEC 2 - Be-thin - G-band (2x2,1ms) - Leak (2x2,1ms) - 60s cad

Term	Pointing (x, y)	Comment
04/27 10:07:00 - 04/27 10:19:54	Fixed (936.0, 0.0)	# HOP 130 full Sun scan. 1/15
04/27 10:23:00 - 04/27 10:35:54	Fixed (468.0, 0.0)	# 2/15
04/27 10:55:00 - 04/27 11:22:00	Fixed (-468.0, 0.0)	# 4/15
04/27 11:45:36 - 04/27 11:58:24	Fixed (-936.0, 0.0)	# 5/15
04/27 12:01:30 - 04/27 12:08:24	Fixed (815.0, 470.0)	# 6/15
04/27 12:11:30 - 04/27 12:24:24	Fixed (411.0, 847.0)	# 7/15
04/27 12:27:30 - 04/27 12:40:24	Fixed (0.0, 941.0)	# 8/15
04/27 12:43:30 - 04/27 13:00:30	Fixed (-411.0, 847.0)	# 9/15
04/27 13:21:08 - 04/27 13:30:24	Fixed (-815.0, 470.0)	# 10/15
04/27 13:33:30 - 04/27 13:40:24	Fixed (815.0, -470.0)	# 11/15
04/27 13:43:30 - 04/27 13:56:24	Fixed (411.0, -847.0)	# 12/15
04/27 13:59:30 - 04/27 14:12:24	Fixed (0.0, -941.0)	# 13/15
04/27 14:15:30 - 04/27 14:28:24	Fixed (-411.0, -847.0)	# 14/15
04/27 14:31:30 - 04/27 14:38:30	Fixed (-815.0, -471.0)	# 15/15

PROG= 18 Inf.-time(s)

Subr= 1	60-time(s)	60.0sec											
└	Seqn= 29	1-time(s)	4.0sec										
└└	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Subr= 2	1-time(s)	2.0sec											
└	Seqn= 26	1-time(s)	2.0sec										
└└	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
└└	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1B15: Synoptic 7 Filter w/ Al-mesh(24/256/2897), Al-poly(45/512/4096), Thin-Be(181/2048/11571) - Thick-Be(65536), Al-poly+Ti-poly(512/4096), Med-Al

Term	Pointing (x, y)	Comment
04/27 10:39:00 - 04/27 10:51:54	Fixed (0.0, 0.0)	# 3/15

PROG= 08 1-time(s)

Subr= 1	1-time(s)	2.0sec											
└	Seqn= 5	1-time(s)	2.0sec										
└└	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└└	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└└	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└└	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
└└	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└	Seqn= 1	1-time(s)	2.0sec										
└└	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└└	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└└	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└	Seqn= 99	1-time(s)	2.0sec										
└└	Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└└	Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└└	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└	Seqn= 67	1-time(s)	2.0sec										
└└	thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└└	thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└└	thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└	Seqn= 54	1-time(s)	4.0sec										
└└	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
└└	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2	1-time(s)	2.0sec											
└	Seqn= 46	2-time(s)	2.0sec										
└└	Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└	Seqn= 4	2-time(s)	2.0sec										
└└	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└└	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└	Seqn= 71	2-time(s)	2.0sec										
└└	med-Al/Open	med-Al/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└└	med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1AEC: G-Band Alignment with North Pole Q90 2x2 (G-band and VLS=CLS) - 1msec (Al/poly) - 4096msec - 5min cadence - Partial Sun-wNGT

Term	Pointing (x, y)	Comment
04/28 02:15:00 - 04/28 03:59:54	Fixed (0.0, 930.0)	# Coalignment at North limb.

PROG= 19 1-time(s)

Subr= 1	24-time(s)	300.0sec											
└	Seqn= 98	1-time(s)	2.0sec										
└└	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec
└	Seqn= 63	1-time(s)	2.0sec										
└└	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec
└	Seqn= 45	1-time(s)	2.0sec										
└└	Al-poly/Open	med-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x1536 (1024, 768)	Q=95	0	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #1AED: G-Band Alignment with East limb Q90 2x2 (G-band and VLS=CLS) - 1msec - (Al/poly) 1443msec - 8 min cadence-wNGT

Term	Pointing (x, y)	Comment									
04/28 04:15:00 - 04/28 05:59:54	Fixed (-970.0, 0.0)	# Coalignment at East limb.									
PROG= 12 1-time(s)											
└─ Subr= 1 15-time(s) 480.0sec											
└─ Seqn= 19 1-time(s) 2.0sec											
└─ Open/G-band	Open/G-band open	Safe Norm 1ms Obs 2x2 1536x2048 (1280, 1024) Q=90 0 0 2.0sec									
└─ Seqn= 43 1-time(s) 2.0sec											
└─ Open/G-band	Open/G-band close	Safe Norm 1ms Obs 2x2 1536x2048 (1280, 1024) Q=90 0 0 2.0sec									
└─ Seqn= 70 1-time(s) 2.0sec											
└─ Al-poly/Open	med-Be/Open close	Safe Norm 1.41s Obs 2x2 1536x2048 (1280, 1024) Q=95 0 0 2.0sec									
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

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Flare mode

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XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment									
04/26 11:09:36 - 04/26 17:20:00	Track (820.6, 82.1) @ 04/26 10:37:00	# OP start + 10min, AR 12532 observations.									
04/26 18:13:00 - 04/26 23:53:30	Track (853.7, 77.0) @ 04/26 18:10:00	# AR cont.									
04/27 00:14:38 - 04/27 02:59:54	Track (-258.7, 308.2) @ 04/27 00:00:00	# EIS coronal hole observations.									
04/27 03:03:00 - 04/27 05:45:54	Track (167.8, -2.9) @ 04/27 03:00:00	# AR 12533 sunspot.									
04/27 05:59:00 - 04/27 09:34:00	Track (896.0, 68.7) @ 04/27 05:56:00	# AR 12532 cont.									
04/27 10:07:00 - 04/27 10:19:54	Fixed (936.0, 0.0)	# HOP 130 full Sun scan. 1/15									
04/27 10:23:00 - 04/27 10:35:54	Fixed (468.0, 0.0)	# 2/15									
04/27 10:55:00 - 04/27 11:22:00	Fixed (-468.0, 0.0)	# 4/15									
04/27 11:45:36 - 04/27 11:58:24	Fixed (-936.0, 0.0)	# 5/15									
04/27 12:01:30 - 04/27 12:08:24	Fixed (815.0, 470.0)	# 6/15									
04/27 12:11:30 - 04/27 12:24:24	Fixed (411.0, 847.0)	# 7/15									
04/27 12:27:30 - 04/27 12:40:24	Fixed (0.0, 941.0)	# 8/15									
04/27 12:43:30 - 04/27 13:00:30	Fixed (-411.0, 847.0)	# 9/15									
04/27 13:21:08 - 04/27 13:30:24	Fixed (-815.0, 470.0)	# 10/15									
04/27 13:33:30 - 04/27 13:40:24	Fixed (815.0, -470.0)	# 11/15									
04/27 13:43:30 - 04/27 13:56:24	Fixed (411.0, -847.0)	# 12/15									
04/27 13:59:30 - 04/27 14:12:24	Fixed (0.0, -941.0)	# 13/15									
04/27 14:15:30 - 04/27 14:28:24	Fixed (-411.0, -847.0)	# 14/15									
04/27 14:31:30 - 04/27 14:38:30	Fixed (-815.0, -471.0)	# 15/15									
04/27 15:12:38 - 04/27 17:45:24	Fixed (912.0, 79.0)	# AR cont.									
04/27 18:35:38 - 04/27 22:50:30	Fixed (912.0, 79.0)	# AR cont.									
04/27 23:23:38 - 04/28 01:59:54	Track (-57.6, 309.5) @ 04/27 23:00:00	# EIS coronal hole observations.									
04/28 06:13:00 - 04/28 10:11:00	Track (406.1, -10.6) @ 04/28 06:10:00	# AR 12533 sunspot.									
PROG= 07 30-time(s)											
└─ Subr= 1 20-time(s) 2.0sec											
└─ Seqn= 11 1-time(s) 2.0sec											
└─ Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec									
└─ Seqn=100 1-time(s) 10.0sec											
└─ thin-Be/Open	med-Be/Open close	Safe Norm 125ms Obs 1x1 384x384 (1024, 1024) Q=95 2 0 2.0sec									
└─ med-Be/Open	Open/thick-Al close	Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec									
└─ Open/thick-Al	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec									
└─ Subr= 2 1-time(s) 2.0sec											
└─ Seqn= 10 1-time(s) 2.0sec											
└─ med-Al/Open	med-Al/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec									
└─ Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec									
└─ Seqn= 11 1-time(s) 2.0sec											
└─ Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec									
└─ Seqn= 84 1-time(s) 2.0sec											
└─ Open/G-band	Open/G-band open	Safe Norm 3ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec									
└─ Open/G-band	Open/G-band close	Safe Norm 3ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec									
└─ Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec									
└─ Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec									
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

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Active Region Search

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NOT USED

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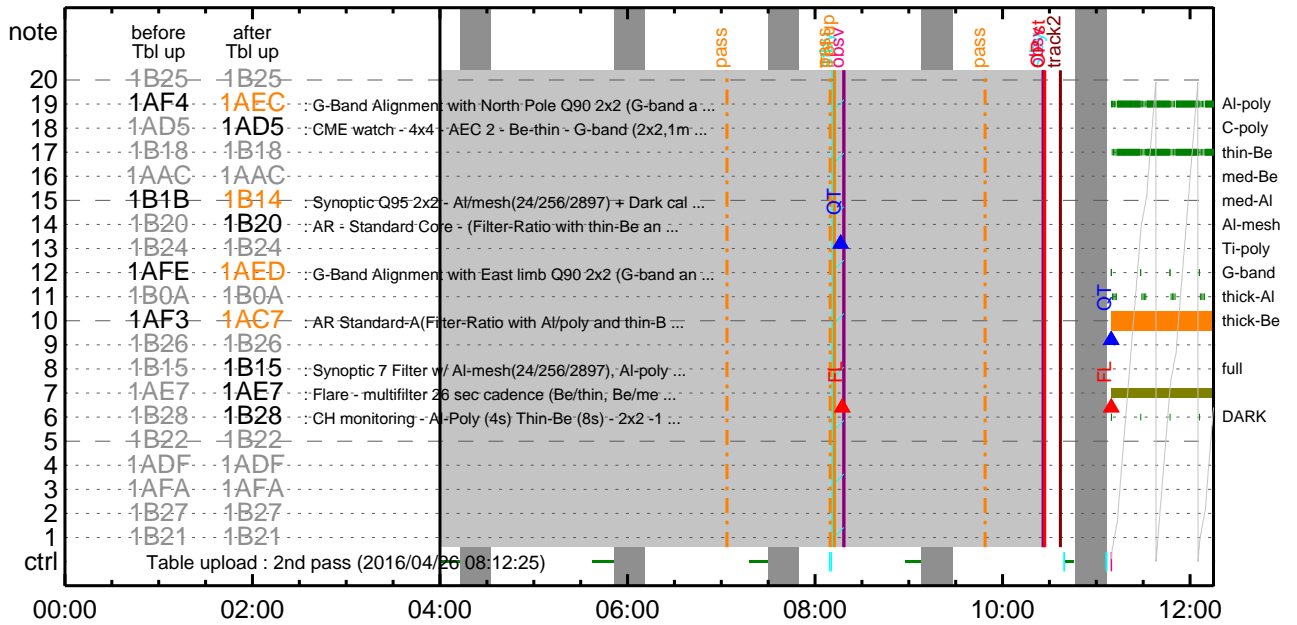
Flare Detection

* * * * *

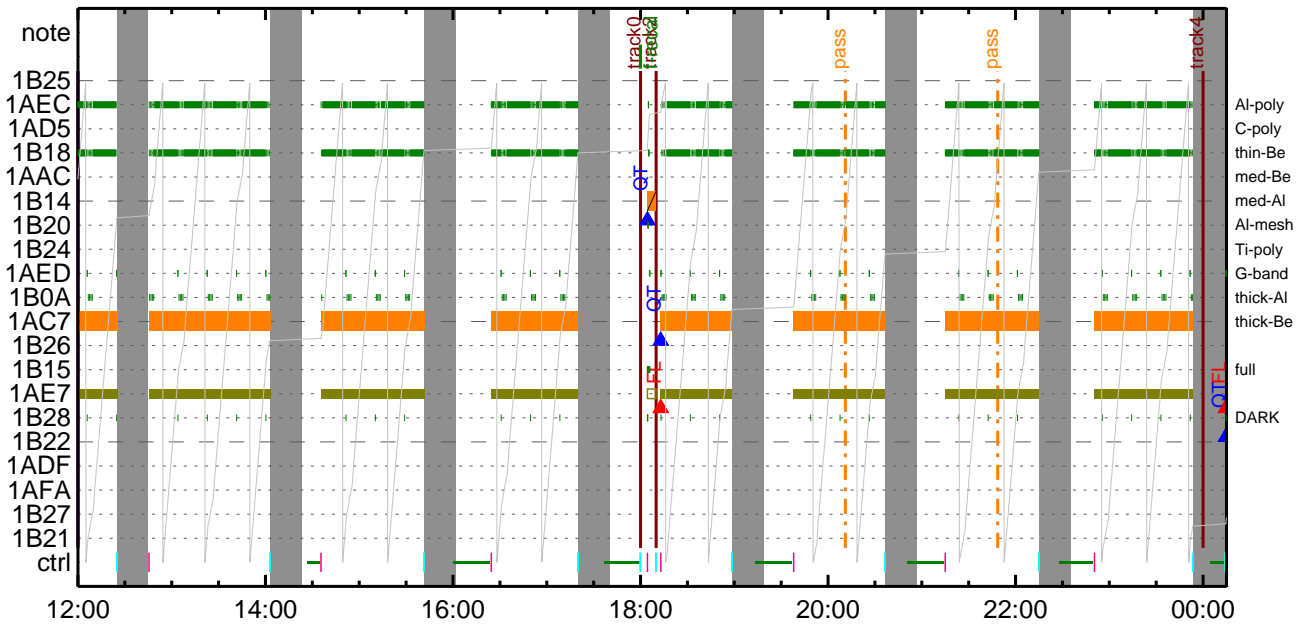
FLD Patrol

Term	Pointing (x, y)	Comment
04/26 18:10:18 - 04/27 05:46:18	Track (853.7, 77.0) @ 04/26 18:10:00	# AR cont.
04/27 05:56:18 - 04/27 10:36:18	Track (896.0, 68.7) @ 04/27 05:56:00	# AR 12532 cont.
04/27 10:52:18 - 04/27 17:45:48	Fixed (-468.0, 0.0)	# 4/15
04/27 18:35:24 - 04/28 02:00:18	Fixed (912.0, 79.0)	# AR cont.
04/28 06:10:18 - 04/30 10:39:00	Track (406.1, -10.6) @ 04/28 06:10:00	# AR 12533 sunspot.
Open/Ti-poly	Open/thick-Al close	Safe Norm 8ms Obs 8x8 Q=50 30sec

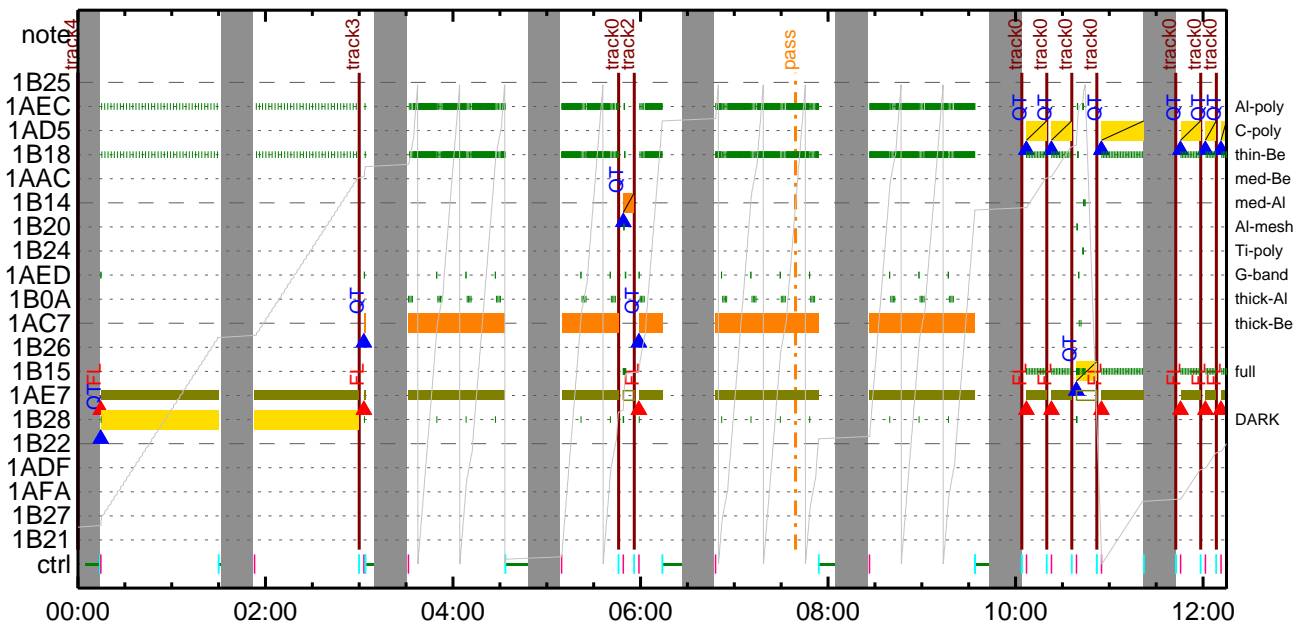
CMDI #0881 2016/04/26



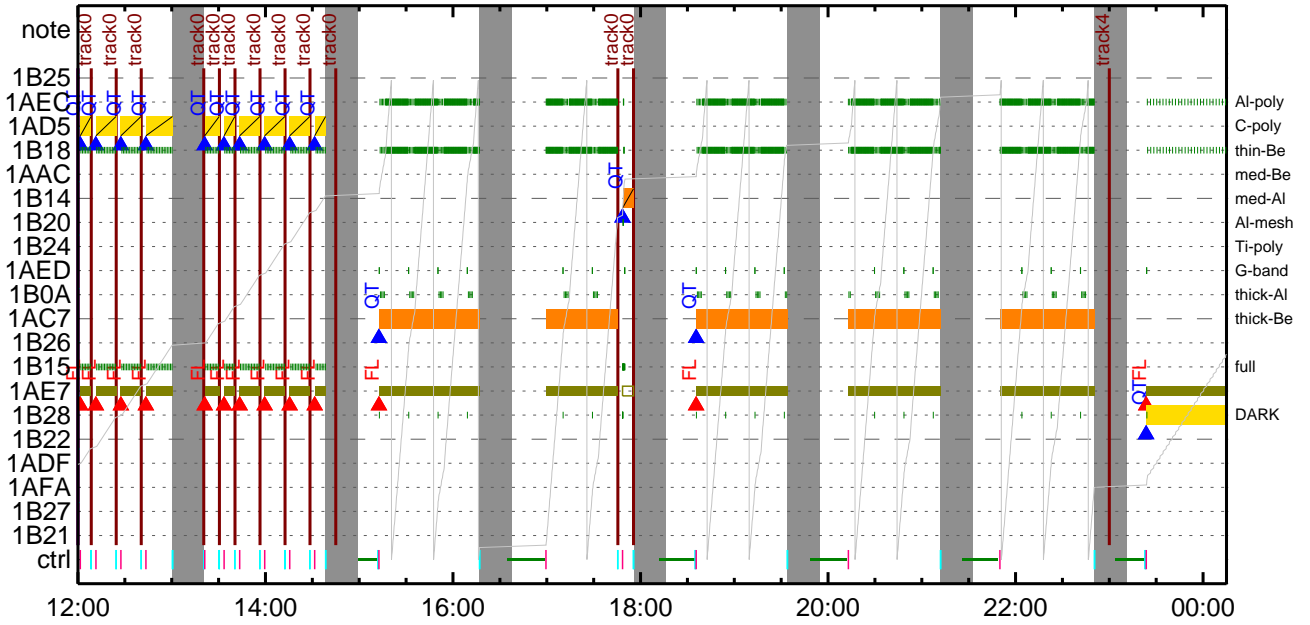
CMDI #0881 2016/04/26



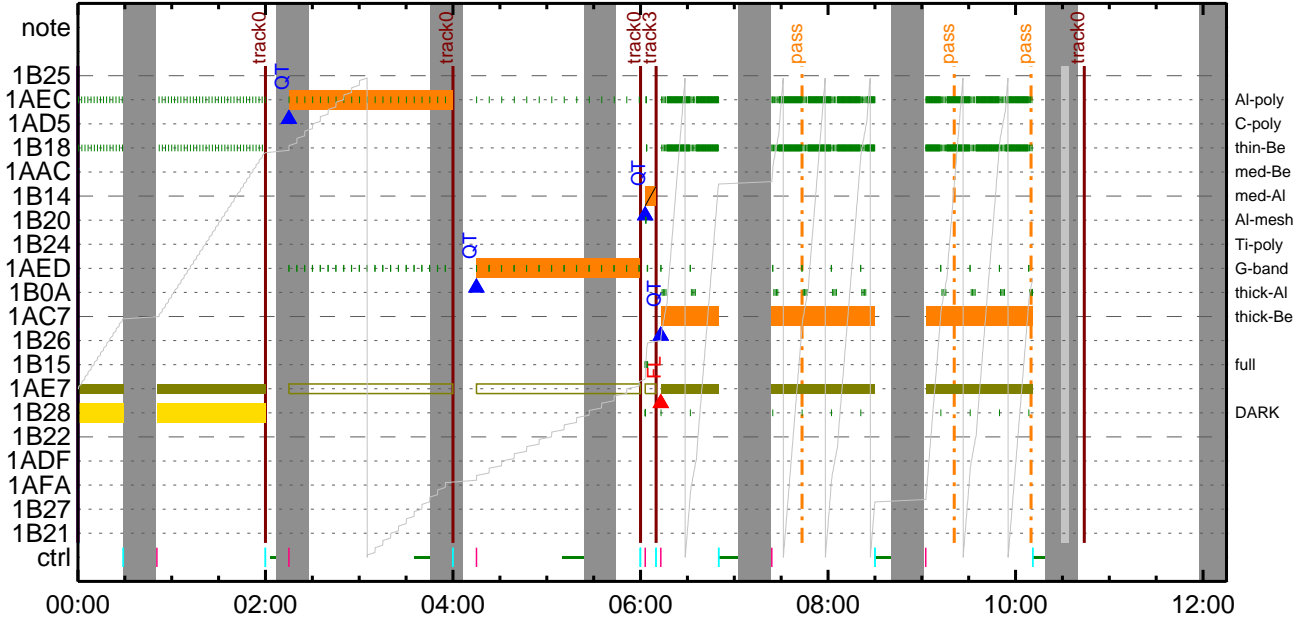
CMDI #0881 2016/04/27



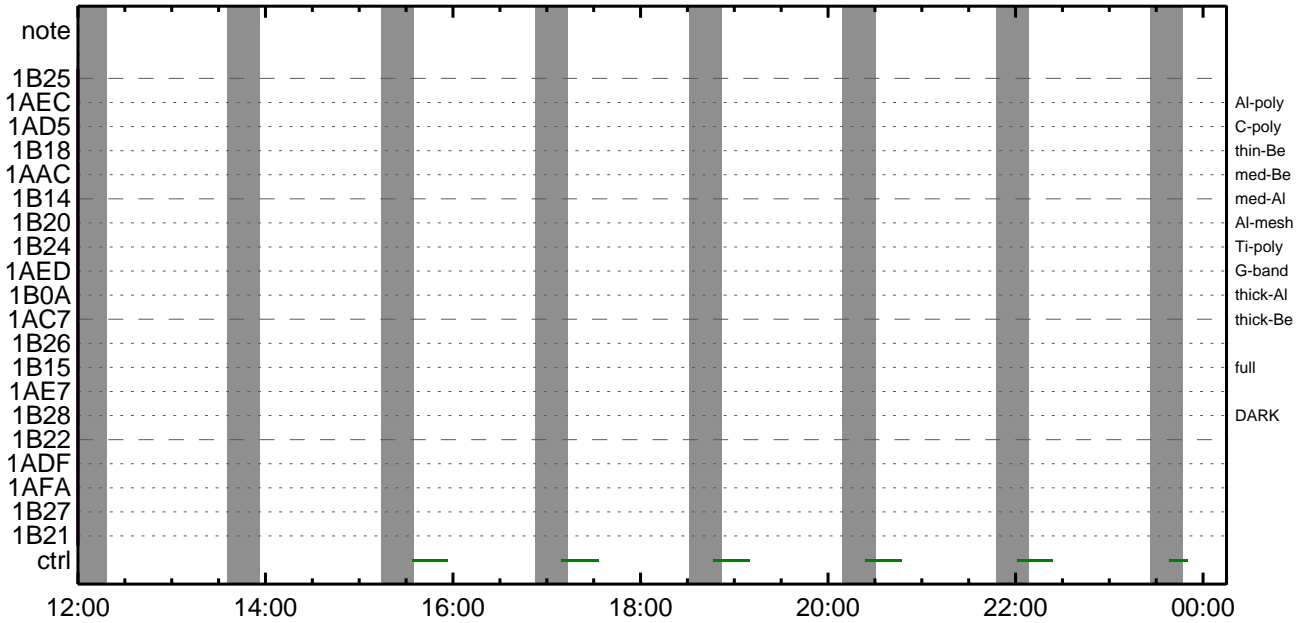
CMDI #0881 2016/04/27



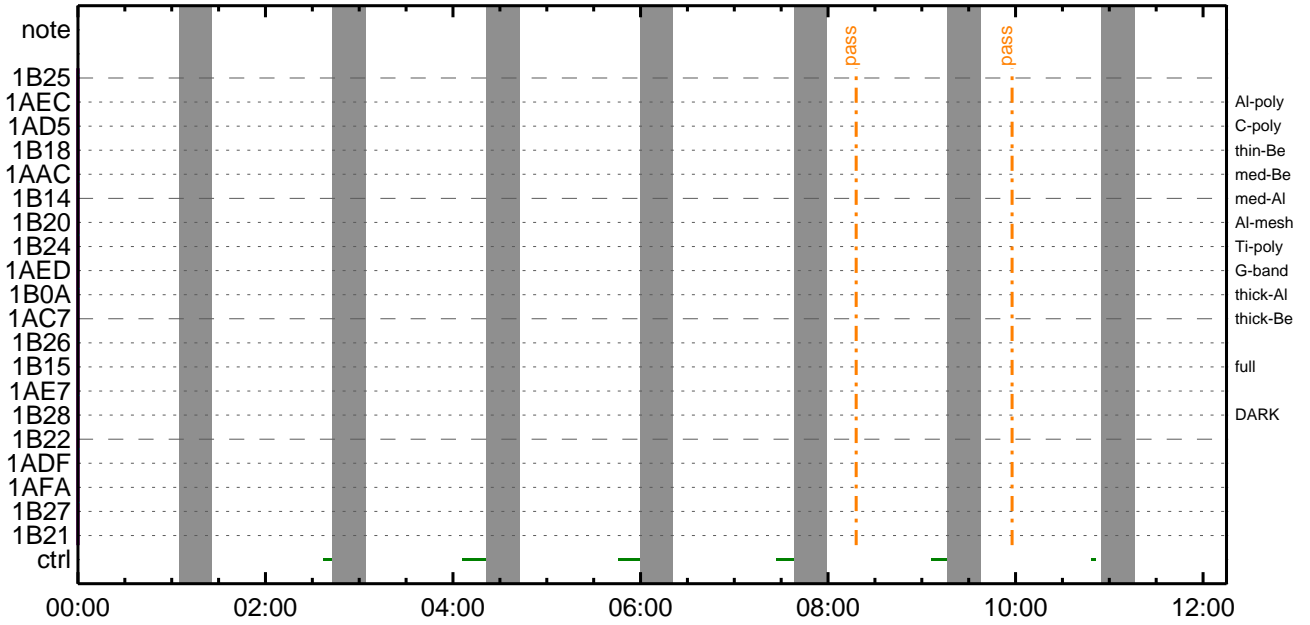
CMDI #0881 2016/04/28



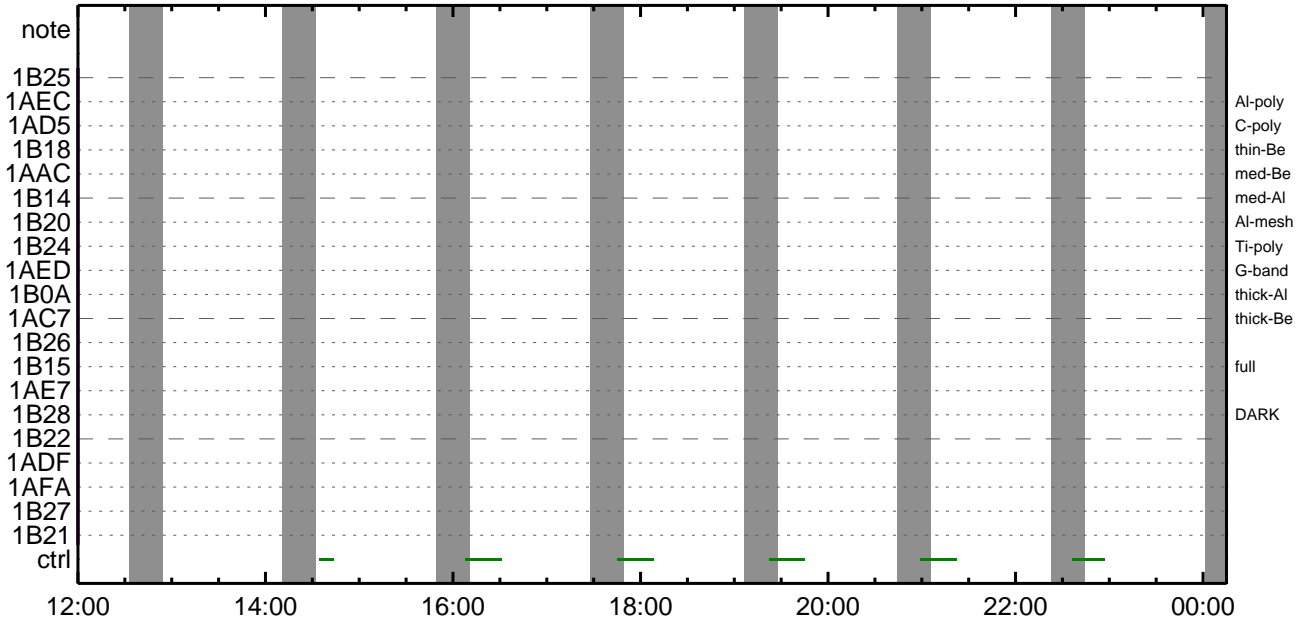
CMDI #0881 2016/04/28



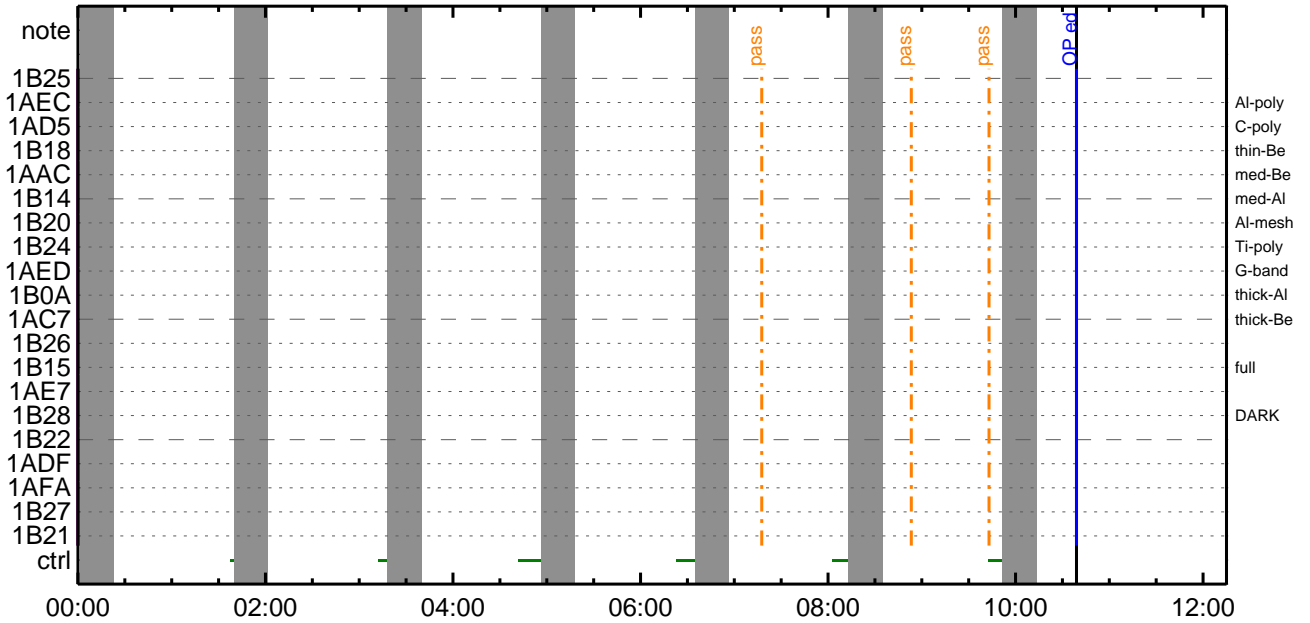
CMDI #0881 2016/04/29

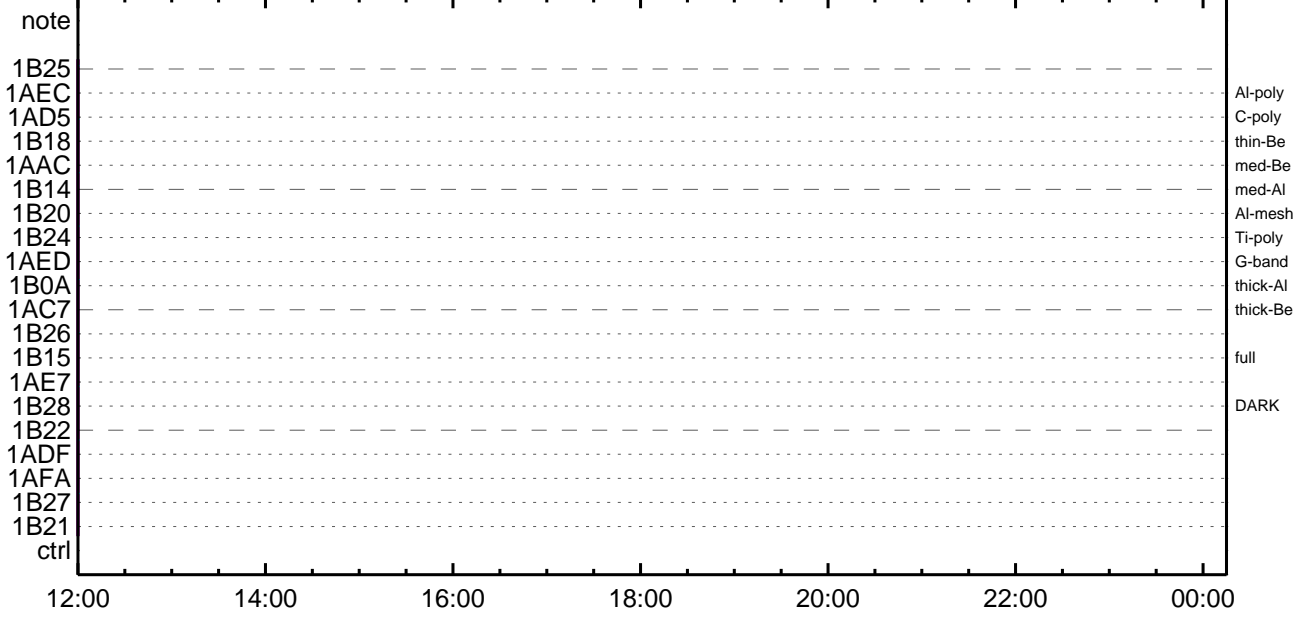


CMDI #0881 2016/04/29



CMDI #0881 2016/04/30





(a) Spacecraft Operation Procedure (real-commands)

```
main-816 2016-04-26 13:41:41 205 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOS¥Ã¥S¥Ã¥-¼Ã»Û;ä
0005 C.
0006 C. ¥Ã¥S;¼¥³¥P¥Ö¥EÃ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Åí;È¿¿ðÄð•µ°È»Í×ÃÇ¿Í¥Ç¥Ã¥×¥Í;¼¥E;ÈEè%µ•ííE;ÈðE¼°ÇÓð•µ¿¼l¹Ç¿Í;ÇÃ®, ùñ¹ðÈððÇÃ+¿®ð•ðÈððð³ðÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OG¥Í;¼¥E;|¥Ã¥Ö¥×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OG¥Í;¼¥E;ä
0019 . S. OP op-816:OP
0020 ()
0021 . S. OG og-816:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPÍ°è¥Ã¥Ö¥×;ä
0025 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. ÇÇ[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. ÇÇ[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. ÇÇ[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. ÇÇ[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. ÇÇ[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. ÇÇ[HK1_PKT_FORM_NO] EQ 7
0036 C. ÇÇ[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. ÇÇ[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. ÇÇ[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. ÇÇ[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. ¥Ã¥Ö¥×¼ªªÎ»ðð³ÎÇ§
0041 C. ÇÇ[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGðÍ¼È¹Ç•è²ÍOKðð³ÎÇ§
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. ÇÇ[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. ÇÇ[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. ÇÇ[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. ÇÇ[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. ÇÇ[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. ÇÇ[HK1_PKT_FORM_NO] EQ 7
0055 C. ÇÇ[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. ÇÇ[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. ÇÇ[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. ÇÇ[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. ¥Ã¥Ö¥×¼ªªÎ»ðð³ÎÇ§
0060 C. ÇÇ[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGðÍ¼È¹Ç•è²ÍOKðð³ÎÇ§
0062 C.
0063 C. NMOG(0x210000-0x210FFF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. ÇÇ[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. ÇÇ[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. ÇÇ[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. ÇÇ[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. ÇÇ[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. ÇÇ[HK1_PKT_FORM_NO] EQ 7
0074 C. ÇÇ[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. ÇÇ[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. ÇÇ[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. ÇÇ[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. ¥Ã¥Ö¥×¼ªªÎ»ðð³ÎÇ§
0079 C. ÇÇ[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG, RAM ID=OPðÍ¼È¹Ç•è²ÍOKðð³ÎÇ§
0081 C.
0082 . C. ***** øÈ²¼ðÍ¼Ã´¶Í°ðÈE-ðÀ+¿®(¼ãµ-¥Ã¥Ö¥×¼®¿çððÄÓÃæÇª¼ªµ°ñ¼¿l¹Ç¿çðâ) *****
0083 C. DHU¥â;¼¥E;È¼¥½; ¥Í;¼¥E;ÈððÍã¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. ÇÇ[HK1_PKT_FORM_NO] EQ 2
0087 C. ÇÇ[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. ÇÇ[HK1_S_TLM_BIT_RATE] EQ 32k
0089 C. ÇÇ[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;§ OPOG UPLOADð-Ã+¿®NGðÍ¼¹¹Ç; Ç°È²¼ðÍTI-CMDÃ+¿®ðÍ¼Ä¹Ôð•ðÈððð³ðÈ;f
```

```
0096 C.                0302;çSET0EDUMP0IÆ±°iYÑY¹0Ç¹Ô0|0³0E;E
0097 C.
0098 . C. TIY³YFYYÖYÉ00ðÄDİ¿(UT)
0099 +. TI 2016-04-26 10:22:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0102 C.
0103 +. TI 2016-04-26 10:22:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0106 C.
0107 +. TI 2016-04-26 10:22:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0110 C.
0111 +. TI 2016-04-26 10:26:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0114 C.
0115 C. 0Ê²¼0İÄê%îÍÑ0İYÁY§YÁY-¹àÛ
0116 C.                çç[HK1_TI_CMD_ENA/DIS]              EQ        ENA
0117 C.                çç[HK1_TI_CMD_NUM]                EQ        4
0118 C.                çç[HK1_NEXT_EXEC_PIM]              EQ        DHU
0119 C.                çç[HK1_NEXT_EXEC_DC]               EQ        0xB3
0120 C.
0121 . C. *****
0122 C. TIİİî°èYÀYÖY×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.                çç[HK1_DMP_TOP_ADRS_1]              EQ        07
0129 C.                çç[HK1_DMP_TOP_ADRS_0]              EQ        2B
0130 C.                çç[HK1_DMP_BLOCK_NUM]               EQ        3
0131 C.                çç[HK1_DMP_REPEAT_NUM]              EQ        0
0132 C.                çç[HK1_DMA_DMP_PIM]                EQ        DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.                çç[HK1_PKT_FORM_NO]                 EQ        7
0136 C.                çç[HK1_PKT_GEN_TIME]                EQ        0.25 s
0137 C.                çç[HK1_S_TLM_BIT_RATE]              EQ        32k
0138 C.                çç[HK1_X_TLM_BIT_RATE]              EQ        4M
0139 C.                çç[HK1_DMP_CHK_FLG]                 EQ        EXEC
0140 C.
0141 . C. YÀYÖY×¼ª¹İ»0ð³İÇ§
0142 C.                çç[HK1_DMP_CHK_FLG]                 EQ        NON
0143 C.
0144 . C. RAM ID=TI_TBL0İ%È¹Ç•è²İOK0ð³İÇ§
0145 C.
0146 . C. DHUYâ;¼YÉ;È¼Y¼, Yİ;¼YÈ;È0ðİá0¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.                çç[HK1_PKT_FORM_NO]                 EQ        2
0150 C.                çç[HK1_PKT_GEN_TIME]                EQ        0.5S
0151 C.                çç[HK1_S_TLM_BIT_RATE]              EQ        32K
0152 C.                çç[HK1_X_TLM_BIT_RATE]              EQ        4M
0153 C.
0154 . C. Stop EIS observation and temporarily disable EIS mode changes
0155 C.
0156 C.
0157 C. ***** Start EIS operation (TI set) *****
0158 C. Execute, after the success of OP upload.
0159 C. Set EIS TI-commands
0160 +. TI 2016-04-26 10:26:30.0
0161 DC 07-FC EIS_MODE_MANU
0162 BC (21 02)
0163 +. TI 2016-04-26 10:26:40.0
0164 DC 07-FC EIS_MODE_CHG_DIS
0165 BC (22)
0166 . C.                [ ] [HK1_TI_CMD_NUM]                EQ        2 COUNTUP
0167 C. ***** End EIS operation (TI set) *****
0168 C.
0169 C.
0170 C. *****
0171 C. SOT TI command set
0172 C. *****
0173 C. Execute, after the success of OP upload.
0174 +. TI 2016-04-26 10:26:16.0
0175 DC 07-F0 MDP_SOT_MODE_STBY
0176 BC (41)
0177 . C. -----
0178 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0179 C. -----
0180 C. ***** SOT END *****
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2016-04-26 10:26:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC (c3)
0187 . C.                [ ] [HK1_TI_CMD_NUM]                EQ        1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 . C. ***** MDP ´úÃİ0İ»ö¼Y0ÈÄ0¹0èDCBC•×²è *****
0192 C. (%â°İYÖYÁYÈYBYYÁYçYè0È¼00¼Ä»Û0¹0é)
0193 . S. DC-BC dcbc-402:DCBC
```

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
```



```

0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. ***** AOCs Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 + DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 297725.8 +- 1.0 (s) [ ]
0136 C.
0137 . C.
0138 C.
0139 C. ***** XRT START *****
0140 C.
0141 +. DC 07-F0 MDP_XRT_CTRL_MANU
0142 BC (c1)
0143 +. DC 07-F0 MDP_XRT_CTRL_MANU
0144 BC (c1)
0145 + DC 07-F0 MDP_XRT_MODE_STBY
0146 BC (c3)
0147 . C. ----- Success Verify ? OK / NG_____
0148 C.
0149 C. XRT Obs. Table Upload
0150 . S. RAM ram-291:MDP_OBS_X
0151 ( )
0152 C.
0153 +. DC 07-F0 MDP_DUMP_XRTTBL
0154 BC (84 07 00 00 00 3a d4)
0155 . C. ----- Comparison Check ? OK / ERR _____
0156 C.
0157 C.
0158 +. DC 07-F0 MDP_XRT_ROI_SET
0159 BC (cd 01 b1 b1 04 04)
0160 + DC 07-F0 MDP_XRT_ROI_SET
0161 BC (cd 02 b1 b1 08 08)
0162 + DC 07-F0 MDP_XRT_ROI_SET
0163 BC (cd 03 b1 b1 08 08)
0164 + DC 07-F0 MDP_XRT_ROI_SET
0165 BC (cd 04 b1 b1 06 06)
0166 + DC 07-F0 MDP_XRT_ROI_SET
0167 BC (cd 05 85 83 06 06)
0168 + DC 07-F0 MDP_XRT_ROI_SET
0169 BC (cd 06 85 83 06 06)
0170 + DC 07-F0 MDP_XRT_ROI_SET
0171 BC (cd 07 85 83 08 08)
0172 + DC 07-F0 MDP_XRT_ROI_SET
0173 BC (cd 08 80 80 20 20)
0174 + DC 07-F0 MDP_XRT_ROI_SET
0175 BC (cd 09 80 80 20 08)
0176 + DC 07-F0 MDP_XRT_ROI_SET
0177 BC (cd 0a 80 80 08 20)
0178 + DC 07-F0 MDP_XRT_ROI_SET
0179 BC (cd 0b 85 83 10 10)
0180 + DC 07-F0 MDP_XRT_ROI_SET
0181 BC (cd 0c 80 60 20 18)
0182 + DC 07-F0 MDP_XRT_ROI_SET
0183 BC (cd 0d a0 80 18 20)
0184 + DC 07-F0 MDP_XRT_ROI_SET
0185 BC (cd 0f 80 80 06 06)
0186 + DC 07-F0 MDP_XRT_ROI_SET
0187 BC (cd 10 80 80 08 08)
0188 + DC 07-F0 MDP_XRT_FLD_ENA
0189 BC (d8)
0190 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0191 BC (c8)
0192 + DC 07-F0 MDP_XRT_ARS_DIS
0193 BC (d5)

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0194 +. DC 07-F0 MDP_XRT_AEC_RESET
0195 BC (d0)
0196 +. DC 07-F0 MDP_XRT_FLD_RESET
0197 BC (da)
0198 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0199 BC (c4 0e)
0200 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0201 BC (c5 07)
0202 . C. ----- Success Verify ? OK / NG ____
0203 . C.
0204 . C.
0205 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0206 . C.
0207 +. DC 07-F0 MDP_XRT_MODE_OBSV
0208 BC (c2)
0209 +. TI 2016-04-26 10:26:02.0
0210 DC 07-F0 MDP_XRT_MODE_OBSV
0211 BC (c2)
0212 . C. ----- Success Verify ? OK / NG ____
0213 . C.
0214 . C. ***** XRT END *****
0215 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0216 +. DC 07-FC EIS_MODE_CHG_ENA
0217 BC (20)
0218 . C. Verify EIS_MODE_CHG_FLG is ENA
0219 +. DC 07-FC EIS_MODE_MANU
0220 BC (21 02)
0221 . C. Verify EIS in MANUAL mode
0222 . C. Estimated OBSTBL upload time is 2ml2s
0223 . C. *****
0224 . C. EIS START OBSTBL LOAD
0225 . C. *****
0226 . S. RAM ram-820:EIS_OBSTBL
0227 . C.
0228 +. DC 07-FC EIS_DUMP_OBSTBL
0229 BC (07 07 07 00 00 70 00)
0230 . C.
0231 . C. Execute, after the success of OBSTBL upload.
0232 . C. Set EIS TI-commands
0233 +. TI 2016-04-26 10:26:50.0
0234 DC 07-FC EIS_MODE_CHG_ENA
0235 BC (20)
0236 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0237 . C. *****
0238 . C. EIS END OBSTBL LOAD
0239 . C. *****
0240 . C.
0241 . C. ***** MDP 'uãîâî»ö¼ÿâÉÂð¹æDCBC•x²è *****
0242 . C. (%ã°îÿÓÿÄÿËÿÏÿÛÿäÿçÿèâÉ¼ã¼Ä»Û¹æè)
0243 . S. DC-BC dcbc-402:DCBC
0244 (MDP_known_event)
0245 . C.
0246 . C.
0247 . C. ***** ÿÐÿ¹•ï Daily±;îÑæ'Ø¹æèDCBC•x²è *****
0248 . S. DC-BC dcbc-153:DCBC
0249 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0250 . C.
0251 . C.
0252 . C. ;ãLOSÿÁÿÏÿÛÿäÿçÿè¼Ä»Û¹æè
0253 . C.
0254 . C. ***** LOS *****
0255 . C.

```



```

0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop SP table >
0102 +. DC 07-F0 MDP_SP_CTRL_MANU
0103 BC (61)
0104 C. -----
0105 C. MDP_SP_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload SP Observation Table>
0109 . S. RAM ram-288:MDP_OBS_S
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_S >
0113 +. DC 07-F0 MDP_DUMP_SPTBL
0114 BC (83 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_S verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 C. *****
0120 C. SOT TI command set
0121 C. *****
0122 C. Execute, after the success of TBL upload.
0123 +. TI 2016-04-26 10:26:18.0
0124 DC 07-F0 MDP_SOT_MODE_OBSV
0125 BC (40)
0126 . C. -----
0127 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0128 C. -----
0129 C.
0130 C.
0131 . C. ***** MDP 'uÄîâî»ö¼ÝðËÄðð¹ªëDCBC•x²è *****
0132 C. (%â°i¥Ö¥Ä¥È¥P¥È¥â¥ç¥Èª¼ª¼Ä»Ûª¹ªè)
0133 . S. DC-BC dcbc-402:DCBC
0134 (MDP_known_event)
0135 C.
0136 C.
0137 . C. ***** ¥D¥¹•î Daily±;îñªÉ´Øª¹ªèDCBC•x²è *****
0138 . S. DC-BC dcbc-153:DCBC
0139 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0140 C.
0141 C.
0142 . C. ;ãLOS¥Ä¥S¥Ä¥¹¼Ä»Û;ä
0143 C.
0144 . C. ***** LOS *****
0145 C.

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*** OP Sequence for XRT ***

2016/04/26	10:37:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02	00	00	00	00
2016/04/26	10:39:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/26	10:39:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/26	10:39:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/26	10:39:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/04/26	10:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/04/26	11:06:30.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/26	11:06:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/26	11:06:34.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2016/04/26	11:06:54.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/04/26	11:06:56.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/04/26	11:06:58.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/04/26	11:07:00.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/04/26	11:07:02.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/26	11:09:32.0	XRT_QT_PROG_SET_442_OG [0x1ba]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2016/04/26	11:09:34.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2016/04/26	11:09:36.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/26	12:25:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/26	12:25:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/26	12:25:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/26	12:25:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/04/26	12:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/04/26	12:44:30.0	XRT_Custom_430_OG [0x1ae]							
2016/04/26	12:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/26	14:03:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/26	14:03:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/26	14:03:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/26	14:03:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/04/26	14:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/04/26	14:34:30.0	XRT_Custom_430_OG [0x1ae]							
2016/04/26	14:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/26	15:41:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/26	15:41:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/26	15:41:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/26	15:41:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/04/26	15:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/04/26	16:23:30.0	XRT_Custom_430_OG [0x1ae]							
2016/04/26	16:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/26	17:20:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/26	17:20:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/26	17:20:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/26	17:20:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/04/26	17:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/04/26	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/26	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/26	17:59:58.0	XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]							
		XRT_FOCUS_RECAL	2	07-F8	78	00			
2016/04/26	18:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00

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2016/04/26	18:03:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2016/04/26	18:04:18.0	XRT_FLD_DIS_438_OG [0x1b6]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/04/26	18:04:20.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/04/26	18:04:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/04/26	18:04:24.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f			
2016/04/26	18:04:26.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/26	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/26	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/26	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2016/04/26	18:10:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02	00	00	00	00
2016/04/26	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/04/26	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/04/26	18:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/04/26	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/04/26	18:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/26	18:12:56.0	XRT_QT_PROG_SET_442_OG [0x1ba]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2016/04/26	18:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2016/04/26	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/26	18:58:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/26	18:58:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/26	18:58:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/26	18:58:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/04/26	19:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/04/26	19:37:00.0	XRT_Custom_430_OG [0x1ae]							
2016/04/26	19:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/26	20:36:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/26	20:36:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/26	20:36:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/26	20:36:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/04/26	20:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/04/26	21:14:00.0	XRT_Custom_430_OG [0x1ae]							
2016/04/26	21:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/26	22:15:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/26	22:15:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/26	22:15:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/26	22:15:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/04/26	22:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/04/26	22:49:30.0	XRT_Custom_430_OG [0x1ae]							
2016/04/26	22:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/26	23:53:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/26	23:53:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/26	23:53:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/26	23:53:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/04/26	23:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/04/27	00:00:00.0	AOCS_OrE-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	04	00	00	00	00
2016/04/27	00:14:00.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	00:14:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	00:14:04.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	

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2016/04/27	00:14:24.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/04/27	00:14:26.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/04/27	00:14:28.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/04/27	00:14:30.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/04/27	00:14:32.0	XRT_FLD_RESET_441_OG [0x1b9]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/27	00:14:34.0	XRT_QT_PROG_SET_429_OG [0x1ad]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	06			
2016/04/27	00:14:36.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2016/04/27	00:14:38.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/27	01:30:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	01:30:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	01:30:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/27	01:30:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/04/27	01:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/04/27	01:52:00.0	XRT_Custom_430_OG [0x1ae]							
2016/04/27	01:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/27	02:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	02:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	02:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2016/04/27	03:00:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	03	00	00	00	00
2016/04/27	03:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/04/27	03:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/04/27	03:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/04/27	03:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/04/27	03:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/27	03:02:56.0	XRT_QT_PROG_SET_442_OG [0x1ba]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2016/04/27	03:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2016/04/27	03:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/27	03:04:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	03:04:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	03:04:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/27	03:04:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/04/27	03:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/04/27	03:30:30.0	XRT_Custom_430_OG [0x1ae]							
2016/04/27	03:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/27	04:33:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	04:33:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	04:33:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/27	04:33:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/04/27	04:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/04/27	05:08:30.0	XRT_Custom_430_OG [0x1ae]							
2016/04/27	05:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/27	05:45:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	05:45:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	05:45:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2016/04/27	05:46:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2016/04/27	05:46:18.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/04/27	05:48:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/04/27	05:48:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				

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2016/04/27	05:48:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f			
2016/04/27	05:49:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/27	05:55:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	05:55:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	05:55:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2016/04/27	05:56:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02	00	00	00	00
2016/04/27	05:56:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/04/27	05:56:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/04/27	05:56:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/04/27	05:56:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/04/27	05:56:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/27	05:58:56.0	XRT_QT_PROG_SET_442_OG [0x1ba]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2016/04/27	05:58:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2016/04/27	05:59:00.5	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/27	06:14:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	06:14:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	06:14:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/27	06:14:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/04/27	06:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/04/27	06:47:00.0	XRT_Custom_430_OG [0x1ae]							
2016/04/27	06:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/27	07:54:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	07:54:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	07:54:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/27	07:54:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/04/27	07:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/04/27	08:25:30.0	XRT_Custom_430_OG [0x1ae]							
2016/04/27	08:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/27	09:34:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	09:34:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	09:34:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/27	09:34:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/04/27	09:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/04/27	10:03:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	10:03:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	10:03:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2016/04/27	10:04:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	00	00	ac	cd
2016/04/27	10:04:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/04/27	10:04:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/04/27	10:04:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/04/27	10:04:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/04/27	10:04:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/27	10:06:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2016/04/27	10:06:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2016/04/27	10:07:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/27	10:19:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	10:19:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	10:19:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							

2016/04/27	10:20:00.0	AOCS_Or-e-point_Start_6_OG [0x09c]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
		AOCU_NM		5	02-76	00	00	00	d6 67
2016/04/27	10:20:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2016/04/27	10:20:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2016/04/27	10:20:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2016/04/27	10:20:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/04/27	10:20:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/04/27	10:22:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12		
2016/04/27	10:22:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07		
2016/04/27	10:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/04/27	10:35:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/04/27	10:35:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/04/27	10:35:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2016/04/27	10:36:00.0	AOCS_Or-e-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00 00
2016/04/27	10:36:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2016/04/27	10:38:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2016/04/27	10:38:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/04/27	10:38:58.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	08		
2016/04/27	10:39:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/04/27	10:51:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/04/27	10:51:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/04/27	10:51:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2016/04/27	10:52:00.0	AOCS_Or-e-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00	00	00	29 99
2016/04/27	10:52:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2016/04/27	10:52:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2016/04/27	10:52:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2016/04/27	10:52:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/04/27	10:52:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/04/27	10:54:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12		
2016/04/27	10:54:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07		
2016/04/27	10:55:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/04/27	11:22:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/04/27	11:22:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/04/27	11:22:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/04/27	11:22:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/04/27	11:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/04/27	11:42:30.0	AOCS_Or-e-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00	00	00	53 33
2016/04/27	11:42:30.5	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/04/27	11:42:32.5	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/04/27	11:42:34.5	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2016/04/27	11:42:54.5	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2016/04/27	11:42:56.5	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2016/04/27	11:42:58.5	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2016/04/27	11:43:00.5	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/04/27	11:43:02.5	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/04/27	11:45:32.5	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12		
2016/04/27	11:45:34.5	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07		

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2016/04/27	11:45:36.5	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/27	11:58:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	11:58:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	11:58:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/04/27	11:58:30.0	AOCS_Ore-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00 d6 36 b7 8e				
2016/04/27	11:58:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/04/27	11:58:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/04/27	11:58:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/04/27	11:58:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/04/27	11:58:56.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/27	12:01:26.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2016/04/27	12:01:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/04/27	12:01:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/27	12:08:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	12:08:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	12:08:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/04/27	12:08:30.0	AOCS_Ore-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00 b4 b5 db 75				
2016/04/27	12:08:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/04/27	12:08:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/04/27	12:08:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/04/27	12:08:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/04/27	12:08:56.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/27	12:11:26.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2016/04/27	12:11:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/04/27	12:11:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/27	12:24:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	12:24:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	12:24:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/04/27	12:24:30.0	AOCS_Ore-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00 ac 5b 00 00				
2016/04/27	12:24:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/04/27	12:24:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/04/27	12:24:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/04/27	12:24:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/04/27	12:24:56.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/27	12:27:26.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2016/04/27	12:27:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/04/27	12:27:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/27	12:40:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	12:40:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	12:40:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/04/27	12:40:30.0	AOCS_Ore-point_Start_12_OG [0x0a2]							
		AOCU_NM	5	02-76	00 b4 b5 24 8b				
2016/04/27	12:40:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/04/27	12:40:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/04/27	12:40:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/04/27	12:40:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/04/27	12:40:56.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/27	12:43:26.0	XRT_QT_PROG_SET_447_OG [0x1bf]							

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2016/04/27	12:43:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2016/04/27	12:43:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/04/27	13:00:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/27	13:00:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/27	13:00:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/04/27	13:00:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/04/27	13:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/04/27	13:20:30.0	AOCS_OrE-point_Start_13_OG [0x0a3]	AOCU_NM	5	02-76	00 d6 36 48 72	
2016/04/27	13:20:30.5	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/27	13:20:32.5	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/27	13:20:34.5	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2016/04/27	13:20:54.5	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/04/27	13:20:56.5	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/04/27	13:20:58.5	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/04/27	13:21:00.5	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/04/27	13:21:02.5	XRT_FLD_RESET_441_OG [0x1b9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/04/27	13:21:04.5	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12	
2016/04/27	13:21:06.5	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2016/04/27	13:21:08.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/04/27	13:30:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/27	13:30:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/27	13:30:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2016/04/27	13:30:30.0	AOCS_OrE-point_Start_14_OG [0x0a4]	AOCU_NM	5	02-76	00 29 ca b7 8e	
2016/04/27	13:30:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/04/27	13:30:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/04/27	13:30:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/04/27	13:30:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/04/27	13:30:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/04/27	13:33:26.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12	
2016/04/27	13:33:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2016/04/27	13:33:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/04/27	13:40:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/27	13:40:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/27	13:40:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2016/04/27	13:40:30.0	AOCS_OrE-point_Start_15_OG [0x0a5]	AOCU_NM	5	02-76	00 4b 4b db 75	
2016/04/27	13:40:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/04/27	13:40:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/04/27	13:40:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/04/27	13:40:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/04/27	13:40:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/04/27	13:43:26.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12	
2016/04/27	13:43:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2016/04/27	13:43:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/04/27	13:56:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/27	13:56:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/27	13:56:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	

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2016/04/27	13:56:30.0	AOCS_OrE-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	00	53	a5	00	00
2016/04/27	13:56:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0				d8	
2016/04/27	13:56:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0				c8	
2016/04/27	13:56:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0				d0	
2016/04/27	13:56:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0				d5	
2016/04/27	13:56:56.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0				da	
2016/04/27	13:59:26.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0				c4	12
2016/04/27	13:59:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0				c5	07
2016/04/27	13:59:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0				c0	
2016/04/27	14:12:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0				c1	
2016/04/27	14:12:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0				c1	
2016/04/27	14:12:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8				ff	aa 00
2016/04/27	14:12:30.0	AOCS_OrE-point_Start_17_OG [0x0a7]							
		AOCU_NM	5	02-76	00			4b	4b 24 8b
2016/04/27	14:12:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0				d8	
2016/04/27	14:12:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0				c8	
2016/04/27	14:12:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0				d0	
2016/04/27	14:12:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0				d5	
2016/04/27	14:12:56.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0				da	
2016/04/27	14:15:26.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0				c4	12
2016/04/27	14:15:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0				c5	07
2016/04/27	14:15:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0				c0	
2016/04/27	14:28:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0				c1	
2016/04/27	14:28:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0				c1	
2016/04/27	14:28:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8				ff	aa 00
2016/04/27	14:28:30.0	AOCS_OrE-point_Start_18_OG [0x0a8]							
		AOCU_NM	5	02-76	00			29	db 48 72
2016/04/27	14:28:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0				d8	
2016/04/27	14:28:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0				c8	
2016/04/27	14:28:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0				d0	
2016/04/27	14:28:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0				d5	
2016/04/27	14:28:56.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0				da	
2016/04/27	14:31:26.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0				c4	12
2016/04/27	14:31:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0				c5	07
2016/04/27	14:31:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0				c0	
2016/04/27	14:38:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0				c1	
2016/04/27	14:38:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0				c1	
2016/04/27	14:38:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0				da	
2016/04/27	14:38:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0				e8	
2016/04/27	14:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0				e9	
2016/04/27	14:45:00.0	AOCS_OrE-point_Start_19_OG [0x0a9]							
		AOCU_NM	5	02-76	00			f8	fe ae f2
2016/04/27	15:12:00.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0				c1	
2016/04/27	15:12:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0				c1	
2016/04/27	15:12:04.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8				fe	97 00
2016/04/27	15:12:24.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0				d8	
2016/04/27	15:12:26.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0				c8	
2016/04/27	15:12:28.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0				d0	
2016/04/27	15:12:30.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0				d5	
2016/04/27	15:12:32.0	XRT_FLD_RESET_441_OG [0x1b9]							

2016/04/27	15:12:34.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/27	15:12:36.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2016/04/27	15:12:38.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2016/04/27	16:17:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/27	16:17:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	16:17:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	16:17:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/27	16:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/04/27	16:58:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/04/27	16:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]							
2016/04/27	17:45:24.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_CTRL_AUTO_424_OG [0x1a8]							
2016/04/27	17:45:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/27	17:45:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	17:45:30.0	AOCS_Ore-point_Start_2_OG [0x098]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	17:45:48.0	XRT_FLD_DIS_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2016/04/27	17:48:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	AOCS_Ore-point_Start_2_OG [0x098]							
2016/04/27	17:48:26.0	XRT_ARS_DIS_423_OG [0x1a7]	AOCU_NM	5	02-76	00	00	00	00	00
2016/04/27	17:48:28.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/04/27	17:48:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/04/27	17:55:30.0	AOCS_Ore-point_Start_19_OG [0x0a9]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/04/27	17:55:30.5	XRT_CTRL_MANU_400_OG [0x190]	XRT_QT_PROG_SET_434_OG [0x1b2]							
2016/04/27	17:55:32.5	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f			
2016/04/27	17:55:34.5	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/27	17:55:36.5	XRT_PREFLR_STRT_432_OG [0x1b0]	AOCS_Ore-point_Start_19_OG [0x0a9]							
2016/04/27	17:58:44.5	XRT_PREFLR_STOP_419_OG [0x1a3]	AOCU_NM	5	02-76	00	f8	fe	ae	f2
2016/04/27	18:35:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	18:35:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	18:35:04.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/27	18:35:24.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/04/27	18:35:26.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/04/27	18:35:28.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	18:35:30.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	18:35:32.0	XRT_FLD_RESET_441_OG [0x1b9]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2016/04/27	18:35:34.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/04/27	18:35:36.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/04/27	18:35:38.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/04/27	19:34:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/04/27	19:34:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/27	19:34:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2016/04/27	19:34:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2016/04/27	19:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/27	20:12:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	20:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	21:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/27	21:12:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/04/27	21:12:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/04/27	21:12:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	XRT_Custom_430_OG [0x1ae]							
2016/04/27	21:12:06.0	XRT_PREFLR_STOP_419_OG [0x1a3]	XRT_CTRL_AUTO_424_OG [0x1a8]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_FLD_RESET	1	07-F0	da				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				

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2016/04/27	21:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/04/27	21:49:00.0	XRT_Custom_430_OG [0x1ae]							
2016/04/27	21:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/27	22:50:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	22:50:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	22:50:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/27	22:50:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/04/27	22:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/04/27	23:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2016/04/27	23:23:00.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	23:23:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/27	23:23:04.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/04/27	23:23:24.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/04/27	23:23:26.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/04/27	23:23:28.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/04/27	23:23:30.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/04/27	23:23:32.0	XRT_FLD_RESET_441_OG [0x1b9]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/27	23:23:34.0	XRT_QT_PROG_SET_429_OG [0x1ad]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2016/04/27	23:23:36.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/04/27	23:23:38.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/28	00:29:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/28	00:29:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/28	00:29:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/28	00:29:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/04/28	00:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/04/28	00:49:30.0	XRT_Custom_430_OG [0x1ae]							
2016/04/28	00:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/28	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/28	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/28	01:59:58.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/04/28	02:00:00.0	AOCS_ORe-point_Start_20_OG [0x0aa]							
		AOCU_NM	5	02-76	00 ad 59 00 00				
2016/04/28	02:00:18.0	XRT_FLD_DIS_421_OG [0x1a5]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/04/28	02:14:54.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/04/28	02:14:56.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/04/28	02:14:58.0	XRT_QT_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2016/04/28	02:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/28	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/28	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/28	03:59:58.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/04/28	04:00:00.0	AOCS_ORe-point_Start_21_OG [0x0ab]							
		AOCU_NM	5	02-76	00 00 00 56 35				
2016/04/28	04:00:18.0	XRT_FLD_DIS_421_OG [0x1a5]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/04/28	04:14:54.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/04/28	04:14:56.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/04/28	04:14:58.0	XRT_QT_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2016/04/28	04:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/28	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/28	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/28	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							

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2016/04/28	06:00:00.0	AOCS_Or-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
		AOCU_NM		5	02-76	00	00	00	00
2016/04/28	06:00:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2016/04/28	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2016/04/28	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/04/28	06:02:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f		
2016/04/28	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/04/28	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/04/28	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/04/28	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2016/04/28	06:10:00.0	AOCS_Or-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	03	00	00	00
2016/04/28	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2016/04/28	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2016/04/28	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2016/04/28	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/04/28	06:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/04/28	06:12:56.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a		
2016/04/28	06:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07		
2016/04/28	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/04/28	06:50:00.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/04/28	06:50:03.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/04/28	06:50:05.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/04/28	06:50:07.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/04/28	06:53:15.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/04/28	07:23:00.0	XRT_Custom_430_OG [0x1ae]							
2016/04/28	07:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/04/28	08:30:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/04/28	08:30:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/04/28	08:30:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/04/28	08:30:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/04/28	08:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/04/28	09:01:30.0	XRT_Custom_430_OG [0x1ae]							
2016/04/28	09:02:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/04/28	10:11:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/04/28	10:11:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/04/28	10:11:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/04/28	10:11:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/04/28	10:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/04/28	10:44:00.0	AOCS_Or-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00