

XRT Timeline to be uploaded on 2016/05/05

Period: 2016/05/05 09:49:00 - 2016/05/10 10:02:00

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Normal mode

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XOB #1AAC: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 60s cadence, G-band - 384x384 3ms												
Term	Pointing (x, y)					Comment						
05/05 10:02:00 - 05/05 16:05:30	Fixed (-18.0, -957.0)					# OP start + 10min, HOP81 at S-pole						
PROG= 09 Inf.-time(s)												
├─ Subr= 1 1-time(s) 2.0sec												
│ └─ Seqn= 9 2-time(s) 2.0sec												
│ │ Open/G-band Open/G-band close Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
├─ Subr= 2 1-time(s) 2.0sec												
│ └─ Seqn= 7 1-time(s) 30.0sec												
│ │ Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec												
├─ Subr= 3 30-time(s) 2.0sec												
│ └─ Seqn= 57 1-time(s) 60.0sec												
│ │ Open/Al-mesh Open/Al-mesh close Safe Norm 4.00s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec												
│ │ Al-poly/Open Al-poly/Open close Safe Norm 5.66s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec												
<div style="display: flex; justify-content: space-between; font-size: small;"> Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval </div>												

XOB #1B14: Synoptic Q95 2x2 - Al/mesh(24/256/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(45/512/4096) + Ti												
Term	Pointing (x, y)					Comment						
05/05 16:53:00 - 05/05 16:59:54	Fixed (0.0, 0.0)					synoptic, shifted manually						
05/06 05:59:00 - 05/06 06:05:54	Fixed (0.0, 0.0)					synoptic, shifted -4.0 min						
05/06 16:23:00 - 05/06 16:29:54	Fixed (0.0, 0.0)					synoptic shifted manually						
05/07 06:15:30 - 05/07 06:22:24	Fixed (0.0, 0.0)					synoptic, shifted 12.5 min						
PROG= 15 1-time(s)												
├─ Subr= 1 1-time(s) 2.0sec												
│ └─ Seqn= 5 1-time(s) 2.0sec												
│ │ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
│ │ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
│ │ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
│ │ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec												
│ │ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec												
│ └─ Seqn= 1 1-time(s) 2.0sec												
│ │ Open/Al-mesh Open/Al-mesh close Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
│ │ Open/Al-mesh Open/Al-mesh close Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
│ │ Open/Al-mesh Open/Al-mesh close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
│ └─ Seqn= 99 1-time(s) 2.0sec												
│ │ Al-poly/Open Al-poly/Open close Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
│ │ Al-poly/Open Al-poly/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
│ │ Al-poly/Open Al-poly/thick-Al close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
│ └─ Seqn= 67 1-time(s) 2.0sec												
│ │ thin-Be/Open thin-Be/Open close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
│ │ thin-Be/Open thin-Be/Open close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
│ │ thin-Be/Open thin-Be/Open close Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
│ └─ Seqn= 54 1-time(s) 2.0sec												
│ │ Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
│ │ Open/G-band Open/G-band close Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
<div style="display: flex; justify-content: space-between; font-size: small;"> Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval </div>												

XOB #1B2C: HOP308 - Thin-Be AEC 2/3 with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with G-band (3ms/3ms VLS=CLS), 10 cad												
Term	Pointing (x, y)					Comment						
05/05 17:03:00 - 05/05 21:01:00	Track (377.0, 284.6) @ 05/05 17:00:00					# HOP308 (16:30 - 21:30) and AR12539 obs						
05/06 16:33:00 - 05/06 21:36:30	Track (556.3, 276.3) @ 05/06 16:30:00					# HOP308 (16:30 - 21:30) and AR12539 obs						
PROG= 13 Inf.-time(s)												
├─ Subr= 1 1-time(s) 2.0sec												
│ └─ Seqn= 56 1-time(s) 2.0sec												
│ │ Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
│ │ Open/G-band Open/G-band close Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
│ │ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
├─ Subr= 2 12-time(s) 2.0sec												
│ └─ Seqn= 89 15-time(s) 10.0sec												
│ │ thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
│ │ thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec												
│ └─ Seqn= 96 4-time(s) 30.0sec												
│ │ Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec												
│ │ thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 15.0sec												
│ │ Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 2.0sec												
│ │ thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 15.0sec												
│ │ Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec												
│ │ thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec												
<div style="display: flex; justify-content: space-between; font-size: small;"> Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval </div>												

XOB #1B02: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
05/05 21:43:06 - 05/06 04:59:30	Track (377.0, 284.6) @ 05/05 17:00:00	# HOP308 (16:30 - 21:30) and AR12539 obs
05/06 06:09:05 - 05/06 10:00:00	Track (-414.0, 62.0) @ 05/06 06:06:00	# AR12541 obs
05/06 22:17:06 - 05/07 01:59:54	Track (556.3, 276.3) @ 05/06 16:30:00	# HOP308 (16:30 - 21:30) and AR12539 obs
05/07 06:25:30 - 05/07 10:37:30	Track (649.3, 270.5) @ 05/07 06:22:30	# AR12539 obs

PROG= 17 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec										
Seqn= 56	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2	5-time(s)	2.0sec										
Seqn= 75	1-time(s)	2.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 79	4-time(s)	120.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	34.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	34.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1B2D: Disk Plume Filter-Ratio with Al/poly, thin-Be, andf Al/mesh long/short pairs 256x512, 1064 904, with G-band (3ms/3ms VLS=CLS), 120 cad

Term	Pointing (x, y)	Comment
05/06 10:36:06 - 05/06 16:19:54	Track (-7.5, 79.9) @ 05/06 10:20:00	# EIS plume observation

PROG= 01 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec										
Seqn= 60	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	256x512 (1064, 904)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	256x512 (1064, 904)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	256x512 (1064, 904)	Q=98	0	0	2.0sec
Subr= 2	5-time(s)	2.0sec										
Seqn= 62	1-time(s)	2.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	256x512 (1064, 904)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	256x512 (1064, 904)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	256x512 (1064, 904)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	256x512 (1064, 904)	Q=95	3	0	2.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	1x1	256x512 (1064, 904)	Q=95	2	0	2.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	256x512 (1064, 904)	Q=95	3	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1AEC: G-Band Alignment with North Pole Q90 2x2 (G-band and VLS=CLS) - 1msec (Al/poly) - 4096msec - 5min cadence - Partial Sun-wNGT

Term	Pointing (x, y)	Comment
05/07 02:15:00 - 05/07 03:59:54	Fixed (0.0, 930.0)	# Co-alignment at N-pole

PROG= 19 1-time(s)

Subr= 1	24-time(s)	300.0sec										
Seqn= 98	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec
Seqn= 63	1-time(s)	2.0sec										
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec
Seqn= 45	1-time(s)	2.0sec										
Al-poly/Open	med-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x1536 (1024, 768)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1AED: G-Band Alignment with East limb Q90 2x2 (G-band and VLS=CLS) - 1msec - (Al/poly) 1443msec - 8 min cadence-wNGT

Term	Pointing (x, y)	Comment
05/07 04:15:00 - 05/07 06:12:24	Fixed (-970.0, 0.0)	# Co-alignment at E-limb

PROG= 12 1-time(s)

Subr= 1	15-time(s)	480.0sec										
Seqn= 19	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	2x2	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec
Seqn= 43	1-time(s)	2.0sec										
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec
Seqn= 70	1-time(s)	2.0sec										
Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	1536x2048 (1280, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
05/05 10:02:00 - 05/05 16:05:30	Fixed (-18.0, -957.0)	# OP start + 10min, HOP81 at S-pole

05/05 17:03:00 - 05/05 21:01:00	Track (377.0, 284.6)	@ 05/05 17:00:00	# HOP308 (16:30 - 21:30) and AR12539 obs
05/05 21:43:06 - 05/06 04:59:30	Track (377.0, 284.6)	@ 05/05 17:00:00	# HOP308 (16:30 - 21:30) and AR12539 obs
05/06 06:09:05 - 05/06 10:00:00	Track (-414.0, 62.0)	@ 05/06 06:06:00	# AR12541 obs
05/06 10:36:06 - 05/06 16:19:54	Track (-7.5, 79.9)	@ 05/06 10:20:00	# EIS plume observation
05/06 16:33:00 - 05/06 21:36:30	Track (556.3, 276.3)	@ 05/06 16:30:00	# HOP308 (16:30 - 21:30) and AR12539 obs
05/06 22:17:06 - 05/07 01:59:54	Track (556.3, 276.3)	@ 05/06 16:30:00	# HOP308 (16:30 - 21:30) and AR12539 obs
05/07 06:25:30 - 05/07 10:37:30	Track (649.3, 270.5)	@ 05/07 06:22:30	# AR12539 obs

PROG= 07 30-time(s)

Subr= 1 20-time(s) 2.0sec		Seqn= 11 1-time(s) 2.0sec		Al-poly/Open	Al-poly/thick-Al close	Safe	Norm	125ms	Obs 2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
		Seqn=100 1-time(s) 10.0sec		thin-Be/Open	med-Be/Open close	Safe	Norm	125ms	Obs 1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
				med-Be/Open	Open/thick-Al close	Safe	Norm	250ms	Obs 1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
				Open/thick-Al	Open/thick-Be close	Safe	Norm	1.00s	Obs 1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec		Seqn= 10 1-time(s) 2.0sec		med-Al/Open	med-Al/thick-Al close	Safe	Norm	500ms	Obs 1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
				Open/thick-Be	Open/thick-Be close	Safe	Norm	2.00s	Obs 1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
		Seqn= 11 1-time(s) 2.0sec		Al-poly/Open	Al-poly/thick-Al close	Safe	Norm	125ms	Obs 2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
		Seqn= 84 1-time(s) 2.0sec		Open/G-band	Open/G-band open	Safe	Norm	3ms	Obs 1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
				Open/G-band	Open/G-band close	Safe	Norm	3ms	Obs 1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
				Open/thick-Al	Open/thick-Al close	Safe	Dark	1.00s	Obs 1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
				Open/thick-Al	Open/thick-Al close	Safe	Dark	1.00s	Obs 2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval			

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Active Region Search

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NOT USED

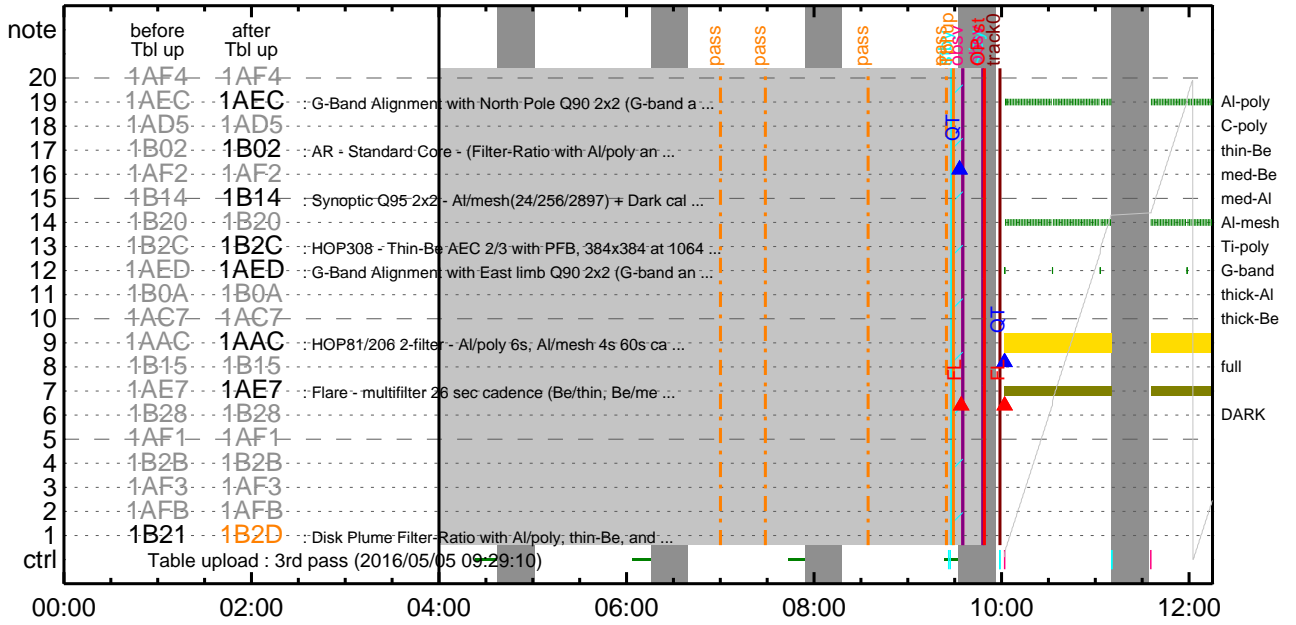
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Flare Detection

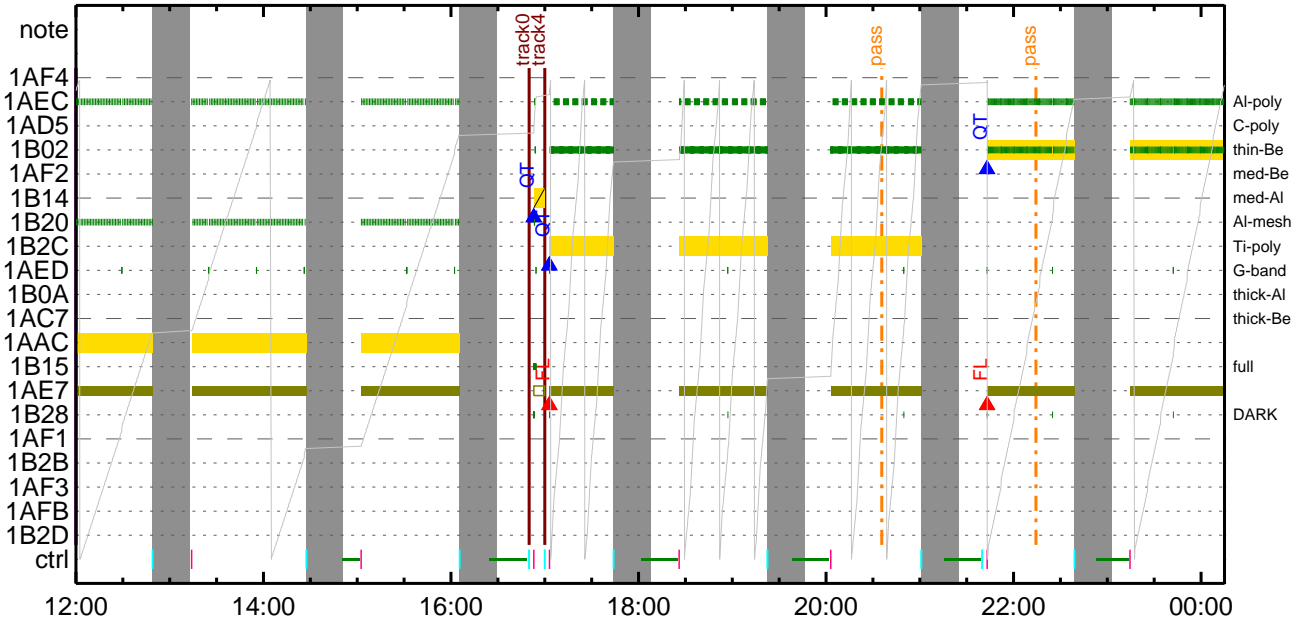
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FLD Patrol													
Term		Pointing (x, y)				Comment							
05/05 17:00:18 - 05/06 05:56:18		Track (377.0, 284.6)		@ 05/05 17:00:00		# HOP308 (16:30 - 21:30) and AR12539 obs							
05/06 06:06:18 - 05/06 16:20:18		Track (-414.0, 62.0)		@ 05/06 06:06:00		# AR12541 obs							
05/06 16:30:18 - 05/07 02:00:18		Track (556.3, 276.3)		@ 05/06 16:30:00		# HOP308 (16:30 - 21:30) and AR12539 obs							
05/07 06:22:48 - 05/10 10:02:00		Track (649.3, 270.5)		@ 05/07 06:22:30		# AR12539 obs							
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8					Q=50	80sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

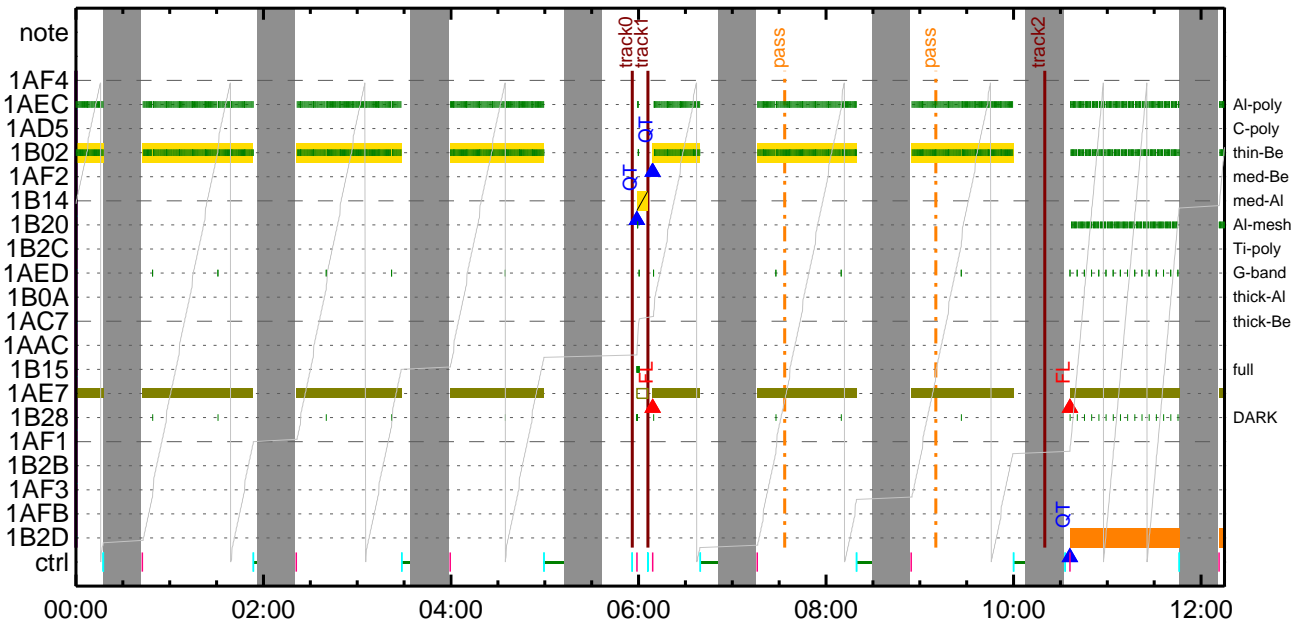
CMDI #0899 2016/05/05



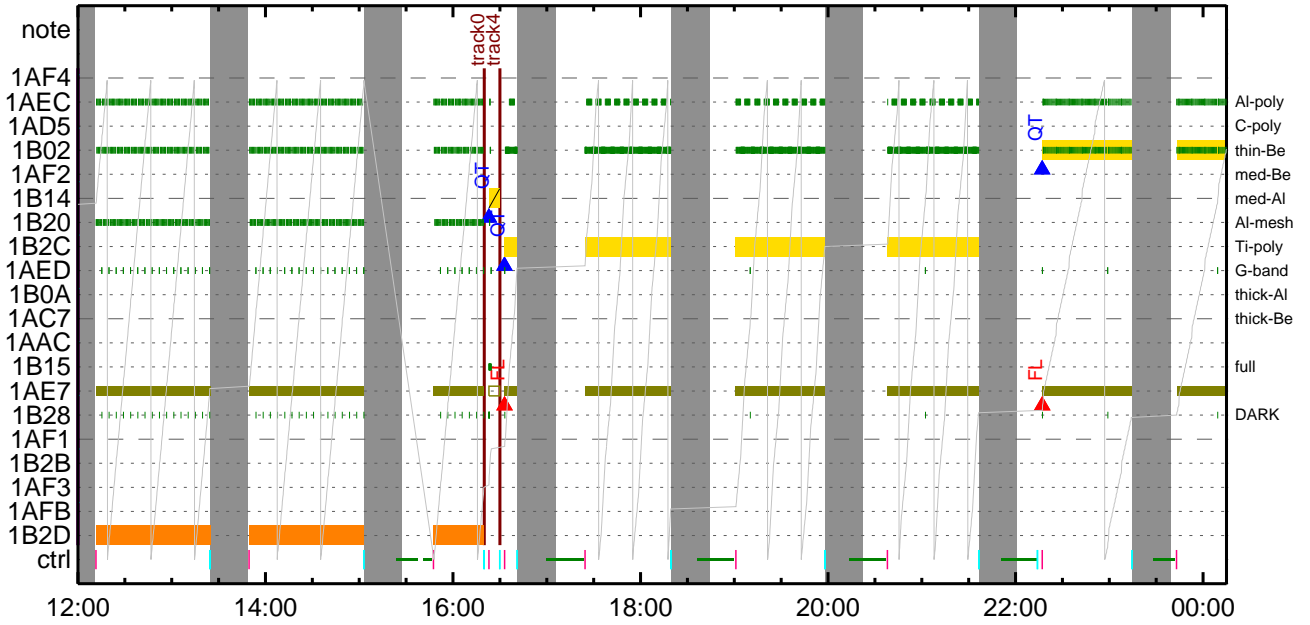
CMDI #0899 2016/05/05



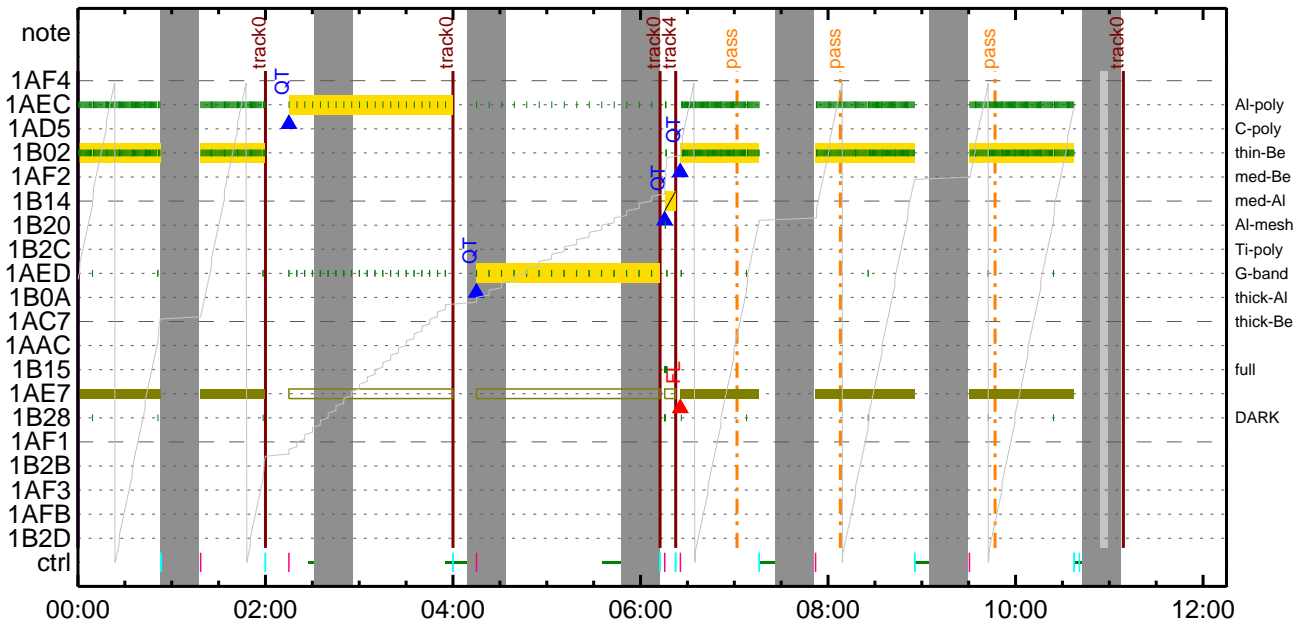
CMDI #0899 2016/05/06



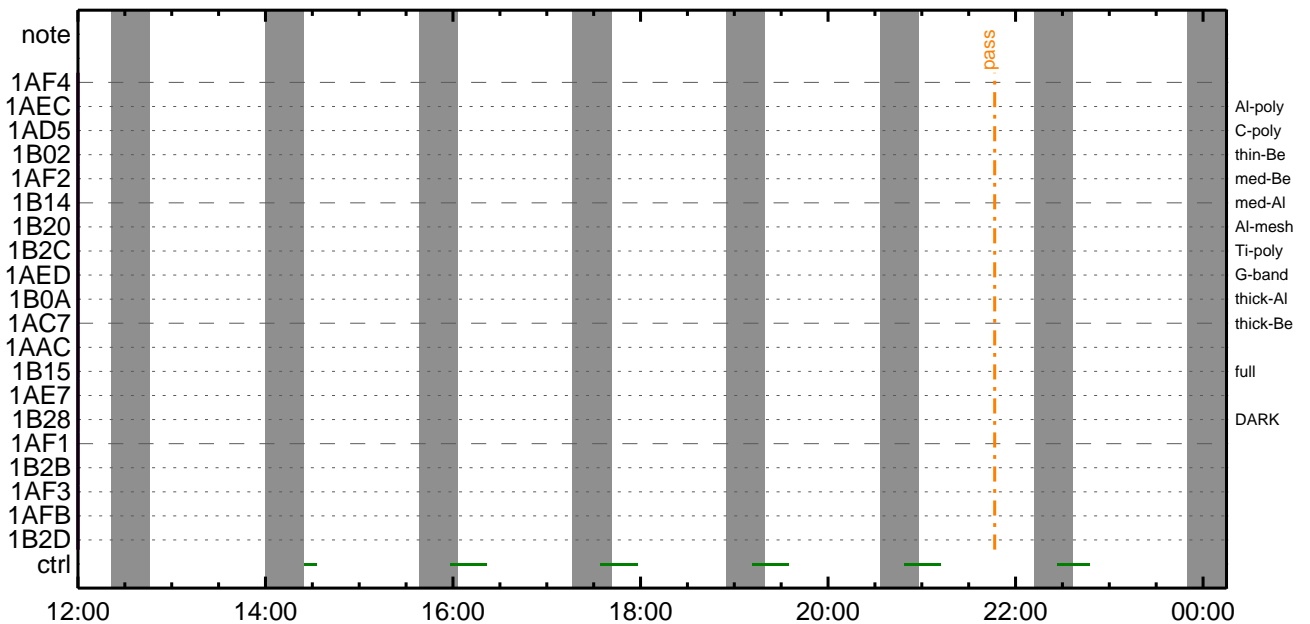
CMDI #0899 2016/05/06



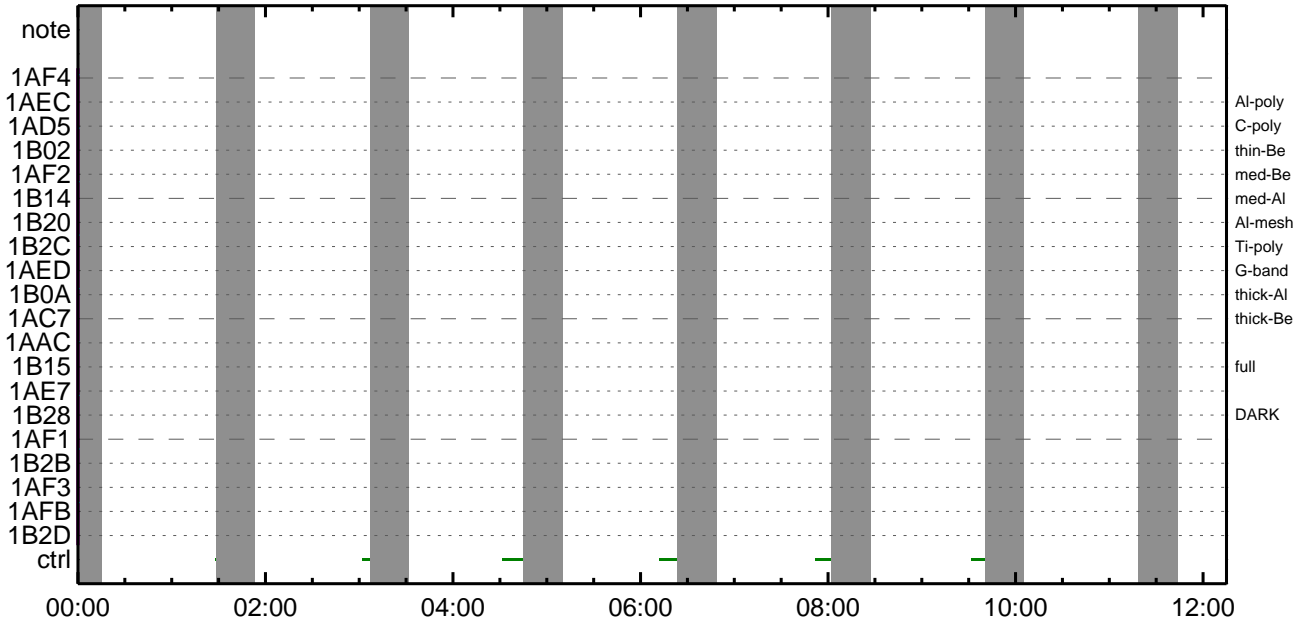
CMDI #0899 2016/05/07



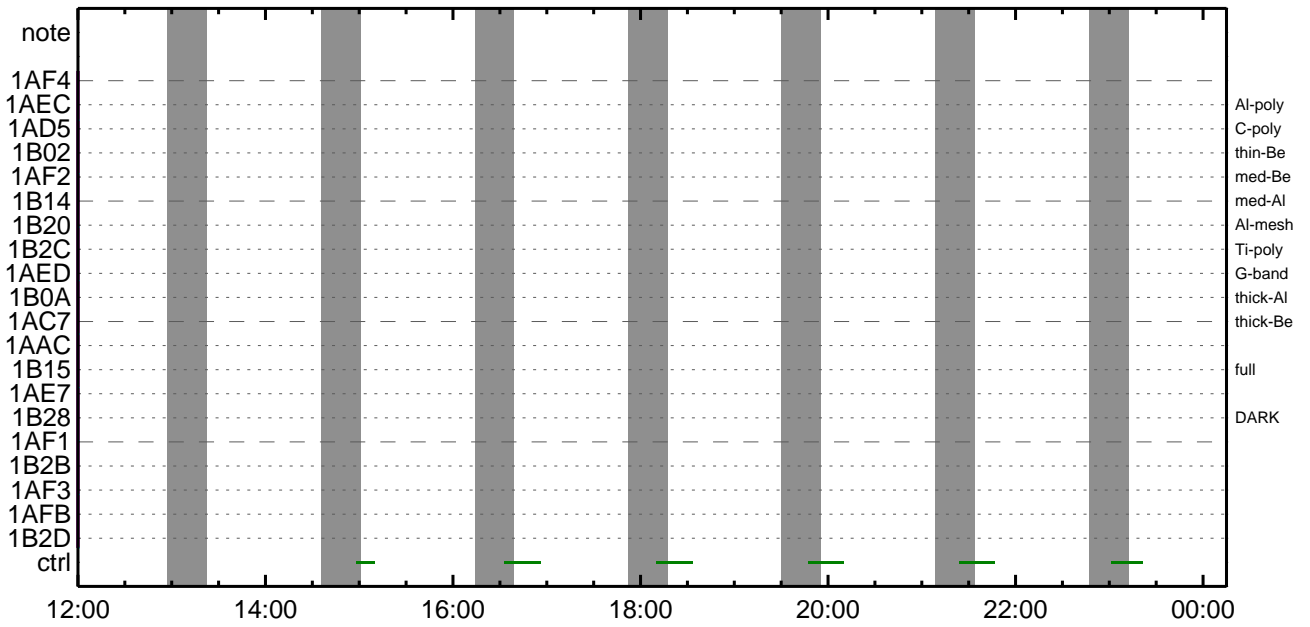
CMDI #0899 2016/05/07



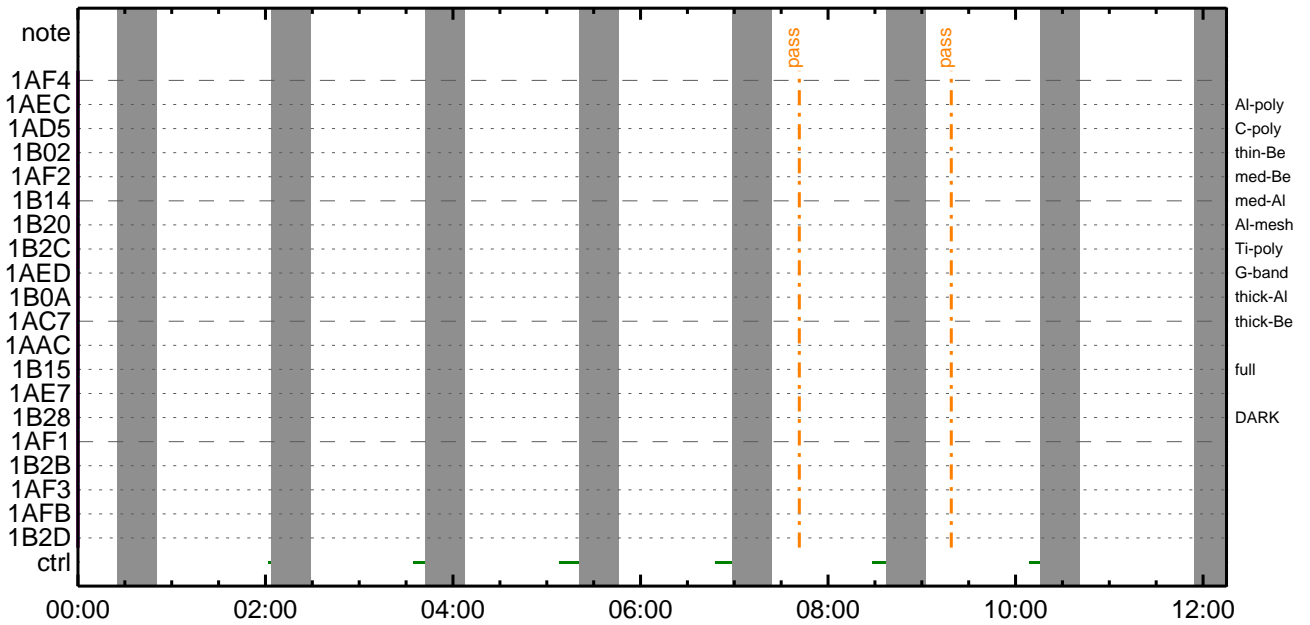
CMDI #0899 2016/05/08



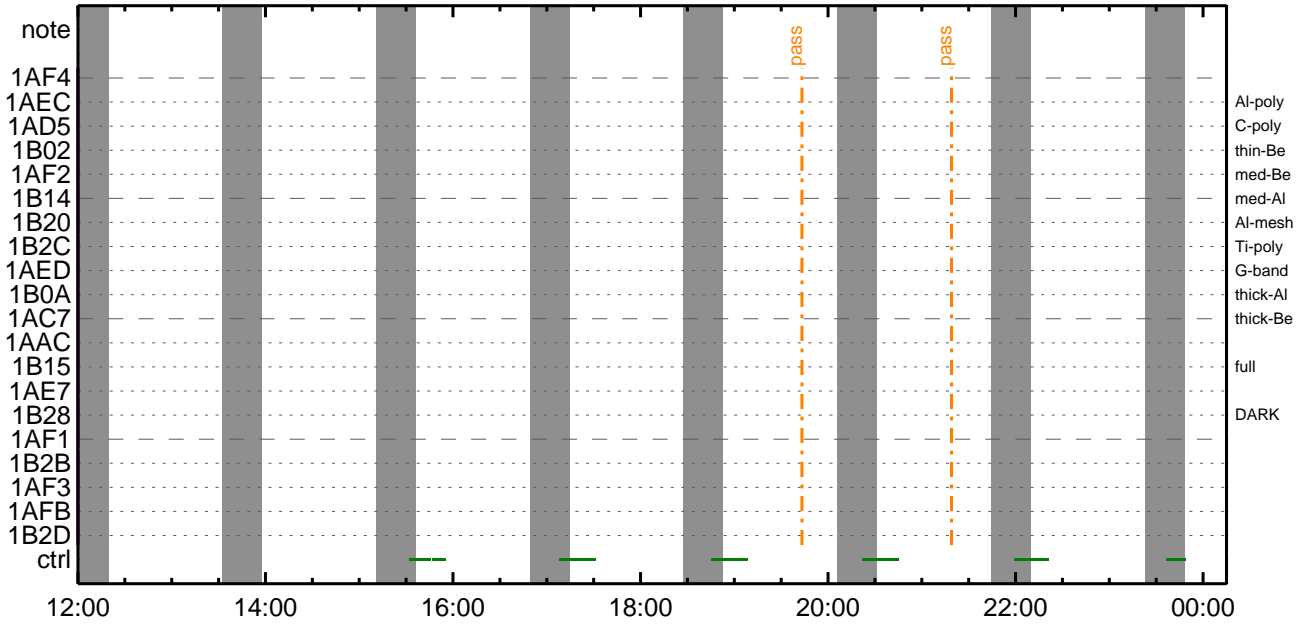
CMDI #0899 2016/05/08



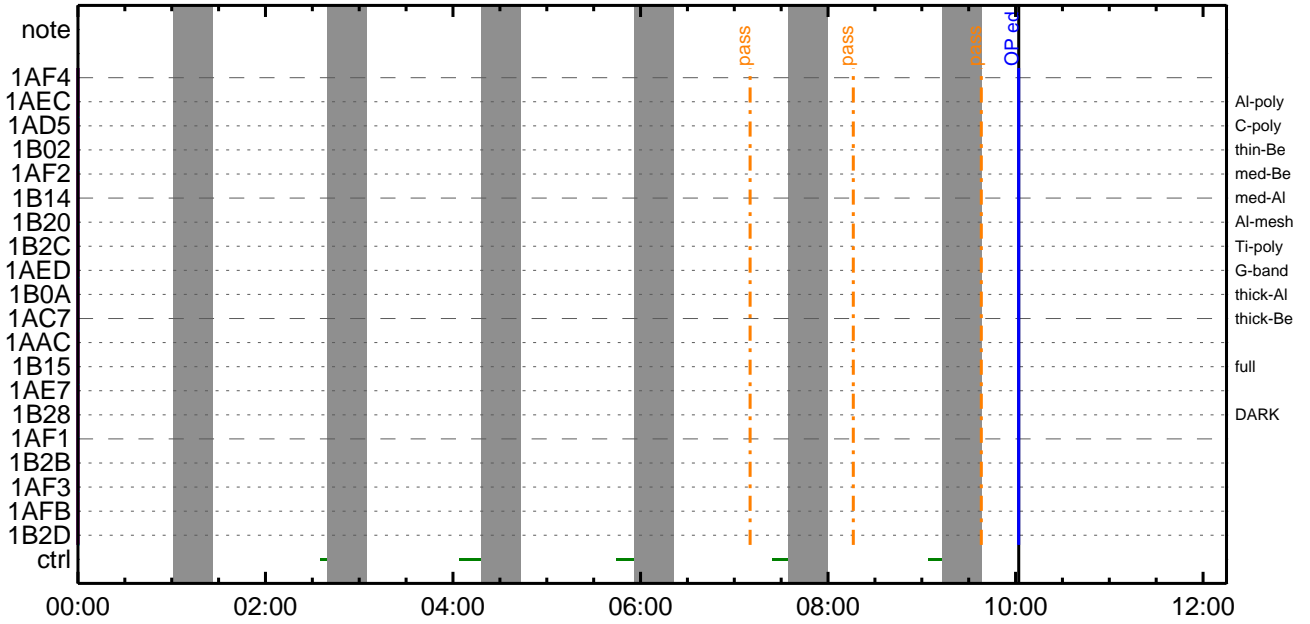
CMDI #0899 2016/05/09



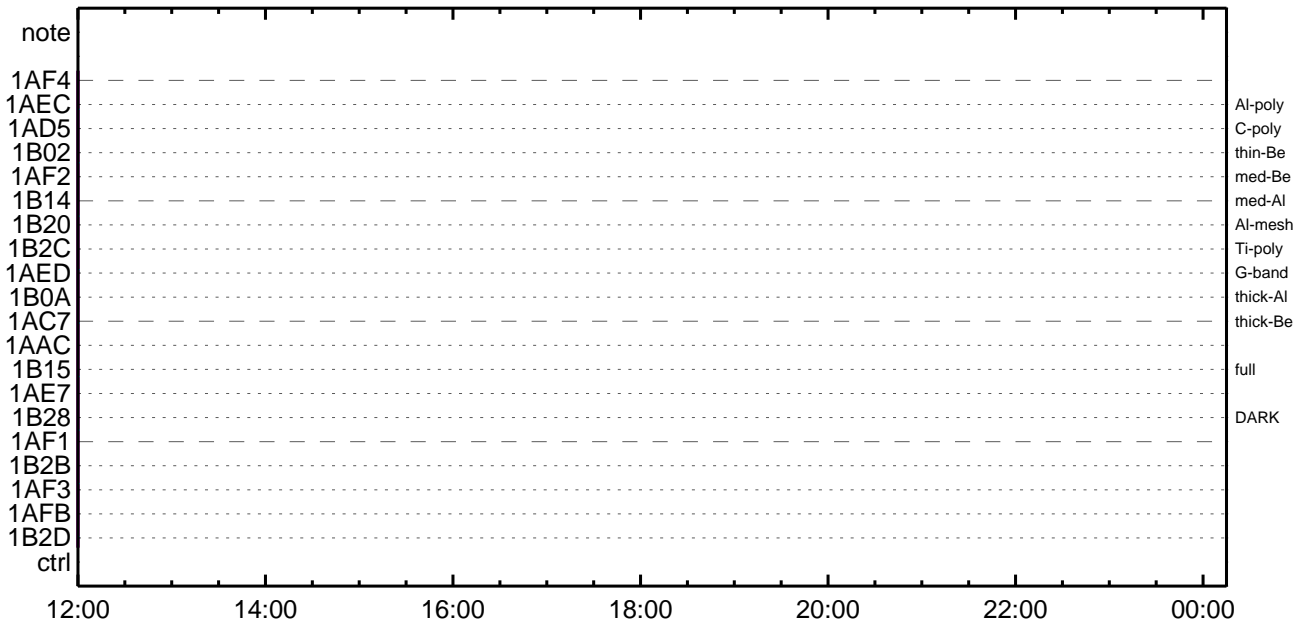
CMDI #0899 2016/05/09



CMDI #0899 2016/05/10



CMDI #0899 2016/05/10



0096 C. ...
0097 C.
0098 C. TI... (UT)
0099 +. TI 2016-05-05 09:44:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C. ... EQ 1COUNTUP
0102 C.
0103 +. TI 2016-05-05 09:44:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C. ... EQ 1COUNTUP
0106 C.
0107 +. TI 2016-05-05 09:44:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C. ... EQ 1COUNTUP
0110 C.
0111 +. TI 2016-05-05 09:48:59.5
0112 DC 01-B2 DHU_OP_START
0113 C. ... EQ 1COUNTUP
0114 C.
0115 C. ...
0116 C. ... EQ ENA
0117 C. ... EQ 4
0118 C. ... EQ DHU
0119 C. ... EQ 0xB3
0120 C.
0121 C. *****
0122 C. TI...
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF; \$ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C. ... EQ 07
0129 C. ... EQ 2B
0130 C. ... EQ 3
0131 C. ... EQ 0
0132 C. ... EQ DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C. ... EQ 7
0136 C. ... EQ 0.25 s
0137 C. ... EQ 32k
0138 C. ... EQ 4M
0139 C. ... EQ EXEC
0140 C.
0141 C. ...
0142 C. ... EQ NON
0143 C.
0144 C. RAM ID=TI_TBL...
0145 C.
0146 C. DHU...
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C. ... EQ 2
0150 C. ... EQ 0.5S
0151 C. ... EQ 32K
0152 C. ... EQ 4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2016-05-05 09:48:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC (41)
0161 C. -----
0162 C. HK1_TI_CMD_NUM = 1 CNTUP []
0163 C. -----
0164 C. ***** SOT END *****
0165 C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2016-05-05 09:48:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC (21 02)
0174 +. TI 2016-05-05 09:48:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC (22)
0177 C. [] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2016-05-05 09:48:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC (c3)
0187 C. [] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 C. ***** MDP ... *****
0192 C. (...)
0193 S. DC-BC dcbc-402:DCBC

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
```



```
0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_CHG_ENA
0131 BC (20)
0132 . C. Verify EIS_MODE_CHG_FLG is ENA
0133 +. DC 07-FC EIS_MODE_MANU
0134 BC (21 02)
0135 . C. Verify EIS in MANUAL mode
0136 . C. Estimated OBSTBL upload time is 48s
0137 C. *****
0138 C. EIS START OBSTBL LOAD
0139 C. *****
0140 . S. RAM ram-820:EIS_OBSTBL
0141 ( )
0142 +. DC 07-FC EIS_DUMP_OBSTBL
0143 BC (07 07 07 00 00 70 00)
0144 C.
0145 C. Execute, after the success of OBSTBL upload.
0146 C. Set EIS TI-commands
0147 +. TI 2016-05-05 09:48:50.0
0148 DC 07-FC EIS_MODE_CHG_ENA
0149 BC (20)
0150 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0151 C. *****
0152 C. EIS END OBSTBL LOAD
0153 C. *****
0154 C.
0155 . C. ***** MDP `ûÃîâî»ò¼ýðÊÃðð¹ñèDCBC•×²è *****
0156 C. (¼â°î¼ýðÊÃðð¹ñèDCBC•×²è *****
0157 . S. DC-BC dcbc-402:DCBC
0158 (MDP_known_event)
0159 C.
0160 C.
0161 . C. ***** ¼D¼¹•İ Daily±¿İñðÈ'Øñ¹ñèDCBC•×²è *****
0162 . S. DC-BC dcbc-153:DCBC
0163 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164 C.
0165 C.
0166 . C. ;ãLOS¼Á¼S¼Å¼Û¼Ä»Û;ã
0167 C.
0168 . C. ***** LOS *****
0169 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-836 2016-05-05 11:51:22 134 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. ÁíË¿ðÁð•µ°Æ»Í×ÁÇóÍYçYÁY×Yí;¼YÉ;ËÈèµ•ííË;ÈðÈ¼°ÇÓð•ð¿¼í¹çðÍ;çÀ®, ùð¹ðèððçÁ+¿®ð•ðÈððð³ðÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop SP table >
0018 +. DC 07-F0 MDP_SP_CTRL_MANU
0019 BC (61)
0020 C. -----
0021 C. MDP_SP_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload SP Observation Table>
0025 . S. RAM ram-283:MDP_OBS_S
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_S >
0029 +. DC 07-F0 MDP_DUMP_SPTBL
0030 BC (83 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_S verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2016-05-05 09:48:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 +. DC 07-F0 MDP_XRT_CTRL_MANU
0052 BC (c1)
0053 +. DC 07-F0 MDP_XRT_MODE_STBY
0054 BC (c3)
0055 . C. ----- Success Verify ? OK / NG____
0056 C.
0057 C. XRT Obs. Table Upload
0058 . S. RAM ram-291:MDP_OBS_X
0059 ( )
0060 C.
0061 +. DC 07-F0 MDP_DUMP_XRTTBL
0062 BC (84 07 00 00 00 3a d4)
0063 . C. ----- Comparison Check ? OK / ERR ____
0064 C.
0065 C.
0066 +. DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 01 b1 b1 04 04)
0068 +. DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 02 b1 b1 08 08)
0070 +. DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 03 b1 b1 08 08)
0072 +. DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 04 b1 b1 06 06)
0074 +. DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 06 85 83 06 06)
0076 +. DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 07 80 80 20 20)
0078 +. DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 08 80 80 20 08)
0080 +. DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 09 80 80 08 20)
0082 +. DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 0a 85 71 04 08)
0084 +. DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0b 80 60 20 18)
0086 +. DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 0c a0 80 18 20)
0088 +. DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 0f 80 80 06 06)
0090 +. DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 10 80 80 08 08)
0092 +. DC 07-F0 MDP_XRT_FLD_ENA
0093 BC (d8)
0094 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0095 BC (c8)
```

```
0096 + DC 07-F0 MDP_XRT_ARS_DIS
0097 BC (d5)
0098 +. DC 07-F0 MDP_XRT_AEC_RESET
0099 BC (d0)
0100 +. DC 07-F0 MDP_XRT_FLD_RESET
0101 BC (da)
0102 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0103 BC (c4 11)
0104 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0105 BC (c5 07)
0106 . C. ----- Success Verify ? OK / NG ____
0107 C.
0108 C.
0109 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0110 C.
0111 +. DC 07-F0 MDP_XRT_MODE_OBSV
0112 BC (c2)
0113 +. TI 2016-05-05 09:48:02.0
0114 DC 07-F0 MDP_XRT_MODE_OBSV
0115 BC (c2)
0116 . C. ----- Success Verify ? OK / NG ____
0117 C.
0118 C. ***** XRT END *****
0119 C.
0120 . C. ***** MDP `úÃîñî»ò%ÝñÊÃðñ¹ñèDCBC•x²è *****
0121 C. (%á°îÝÓÝÃÝÈÝÞÝËÝáÝçÝèñÊ½¼ñ¼Ã»Ûñ¹ñè)
0122 . S. DC-BC dcbc-402:DCBC
0123 (MDP_known_event)
0124 C.
0125 C.
0126 . C. ***** ¥DÝ¹•Ï Daily±¿ÍÑñÊ´Øñ¹ñèDCBC•x²è *****
0127 . S. DC-BC dcbc-153:DCBC
0128 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0129 C.
0130 C.
0131 . C. ¡ãLOS¥Á¥$¥Ã¥-¼Ã»Ûñ¹ñè
0132 C.
0133 . C. ***** LOS *****
0134 C.
```

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*** OP Sequence for XRT ***

2016/05/05	09:58:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/05	09:58:56.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/05	09:58:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2016/05/05	09:59:00.0	AOCS_Ore-point_Start_1_OG [0x097]						
		AOCU_NM	5	02-76	00 55 0e 01 99			
2016/05/05	09:59:18.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2016/05/05	09:59:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2016/05/05	09:59:22.0	XRT_AEC_RESET_448_OG [0x1c0]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2016/05/05	09:59:24.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/05/05	09:59:26.0	XRT_FLD_RESET_433_OG [0x1b1]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/05/05	10:01:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09			
2016/05/05	10:01:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07			
2016/05/05	10:02:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/05/05	11:10:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/05	11:10:32.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/05	11:10:34.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/05/05	11:10:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/05/05	11:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/05/05	11:34:30.0	XRT_Custom_430_OG [0x1ae]						
2016/05/05	11:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/05/05	12:49:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/05	12:49:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/05	12:49:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/05/05	12:49:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/05/05	12:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/05/05	13:13:00.0	XRT_Custom_430_OG [0x1ae]						
2016/05/05	13:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/05/05	14:27:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/05	14:27:32.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/05	14:27:34.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/05/05	14:27:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/05/05	14:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/05/05	15:01:30.0	XRT_Custom_430_OG [0x1ae]						
2016/05/05	15:02:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/05/05	16:05:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/05	16:05:32.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/05	16:05:34.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/05/05	16:05:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/05/05	16:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/05/05	16:49:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/05	16:49:56.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/05	16:49:58.0	XRT_FOCUS_POSITION_403_OG [0x193]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2016/05/05	16:50:00.0	AOCS_Ore-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2016/05/05	16:50:18.0	XRT_FLD_DIS_406_OG [0x196]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2016/05/05	16:52:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2016/05/05	16:52:56.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/05/05	16:52:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f			
2016/05/05	16:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			

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2016/05/05	16:59:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/05	16:59:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/05	16:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/05/05	17:00:00.0	AOCS_Ore-point_Start_3_OG [0x099] AOCU_NM	5	02-76	04 00 00 00 00
2016/05/05	17:00:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2016/05/05	17:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/05/05	17:00:22.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0
2016/05/05	17:00:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/05	17:00:26.0	XRT_FLD_RESET_433_OG [0x1b1] MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/05	17:02:56.0	XRT_QT_PROG_SET_439_OG [0x1b7] MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2016/05/05	17:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4] MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/05/05	17:03:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/05	17:44:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/05	17:44:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/05	17:44:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/05	17:44:06.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/05	17:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/05	18:25:00.0	XRT_Custom_430_OG [0x1ae] MDP_XRT_CUSTOM_430	1	07-F0	c0
2016/05/05	18:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/05	19:22:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/05	19:22:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/05	19:22:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/05	19:22:36.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/05	19:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/05	20:02:00.0	XRT_Custom_430_OG [0x1ae] MDP_XRT_CUSTOM_430	1	07-F0	c0
2016/05/05	20:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/05	21:01:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/05	21:01:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/05	21:01:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/05	21:01:06.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/05	21:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/05	21:40:00.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/05	21:40:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/05	21:40:04.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/05/05	21:40:24.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2016/05/05	21:40:26.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/05/05	21:40:28.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0
2016/05/05	21:40:30.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/05	21:40:32.0	XRT_FLD_RESET_433_OG [0x1b1] MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/05	21:43:02.0	XRT_QT_PROG_SET_428_OG [0x1ac] MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2016/05/05	21:43:04.0	XRT_FL_PROG_SET_436_OG [0x1b4] MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/05/05	21:43:06.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/05	22:39:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/05	22:39:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/05	22:39:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/05	22:39:06.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/05	22:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/05	23:13:30.0	XRT_Custom_430_OG [0x1ae] MDP_XRT_CUSTOM_430	1	07-F0	c0

2016/05/05	23:14:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/05/06	00:17:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/06	00:17:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/06	00:17:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/05/06	00:17:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/05/06	00:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/05/06	00:41:30.0	XRT_Custom_430_OG [0x1ae]							
2016/05/06	00:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/05/06	01:53:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/06	01:53:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/06	01:53:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/05/06	01:53:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/05/06	01:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/05/06	02:20:00.0	XRT_Custom_430_OG [0x1ae]							
2016/05/06	02:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/05/06	03:28:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/06	03:28:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/06	03:28:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/05/06	03:28:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/05/06	03:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/05/06	03:58:30.0	XRT_Custom_430_OG [0x1ae]							
2016/05/06	03:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/05/06	04:59:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/06	04:59:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/06	04:59:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/05/06	04:59:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/05/06	05:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/05/06	05:55:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/06	05:55:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/06	05:55:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/05/06	05:56:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2016/05/06	05:56:18.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/05/06	05:58:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/05/06	05:58:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/05/06	05:58:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f				
2016/05/06	05:59:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/05/06	06:05:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/06	06:05:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/06	06:05:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/05/06	06:06:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2016/05/06	06:06:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/05/06	06:06:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/05/06	06:06:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/05/06	06:06:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/05/06	06:06:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/05/06	06:08:56.0	XRT_ROI_A_409_OG [0x199]							
		MDP_XRT_ROI_SET	6	07-F0	cd 06 85 83 06 06				
		MDP_XRT_ROI_SET	6	07-F0	cd 07 80 80 20 20				
		MDP_XRT_ROI_SET	6	07-F0	cd 08 80 80 20 08				
		MDP_XRT_ROI_SET	6	07-F0	cd 09 80 80 08 20				
		MDP_XRT_ROI_SET	6	07-F0	cd 0a 85 71 04 08				

			MDP_XRT_ROI_SET	6	07-F0	cd 0b 80 60 20 18
			MDP_XRT_ROI_SET	6	07-F0	cd 0c a0 80 18 20
			MDP_XRT_ROI_SET	6	07-F0	cd 0f 80 80 06 06
2016/05/06	06:08:56.5	XRT_ROI_B_421_OG	[0x1a5]			
			MDP_XRT_ROI_SET	6	07-F0	cd 0f 80 80 06 06
			MDP_XRT_ROI_SET	6	07-F0	cd 10 80 80 08 08
2016/05/06	06:09:01.5	XRT_QT_PROG_SET_428_OG	[0x1ac]			
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2016/05/06	06:09:03.5	XRT_FL_PROG_SET_436_OG	[0x1b4]			
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/05/06	06:09:05.5	XRT_CTRL_AUTO_408_OG	[0x198]			
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/06	06:39:30.0	XRT_CTRL_MANU_400_OG	[0x190]			
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/06	06:39:32.0	XRT_CTRL_MANU_402_OG	[0x192]			
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/06	06:39:34.0	XRT_FLD_RESET_415_OG	[0x19f]			
			MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/06	06:39:36.0	XRT_PREFLR_STRT_432_OG	[0x1b0]			
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/06	06:42:44.0	XRT_PREFLR_STOP_419_OG	[0x1a3]			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/06	07:15:00.0	XRT_Custom_430_OG	[0x1ae]			
2016/05/06	07:16:00.0	XRT_CTRL_AUTO_424_OG	[0x1a8]			
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/06	08:19:30.0	XRT_CTRL_MANU_400_OG	[0x190]			
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/06	08:19:32.0	XRT_CTRL_MANU_402_OG	[0x192]			
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/06	08:19:34.0	XRT_FLD_RESET_415_OG	[0x19f]			
			MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/06	08:19:36.0	XRT_PREFLR_STRT_432_OG	[0x1b0]			
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/06	08:22:44.0	XRT_PREFLR_STOP_419_OG	[0x1a3]			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/06	08:53:30.0	XRT_Custom_430_OG	[0x1ae]			
2016/05/06	08:54:30.0	XRT_CTRL_AUTO_424_OG	[0x1a8]			
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/06	10:00:00.0	XRT_CTRL_MANU_400_OG	[0x190]			
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/06	10:00:02.0	XRT_CTRL_MANU_402_OG	[0x192]			
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/06	10:00:04.0	XRT_FLD_RESET_415_OG	[0x19f]			
			MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/06	10:00:06.0	XRT_PREFLR_STRT_432_OG	[0x1b0]			
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/06	10:03:14.0	XRT_PREFLR_STOP_419_OG	[0x1a3]			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/06	10:20:00.0	AOCS_ORe-point_Start_5_OG	[0x09b]			
			AOCU_NM	5	02-76	02 00 00 00 00
2016/05/06	10:33:00.0	XRT_CTRL_MANU_402_OG	[0x192]			
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/06	10:33:02.0	XRT_CTRL_MANU_402_OG	[0x192]			
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/06	10:33:04.0	XRT_FOCUS_POSITION_410_OG	[0x19a]			
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/05/06	10:33:24.0	XRT_FLD_ENA_411_OG	[0x19b]			
			MDP_XRT_FLD_ENA	1	07-F0	d8
2016/05/06	10:33:26.0	XRT_FLRCTRL_ENA_412_OG	[0x19c]			
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/05/06	10:33:28.0	XRT_AEC_RESET_448_OG	[0x1c0]			
			MDP_XRT_AEC_RESET	1	07-F0	d0
2016/05/06	10:33:30.0	XRT_ARS_DIS_423_OG	[0x1a7]			
			MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/06	10:33:32.0	XRT_FLD_RESET_433_OG	[0x1b1]			
			MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/06	10:36:02.0	XRT_QT_PROG_SET_425_OG	[0x1a9]			
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2016/05/06	10:36:04.0	XRT_FL_PROG_SET_436_OG	[0x1b4]			
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/05/06	10:36:06.0	XRT_CTRL_AUTO_408_OG	[0x198]			
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/06	11:46:00.0	XRT_CTRL_MANU_400_OG	[0x190]			
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/06	11:46:02.0	XRT_CTRL_MANU_402_OG	[0x192]			
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/06	11:46:04.0	XRT_FLD_RESET_415_OG	[0x19f]			
			MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/06	11:46:06.0	XRT_PREFLR_STRT_432_OG	[0x1b0]			
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/06	11:49:14.0	XRT_PREFLR_STOP_419_OG	[0x1a3]			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/06	12:10:30.0	XRT_Custom_430_OG	[0x1ae]			
2016/05/06	12:11:30.0	XRT_CTRL_AUTO_424_OG	[0x1a8]			
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/06	13:24:30.0	XRT_CTRL_MANU_400_OG	[0x190]			
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/06	13:24:32.0	XRT_CTRL_MANU_402_OG	[0x192]			
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/06	13:24:34.0	XRT_FLD_RESET_415_OG	[0x19f]			
			MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/06	13:24:36.0	XRT_PREFLR_STRT_432_OG	[0x1b0]			
			MDP_XRT_PREFLR_STRT	1	07-F0	e8

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2016/05/06	13:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/05/06	13:48:30.0	XRT_Custom_430_OG [0x1ae]							
2016/05/06	13:49:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/05/06	15:03:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/06	15:03:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/06	15:03:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/05/06	15:03:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/05/06	15:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/05/06	15:46:30.0	XRT_Custom_430_OG [0x1ae]							
2016/05/06	15:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/05/06	16:19:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/06	16:19:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/06	16:19:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/05/06	16:20:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2016/05/06	16:20:18.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/05/06	16:22:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/05/06	16:22:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/05/06	16:22:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f				
2016/05/06	16:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/05/06	16:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/06	16:29:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/06	16:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/05/06	16:30:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2016/05/06	16:30:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/05/06	16:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/05/06	16:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/05/06	16:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/05/06	16:30:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/05/06	16:32:56.0	XRT_QT_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2016/05/06	16:32:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/05/06	16:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/05/06	16:41:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/06	16:41:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/06	16:41:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/05/06	16:41:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/05/06	16:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/05/06	17:23:30.0	XRT_Custom_430_OG [0x1ae]							
2016/05/06	17:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/05/06	18:19:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/06	18:19:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/06	18:19:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/05/06	18:19:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/05/06	18:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/05/06	19:00:01.0	XRT_Custom_430_OG [0x1ae]							
2016/05/06	19:01:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/05/06	19:58:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/06	19:58:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/06	19:58:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/05/06	19:58:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]							

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2016/05/06	20:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/06	20:37:00.0	XRT_Custom_430_OG [0x1ae]					
2016/05/06	20:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/06	21:36:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/06	21:36:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/06	21:36:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/06	21:36:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/05/06	21:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/06	22:14:00.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/06	22:14:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/06	22:14:04.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2016/05/06	22:14:24.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/05/06	22:14:26.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/05/06	22:14:28.0	XRT_AEC_RESET_448_OG [0x1c0]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/05/06	22:14:30.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/05/06	22:14:32.0	XRT_FLD_RESET_433_OG [0x1b1]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/06	22:17:02.0	XRT_QT_PROG_SET_428_OG [0x1ac]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2016/05/06	22:17:04.0	XRT_FL_PROG_SET_436_OG [0x1b4]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2016/05/06	22:17:06.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/06	23:14:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/06	23:14:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/06	23:14:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/06	23:14:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/05/06	23:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/06	23:42:00.0	XRT_Custom_430_OG [0x1ae]					
2016/05/06	23:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/07	00:53:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/07	00:53:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/07	00:53:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/07	00:53:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/05/07	00:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/07	01:17:30.0	XRT_Custom_430_OG [0x1ae]					
2016/05/07	01:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/07	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/07	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/07	01:59:58.0	XRT_FOCUS_POSITION_438_OG [0x1b6]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2016/05/07	02:00:00.0	AOCS_OrE-point_Start_6_OG [0x09c]					
			AOCU_NM	5	02-76	00 ad 59 00 00	
2016/05/07	02:00:18.0	XRT_FLD_DIS_440_OG [0x1b8]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2016/05/07	02:14:54.0	XRT_FLRCTRL_DIS_441_OG [0x1b9]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2016/05/07	02:14:56.0	XRT_ARS_DIS_435_OG [0x1b3]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/05/07	02:14:58.0	XRT_QT_PROG_SET_444_OG [0x1bc]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 13	
2016/05/07	02:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/07	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/07	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/07	03:59:58.0	XRT_FOCUS_POSITION_438_OG [0x1b6]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2016/05/07	04:00:00.0	AOCS_OrE-point_Start_7_OG [0x09d]					
			AOCU_NM	5	02-76	00 00 00 56 35	
2016/05/07	04:00:18.0	XRT_FLD_DIS_440_OG [0x1b8]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2016/05/07	04:14:54.0	XRT_FLRCTRL_DIS_441_OG [0x1b9]					

2016/05/07	04:14:56.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/05/07	04:14:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/07	04:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2016/05/07	06:12:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/07	06:12:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/07	06:12:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/07	06:12:30.5	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/05/07	06:12:48.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00 00 00 00 00
2016/05/07	06:15:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2016/05/07	06:15:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/05/07	06:15:28.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/07	06:15:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2016/05/07	06:22:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/07	06:22:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/07	06:22:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/07	06:22:30.0	AOCS_ORe-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/05/07	06:22:48.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	04 00 00 00 00
2016/05/07	06:22:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/05/07	06:22:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/05/07	06:22:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/05/07	06:22:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/07	06:25:26.0	XRT_QT_PROG_SET_428_OG [0x1ac]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/07	06:25:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2016/05/07	06:25:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/05/07	07:16:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/07	07:16:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/07	07:16:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/07	07:16:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/07	07:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/07	07:51:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/07	07:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2016/05/07	08:55:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/07	08:55:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/07	08:55:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/07	08:55:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/07	08:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/07	09:29:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/07	09:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2016/05/07	10:37:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/07	10:37:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/07	10:37:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/07	10:37:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/07	10:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/07	10:40:49.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/07	11:09:00.5	AOCS_ORe-point_Start_2_OG [0x098]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		AOCU_NM		5	02-76	00 00 00 00 00