

# XRT Timeline to be uploaded on 2016/05/07

Period: 2016/05/07 10:59:00 - 2016/05/12 10:41:00

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## Normal mode

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### XOB #1AFF: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
05/07 11:12:00 - 05/07 17:04:54	Track ( -592.6, 192.7) <sup>Ⓢ 05/07 11:09:00</sup>	# OP start + 10min. AR 12542.
05/07 21:03:00 - 05/08 05:44:24	Track ( -520.4, 195.4) <sup>Ⓢ 05/07 21:00:00</sup>	# AR 12542.
05/08 05:57:30 - 05/08 17:41:54	Track ( -451.0, 197.3) <sup>Ⓢ 05/08 05:54:30</sup>	# AR 12542.
05/08 18:34:30 - 05/09 05:59:54	Track ( -352.7, 199.3) <sup>Ⓢ 05/08 17:52:00</sup>	# AR cont.
05/09 20:58:00 - 05/10 06:22:54	Track ( -114.9, 200.9) <sup>Ⓢ 05/09 20:55:00</sup>	# AR cont.
05/10 06:36:00 - 05/10 09:50:00	Track ( -27.5, 200.5) <sup>Ⓢ 05/10 06:33:00</sup>	# AR cont.

**PROG= 11 Inf.-time(s)**

Subr= 1	1-time(s)	2.0sec										
Seqn= 56	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2	5-time(s)	2.0sec										
Seqn= 75	1-time(s)	2.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 96	4-time(s)	60.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	15.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	15.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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### XOB #1B15: Synoptic 7 Filter w/ Al-mesh(24/256/2897), Al-poly(45/512/4096), Thin-Be(181/2048/11571) - Thick-Be(65536), Al-poly+Ti-poly(512/4096), Med-Al

Term	Pointing (x, y)	Comment
05/07 17:08:00 - 05/07 17:14:54	Fixed ( 0.0, 0.0)	synoptic
05/08 17:45:00 - 05/08 17:51:54	Fixed ( 0.0, 0.0)	synoptic, shifted -18.0 min
05/09 06:03:00 - 05/09 06:09:54	Fixed ( 0.0, 0.0)	synoptic
05/10 06:26:00 - 05/10 06:32:54	Fixed ( 0.0, 0.0)	synoptic, shifted 23.0 min

**PROG= 08 1-time(s)**

Subr= 1	1-time(s)	2.0sec										
Seqn= 5	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 1	1-time(s)	2.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 99	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 67	1-time(s)	2.0sec										
thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54	1-time(s)	4.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2	1-time(s)	2.0sec										
Seqn= 46	2-time(s)	2.0sec										
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 4	2-time(s)	2.0sec										
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 71	2-time(s)	2.0sec										
med-Al/Open	med-Al/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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### XOB #1B2C: HOP308 - Thin-Be AEC 2/3 with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with G-band (3ms/3ms VLS=CLS), 10 cad

Term	Pointing (x, y)	Comment
05/07 17:59:30 - 05/07 20:33:30	Track ( 714.1, 265.5) <sup>Ⓢ 05/07 17:15:00</sup>	# HOP 308 (officially 17-21UT). AR 12539.

PROG= 13 Inf.-time(s)													
Subr= 1		1-time(s)		2.0sec									
Seqn= 56		1-time(s)		2.0sec									
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2		12-time(s)		2.0sec									
Seqn= 89		15-time(s)		10.0sec									
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Seqn= 96		4-time(s)		30.0sec									
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	15.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	15.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1B14: Synoptic Q95 2x2 - Al/mesh(24/256/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(45/512/4096) + Ti**

Term	Pointing (x, y)	Comment
05/08 05:47:30 - 05/08 05:54:24	Fixed ( 0.0, 0.0)	synoptic, shifted -15.5 min
05/09 20:48:00 - 05/09 20:54:54	Fixed ( 0.0, 0.0)	# synoptic

PROG= 15 1-time(s)													
Subr= 1		1-time(s)		2.0sec									
Seqn= 5		1-time(s)		2.0sec									
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 1		1-time(s)		2.0sec									
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 99		1-time(s)		2.0sec									
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 67		1-time(s)		2.0sec									
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54		1-time(s)		2.0sec									
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1B18: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with**

Term	Pointing (x, y)	Comment
05/09 06:13:00 - 05/09 09:29:54	Track ( -246.6, 200.5) @ 05/09 06:10:00	# AR cont.

PROG= 14 Inf.-time(s)													
Subr= 1		1-time(s)		2.0sec									
Seqn= 56		1-time(s)		2.0sec									
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2		5-time(s)		2.0sec									
Seqn= 75		1-time(s)		2.0sec									
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 50		4-time(s)		90.0sec									
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	25.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	25.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1B30: Mercury transit Mc-pnt with Al-poly (4s/8s) 512x512 center readout**

Term	Pointing (x, y)	Comment
05/09 09:33:00 - 05/09 11:54:00	Fixed ( -908.0, -320.0)	# Mercury Transit 1/6 - Limb Pointing
05/09 14:00:00 - 05/09 15:11:00	Fixed ( -187.0, -350.0)	# Mercury Transit 3/6 - Mercury Pointing (tentative)
05/09 19:09:30 - 05/09 20:06:00	Fixed ( 872.0, -320.0)	# Mercury Transit 6/6 - Limb Pointing

<b>PROG= 10 1-time(s)</b>													
└─ <b>Subr= 1 3-time(s) 2.0sec</b>													
└─ <b>Seqn= 53 1-time(s) 60.0sec</b>													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ <b>Seqn= 82 48-time(s) 30.0sec</b>													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	8.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #1B2E: Mercury transit DC-pnt E1 with Al-poly (4s/8s) 768x384 offset readout</b>												
Term			Pointing (x, y)						Comment			
05/09 12:20:30 - 05/09 13:32:30			Fixed ( 0.0, 0.0)						# Mercury Transit 2/6 - Disk Center			

<b>PROG= 06 2-time(s)</b>													
└─ <b>Subr= 1 3-time(s) 2.0sec</b>													
└─ <b>Seqn= 53 1-time(s) 60.0sec</b>													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ <b>Seqn= 95 28-time(s) 30.0sec</b>													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	1x1	768x384 (504, 704)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	8.00s	Obs	1x1	768x384 (504, 704)	Q=95	0	0	2.0sec
└─ <b>Subr= 2 1-time(s) 2.0sec</b>													
└─ <b>Seqn= 74 1-time(s) 60.0sec</b>													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ <b>Seqn= 95 28-time(s) 30.0sec</b>													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	1x1	768x384 (504, 704)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	8.00s	Obs	1x1	768x384 (504, 704)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #1B2F: Mercury transit DC-pnt W1 with Al-poly (4s/8s) 768x384 offset readout</b>												
Term			Pointing (x, y)						Comment			
05/09 15:56:00 - 05/09 18:27:30			Fixed ( 0.0, 0.0)						# Mercury Transit 4/6 & 5/6 - Disk Center			

<b>PROG= 18 1-time(s)</b>													
└─ <b>Subr= 1 3-time(s) 2.0sec</b>													
└─ <b>Seqn= 53 1-time(s) 60.0sec</b>													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ <b>Seqn= 31 28-time(s) 30.0sec</b>													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	1x1	768x384 (1528, 704)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	8.00s	Obs	1x1	768x384 (1528, 704)	Q=95	0	0	2.0sec
└─ <b>Subr= 2 1-time(s) 2.0sec</b>													
└─ <b>Seqn= 74 1-time(s) 60.0sec</b>													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ <b>Seqn= 31 28-time(s) 30.0sec</b>													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	1x1	768x384 (1528, 704)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	8.00s	Obs	1x1	768x384 (1528, 704)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

\* \* \* \* \*

**Flare mode**

\* \* \* \* \*

<b>XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x384)</b>												
Term			Pointing (x, y)						Comment			
05/07 11:12:00 - 05/07 17:04:54			Track ( -592.6, 192.7) @ 05/07 11:09:00						# OP start + 10min. AR 12542.			
05/07 17:59:30 - 05/07 20:33:30			Track ( 714.1, 265.5) @ 05/07 17:15:00						# HOP 308 (officially 17-21UT). AR 12539.			
05/07 21:03:00 - 05/08 05:44:24			Track ( -520.4, 195.4) @ 05/07 21:00:00						# AR 12542.			
05/08 05:57:30 - 05/08 17:41:54			Track ( -451.0, 197.3) @ 05/08 05:54:30						# AR 12542.			
05/08 18:34:30 - 05/09 05:59:54			Track ( -352.7, 199.3) @ 05/08 17:52:00						# AR cont.			
05/09 06:13:00 - 05/09 09:29:54			Track ( -246.6, 200.5) @ 05/09 06:10:00						# AR cont.			
05/09 20:58:00 - 05/10 06:22:54			Track ( -114.9, 200.9) @ 05/09 20:55:00						# AR cont.			
05/10 06:36:00 - 05/10 09:50:00			Track ( -27.5, 200.5) @ 05/10 06:33:00						# AR cont.			

<b>PROG= 07 30-time(s)</b>													
└─ <b>Subr= 1 20-time(s) 2.0sec</b>													
└─ <b>Seqn= 11 1-time(s) 2.0sec</b>													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ <b>Seqn=100 1-time(s) 10.0sec</b>													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
	med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ <b>Subr= 2 1-time(s) 2.0sec</b>													
└─ <b>Seqn= 10 1-time(s) 2.0sec</b>													

med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 11</b>			<b>1-time(s)</b>			<b>2.0sec</b>						
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Seqn= 84</b>			<b>1-time(s)</b>			<b>2.0sec</b>						
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

\* \* \* \* \*

### Active Region Search

\* \* \* \* \*

NOT USED

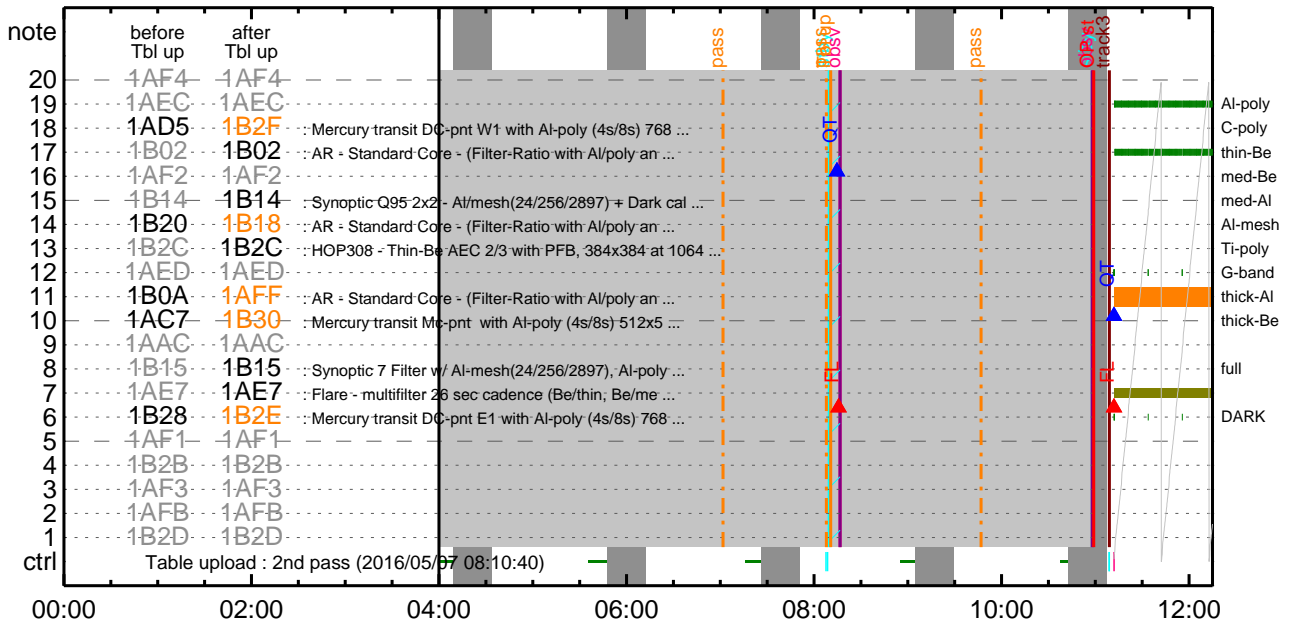
\* \* \* \* \*

### Flare Detection

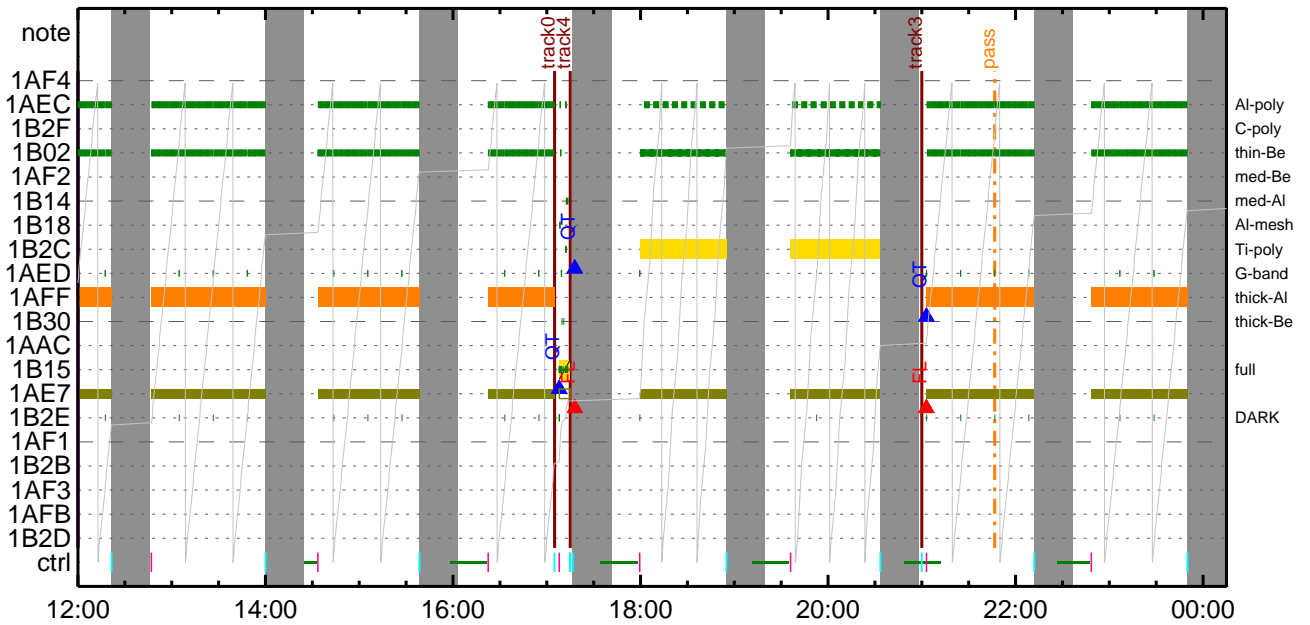
\* \* \* \* \*

FLD Patrol												
Term	Pointing (x, y)							Comment				
05/07 17:15:18 - 05/08 05:44:48	Track ( 714.1, 265.5)	<sup>Ⓢ</sup> 05/07 17:15:00	# HOP 308 (officially 17-21UT). AR 12539.									
05/08 05:54:48 - 05/08 17:42:18	Track ( -451.0, 197.3)	<sup>Ⓢ</sup> 05/08 05:54:30	# AR 12542.									
05/08 17:52:18 - 05/09 06:00:18	Track ( -352.7, 199.3)	<sup>Ⓢ</sup> 05/08 17:52:00	# AR cont.									
05/09 06:10:18 - 05/09 09:30:18	Track ( -246.6, 200.5)	<sup>Ⓢ</sup> 05/09 06:10:00	# AR cont.									
05/09 20:55:18 - 05/10 06:23:18	Track ( -114.9, 200.9)	<sup>Ⓢ</sup> 05/09 20:55:00	# AR cont.									
05/10 06:33:18 - 05/12 10:41:00	Track ( -27.5, 200.5)	<sup>Ⓢ</sup> 05/10 06:33:00	# AR cont.									
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50			80sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

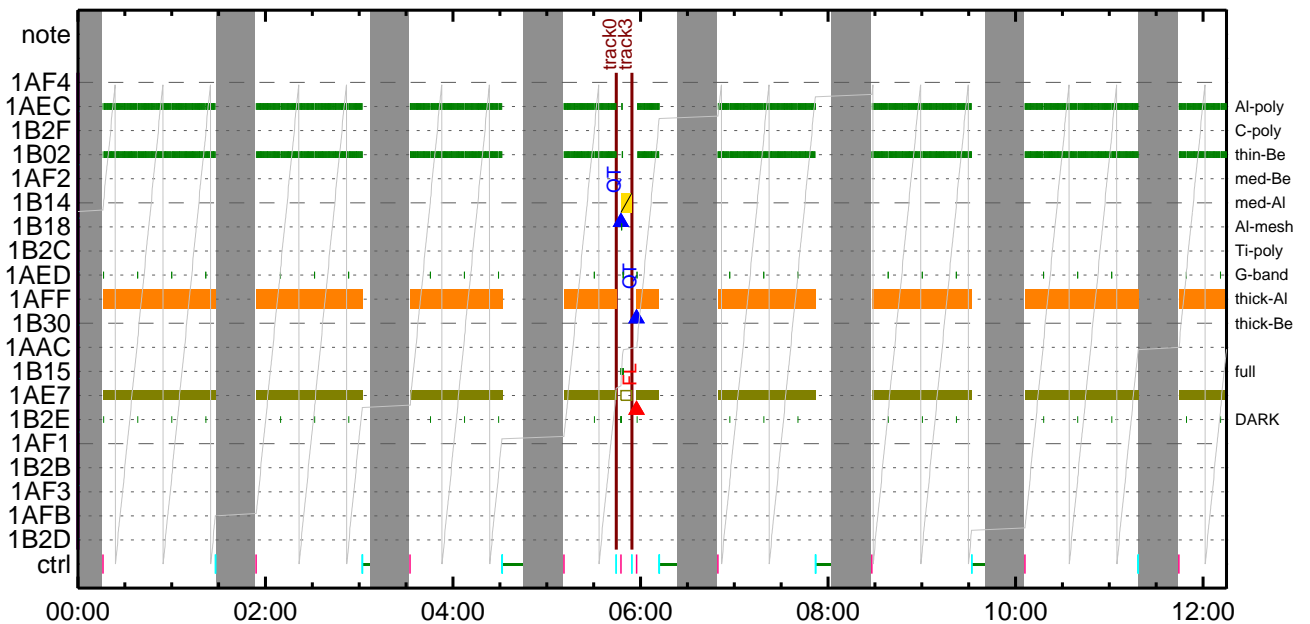
### CMDI #0904 2016/05/07



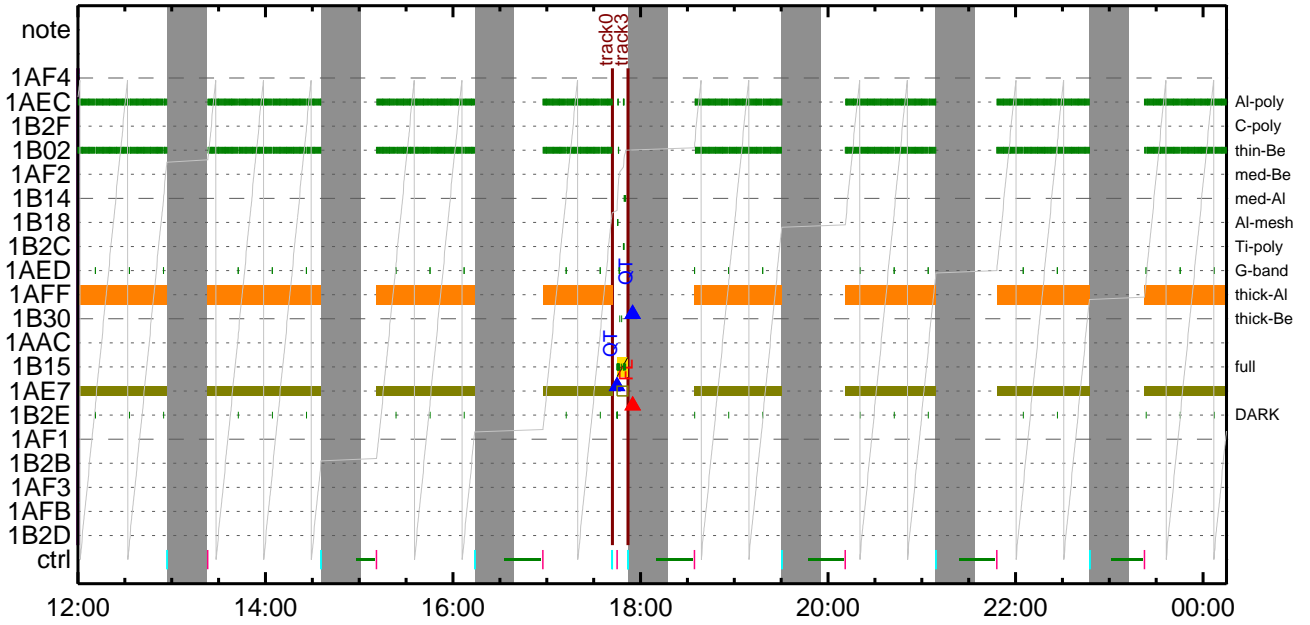
### CMDI #0904 2016/05/07



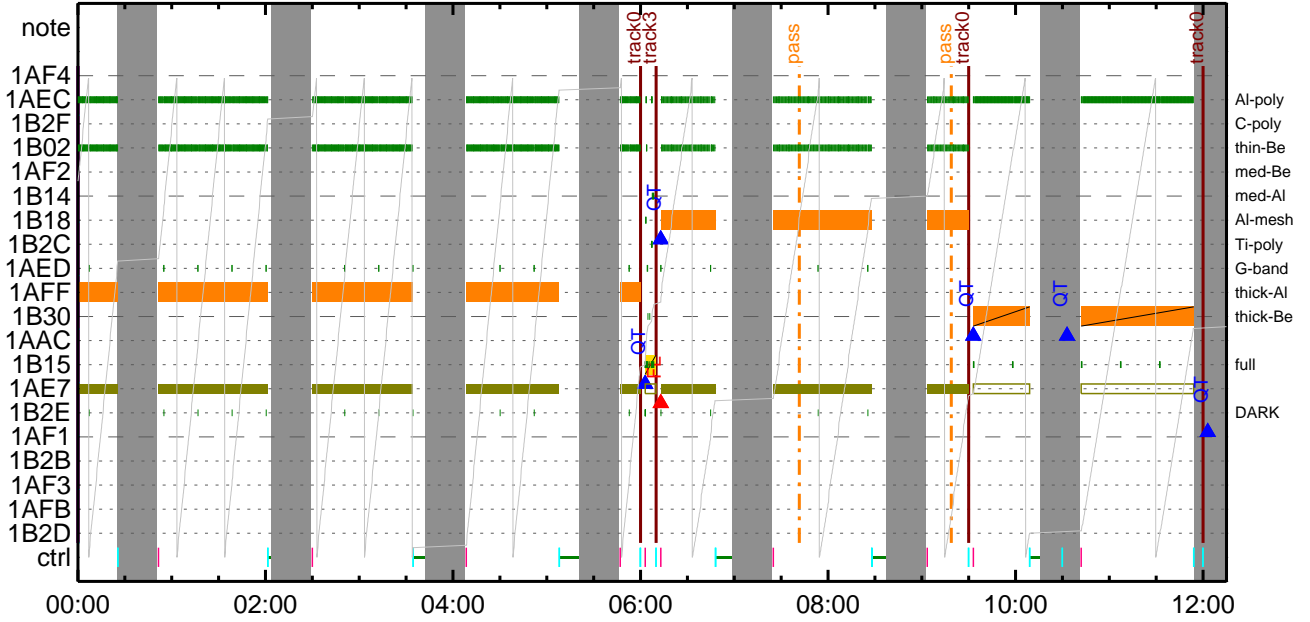
### CMDI #0904 2016/05/08



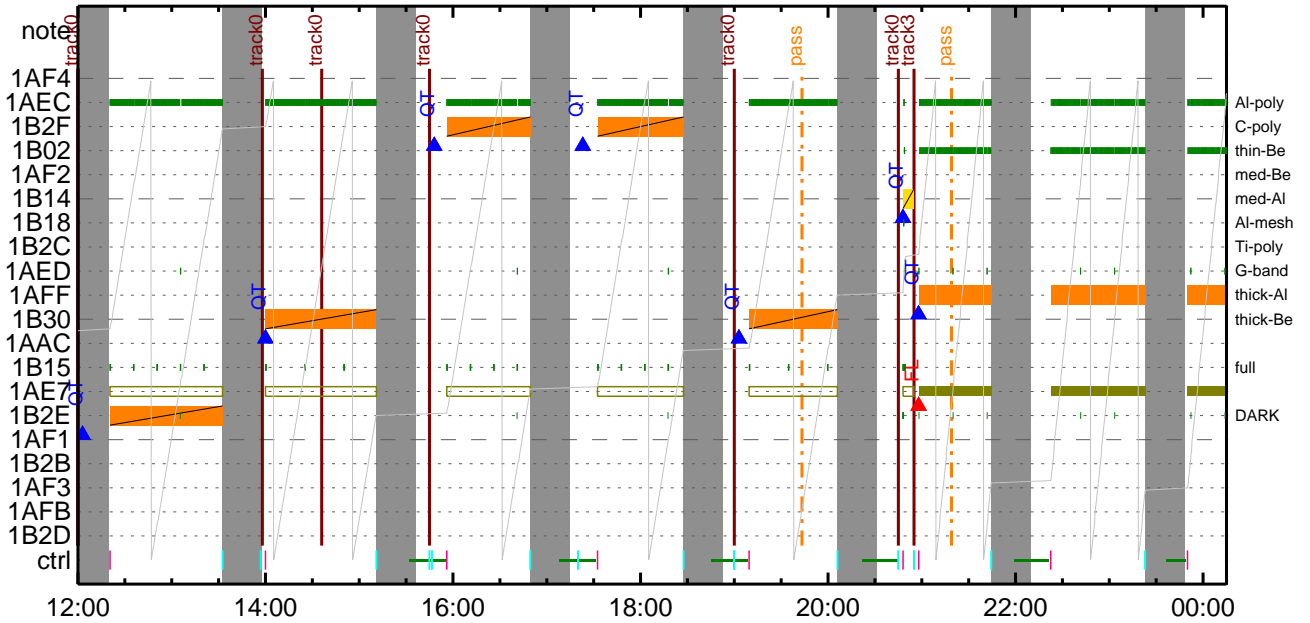
CMDI #0904 2016/05/08



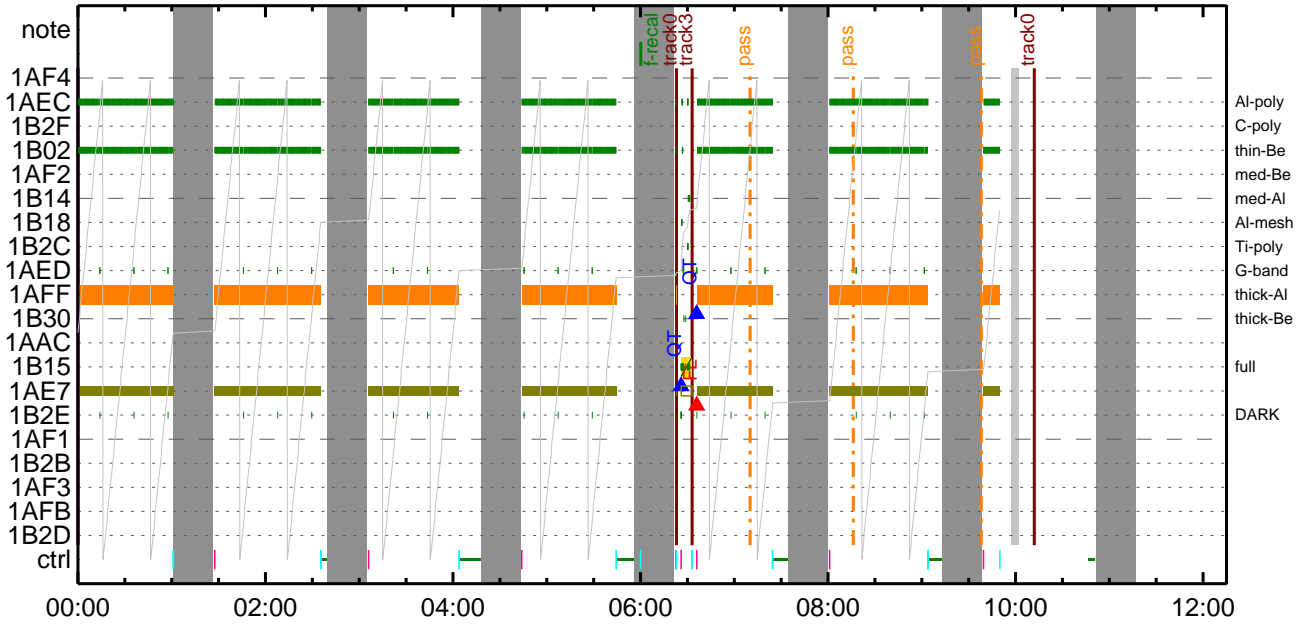
CMDI #0904 2016/05/09



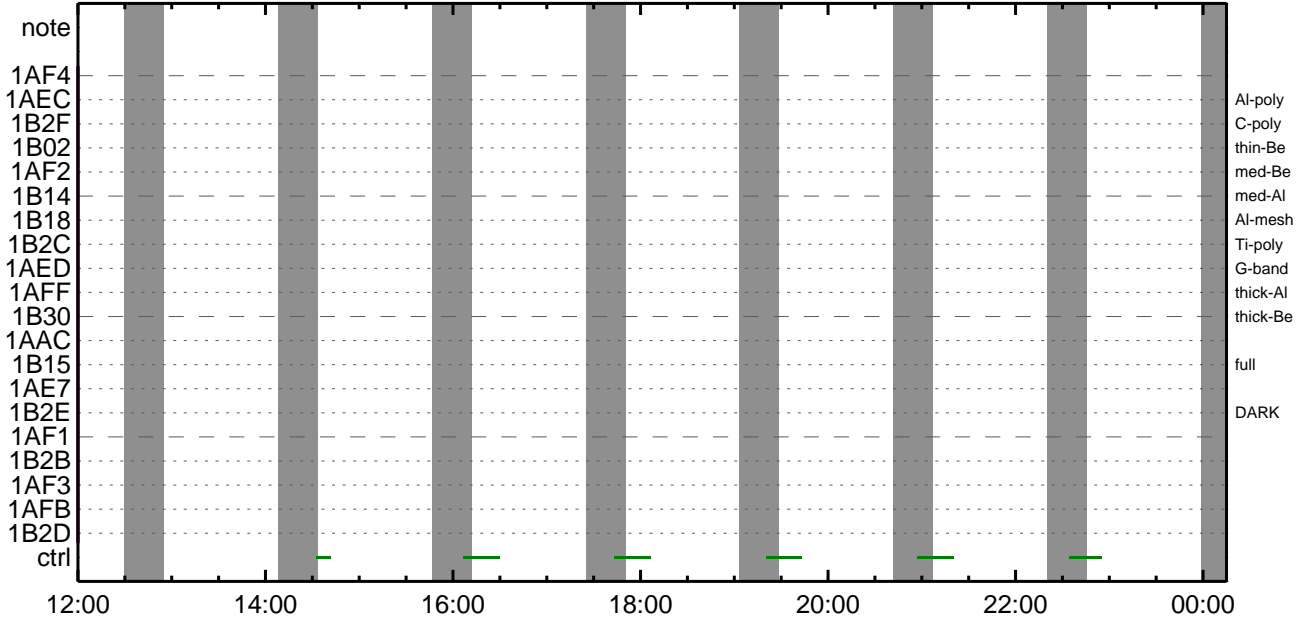
CMDI #0904 2016/05/09



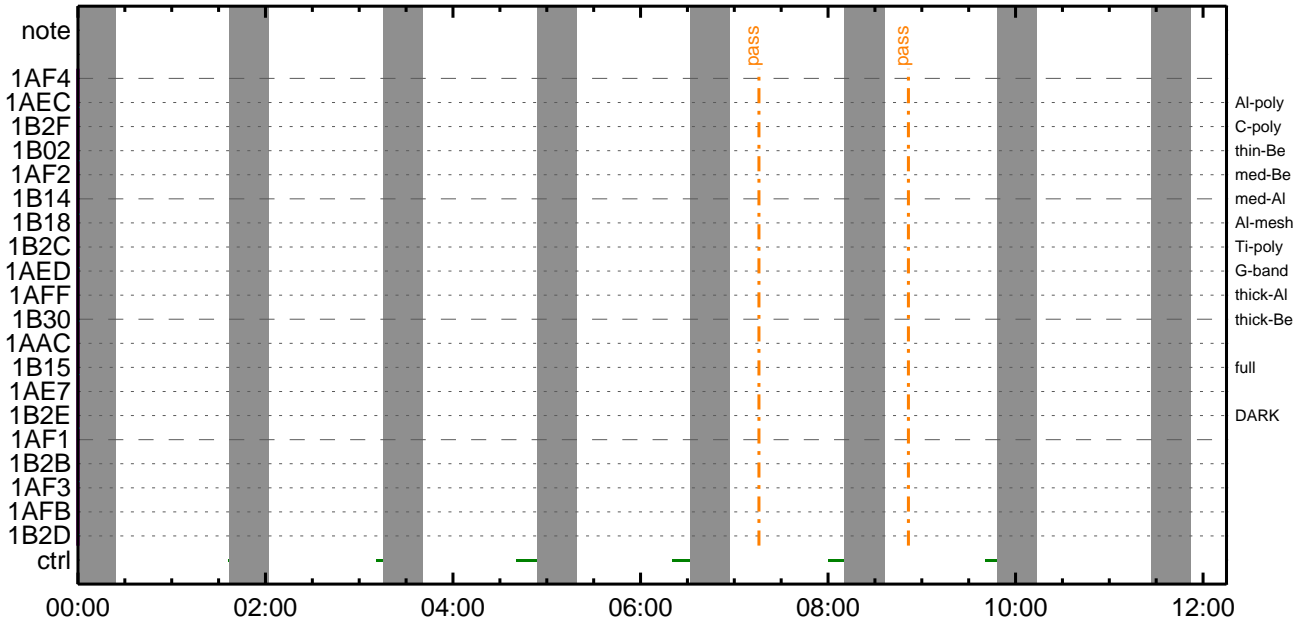
CMDI #0904 2016/05/10



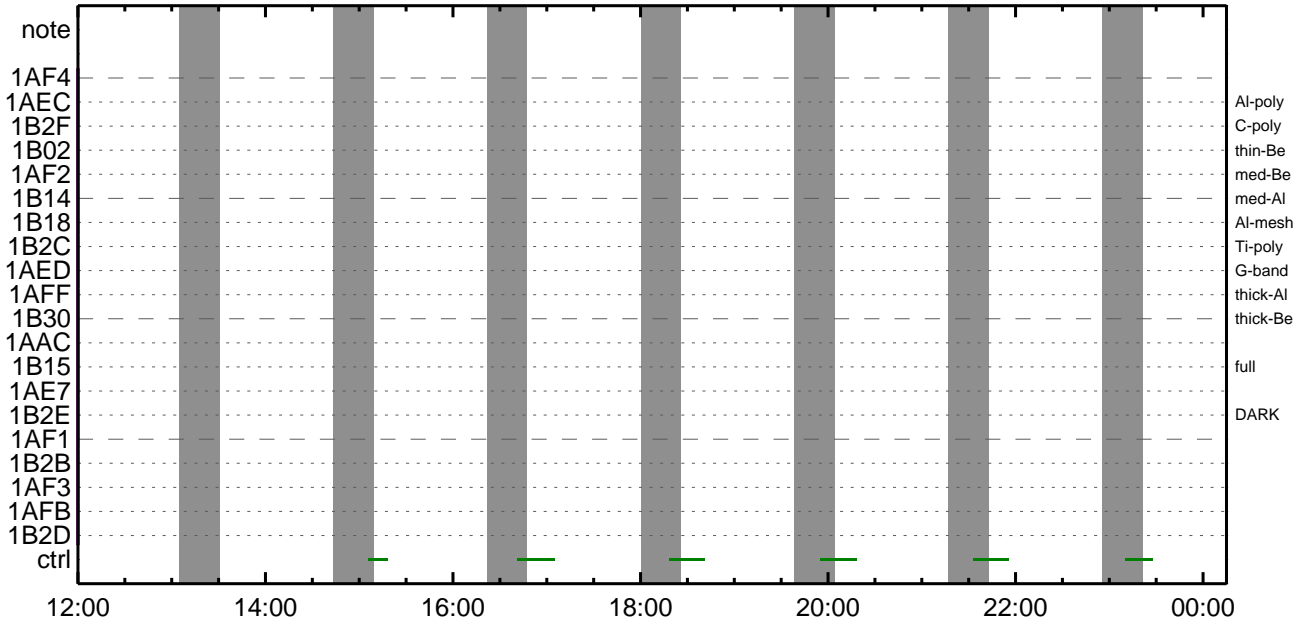
CMDI #0904 2016/05/10



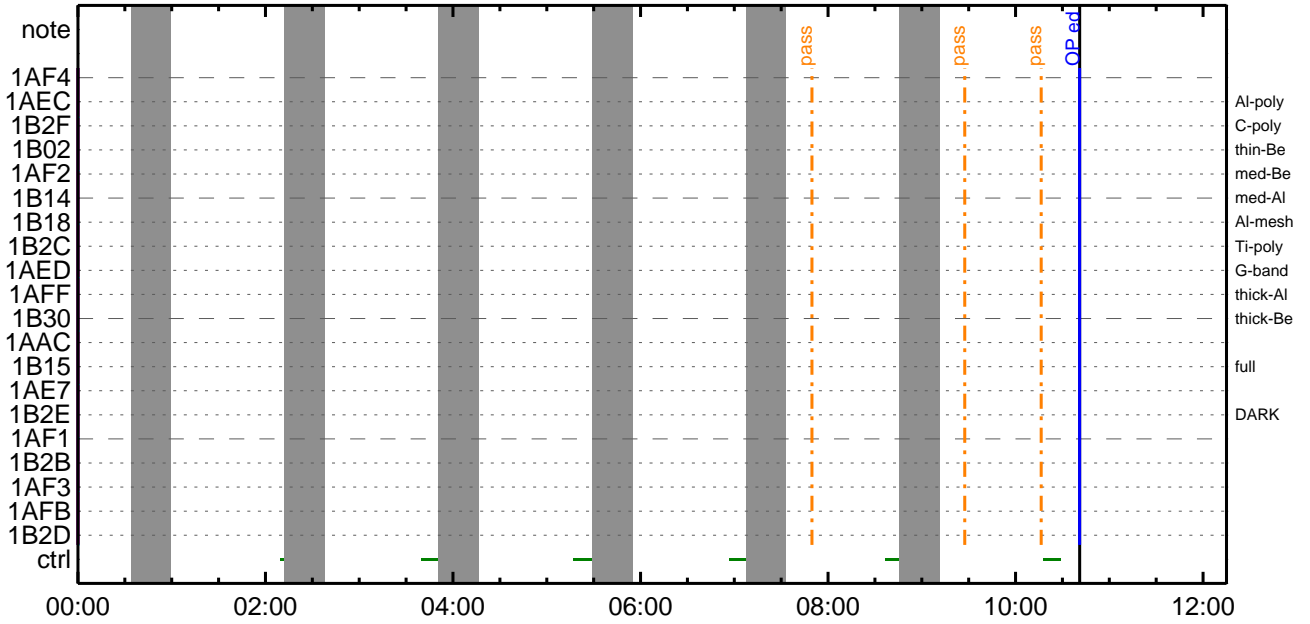
CMDI #0904 2016/05/11



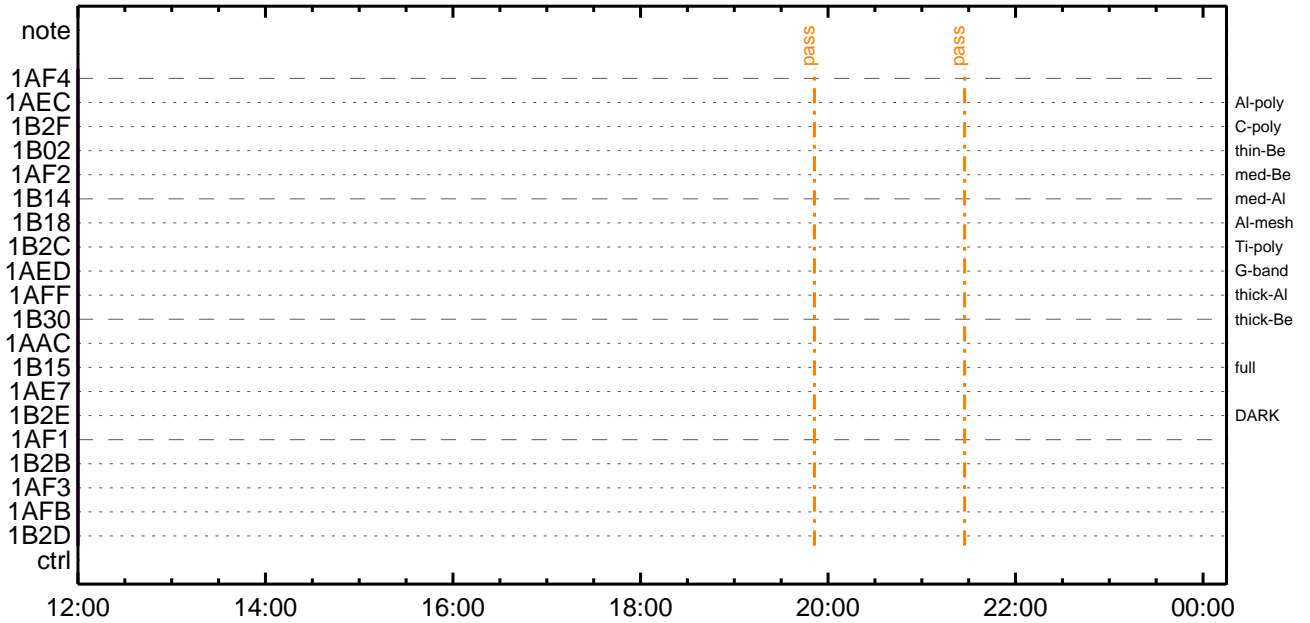
CMDI #0904 2016/05/11



CMDI #0904 2016/05/12



CMDI #0904 2016/05/12







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0096 C.          SET EDUMP I±°iYÑY¹aÇ¹Öa|a³aE;f
0097 C.
0098 C. TIY³YFYYÖYÉaðdÄDİ¿(UT)
0099 +. TI 2016-05-07 10:54:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.          ÇÇ[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0102 C.
0103 +. TI 2016-05-07 10:54:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.          ÇÇ[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0106 C.
0107 +. TI 2016-05-07 10:54:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.          ÇÇ[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0110 C.
0111 +. TI 2016-05-07 10:58:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.          ÇÇ[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0114 C.
0115 C. °E²¼aİÄè%îÍÑaİYÁY§YÄY-¹àÛ
0116 C.          ÇÇ[HK1_TI_CMD_ENA/DIS]        EQ          ENA
0117 C.          ÇÇ[HK1_TI_CMD_NUM]          EQ          4
0118 C.          ÇÇ[HK1_NEXT_EXEC_PIM]        EQ          DHU
0119 C.          ÇÇ[HK1_NEXT_EXEC_DC]        EQ          0xB3
0120 C.
0121 C. *****
0122 C. TIİİ°èYÄYÖY×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC          (03 ab 03 01 02)
0128 C.          ÇÇ[HK1_DMP_TOP_ADRS_1]        EQ          07
0129 C.          ÇÇ[HK1_DMP_TOP_ADRS_0]        EQ          2B
0130 C.          ÇÇ[HK1_DMP_BLOCK_NUM]        EQ          3
0131 C.          ÇÇ[HK1_DMP_REPEAT_NUM]       EQ          0
0132 C.          ÇÇ[HK1_DMA_DMP_PIM]         EQ          DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC          (07 0b f8)
0135 C.          ÇÇ[HK1_PKT_FORM_NO]          EQ          7
0136 C.          ÇÇ[HK1_PKT_GEN_TIME]         EQ          0.25 s
0137 C.          ÇÇ[HK1_S_TLM_BIT_RATE]       EQ          32k
0138 C.          ÇÇ[HK1_X_TLM_BIT_RATE]      EQ          4M
0139 C.          ÇÇ[HK1_DMP_CHK_FLG]         EQ          EXEC
0140 C.
0141 C. YÄYÖY×½ªİ»að³İÇ§
0142 C.          ÇÇ[HK1_DMP_CHK_FLG]         EQ          NON
0143 C.
0144 C. RAM ID=TI_TBLaİ%È¹Ç•è²İOKað³İÇ§
0145 C.
0146 C. DHUYâ;¼YÉ;È¼Y½,¥i;¼YÈ;Èaðİã¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC          (02 0a f8)
0149 C.          ÇÇ[HK1_PKT_FORM_NO]          EQ          2
0150 C.          ÇÇ[HK1_PKT_GEN_TIME]         EQ          0.5S
0151 C.          ÇÇ[HK1_S_TLM_BIT_RATE]       EQ          32K
0152 C.          ÇÇ[HK1_X_TLM_BIT_RATE]      EQ          4M
0153 C.
0154 C. Stop EIS observation and temporarily disable EIS mode changes
0155 C.
0156 C.
0157 C. ***** Start EIS operation (TI set) *****
0158 C. Execute, after the success of OP upload.
0159 C. Set EIS TI-commands
0160 +. TI 2016-05-07 10:58:30.0
0161 DC 07-FC EIS_MODE_MANU
0162 BC          (21 02)
0163 +. TI 2016-05-07 10:58:40.0
0164 DC 07-FC EIS_MODE_CHG_DIS
0165 BC          (22)
0166 C.          [ ] [HK1_TI_CMD_NUM]          EQ          2 COUNTUP
0167 C. ***** End EIS operation (TI set) *****
0168 C.
0169 C.
0170 C.
0171 C. ***** XRT START *****
0172 C. Execute, after the success of OP upload.
0173 +. TI 2016-05-07 10:58:00.0
0174 DC 07-F0 MDP_XRT_MODE_STBY
0175 BC          (c3)
0176 C.          [ ] [HK1_TI_CMD_NUM]          EQ          1COUNTUP
0177 C.
0178 C. ***** XRT END *****
0179 C.
0180 C. ***** MDP ´úÄîaİ»ö¼YªEÄa¹aèDCBC•x²è *****
0181 C. (%ã°îYÖYÄYÉYFYYÉYÄYÇYèaE¼aª¼Ä»Ü¹aè)
0182 C. S. DC-BC dcbc-402:DCBC
0183 C. (MDP_known_event)
0184 C.
0185 C.
0186 C. ***** YD¥¹•İ Daily±¿İÑaÈ¹Øa¹aèDCBC•x²è *****
0187 C. S. DC-BC dcbc-153:DCBC
0188 C. (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0189 C.
0190 C.
0191 C. ;ãLOS¥ÄY§YÄY-¼Ä»Ü;ä
0192 C.
0193 C. ***** LOS *****

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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 C.
0130 C. ***** XRT START *****
0131 C.
0132 +. DC 07-F0 MDP_XRT_CTRL_MANU
0133 BC (c1)
0134 +. DC 07-F0 MDP_XRT_CTRL_MANU
0135 BC (c1)
0136 + DC 07-F0 MDP_XRT_MODE_STBY
0137 BC (c3)
0138 . C. ----- Success Verify ? OK / NG____
0139 C.
0140 C. XRT Obs. Table Upload
0141 . S. RAM ram-291:MDP_OBS_X
0142 ( )
0143 C.
0144 +. DC 07-F0 MDP_DUMP_XRTTBL
0145 BC (84 07 00 00 00 3a d4)
0146 . C. ----- Comparison Check ? OK / ERR ____
0147 C.
0148 C.
0149 +. DC 07-F0 MDP_XRT_ROI_SET
0150 BC (cd 01 b1 b1 04 04)
0151 + DC 07-F0 MDP_XRT_ROI_SET
0152 BC (cd 02 b1 b1 08 08)
0153 + DC 07-F0 MDP_XRT_ROI_SET
0154 BC (cd 03 b1 b1 08 08)
0155 + DC 07-F0 MDP_XRT_ROI_SET
0156 BC (cd 04 b1 b1 06 06)
0157 + DC 07-F0 MDP_XRT_ROI_SET
0158 BC (cd 06 85 83 06 06)
0159 + DC 07-F0 MDP_XRT_ROI_SET
0160 BC (cd 07 80 80 20 20)
0161 + DC 07-F0 MDP_XRT_ROI_SET
0162 BC (cd 08 80 80 20 08)
0163 + DC 07-F0 MDP_XRT_ROI_SET
0164 BC (cd 09 80 80 08 20)
0165 + DC 07-F0 MDP_XRT_ROI_SET
0166 BC (cd 0a 85 83 08 08)
0167 + DC 07-F0 MDP_XRT_ROI_SET
0168 BC (cd 0b bf 58 0c 06)
0169 + DC 07-F0 MDP_XRT_ROI_SET
0170 BC (cd 0c 3f 58 0c 06)
0171 + DC 07-F0 MDP_XRT_ROI_SET
0172 BC (cd 0f 80 80 06 06)
0173 + DC 07-F0 MDP_XRT_ROI_SET
0174 BC (cd 10 80 80 08 08)
0175 + DC 07-F0 MDP_XRT_FLD_ENA
0176 BC (d8)
0177 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0178 BC (c8)
0179 + DC 07-F0 MDP_XRT_ARS_DIS
0180 BC (d5)
0181 +. DC 07-F0 MDP_XRT_AEC_RESET
0182 BC (d0)
0183 +. DC 07-F0 MDP_XRT_FLD_RESET
0184 BC (da)
0185 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0186 BC (c4 11)
0187 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0188 BC (c5 07)
0189 . C. ----- Success Verify ? OK / NG ____
0190 C.
0191 C.
0192 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0193 C.

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0096 C.  
0097 C.  
0098 C.  
0099 . C. \*\*\*\*\* MDP 'ûÃîñî»ò¼ŷñËÄðñ¹ñèDCBC•x²è \*\*\*\*\*  
0100 C. (¼á°îŷÓŷÃŷËŷŲŷËŷáŷçŷèñË¼ñ¼¼Ä»Ûñ¹ñè)  
0101 . S. DC-BC dcbc-402:DCBC  
0102 (MDP\_known\_event)  
0103 C.  
0104 C.  
0105 . C. \*\*\*\*\* ŷĐŷ¹•İ Daily±;İÑñË´Øñ¹ñèDCBC•x²è \*\*\*\*\*  
0106 . S. DC-BC dcbc-153:DCBC  
0107 (SPECIAL-CMD\_DAILY\_OPERATIN\_DCB)  
0108 C.  
0109 C.  
0110 . C. ;ãLOSŷÁŷŲŷÃŷ¬¼Ä»Û;ã  
0111 C.  
0112 . C. \*\*\*\*\* LOS \*\*\*\*\*  
0113 C.



May 07, 16 13:56

XRT\_OGLIST\_0904.chk

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\*\*\* OP Sequence for XRT \*\*\*

2016/05/07	11:08:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/07	11:08:56.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/07	11:08:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2016/05/07	11:09:00.5	AOCS_Ore-point_Start_1_OG [0x097]						
		AOCU_NM	5	02-76	03 00 00 00 00			
2016/05/07	11:09:18.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2016/05/07	11:09:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2016/05/07	11:09:22.0	XRT_AEC_RESET_448_OG [0x1c0]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2016/05/07	11:09:24.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/05/07	11:09:26.0	XRT_FLD_RESET_433_OG [0x1b1]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/05/07	11:11:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b			
2016/05/07	11:11:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07			
2016/05/07	11:12:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/05/07	12:21:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/07	12:21:32.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/07	12:21:34.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/05/07	12:21:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/05/07	12:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/05/07	12:46:00.0	XRT_Custom_430_OG [0x1ae]						
2016/05/07	12:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/05/07	14:00:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/07	14:00:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/07	14:00:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/05/07	14:00:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/05/07	14:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/05/07	14:32:30.0	XRT_Custom_430_OG [0x1ae]						
2016/05/07	14:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/05/07	15:38:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/07	15:38:32.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/07	15:38:34.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/05/07	15:38:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/05/07	15:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/05/07	16:21:30.0	XRT_Custom_430_OG [0x1ae]						
2016/05/07	16:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/05/07	17:04:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/07	17:04:56.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/07	17:04:58.0	XRT_FOCUS_POSITION_403_OG [0x193]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2016/05/07	17:05:00.0	AOCS_Ore-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2016/05/07	17:05:18.0	XRT_FLD_DIS_406_OG [0x196]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2016/05/07	17:07:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2016/05/07	17:07:56.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/05/07	17:07:58.0	XRT_QT_PROG_SET_431_OG [0x1af]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08			
2016/05/07	17:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/05/07	17:14:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/07	17:14:56.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/07	17:14:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2016/05/07	17:15:00.0	AOCS_Ore-point_Start_3_OG [0x099]						
		AOCU_NM	5	02-76	04 00 00 00 00			
2016/05/07	17:15:18.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			

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2016/05/07	17:15:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/05/07	17:15:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/05/07	17:15:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/05/07	17:15:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/07	17:16:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/07	17:16:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/07	17:16:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/07	17:16:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/05/07	17:17:56.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d
2016/05/07	17:17:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2016/05/07	17:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/07	17:58:30.0	XRT_Custom_430_OG [0x1ae]					
2016/05/07	17:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/07	18:55:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/07	18:55:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/07	18:55:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/07	18:55:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/05/07	18:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/07	19:35:00.0	XRT_Custom_430_OG [0x1ae]					
2016/05/07	19:36:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/07	20:33:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/07	20:33:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/07	20:33:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/07	20:33:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/05/07	20:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/07	20:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/07	20:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/07	20:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2016/05/07	21:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03	00 00 00 00
2016/05/07	21:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/05/07	21:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/05/07	21:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/05/07	21:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/05/07	21:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/07	21:02:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b
2016/05/07	21:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2016/05/07	21:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/07	22:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/07	22:12:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/07	22:12:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/07	22:12:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/05/07	22:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/07	22:47:30.0	XRT_Custom_430_OG [0x1ae]					
2016/05/07	22:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/07	23:50:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/07	23:50:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/07	23:50:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/07	23:50:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	

2016/05/07	23:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/05/08	00:15:00.0	XRT_Custom_430_OG [0x1ae]							
2016/05/08	00:16:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/05/08	01:28:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/08	01:28:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/08	01:28:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/05/08	01:28:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/05/08	01:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/05/08	01:53:00.0	XRT_Custom_430_OG [0x1ae]							
2016/05/08	01:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/05/08	03:02:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/08	03:02:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/08	03:02:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/05/08	03:02:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/05/08	03:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/05/08	03:31:30.0	XRT_Custom_430_OG [0x1ae]							
2016/05/08	03:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/05/08	04:31:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/08	04:31:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/08	04:31:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/05/08	04:31:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/05/08	04:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/05/08	05:10:00.0	XRT_Custom_430_OG [0x1ae]							
2016/05/08	05:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/05/08	05:44:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/08	05:44:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/08	05:44:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/05/08	05:44:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2016/05/08	05:44:48.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/05/08	05:47:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/05/08	05:47:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/05/08	05:47:28.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f				
2016/05/08	05:47:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/05/08	05:54:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/08	05:54:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/08	05:54:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/05/08	05:54:30.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2016/05/08	05:54:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/05/08	05:54:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/05/08	05:54:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/05/08	05:54:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/05/08	05:54:56.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/05/08	05:57:26.0	XRT_QT_PROG_SET_449_OG [0x1c1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2016/05/08	05:57:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/05/08	05:57:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/05/08	06:12:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/08	06:12:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/08	06:12:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/05/08	06:12:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]							

2016/05/08	06:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/08	06:48:30.0	XRT_Custom_430_OG [0x1ae]				
2016/05/08	06:49:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/08	07:52:00.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/08	07:52:02.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/08	07:52:04.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/08	07:52:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/08	07:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/08	08:27:00.0	XRT_Custom_430_OG [0x1ae]				
2016/05/08	08:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/08	09:32:00.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/08	09:32:02.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/08	09:32:04.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/08	09:32:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/08	09:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/08	10:05:00.0	XRT_Custom_430_OG [0x1ae]				
2016/05/08	10:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/08	11:18:30.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/08	11:18:32.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/08	11:18:34.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/08	11:18:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/08	11:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/08	11:43:30.0	XRT_Custom_430_OG [0x1ae]				
2016/05/08	11:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/08	12:57:00.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/08	12:57:02.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/08	12:57:04.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/08	12:57:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/08	13:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/08	13:22:00.0	XRT_Custom_430_OG [0x1ae]				
2016/05/08	13:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/08	14:35:30.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/08	14:35:32.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/08	14:35:34.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/08	14:35:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/08	14:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/08	15:10:00.0	XRT_Custom_430_OG [0x1ae]				
2016/05/08	15:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/08	16:14:00.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/08	16:14:02.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/08	16:14:04.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/08	16:14:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/08	16:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/08	16:56:30.0	XRT_Custom_430_OG [0x1ae]				
2016/05/08	16:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/08	17:41:54.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/08	17:41:56.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/08	17:41:58.0	XRT_FOCUS_POSITION_403_OG [0x193]				
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/05/08	17:42:00.0	AOCS_Ore-point_Start_2_OG [0x098]				
			AOCU_NM	5	02-76	00 00 00 00 00
2016/05/08	17:42:18.0	XRT_FLD_DIS_406_OG [0x196]				

2016/05/08	17:44:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2016/05/08	17:44:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/05/08	17:44:58.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/08	17:45:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2016/05/08	17:51:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/08	17:51:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/08	17:51:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/08	17:52:00.0	AOCS_Ore-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/05/08	17:52:00.5	XRT_CTRL_MANU_400_OG [0x190]	AOCU_NM	5	02-76	03 00 00 00 00
2016/05/08	17:52:02.5	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/08	17:52:04.5	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/08	17:52:06.5	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/08	17:52:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/08	17:52:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/05/08	17:52:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/05/08	17:52:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/05/08	17:52:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/08	17:54:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/08	17:54:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2016/05/08	17:55:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/05/08	18:33:30.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/08	18:34:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2016/05/08	19:30:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/08	19:30:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/08	19:30:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/08	19:30:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/08	19:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/08	20:10:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/08	20:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2016/05/08	21:09:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/08	21:09:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/08	21:09:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/08	21:09:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/08	21:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/08	21:47:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/08	21:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2016/05/08	22:47:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/08	22:47:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/08	22:47:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/08	22:47:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/08	22:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/08	23:21:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/08	23:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2016/05/09	00:25:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/09	00:25:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/09	00:25:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/09	00:25:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/09	00:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/09			MDP_XRT_PREFLR_STOP	1	07-F0	e9

2016/05/09	00:50:30.0	XRT_Custom_430_OG [0x1ae]						
2016/05/09	00:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/05/09	02:01:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/09	02:01:32.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/09	02:01:34.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/05/09	02:01:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/05/09	02:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/05/09	02:29:00.5	XRT_Custom_430_OG [0x1ae]						
2016/05/09	02:30:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/05/09	03:34:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/09	03:34:32.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/09	03:34:34.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/05/09	03:34:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/05/09	03:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/05/09	04:07:30.0	XRT_Custom_430_OG [0x1ae]						
2016/05/09	04:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/05/09	05:08:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/09	05:08:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/09	05:08:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/05/09	05:08:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/05/09	05:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/05/09	05:46:00.0	XRT_Custom_430_OG [0x1ae]						
2016/05/09	05:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/05/09	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/09	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/09	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2016/05/09	06:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2016/05/09	06:00:18.0	XRT_FLD_DIS_406_OG [0x196]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2016/05/09	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2016/05/09	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/05/09	06:02:58.0	XRT_QT_PROG_SET_431_OG [0x1af]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08			
2016/05/09	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/05/09	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/09	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/09	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2016/05/09	06:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]						
		AOCU_NM	5	02-76	03 00 00 00 00			
2016/05/09	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2016/05/09	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2016/05/09	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2016/05/09	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/05/09	06:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/05/09	06:12:56.0	XRT_QT_PROG_SET_401_OG [0x191]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e			
2016/05/09	06:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07			
2016/05/09	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/05/09	06:48:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/09	06:48:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/09	06:48:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/05/09	06:48:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/05/09	06:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						

2016/05/09	07:24:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/05/09	07:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2016/05/09	08:28:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/05/09	08:28:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/09	08:28:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/09	08:28:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/05/09	08:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/05/09	09:02:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/05/09	09:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2016/05/09	09:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/05/09	09:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/09	09:29:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/09	09:30:00.5	AOCs_OrE-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2016/05/09	09:30:18.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00 1c 72 50 b4		
2016/05/09	09:32:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2016/05/09	09:32:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2016/05/09	09:32:58.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/05/09	09:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a		
2016/05/09	10:09:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/05/09	10:09:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/09	10:09:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/09	10:09:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/05/09	10:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/05/09	10:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/05/09	10:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/09	10:29:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/09	10:30:18.0	XRT_FLD_DIS_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2016/05/09	10:32:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2016/05/09	10:32:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2016/05/09	10:32:58.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/05/09	10:41:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a		
2016/05/09	10:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2016/05/09	11:54:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/05/09	11:54:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/09	11:54:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/09	11:54:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/05/09	11:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/05/09	11:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/05/09	11:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/09	11:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/09	12:00:00.0	AOCs_OrE-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2016/05/09	12:00:18.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00 00 00 00 00		
2016/05/09	12:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2016/05/09	12:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2016/05/09	12:02:58.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/05/09	12:19:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06		
2016/05/09	12:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2016/05/09	13:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		

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2016/05/09	13:32:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/09	13:32:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/05/09	13:32:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/05/09	13:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/05/09	13:56:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/09	13:56:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/09	13:56:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2016/05/09	13:57:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2016/05/09	13:58:00.0	AOCS_Ore-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00 1f 1a 10 9b		
2016/05/09	13:59:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2016/05/09	13:59:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/05/09	13:59:58.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a		
2016/05/09	14:00:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/05/09	14:36:00.0	AOCS_Ore-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00 1f 8d 05 26		
2016/05/09	15:11:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/09	15:11:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/09	15:11:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/05/09	15:11:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/05/09	15:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/05/09	15:44:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/09	15:44:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/09	15:44:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2016/05/09	15:45:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00		
2016/05/09	15:45:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2016/05/09	15:46:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/09	15:46:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/09	15:46:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/05/09	15:46:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/05/09	15:47:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2016/05/09	15:47:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/05/09	15:47:58.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12		
2016/05/09	15:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/05/09	15:55:00.0	XRT_Custom_430_OG [0x1ae]						
2016/05/09	15:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/05/09	16:49:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/09	16:49:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/09	16:49:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/05/09	16:49:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/05/09	16:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/05/09	17:20:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/09	17:20:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/09	17:20:04.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2016/05/09	17:20:24.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2016/05/09	17:23:00.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2016/05/09	17:23:02.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/05/09	17:23:04.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12		
2016/05/09	17:31:30.0	XRT_Custom_430_OG [0x1ae]						
2016/05/09	17:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						



2016/05/09	18:27:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/09	18:27:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/09	18:27:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/05/09	18:27:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/05/09	18:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/05/09	18:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/09	18:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/09	18:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2016/05/09	19:00:00.0	AOCS_Orе-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 1c 72 b2 7f		
2016/05/09	19:00:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2016/05/09	19:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2016/05/09	19:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/05/09	19:02:58.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a		
2016/05/09	19:08:30.0	XRT_Custom_430_OG [0x1ae]						
2016/05/09	19:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/05/09	20:06:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/09	20:06:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/09	20:06:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/05/09	20:06:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/05/09	20:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/05/09	20:44:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/09	20:44:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/09	20:44:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2016/05/09	20:45:00.0	AOCS_Orе-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00		
2016/05/09	20:45:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2016/05/09	20:47:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2016/05/09	20:47:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/05/09	20:47:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f		
2016/05/09	20:48:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/05/09	20:54:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/09	20:54:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/09	20:54:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2016/05/09	20:55:00.0	AOCS_Orе-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 00 00 00 00		
2016/05/09	20:55:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2016/05/09	20:55:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/05/09	20:55:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/05/09	20:55:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/05/09	20:55:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/05/09	20:57:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b		
2016/05/09	20:57:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07		
2016/05/09	20:58:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/05/09	21:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/09	21:44:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/09	21:44:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/05/09	21:44:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/05/09	21:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/05/09	22:21:30.0	XRT_Custom_430_OG [0x1ae]						

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2016/05/09	22:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/09	23:23:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/09	23:23:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/09	23:23:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/09	23:23:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/09	23:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/09	23:49:00.0	XRT_Custom_430_OG [0x1ae]			
2016/05/09	23:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/10	01:01:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/10	01:01:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/10	01:01:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/10	01:01:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/10	01:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/10	01:26:30.0	XRT_Custom_430_OG [0x1ae]			
2016/05/10	01:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/10	02:35:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/10	02:35:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/10	02:35:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/10	02:35:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/10	02:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/10	03:05:00.0	XRT_Custom_430_OG [0x1ae]			
2016/05/10	03:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/10	04:04:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/10	04:04:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/10	04:04:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/10	04:04:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/10	04:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/10	04:43:00.0	XRT_Custom_430_OG [0x1ae]			
2016/05/10	04:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/10	05:44:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/10	05:44:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/10	05:44:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/10	05:44:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/10	05:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/10	06:00:00.0	XRT_CTRL_MANU_416_OG [0x1a0]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/10	06:00:10.0	XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]			
		XRT_FOCUS_RECAL	2	07-F8	78 00
2016/05/10	06:04:10.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/05/10	06:21:30.0	XRT_Custom_430_OG [0x1ae]			
2016/05/10	06:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/10	06:22:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/10	06:22:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/10	06:22:58.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/05/10	06:23:00.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2016/05/10	06:23:18.0	XRT_FLD_DIS_406_OG [0x196]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2016/05/10	06:25:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/05/10	06:25:56.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/10	06:25:58.0	XRT_QT_PROG_SET_431_OG [0x1af]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2016/05/10	06:26:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/10	06:32:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1

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2016/05/10	06:32:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/10	06:32:58.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/05/10	06:33:00.0	AOCS_ORe-point_Start_1_OG [0x097] AOCU_NM	5	02-76	03 00 00 00 00
2016/05/10	06:33:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2016/05/10	06:33:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/05/10	06:33:22.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0
2016/05/10	06:33:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/10	06:33:26.0	XRT_FLD_RESET_433_OG [0x1b1] MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/10	06:35:56.0	XRT_QT_PROG_SET_449_OG [0x1c1] MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2016/05/10	06:35:58.0	XRT_FL_PROG_SET_436_OG [0x1b4] MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/05/10	06:36:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/10	07:24:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/10	07:24:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/10	07:24:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/10	07:24:36.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/10	07:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/10	08:00:00.0	XRT_Custom_430_OG [0x1ae]			
2016/05/10	08:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/10	09:04:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/10	09:04:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/10	09:04:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/10	09:04:06.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/10	09:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/10	09:38:30.0	XRT_Custom_430_OG [0x1ae]			
2016/05/10	09:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/10	09:50:00.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/10	10:12:00.0	AOCS_ORe-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00 00 00 00 00