

XRT Timeline to be uploaded on 2016/05/10

Period: 2016/05/10 10:02:00 - 2016/05/14 10:46:00

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Normal mode

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XOB #1AFF: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
05/10 10:15:00 - 05/10 17:09:54	Track (475.7, 53.4) @ 05/10 10:12:00	# OP start + 10min, AR 12541.
05/11 18:42:30 - 05/11 22:29:54	Track (704.8, 41.5) @ 05/11 18:15:00	# AR cont.
05/12 06:23:00 - 05/12 10:17:30	Track (774.4, 36.4) @ 05/12 06:20:00	# AR cont.

PROG= 11 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec
Seqn= 56	1-time(s)	2.0sec
Open/G-band	Open/G-band open	Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Subr= 2	5-time(s)	2.0sec
Seqn= 75	1-time(s)	2.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Seqn= 96	4-time(s)	60.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 15.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 15.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1B14: Synoptic Q95 2x2 - Al/mesh(24/256/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(45/512/4096) + Ti

Term	Pointing (x, y)	Comment
05/10 17:13:00 - 05/10 17:19:00	Fixed (0.0, 0.0)	synoptic, shifted. XRT stray-light measurement.
05/11 05:55:30 - 05/11 06:02:24	Fixed (0.0, 0.0)	synoptic, shifted -7.5 min
05/11 17:48:00 - 05/11 17:57:30	Fixed (0.0, 0.0)	synoptic, shifted. XRT stray-light measurement.
05/12 02:39:00 - 05/12 02:45:54	Track (-13.9, 0.1) @ 05/12 02:30:00	# EIS sensitivity monitoring.
05/12 06:13:00 - 05/12 06:19:54	Fixed (0.0, 0.0)	synoptic

PROG= 15 1-time(s)

Subr= 1	1-time(s)	2.0sec
Seqn= 5	1-time(s)	2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
Seqn= 1	1-time(s)	2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 99	1-time(s)	2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 67	1-time(s)	2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 54	1-time(s)	2.0sec
Open/G-band	Open/G-band open	Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1B31: Stray light study 2016-1 ;Al-mesh and Ti-poly, 2x2 full FOV(1min-cad) and 2x2 256 on AR(10sec-cad)

Term	Pointing (x, y)	Comment
05/10 17:21:30 - 05/10 17:39:54	Fixed (0.0, 0.0)	synoptic, shifted. XRT stray-light measurement.

PROG= 16 1-time(s)

Subr= 1	1-time(s)	2.0sec
Seqn= 38	1-time(s)	150.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Subr= 2	21-time(s)	2.0sec
Seqn= 83	1-time(s)	10.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 250ms Obs 2x2 512x512 (1024, 1200) Q=95 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 2x2 512x512 (1024, 1200) Q=95 0 0 2.0sec

Subr= 3 9-time(s) 2.0sec													
Seqn= 17 1-time(s) 2.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 83 5-time(s) 10.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	2x2	512x512 (1024, 1200)	Q=95	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	512x512 (1024, 1200)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1B18: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)		Comment										
05/10 18:07:30 - 05/11 05:52:24	Track (534.1,	50.9)	# AR cont. @ 05/10 17:40:00										
05/11 06:05:30 - 05/11 17:44:54	Track (624.5,	46.3)	# AR cont. @ 05/11 06:02:30										
PROG= 14 Inf.-time(s)													
Subr= 1 1-time(s) 2.0sec													
Seqn= 56 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2 5-time(s) 2.0sec													
Seqn= 75 1-time(s) 2.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 50 4-time(s) 90.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	25.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	25.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1B32: Stray light study 2016-2 ;Al-poly and thin-Be, 2x2 full FOV(1min-cad) and 2x2 256 on AR(10sec-cad)

Term	Pointing (x, y)		Comment										
05/11 18:00:00 - 05/11 18:14:54	Fixed (0.0,	0.0)	synoptic, shifted. XRT stray-light measurement.										
PROG= 03 1-time(s)													
Subr= 2 21-time(s) 2.0sec													
Seqn= 83 1-time(s) 10.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	2x2	512x512 (1024, 1200)	Q=95	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	512x512 (1024, 1200)	Q=95	0	0	2.0sec
Subr= 3 9-time(s) 2.0sec													
Seqn= 39 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 83 5-time(s) 10.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	2x2	512x512 (1024, 1200)	Q=95	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	512x512 (1024, 1200)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1AEC: G-Band Alignment with North Pole Q90 2x2 (G-band and VLS=CLS) - 1msec (Al/poly) - 4096msec - 5min cadence - Partial Sun-wNGT

Term	Pointing (x, y)		Comment										
05/11 22:45:00 - 05/12 00:29:54	Fixed (0.0,	930.0)	# Coalignment at North Pole.										
PROG= 19 1-time(s)													
Subr= 1 24-time(s) 300.0sec													
Seqn= 98 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec
Seqn= 63 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec
Seqn= 45 1-time(s) 2.0sec													
	Al-poly/Open	med-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x1536 (1024, 768)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1AED: G-Band Alignment with East limb Q90 2x2 (G-band and VLS=CLS) - 1msec - (Al/poly) 1443msec - 8 min cadence-wNGT

Term	Pointing (x, y)		Comment										
05/12 00:45:00 - 05/12 02:35:00	Fixed (-970.0,	0.0)	# Coalignment at East limb.										
PROG= 12 1-time(s)													
Subr= 1 15-time(s) 480.0sec													
Seqn= 19 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	2x2	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec
Seqn= 43 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec
Seqn= 70 1-time(s) 2.0sec													
	Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	1536x2048 (1280, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1AD5: CME watch - 4x4 - AEC 2 - Be-thin - G-band (2x2,1ms) - Leak (2x2,1ms) - 60s cad												
Term		Pointing (x, y)				Comment						
05/12 02:49:00 - 05/12 06:09:54		Track (-13.9, 0.1) @ 05/12 02:30:00				# EIS sensitivity monitoring.						
PROG= 05 Inf.-time(s)												
Subr= 1 60-time(s) 60.0sec												
└─ Seqn= 29 1-time(s) 4.0sec												
└─ thin-Be/Open med-Be/Open close		Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec	
Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 26 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open		Safe	Norm	1ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec	
└─ Open/G-band Open/G-band close		Safe	Norm	1ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512												
Term		Pointing (x, y)				Comment						
05/10 10:15:00 - 05/10 17:09:54		Track (475.7, 53.4) @ 05/10 10:12:00				# OP start + 10min, AR 12541.						
05/10 18:07:30 - 05/11 05:52:24		Track (534.1, 50.9) @ 05/10 17:40:00				# AR cont.						
05/11 06:05:30 - 05/11 17:44:54		Track (624.5, 46.3) @ 05/11 06:02:30				# AR cont.						
05/11 18:42:30 - 05/11 22:29:54		Track (704.8, 41.5) @ 05/11 18:15:00				# AR cont.						
05/12 02:49:00 - 05/12 06:09:54		Track (-13.9, 0.1) @ 05/12 02:30:00				# EIS sensitivity monitoring.						
05/12 06:23:00 - 05/12 10:17:30		Track (774.4, 36.4) @ 05/12 06:20:00				# AR cont.						
PROG= 07 30-time(s)												
Subr= 1 20-time(s) 2.0sec												
└─ Seqn= 11 1-time(s) 2.0sec												
└─ Al-poly/Open Al-poly/thick-Al close		Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec	
└─ Seqn=100 1-time(s) 10.0sec												
└─ thin-Be/Open med-Be/Open close		Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec	
└─ med-Be/Open Open/thick-Al close		Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
└─ Open/thick-Al Open/thick-Be close		Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 10 1-time(s) 2.0sec												
└─ med-Al/Open med-Al/thick-Al close		Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
└─ Open/thick-Be Open/thick-Be close		Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
└─ Seqn= 11 1-time(s) 2.0sec												
└─ Al-poly/Open Al-poly/thick-Al close		Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec	
└─ Seqn= 84 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open		Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
└─ Open/G-band Open/G-band close		Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
└─ Open/thick-Al Open/thick-Al close		Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
└─ Open/thick-Al Open/thick-Al close		Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

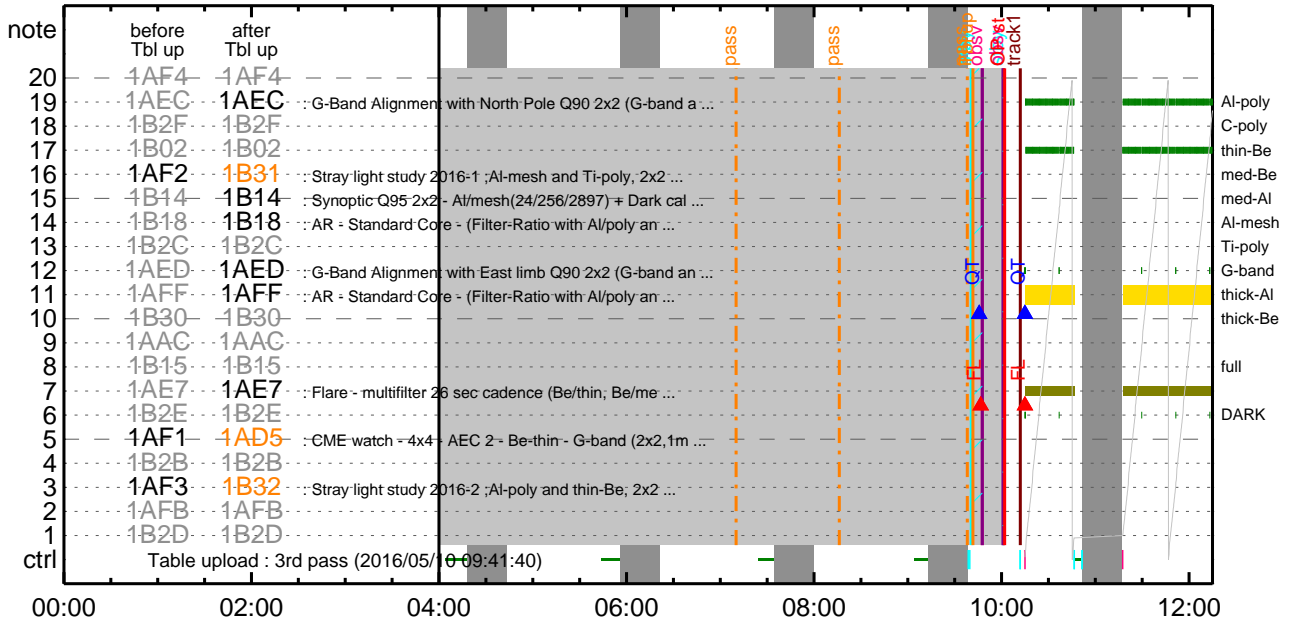
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Flare Detection

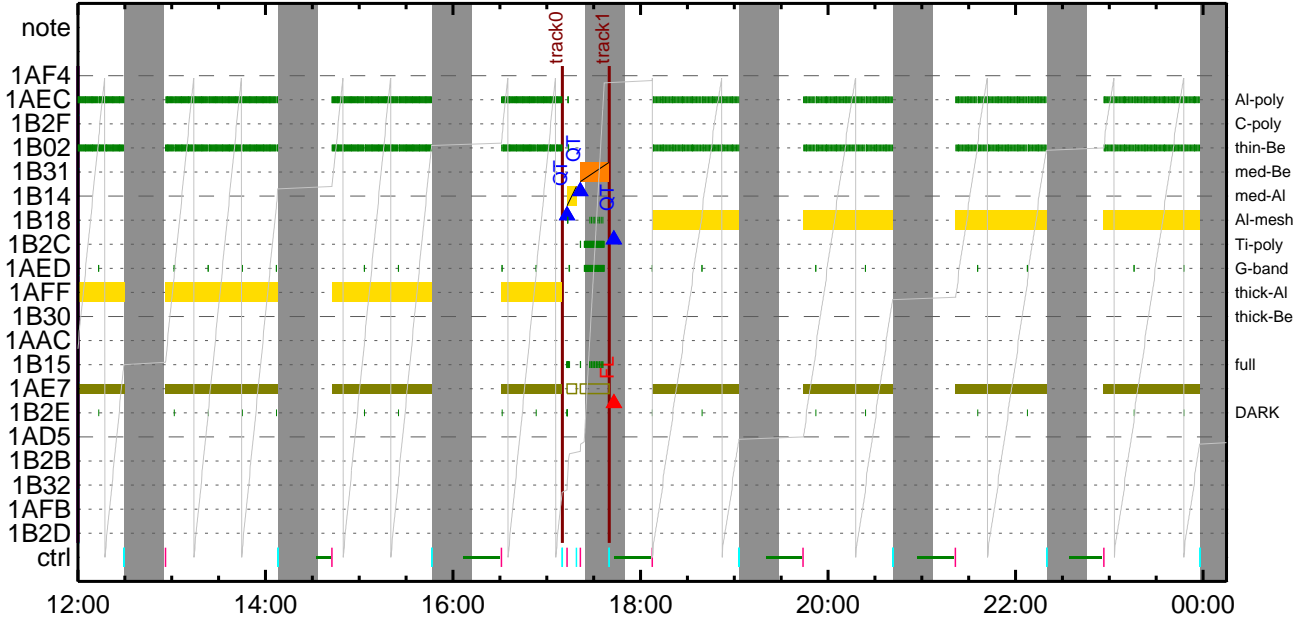
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FLD Patrol												
Term		Pointing (x, y)				Comment						
05/10 17:40:18 - 05/11 05:52:48		Track (534.1, 50.9) @ 05/10 17:40:00				# AR cont.						
05/11 06:02:48 - 05/11 17:45:18		Track (624.5, 46.3) @ 05/11 06:02:30				# AR cont.						
05/11 18:15:18 - 05/11 22:30:18		Track (704.8, 41.5) @ 05/11 18:15:00				# AR cont.						
05/12 02:46:18 - 05/12 06:10:18		Track (-13.9, 0.1) @ 05/12 02:30:00				# EIS sensitivity monitoring.						
05/12 06:20:18 - 05/14 10:46:00		Track (774.4, 36.4) @ 05/12 06:20:00				# AR cont.						
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50			30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

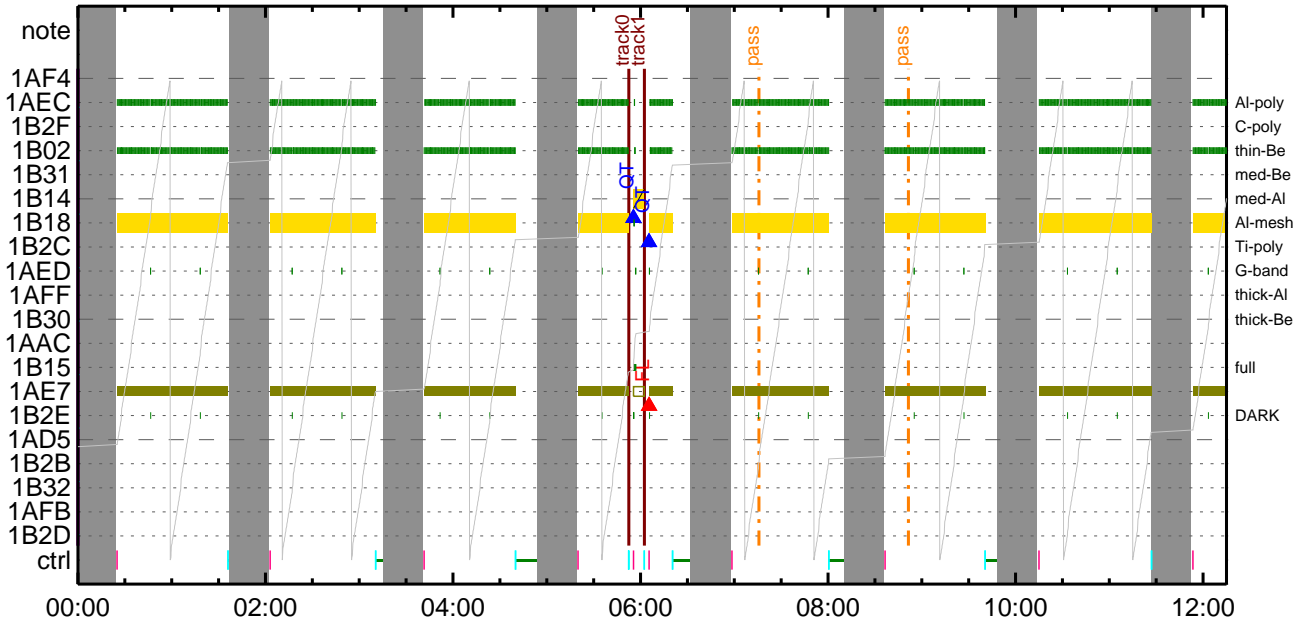
CMDI #0909 2016/05/10



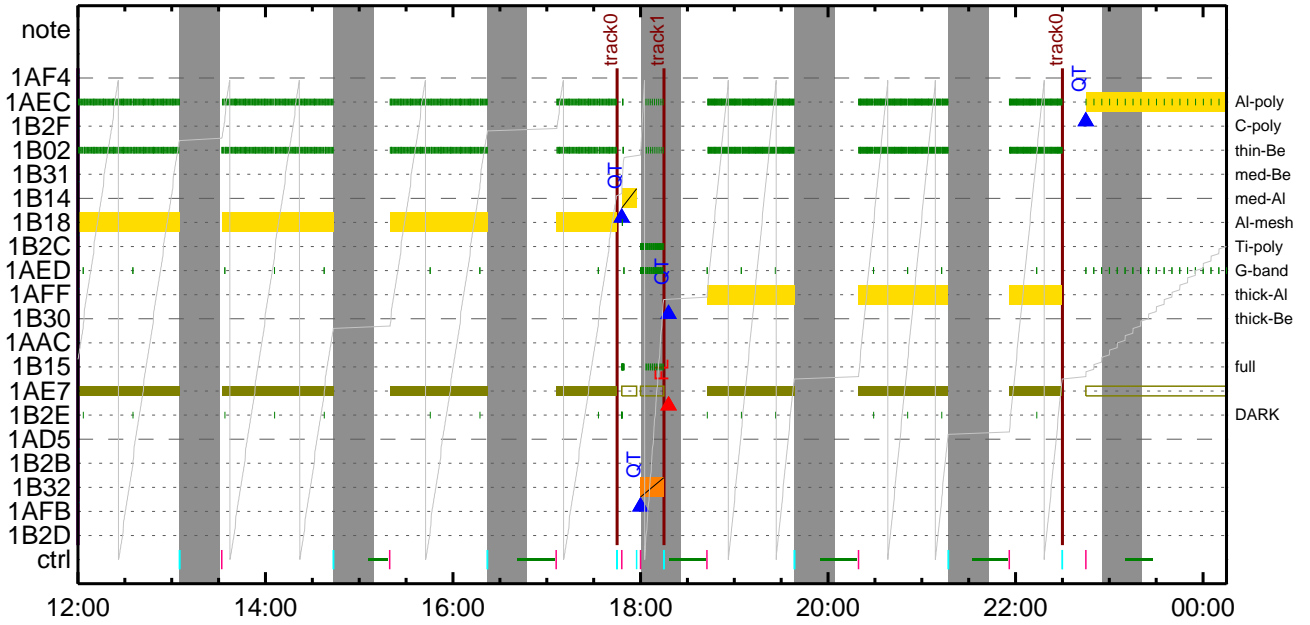
CMDI #0909 2016/05/10



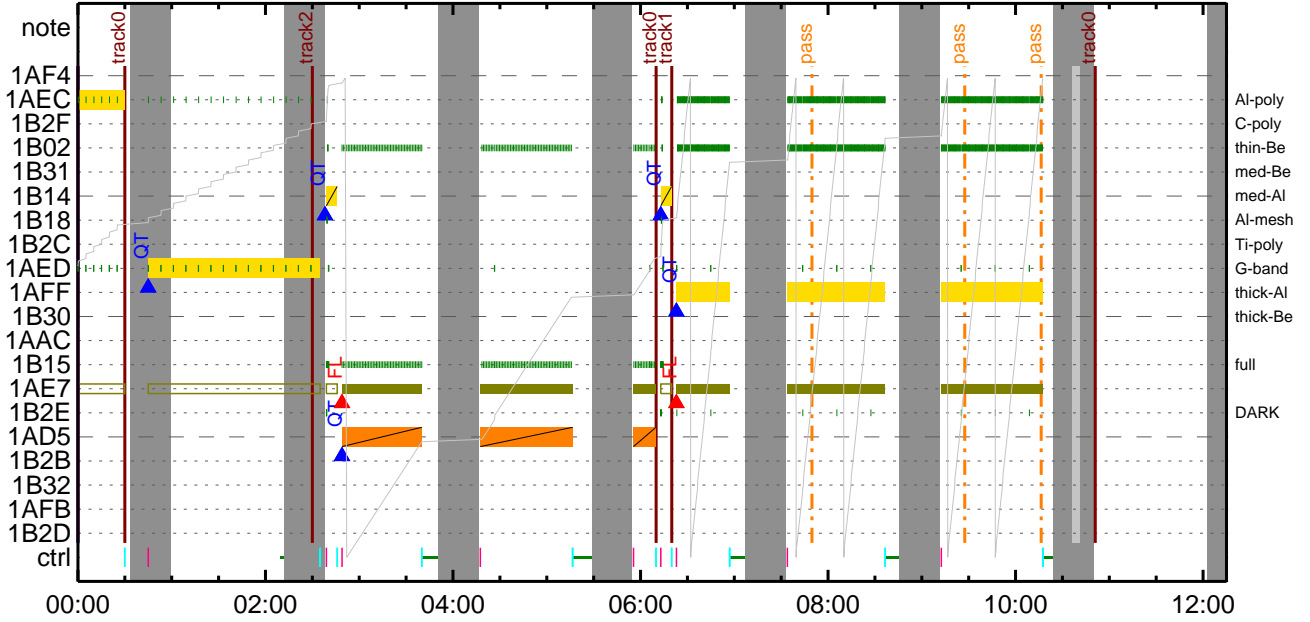
CMDI #0909 2016/05/11



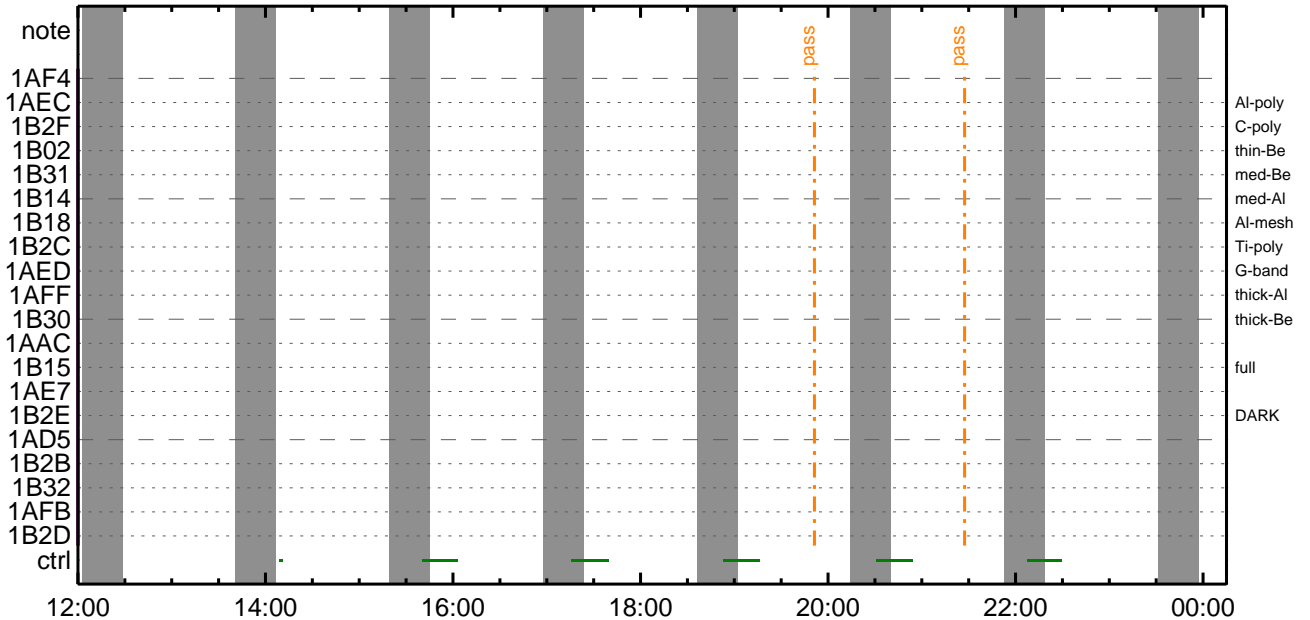
CMDI #0909 2016/05/11



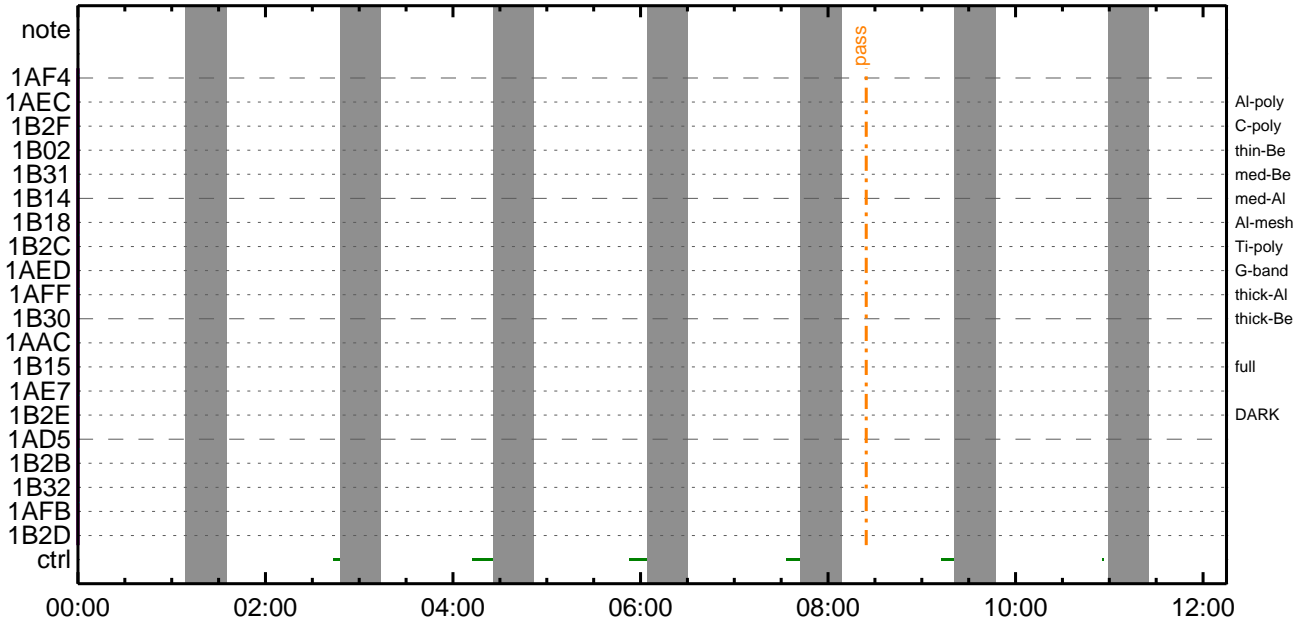
CMDI #0909 2016/05/12



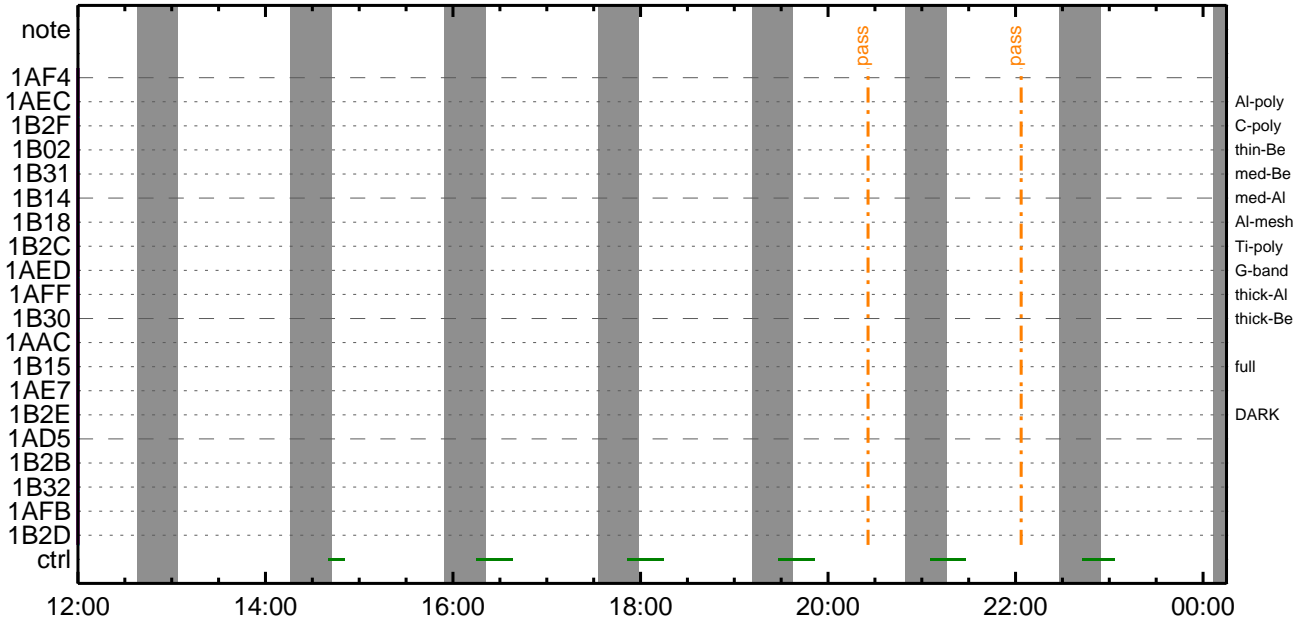
CMDI #0909 2016/05/12



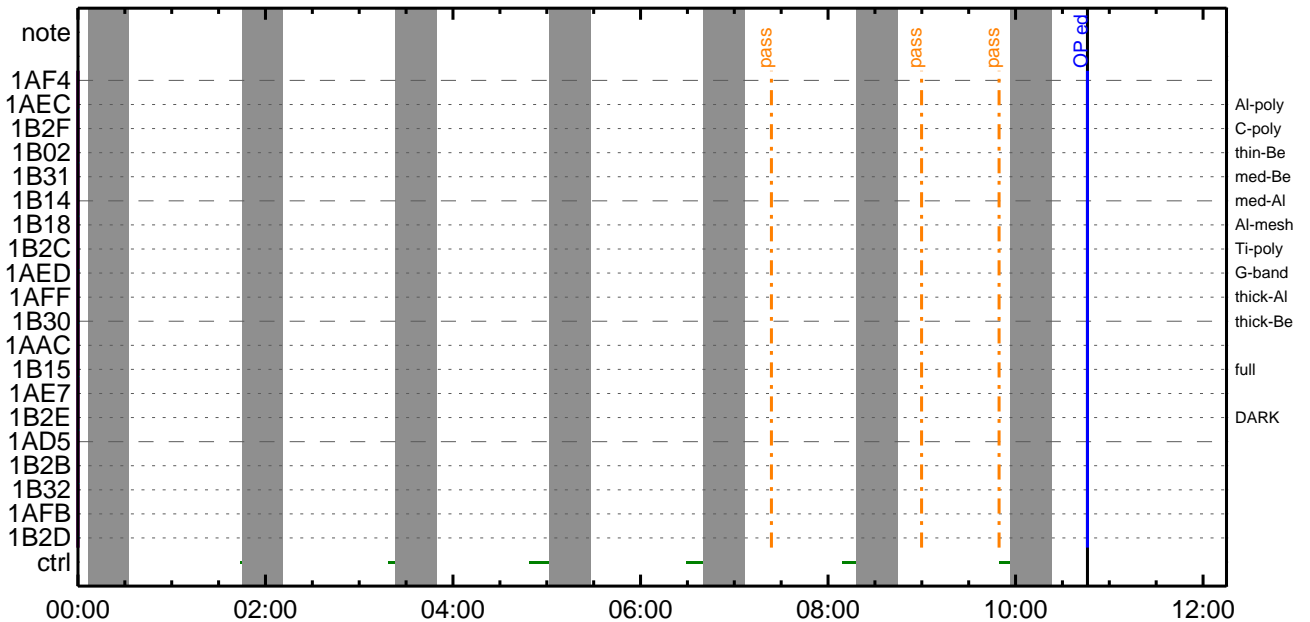
CMDI #0909 2016/05/13

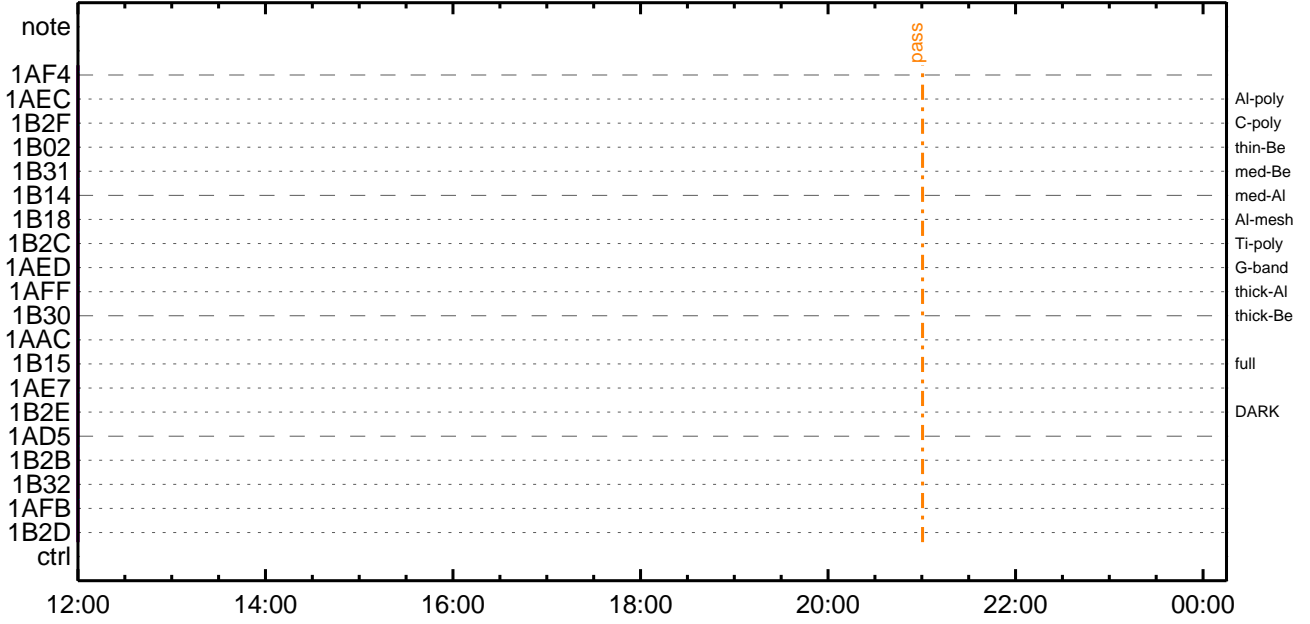


CMDI #0909 2016/05/13



CMDI #0909 2016/05/14





(a) Spacecraft Operation Procedure (real-commands)

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main-844 2016-05-10 12:50:17 205 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÁY$;¼Y³YFÝÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ê¿¿ðÁð•µ°Æ»Ì×ÁÇ¿íYçYÁY×Yí;¼YÉ;ÊÈè¿µ•ííÉ;ÈðÉ¼°ÇÓð•α¿¼l¹ç¿í;çÁ®, ùα¹αè¿ðçÁ+¿®α•ðÊðð³ðÈ; ¢
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGYí;¼YÉ; | YÁYÖY×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGYí;¼YÉ;ä
0019 . S. OP op-844:OP
0020 ( )
0021 . S. OG og-844:OG
0022 ( )
0023 C.
0024 . C. ;ãNMOG&OPíî°èYÁYÖY×;ä
0025 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRES_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRES_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. YÁYÖY×¾ªí»ðð³íç§
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGðí¼È¹ç•ë²ìOKðð³íç§
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRES_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRES_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. YÁYÖY×¾ªí»ðð³íç§
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGðí¼È¹ç•ë²ìOKðð³íç§
0062 C.
0063 C. NMOG(0x210000-0x210FFF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRES_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRES_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. YÁYÖY×¾ªí»ðð³íç§
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG, RAM ID=OPðí¼È¹ç•ë²ìOKðð³íç§
0081 C.
0082 . C. ***** øÈ²¼ðí¼Á´¶¼ºαÈÈ-ð°Á+¿®(¼áµ-YÁYÖY×¼è¼çððÁÓÁæç¾ªα°¼ðè¼í¹çççá) *****
0083 C. DHU¿á;¼YÉ;È¼Y½, Yí;¼YÉ;Èððíáð¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;§ OPOG UPLOADα-Á+¿®NGαí¼¹ç; ç°È²¼ðí¼TI-CMDÁ+¿®ðí¼Á¹Òα•ðÈðð³ðÈ; ¢
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0096 . C. $\alpha \beta \gamma; \delta \text{SET} \alpha \beta \text{DUMP} \alpha \beta \gamma \delta \epsilon \zeta \eta \theta \iota \kappa \lambda \text{m} \text{n} \text{o} \text{p} \text{q} \text{r} \text{s} \text{t} \text{u} \text{v} \text{w} \text{x} \text{y} \text{z}$
0097 . C.
0098 . C. TII $\alpha \beta \gamma \delta \epsilon \zeta \eta \theta \iota \kappa \lambda \text{m} \text{n} \text{o} \text{p} \text{q} \text{r} \text{s} \text{t} \text{u} \text{v} \text{w} \text{x} \text{y} \text{z}$ (UT)
0099 +. TI 2016-05-10 09:57:00.0
0100 . DC 01-B3 DHU_OP_STOP
0101 . C. $\delta \epsilon \zeta \eta \theta \iota \kappa \lambda \text{m} \text{n} \text{o} \text{p} \text{q} \text{r} \text{s} \text{t} \text{u} \text{v} \text{w} \text{x} \text{y} \text{z}$ EQ 1COUNTUP
0102 . C.
0103 +. TI 2016-05-10 09:57:01.0
0104 . DC 01-B4 DHU_OP_COPY
0105 . C. $\delta \epsilon \zeta \eta \theta \iota \kappa \lambda \text{m} \text{n} \text{o} \text{p} \text{q} \text{r} \text{s} \text{t} \text{u} \text{v} \text{w} \text{x} \text{y} \text{z}$ EQ 1COUNTUP
0106 . C.
0107 +. TI 2016-05-10 09:57:01.0
0108 . DC 01-B5 DHU_OPOG_COPY
0109 . C. $\delta \epsilon \zeta \eta \theta \iota \kappa \lambda \text{m} \text{n} \text{o} \text{p} \text{q} \text{r} \text{s} \text{t} \text{u} \text{v} \text{w} \text{x} \text{y} \text{z}$ EQ 1COUNTUP
0110 . C.
0111 +. TI 2016-05-10 10:01:59.5
0112 . DC 01-B2 DHU_OP_START
0113 . C. $\delta \epsilon \zeta \eta \theta \iota \kappa \lambda \text{m} \text{n} \text{o} \text{p} \text{q} \text{r} \text{s} \text{t} \text{u} \text{v} \text{w} \text{x} \text{y} \text{z}$ EQ 1COUNTUP
0114 . C.
0115 . C. $\alpha \beta \gamma \delta \epsilon \zeta \eta \theta \iota \kappa \lambda \text{m} \text{n} \text{o} \text{p} \text{q} \text{r} \text{s} \text{t} \text{u} \text{v} \text{w} \text{x} \text{y} \text{z}$
0116 . C. $\delta \epsilon \zeta \eta \theta \iota \kappa \lambda \text{m} \text{n} \text{o} \text{p} \text{q} \text{r} \text{s} \text{t} \text{u} \text{v} \text{w} \text{x} \text{y} \text{z}$ EQ ENA
0117 . C. $\delta \epsilon \zeta \eta \theta \iota \kappa \lambda \text{m} \text{n} \text{o} \text{p} \text{q} \text{r} \text{s} \text{t} \text{u} \text{v} \text{w} \text{x} \text{y} \text{z}$ EQ 4
0118 . C. $\delta \epsilon \zeta \eta \theta \iota \kappa \lambda \text{m} \text{n} \text{o} \text{p} \text{q} \text{r} \text{s} \text{t} \text{u} \text{v} \text{w} \text{x} \text{y} \text{z}$ EQ DHU
0119 . C. $\delta \epsilon \zeta \eta \theta \iota \kappa \lambda \text{m} \text{n} \text{o} \text{p} \text{q} \text{r} \text{s} \text{t} \text{u} \text{v} \text{w} \text{x} \text{y} \text{z}$ EQ 0xB3
0120 . C.
0121 . C. *****
0122 . C. TII $\alpha \beta \gamma \delta \epsilon \zeta \eta \theta \iota \kappa \lambda \text{m} \text{n} \text{o} \text{p} \text{q} \text{r} \text{s} \text{t} \text{u} \text{v} \text{w} \text{x} \text{y} \text{z}$
0123 . C. *****
0124 . C.
0125 . C. TI_TBL(0x03AB00-0x03AEFF; § 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 . BC (03 ab 03 01 02)
0128 . C. $\delta \epsilon \zeta \eta \theta \iota \kappa \lambda \text{m} \text{n} \text{o} \text{p} \text{q} \text{r} \text{s} \text{t} \text{u} \text{v} \text{w} \text{x} \text{y} \text{z}$ EQ 07
0129 . C. $\delta \epsilon \zeta \eta \theta \iota \kappa \lambda \text{m} \text{n} \text{o} \text{p} \text{q} \text{r} \text{s} \text{t} \text{u} \text{v} \text{w} \text{x} \text{y} \text{z}$ EQ 2B
0130 . C. $\delta \epsilon \zeta \eta \theta \iota \kappa \lambda \text{m} \text{n} \text{o} \text{p} \text{q} \text{r} \text{s} \text{t} \text{u} \text{v} \text{w} \text{x} \text{y} \text{z}$ EQ 3
0131 . C. $\delta \epsilon \zeta \eta \theta \iota \kappa \lambda \text{m} \text{n} \text{o} \text{p} \text{q} \text{r} \text{s} \text{t} \text{u} \text{v} \text{w} \text{x} \text{y} \text{z}$ EQ 0
0132 . C. $\delta \epsilon \zeta \eta \theta \iota \kappa \lambda \text{m} \text{n} \text{o} \text{p} \text{q} \text{r} \text{s} \text{t} \text{u} \text{v} \text{w} \text{x} \text{y} \text{z}$ EQ DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 . BC (07 0b f8)
0135 . C. $\delta \epsilon \zeta \eta \theta \iota \kappa \lambda \text{m} \text{n} \text{o} \text{p} \text{q} \text{r} \text{s} \text{t} \text{u} \text{v} \text{w} \text{x} \text{y} \text{z}$ EQ 7
0136 . C. $\delta \epsilon \zeta \eta \theta \iota \kappa \lambda \text{m} \text{n} \text{o} \text{p} \text{q} \text{r} \text{s} \text{t} \text{u} \text{v} \text{w} \text{x} \text{y} \text{z}$ EQ 0.25 s
0137 . C. $\delta \epsilon \zeta \eta \theta \iota \kappa \lambda \text{m} \text{n} \text{o} \text{p} \text{q} \text{r} \text{s} \text{t} \text{u} \text{v} \text{w} \text{x} \text{y} \text{z}$ EQ 32k
0138 . C. $\delta \epsilon \zeta \eta \theta \iota \kappa \lambda \text{m} \text{n} \text{o} \text{p} \text{q} \text{r} \text{s} \text{t} \text{u} \text{v} \text{w} \text{x} \text{y} \text{z}$ EQ 4M
0139 . C. $\delta \epsilon \zeta \eta \theta \iota \kappa \lambda \text{m} \text{n} \text{o} \text{p} \text{q} \text{r} \text{s} \text{t} \text{u} \text{v} \text{w} \text{x} \text{y} \text{z}$ EQ EXEC
0140 . C.
0141 . C. $\alpha \beta \gamma \delta \epsilon \zeta \eta \theta \iota \kappa \lambda \text{m} \text{n} \text{o} \text{p} \text{q} \text{r} \text{s} \text{t} \text{u} \text{v} \text{w} \text{x} \text{y} \text{z}$
0142 . C. $\delta \epsilon \zeta \eta \theta \iota \kappa \lambda \text{m} \text{n} \text{o} \text{p} \text{q} \text{r} \text{s} \text{t} \text{u} \text{v} \text{w} \text{x} \text{y} \text{z}$ EQ NON
0143 . C.
0144 . C. RAM ID=TI_TBL $\alpha \beta \gamma \delta \epsilon \zeta \eta \theta \iota \kappa \lambda \text{m} \text{n} \text{o} \text{p} \text{q} \text{r} \text{s} \text{t} \text{u} \text{v} \text{w} \text{x} \text{y} \text{z}$
0145 . C.
0146 . C. DHU $\alpha \beta \gamma \delta \epsilon \zeta \eta \theta \iota \kappa \lambda \text{m} \text{n} \text{o} \text{p} \text{q} \text{r} \text{s} \text{t} \text{u} \text{v} \text{w} \text{x} \text{y} \text{z}$
0147 +. DC 01-22 DHU_MODE_CHNG
0148 . BC (02 0a f8)
0149 . C. $\delta \epsilon \zeta \eta \theta \iota \kappa \lambda \text{m} \text{n} \text{o} \text{p} \text{q} \text{r} \text{s} \text{t} \text{u} \text{v} \text{w} \text{x} \text{y} \text{z}$ EQ 2
0150 . C. $\delta \epsilon \zeta \eta \theta \iota \kappa \lambda \text{m} \text{n} \text{o} \text{p} \text{q} \text{r} \text{s} \text{t} \text{u} \text{v} \text{w} \text{x} \text{y} \text{z}$ EQ 0.5S
0151 . C. $\delta \epsilon \zeta \eta \theta \iota \kappa \lambda \text{m} \text{n} \text{o} \text{p} \text{q} \text{r} \text{s} \text{t} \text{u} \text{v} \text{w} \text{x} \text{y} \text{z}$ EQ 32K
0152 . C. $\delta \epsilon \zeta \eta \theta \iota \kappa \lambda \text{m} \text{n} \text{o} \text{p} \text{q} \text{r} \text{s} \text{t} \text{u} \text{v} \text{w} \text{x} \text{y} \text{z}$ EQ 4M
0153 . C.
0154 . C. *****
0155 . C. SOT TI command set
0156 . C. *****
0157 . C. Execute, after the success of OP upload.
0158 +. TI 2016-05-10 10:01:16.0
0159 . DC 07-F0 MDP_SOT_MODE_STBY
0160 . BC (41)
0161 . C. -----
0162 . C. HK1_TI_CMD_NUM = 1 CNTUP []
0163 . C. -----
0164 . C. ***** SOT END *****
0165 . C. Stop EIS observation and temporarily disable EIS mode changes
0166 . C.
0167 . C.
0168 . C. ***** Start EIS operation (TI set) *****
0169 . C. Execute, after the success of OP upload.
0170 . C. Set EIS TI-commands
0171 +. TI 2016-05-10 10:01:30.0
0172 . DC 07-FC EIS_MODE_MANU
0173 . BC (21 02)
0174 +. TI 2016-05-10 10:01:40.0
0175 . DC 07-FC EIS_MODE_CHG_DIS
0176 . BC (22)
0177 . C. [] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0178 . C. ***** End EIS operation (TI set) *****
0179 . C.
0180 . C.
0181 . C.
0182 . C. ***** XRT START *****
0183 . C. Execute, after the success of OP upload.
0184 +. TI 2016-05-10 10:01:00.0
0185 . DC 07-F0 MDP_XRT_MODE_STBY
0186 . BC (c3)
0187 . C. [] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0188 . C.
0189 . C. ***** XRT END *****
0190 . C.
0191 . C. ***** MDP 'úãîñï»öÿðëâðñ¹ëDCCBC•×²ë *****
0192 . C. (%ã°ïÿÖÿÄÿËÿÏÿáÿçÿë»¼¼»Ûñ¹é)
0193 . S. DC-BC dc3c-402:DCBC

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0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
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(a) Spacecraft Operation Procedure (real-commands)

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main-846 2016-05-10 12:50:17 163 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;È□¿□Á□•µ°È»Í×ÁÇ□íYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;È□È¼°ÇÖ□•□¿¼í¹ç□í;çÀ®, ù□¹□è□È□ÇÁ+¿®□•□È□□□³□È;f
0011 +. DC 02-8E AOCS_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 +. DC 07-F0 MDP_XRT_CTRL_MANU
0020 BC (c1)
0021 +. DC 07-F0 MDP_XRT_MODE_STBY
0022 BC (c3)
0023 . C. ----- Success Verify ? OK / NG____
0024 C.
0025 C. XRT Obs. Table Upload
0026 . S. RAM ram-291:MDP_OBS_X
0027 ( )
0028 C.
0029 +. DC 07-F0 MDP_DUMP_XRTTBL
0030 BC (84 07 00 00 00 3a d4)
0031 . C. ----- Comparison Check ? OK / ERR ____
0032 C.
0033 C.
0034 +. DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 01 b1 b1 04 04)
0036 +. DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 02 b1 b1 08 08)
0038 +. DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 03 b1 b1 08 08)
0040 +. DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 04 b1 b1 06 06)
0042 +. DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 06 06)
0044 +. DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 80 80 20 20)
0046 +. DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 20 08)
0048 +. DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 08 20)
0050 +. DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 80 96 08 08)
0052 +. DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0b 80 60 20 18)
0054 +. DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0c a0 80 18 20)
0056 +. DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 0f 80 80 06 06)
0058 +. DC 07-F0 MDP_XRT_ROI_SET
0059 BC (cd 10 80 80 08 08)
0060 +. DC 07-F0 MDP_XRT_FLD_ENA
0061 BC (d8)
0062 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0063 BC (c8)
0064 +. DC 07-F0 MDP_XRT_ARS_DIS
0065 BC (d5)
0066 +. DC 07-F0 MDP_XRT_AEC_RESET
0067 BC (d0)
0068 +. DC 07-F0 MDP_XRT_FLD_RESET
0069 BC (da)
0070 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0071 BC (c4 0b)
0072 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0073 BC (c5 07)
0074 . C. ----- Success Verify ? OK / NG ____
0075 C.
0076 C.
0077 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0078 C.
0079 +. DC 07-F0 MDP_XRT_MODE_OBSV
0080 BC (c2)
0081 +. TI 2016-05-10 10:01:02.0
0082 DC 07-F0 MDP_XRT_MODE_OBSV
0083 BC (c2)
0084 . C. ----- Success Verify ? OK / NG ____
0085 C.
0086 C. ***** XRT END *****
0087 . C. *****
0088 C. SOT table upload
0089 C. *****
0090 . C. < Stop SP table >
0091 +. DC 07-F0 MDP_SP_CTRL_MANU
0092 BC (61)
0093 C. -----
0094 C. MDP_SP_CTRL_MODE = MANU [ ]
0095 C. -----
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0096 C.
0097 . C. <Upload SP Observation Table>
0098 . S. RAM ram-284:MDP_OBS_S
0099 ( )
0100 C.
0101 . C. < Dump RAMID=MDP_OBS_S >
0102 +. DC 07-F0 MDP_DUMP_SPTBL
0103 BC (83 07 00 00 00 38 b8)
0104 C. -----
0105 C. MDP_OBS_S verify = OK/NG [ ]
0106 C. -----
0107 C.
0108 . C. < Upload DPL table >
0109 +. DC 07-F0 MDP_FG_CTRL_MANU
0110 BC (51)
0111 . C. -----
0112 C. MDP_FG_CTRL_MODE = MANU [ ]
0113 C. -----
0114 C.
0115 C. ¥çYÃ¥×¥í;¥É°îÁ°æËSTS_CHKαðOFFαËα¹æ
0116 C.
0117 . S. RAM ram-271:MDP_DPL
0118 ( )
0119 C.
0120 . C. < Dump RAMID=MDP_DPL >
0121 +. DC 07-F0 MDP_DUMP_FGTBL
0122 BC (82 07 00 38 b8 00 40)
0123 C. -----
0124 C. MDP_DPL verify = OK [ ]
0125 C. -----
0126 C.
0127 C. STS_CHKαðONαËα¹æ
0128 C.
0129 . C. < Update MDP DSC PAR1 >
0130 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0131 BC (4c)
0132 C. MDP_CMD_CODE = F04C0700[ ]
0133 C. MDP_CMD_CNT (count-up 1) [ ]
0134 C. -----
0135 C.
0136 . C.
0137 C. *****
0138 C. SOT TI command set
0139 C. *****
0140 C. Execute, after the success of TBL upload.
0141 +. TI 2016-05-10 10:01:18.0
0142 DC 07-F0 MDP_SOT_MODE_OBSV
0143 BC (40)
0144 . C. -----
0145 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0146 C. -----
0147 C.
0148 C.
0149 . C. ***** MDP ´úÃîî»ö¼ÝαËÊα¹æDCBC•x²è *****
0150 C. (%ã°îYÓYÃYÉY¥YÉYá¥çYèαË¼α¼Ã»Ûα¹æ)
0151 . S. DC-BC dcbc-402:DCBC
0152 (MDP_known_event)
0153 C.
0154 C.
0155 . C. ***** ¥ÐY¹•ï Daily±;îÑαË´Øα¹æDCBC•x²è *****
0156 . S. DC-BC dcbc-153:DCBC
0157 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0158 C.
0159 C.
0160 . C. ;ãLOS¥Ã¥S¥Ã¥-¼Ã»Û;ã
0161 C.
0162 . C. ***** LOS *****
0163 C.
```

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*** OP Sequence for XRT ***

2016/05/10	10:11:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/10	10:11:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/10	10:11:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/05/10	10:12:00.0	AOCS_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 00 00 00 00
2016/05/10	10:12:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2016/05/10	10:12:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/05/10	10:12:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2016/05/10	10:12:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/10	10:12:26.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/10	10:14:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2016/05/10	10:14:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/05/10	10:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/10	10:46:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/10	10:46:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/10	10:46:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/10	10:46:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/10	10:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/10	10:51:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/10	10:51:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/10	10:51:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/10	10:51:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/10	10:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/10	11:16:30.0	XRT_Custom_430_OG [0x1ae]			
2016/05/10	11:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/10	12:29:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/10	12:29:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/10	12:29:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/10	12:29:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/10	12:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/10	12:55:00.0	XRT_Custom_430_OG [0x1ae]			
2016/05/10	12:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/10	14:08:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/10	14:08:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/10	14:08:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/10	14:08:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/10	14:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/10	14:41:30.0	XRT_Custom_430_OG [0x1ae]			
2016/05/10	14:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/10	15:46:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/10	15:46:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/10	15:46:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/10	15:46:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/10	15:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/10	16:30:00.0	XRT_Custom_430_OG [0x1ae]			
2016/05/10	16:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/10	17:09:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/10	17:09:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/10	17:09:58.0	XRT_FOCUS_POSITION_403_OG [0x193]			

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2016/05/10	17:10:00.0	AOCS_OrE-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
		AOCU_NM		5	02-76	00	00	00	00
2016/05/10	17:10:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2016/05/10	17:12:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2016/05/10	17:12:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/05/10	17:12:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f		
2016/05/10	17:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/05/10	17:19:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/10	17:19:02.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2016/05/10	17:19:22.0	XRT_FLD_DIS_407_OG [0x197]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2016/05/10	17:21:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2016/05/10	17:21:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/05/10	17:21:28.0	XRT_QT_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	10		
2016/05/10	17:21:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/05/10	17:39:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/10	17:39:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/10	17:39:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2016/05/10	17:40:00.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01	00	00	00
2016/05/10	17:40:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2016/05/10	17:40:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2016/05/10	17:40:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2016/05/10	17:40:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/05/10	17:40:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/05/10	17:42:56.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e		
2016/05/10	17:42:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07		
2016/05/10	18:06:30.0	XRT_Custom_430_OG [0x1ae]							
2016/05/10	18:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/05/10	19:03:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/10	19:03:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/10	19:03:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/05/10	19:03:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/05/10	19:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/05/10	19:43:00.0	XRT_Custom_430_OG [0x1ae]							
2016/05/10	19:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/05/10	20:41:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/10	20:41:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/10	20:41:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/05/10	20:41:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/05/10	20:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/05/10	21:20:30.0	XRT_Custom_430_OG [0x1ae]							
2016/05/10	21:21:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/05/10	22:20:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/10	22:20:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/10	22:20:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/05/10	22:20:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/05/10	22:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/05/10	22:55:30.0	XRT_Custom_430_OG [0x1ae]							
2016/05/10	22:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/05/10	23:58:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			

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2016/05/10	23:58:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/10	23:58:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/10	23:58:06.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/05/11	00:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/11	00:24:00.0	XRT_Custom_430_OG [0x1ae]				
2016/05/11	00:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/11	01:36:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/11	01:36:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/11	01:36:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/11	01:36:06.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/05/11	01:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/11	02:02:00.0	XRT_Custom_430_OG [0x1ae]				
2016/05/11	02:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/11	03:10:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/11	03:10:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/11	03:10:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/11	03:10:36.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/05/11	03:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/11	03:40:30.0	XRT_Custom_430_OG [0x1ae]				
2016/05/11	03:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/11	04:40:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/11	04:40:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/11	04:40:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/11	04:40:06.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/05/11	04:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/11	05:19:00.0	XRT_Custom_430_OG [0x1ae]				
2016/05/11	05:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/11	05:52:24.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/11	05:52:26.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/11	05:52:28.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2016/05/11	05:52:30.0	AOCs_Orе-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00 00 00 00 00	
2016/05/11	05:52:48.0	XRT_FLD_DIS_406_OG [0x196] MDP_XRT_FLD_DIS	1	07-F0	d9	
2016/05/11	05:55:24.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2016/05/11	05:55:26.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/05/11	05:55:28.0	XRT_QT_PROG_SET_434_OG [0x1b2] MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f	
2016/05/11	05:55:30.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/11	06:02:24.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/11	06:02:26.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/11	06:02:28.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2016/05/11	06:02:30.0	AOCs_Orе-point_Start_1_OG [0x097] AOCU_NM	5	02-76	01 00 00 00 00	
2016/05/11	06:02:48.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/05/11	06:02:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/05/11	06:02:52.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/05/11	06:02:54.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/05/11	06:02:56.0	XRT_FLD_RESET_433_OG [0x1b1] MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/11	06:05:26.0	XRT_QT_PROG_SET_401_OG [0x191] MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e	
2016/05/11	06:05:28.0	XRT_FL_PROG_SET_436_OG [0x1b4] MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2016/05/11	06:05:30.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/11	06:20:30.0	XRT_CTRL_MANU_400_OG [0x190]				

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2016/05/11	06:20:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/11	06:20:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/11	06:20:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/11	06:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/11	06:57:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/11	06:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2016/05/11	08:00:30.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]			
2016/05/11	08:00:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/11	08:00:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/11	08:00:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/11	08:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/11	08:35:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/11	08:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/11	09:40:30.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_Custom_430_OG [0x1ae]			
2016/05/11	09:40:32.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_CTRL_AUTO_424_OG [0x1a8]			
2016/05/11	09:40:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/11	09:40:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/11	09:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/11	10:14:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/11	10:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/11	11:27:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/11	11:27:02.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_Custom_430_OG [0x1ae]			
2016/05/11	11:27:04.0	XRT_FLD_RESET_415_OG [0x19f]	XRT_CTRL_AUTO_424_OG [0x1a8]			
2016/05/11	11:27:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/11	11:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/11	11:52:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/11	11:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/11	13:05:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/11	13:05:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/11	13:05:04.0	XRT_FLD_RESET_415_OG [0x19f]	XRT_Custom_430_OG [0x1ae]			
2016/05/11	13:05:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	XRT_CTRL_AUTO_424_OG [0x1a8]			
2016/05/11	13:05:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/11	13:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/11	13:31:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/11	13:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/11	14:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/11	14:43:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/11	14:43:34.0	XRT_FLD_RESET_415_OG [0x19f]	XRT_Custom_430_OG [0x1ae]			
2016/05/11	14:43:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	XRT_CTRL_AUTO_424_OG [0x1a8]			
2016/05/11	14:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/11	15:18:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/11	15:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/11	16:22:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/11	16:22:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/11	16:22:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/11	16:22:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	XRT_Custom_430_OG [0x1ae]			
2016/05/11	16:22:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	XRT_CTRL_AUTO_424_OG [0x1a8]			
2016/05/11	16:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/11	17:05:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/11	17:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/11	17:44:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/11	17:44:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STRT	1	07-F0	e8

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2016/05/11	17:44:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/11	17:45:00.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/05/11	17:45:18.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00 00 00 00
2016/05/11	17:47:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2016/05/11	17:47:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/05/11	17:47:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/11	17:48:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2016/05/11	17:57:30.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/11	17:57:32.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/11	17:57:52.0	XRT_FLD_DIS_407_OG [0x197]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/05/11	17:59:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2016/05/11	17:59:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/05/11	17:59:58.0	XRT_QT_PROG_SET_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/11	18:00:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2016/05/11	18:14:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/11	18:14:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/11	18:14:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/11	18:15:00.0	AOCS_Ore-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/05/11	18:15:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01 00 00 00
2016/05/11	18:15:20.5	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/05/11	18:15:22.5	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/05/11	18:15:24.5	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/05/11	18:15:26.5	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/11	18:17:56.5	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/11	18:17:58.5	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2016/05/11	18:41:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/05/11	18:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2016/05/11	19:38:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/11	19:38:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/11	19:38:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/11	19:38:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/11	19:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	da
2016/05/11	20:18:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/11	20:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	e8
2016/05/11	21:17:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/11	21:17:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/11	21:17:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/11	21:17:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/11	21:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	da
2016/05/11	21:55:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/11	21:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	e8
2016/05/11	22:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/11	22:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/11	22:29:58.0	XRT_FOCUS_POSITION_438_OG [0x1b6]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/11	22:30:00.0	AOCS_Ore-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/05/11	22:30:18.0	XRT_FLD_DIS_440_OG [0x1b8]	AOCU_NM	5	02-76	00 ad 59 00 00
2016/05/11	22:44:54.0	XRT_FLRCTRL_DIS_441_OG [0x1b9]	MDP_XRT_FLD_DIS	1	07-F0	d9
2016/05/11	22:44:56.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9

2016/05/11	22:44:58.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/05/11	22:45:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	13		
2016/05/12	00:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/05/12	00:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/12	00:29:58.0	XRT_FOCUS_POSITION_438_OG [0x1b6]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/12	00:30:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2016/05/12	00:30:18.0	XRT_FLD_DIS_440_OG [0x1b8]	AOCU_NM	5	02-76	00	00	00	56 35
2016/05/12	00:44:54.0	XRT_FLRCTRL_DIS_441_OG [0x1b9]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2016/05/12	00:44:56.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2016/05/12	00:44:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/05/12	00:45:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c		
2016/05/12	02:30:00.0	AOCS_Ore-point_Start_5_OG [0x09b]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/05/12	02:35:00.0	XRT_CTRL_MANU_402_OG [0x192]	AOCU_NM	5	02-76	02	00	00	00 00
2016/05/12	02:35:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/12	02:35:04.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/12	02:35:24.0	XRT_FLD_DIS_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2016/05/12	02:38:00.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2016/05/12	02:38:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2016/05/12	02:38:02.0	XRT_ARS_DIS_423_OG [0x1a7]	XRT_Custom_430_OG [0x1ae]						
2016/05/12	02:38:04.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/05/12	02:39:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f		
2016/05/12	02:45:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/05/12	02:45:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/12	02:45:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/12	02:46:18.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2016/05/12	02:46:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2016/05/12	02:46:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2016/05/12	02:46:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2016/05/12	02:46:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/05/12	02:48:56.0	XRT_QT_PROG_SET_413_OG [0x19d]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/05/12	02:48:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05		
2016/05/12	02:49:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07		
2016/05/12	03:40:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/05/12	03:40:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/12	03:40:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/12	03:40:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/05/12	03:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/05/12	04:16:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/05/12	04:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]						
2016/05/12	05:16:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/05/12	05:16:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/12	05:16:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/12	05:16:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/05/12	05:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/05/12	05:54:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/05/12	05:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]						
2016/05/12	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/05/12	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			

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2016/05/12	06:09:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/12	06:10:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/05/12	06:10:18.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00 00 00 00 00
2016/05/12	06:12:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2016/05/12	06:12:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/05/12	06:12:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/12	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2016/05/12	06:19:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/12	06:19:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/12	06:19:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/12	06:20:00.0	AOCS_ORe-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/05/12	06:20:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01 00 00 00 00
2016/05/12	06:20:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/05/12	06:20:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/05/12	06:20:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/05/12	06:20:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/12	06:22:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/12	06:22:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2016/05/12	06:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/05/12	06:57:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/12	06:57:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/12	06:57:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/12	06:57:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/12	07:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/12	07:33:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/12	07:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2016/05/12	08:36:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/12	08:36:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/12	08:36:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/12	08:36:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/12	08:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/12	09:11:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/12	09:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2016/05/12	10:17:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/12	10:17:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/12	10:17:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/12	10:17:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/12	10:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/12	10:51:00.0	AOCS_ORe-point_Start_2_OG [0x098]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
		AOCU_NM		5	02-76	00 00 00 00 00