

XRT Timeline to be uploaded on 2016/05/12

Period: 2016/05/12 10:41:00 - 2016/05/17 10:22:00

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Normal mode

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XOB #1AD5: CME watch - 4x4 - AEC 2 - Be-thin - G-band (2x2,1ms) - Leak (2x2,1ms) - 60s cad														
Term		Pointing (x, y)				Comment								
05/12 10:54:00 - 05/12 11:12:54		Fixed (-15.0, -995.0)				# OP start + 10min, HOP 79 (irradiance scan). 1/20								
05/12 11:16:00 - 05/12 11:34:54		Fixed (-15.0, -895.0)				# 2/20								
05/12 11:38:00 - 05/12 12:02:30		Fixed (-15.0, -795.0)				# 3/20								
05/12 12:32:30 - 05/12 12:51:24		Fixed (-15.0, -695.0)				# 4/20								
05/12 12:54:30 - 05/12 13:13:24		Fixed (-15.0, -595.0)				# 5/20								
05/12 13:16:30 - 05/12 14:09:00		Fixed (-15.0, -495.0)				# 6/20								
05/12 14:15:00 - 05/12 14:33:54		Fixed (-15.0, -395.0)				# 7/20								
05/12 14:37:00 - 05/12 15:19:00		Fixed (-15.0, -295.0)				# 8/20								
05/12 16:07:00 - 05/12 16:25:54		Fixed (-15.0, -195.0)				# 9/20								
05/12 16:29:00 - 05/12 16:45:51		Fixed (-15.0, -95.0)				# 10/20								
05/12 18:06:00 - 05/12 18:36:00		Fixed (-15.0, 95.0)				# 12/20								
05/12 19:20:30 - 05/12 19:39:24		Fixed (-15.0, 195.0)				# 13/20								
05/12 19:42:30 - 05/12 20:14:00		Fixed (-15.0, 295.0)				# 14/20								
05/12 20:58:00 - 05/12 21:16:54		Fixed (-15.0, 395.0)				# 15/20								
05/12 21:20:00 - 05/12 21:52:30		Fixed (-15.0, 495.0)				# 16/20								
05/12 22:33:30 - 05/12 22:52:24		Fixed (-15.0, 595.0)				# 17/20								
05/12 22:55:30 - 05/12 23:31:00		Fixed (-15.0, 695.0)				# 18/20								
05/13 00:01:00 - 05/13 00:19:54		Fixed (-15.0, 795.0)				# 19/20								
05/13 00:23:00 - 05/13 00:39:54		Fixed (-15.0, 895.0)				# 20/20								
PROG= 05 Inf.-time(s)														
Subr= 1		60-time(s)		60.0sec										
Seqn= 29		1-time(s)		4.0sec										
thin-Be/Open		med-Be/Open		close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Subr= 2		1-time(s)		2.0sec										
Seqn= 26		1-time(s)		2.0sec										
Open/G-band		Open/G-band		open	Safe	Norm	1ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band		Open/G-band		close	Safe	Norm	1ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1B33: Stray light study 2016-3 ; med-Al, 2x2 full FOV(1min-cad) and 2x2 256 on AR(10sec-cad)															
Term		Pointing (x, y)				Comment									
05/12 16:54:30 - 05/12 17:46:54		Fixed (-15.0, -5.0)				# 11/20 & XRT stray light measurement & synoptic at 17:41UT.									
PROG= 20		1-time(s)													
┌	Subr= 1		1-time(s)		2.0sec										
	└	Seqn= 68		1-time(s)		150.0sec									
		med-Al/Open		med-Al/Open		close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0
	med-Al/Open		med-Al/Open		close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Subr= 2		21-time(s)		2.0sec										
	└	Seqn= 83		1-time(s)		10.0sec									
		Open/Ti-poly		Open/thick-Al		close	Safe	Norm	250ms	Obs	2x2	512x512 (1024, 1200)	Q=95	0	0
	Open/G-band		Open/G-band		close	Safe	Norm	1ms	Obs	2x2	512x512 (1024, 1200)	Q=95	0	0	2.0sec
	┌	Subr= 3		6-time(s)		2.0sec									
		└	Seqn= 23		1-time(s)		2.0sec								
med-Al/Open			med-Al/Open		close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 83		4-time(s)		10.0sec											
Open/Ti-poly		Open/thick-Al		close	Safe	Norm	250ms	Obs	2x2	512x512 (1024, 1200)	Q=95	0	0	2.0sec	
Open/G-band		Open/G-band		close	Safe	Norm	1ms	Obs	2x2	512x512 (1024, 1200)	Q=95	0	0	2.0sec	
Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

XOB #1B14: Synoptic Q95 2x2 - Al/mesh(24/256/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(45/512/4096) + Ti-poly(45/512/4096)													
Term		Pointing (x, y)			Comment								
05/12 17:50:00 - 05/12 18:02:54		Fixed (-15.0, -5.0)			# 11/20 & XRT stray light measurement & synoptic at 17:41UT.								
05/13 06:36:30 - 05/13 06:43:24		Fixed (0.0, 0.0)			synoptic, shifted 33.5 min								
05/13 17:18:00 - 05/13 17:27:30		Fixed (0.0, 0.0)			synoptic, shifted. XRT stray light measurement.								
PROG= 15 1-time(s)													
Subr= 1		1-time(s)		2.0sec									
Seqn= 5		1-time(s)		2.0sec									
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 1		1-time(s)		2.0sec									
└ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 99		1-time(s)		2.0sec									
└ Al-poly/Open		Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└ Al-poly/Open		Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└ Al-poly/Open		Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1AFF: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
05/13 00:43:00 - 05/13 06:33:24	Track (551.6, 170.6) @ 05/13 00:40:00	# AR 12542.
05/13 06:46:30 - 05/13 17:14:54	Track (595.4, 168.5) @ 05/13 06:43:30	# AR cont.

PROG= 11 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec													
	Seqn= 56	1-time(s)	2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec		
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec		
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec		
Subr= 2	5-time(s)	2.0sec													
	Seqn= 75	1-time(s)	2.0sec												
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec		
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec		
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec		
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec		
	Seqn= 96	4-time(s)	60.0sec												
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec		
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	15.0sec		
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec		
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	15.0sec			
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec			
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec			
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC	Buffer	Interval	

XOB #1B34: Stray light study 2016-4 ; med-Be, 2x2 full FOV(1min-cad) and 2x2 256 on AR(10sec-cad)

Term	Pointing (x, y)	Comment
05/13 17:30:00 - 05/13 18:14:54	Fixed (0.0, 0.0)	synoptic, shifted. XRT stray light measurement.

PROG= 02	1-time(s)
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XOB #1B18: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
05/13 18:18:00 - 05/14 05:59:54	Track (672.7, 164.3) @ 05/13 18:15:00	# AR cont.
05/14 06:13:00 - 05/14 10:40:00	Track (743.8, 159.7) @ 05/14 06:10:00	# AR cont.

PROG= 14 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec										
Seqn= 56	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2	5-time(s)	2.0sec										
Seqn= 75	1-time(s)	2.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 50	4-time(s)	90.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	25.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	25.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec

Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval
XOB #1B35: Synoptic 7 Filter w/ Al-mesh(24/256/2897), Al-poly(45/512/4096), Thin-Be(181/2048/11571) - Thick-Be(65536), Al-poly+Ti-poly(362/4096), Med-Al(181/2048/11571)														
Term				Pointing (x, y)				Comment						
05/14 06:03:00 - 05/14 06:09:54				Fixed (0.0, 0.0)				synoptic						
PROG= 04 1-time(s)														
Subr= 1		1-time(s)		2.0sec										
Seqn= 5		1-time(s)		2.0sec										
└		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0 2.0sec
└		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	0	0 2.0sec
└		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)		Q=98	0	0 2.0sec
└		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)		DPCM	0	0 2.0sec
└		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)		DPCM	0	0 2.0sec
Seqn= 1		1-time(s)		2.0sec										
└		Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0 2.0sec
└		Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0 2.0sec
└		Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0 2.0sec
Seqn= 99		1-time(s)		2.0sec										
└		Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0 2.0sec
└		Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0 2.0sec
└		Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0 2.0sec
Seqn= 67		1-time(s)		2.0sec										
└		thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0 2.0sec
└		thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0 2.0sec
└		thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0 2.0sec
Seqn= 54		1-time(s)		4.0sec										
└		Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)		Q=90	0	0 2.0sec
└		Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)		Q=95	0	0 2.0sec
Subr= 2		1-time(s)		2.0sec										
Seqn= 46		2-time(s)		2.0sec										
└		Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0 2.0sec
Seqn= 94		2-time(s)		2.0sec										
└		Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0 2.0sec
└		Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0 2.0sec
Seqn= 68		2-time(s)		2.0sec										
└		med-Al/Open	med-Al/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0 2.0sec
└		med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0 2.0sec
Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval

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Flare mode

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XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term				Pointing (x, y)				Comment			
05/12 10:54:00 - 05/12 11:12:54				Fixed (-15.0, -995.0)				# OP start + 10min, HOP 79 (irradiance scan). 1/20			
05/12 11:16:00 - 05/12 11:34:54				Fixed (-15.0, -895.0)				# 2/20			
05/12 11:38:00 - 05/12 12:02:30				Fixed (-15.0, -795.0)				# 3/20			
05/12 12:32:30 - 05/12 12:51:24				Fixed (-15.0, -695.0)				# 4/20			
05/12 12:54:30 - 05/12 13:13:24				Fixed (-15.0, -595.0)				# 5/20			
05/12 13:16:30 - 05/12 14:09:00				Fixed (-15.0, -495.0)				# 6/20			
05/12 14:15:00 - 05/12 14:33:54				Fixed (-15.0, -395.0)				# 7/20			
05/12 14:37:00 - 05/12 15:19:00				Fixed (-15.0, -295.0)				# 8/20			
05/12 16:07:00 - 05/12 16:25:54				Fixed (-15.0, -195.0)				# 9/20			
05/12 16:29:00 - 05/12 16:45:51				Fixed (-15.0, -95.0)				# 10/20			
05/12 18:06:00 - 05/12 18:36:00				Fixed (-15.0, 95.0)				# 12/20			
05/12 19:20:30 - 05/12 19:39:24				Fixed (-15.0, 195.0)				# 13/20			
05/12 19:42:30 - 05/12 20:14:00				Fixed (-15.0, 295.0)				# 14/20			
05/12 20:58:00 - 05/12 21:16:54				Fixed (-15.0, 395.0)				# 15/20			
05/12 21:20:00 - 05/12 21:52:30				Fixed (-15.0, 495.0)				# 16/20			
05/12 22:33:30 - 05/12 22:52:24				Fixed (-15.0, 595.0)				# 17/20			
05/12 22:55:30 - 05/12 23:31:00				Fixed (-15.0, 695.0)				# 18/20			
05/13 00:01:00 - 05/13 00:19:54				Fixed (-15.0, 795.0)				# 19/20			
05/13 00:23:00 - 05/13 00:39:54				Fixed (-15.0, 895.0)				# 20/20			
05/13 00:43:00 - 05/13 06:33:24				Track (551.6, 170.6) @ 05/13 00:40:00				# AR 12542.			
05/13 06:46:30 - 05/13 17:14:54				Track (595.4, 168.5) @ 05/13 06:43:30				# AR cont.			
05/13 18:18:00 - 05/14 05:59:54				Track (672.7, 164.3) @ 05/13 18:15:00				# AR cont.			
05/14 06:13:00 - 05/14 10:40:00				Track (743.8, 159.7) @ 05/14 06:10:00				# AR cont.			

PROG= 07 30-time(s)

Subr= 1 20-time(s) 2.0sec											
Seqn= 11 1-time(s) 2.0sec											
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs 2x2	512x512 (1024, 1024)	Q=95	2	0 2.0sec
Seqn=100 1-time(s) 10.0sec											
	thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs 1x1	384x384 (1024, 1024)	Q=95	2	0 2.0sec
	med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs 1x1	384x384 (1024, 1024)	Q=95	3	0 2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs 1x1	384x384 (1024, 1024)	Q=95	3	0 2.0sec
Subr= 2 1-time(s) 2.0sec											
Seqn= 10 1-time(s) 2.0sec											
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs 1x1	384x384 (1024, 1024)	Q=95	3	0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs 1x1	384x384 (1024, 1024)	Q=95	3	0 2.0sec

┌	Seqn= 11	1-time(s)	2.0sec											
	└	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	┌	Seqn= 84	1-time(s)	2.0sec										
	└	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	└	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	└	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	└	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * *

Active Region Search

* * * * *

NOT USED

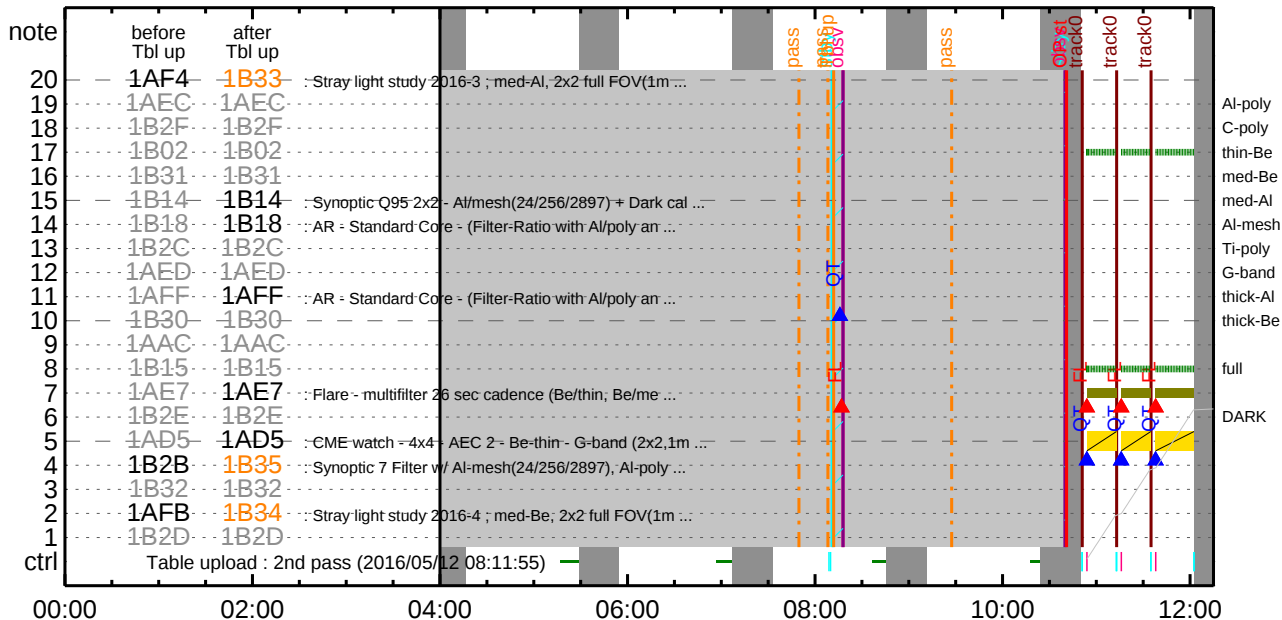
* * * * *

Flare Detection

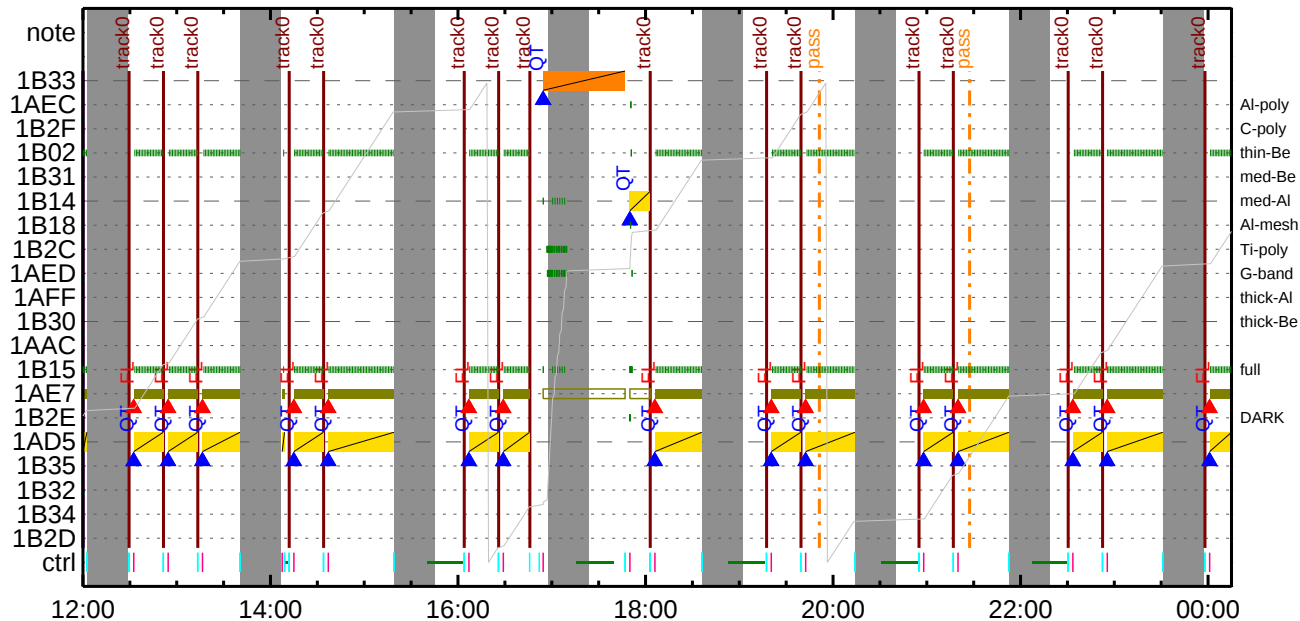
* * * * *

FLD Patrol													
Term				Pointing (x, y)				Comment					
05/12 18:03:18 - 05/13 06:33:48				Fixed (-15.0, 95.0)				# 12/20					
05/13 06:43:48 - 05/13 17:15:18				Track (595.4, 168.5) @ 05/13 06:43:30				# AR cont.					
05/13 18:15:18 - 05/14 06:00:18				Track (672.7, 164.3) @ 05/13 18:15:00				# AR cont.					
05/14 06:10:18 - 05/17 10:22:00				Track (743.8, 159.7) @ 05/14 06:10:00				# AR cont.					
Open/Ti-poly		Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8			Q=50	30sec	
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

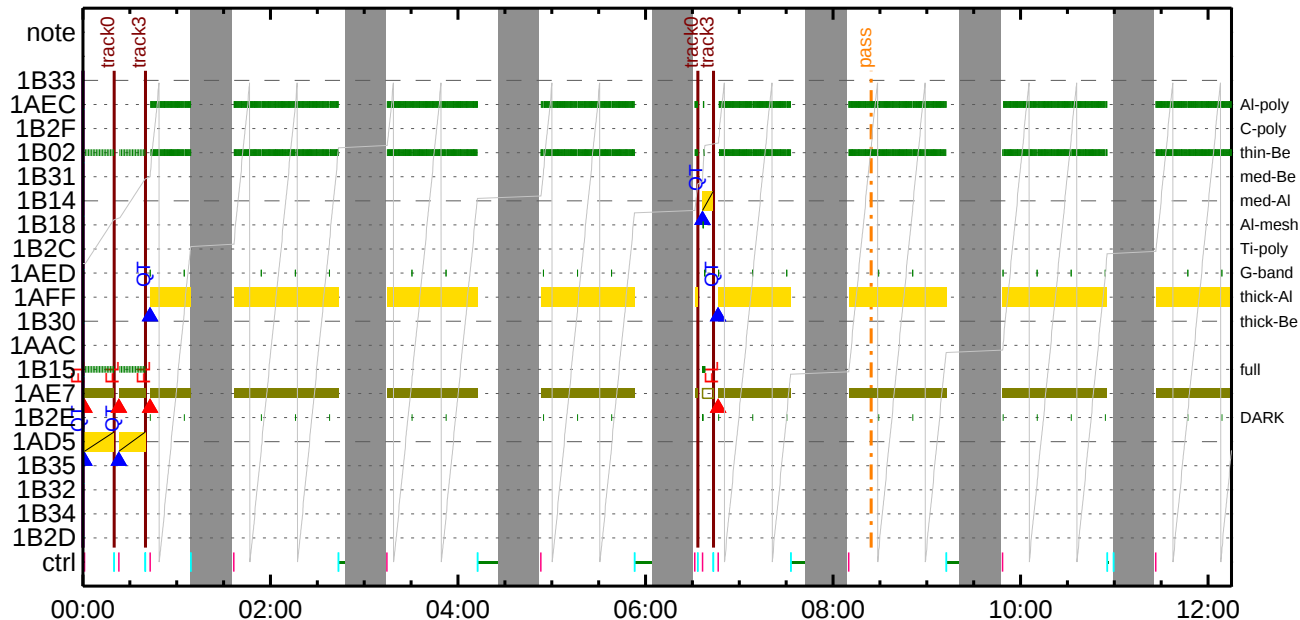
CMDI #0917 2016/05/12



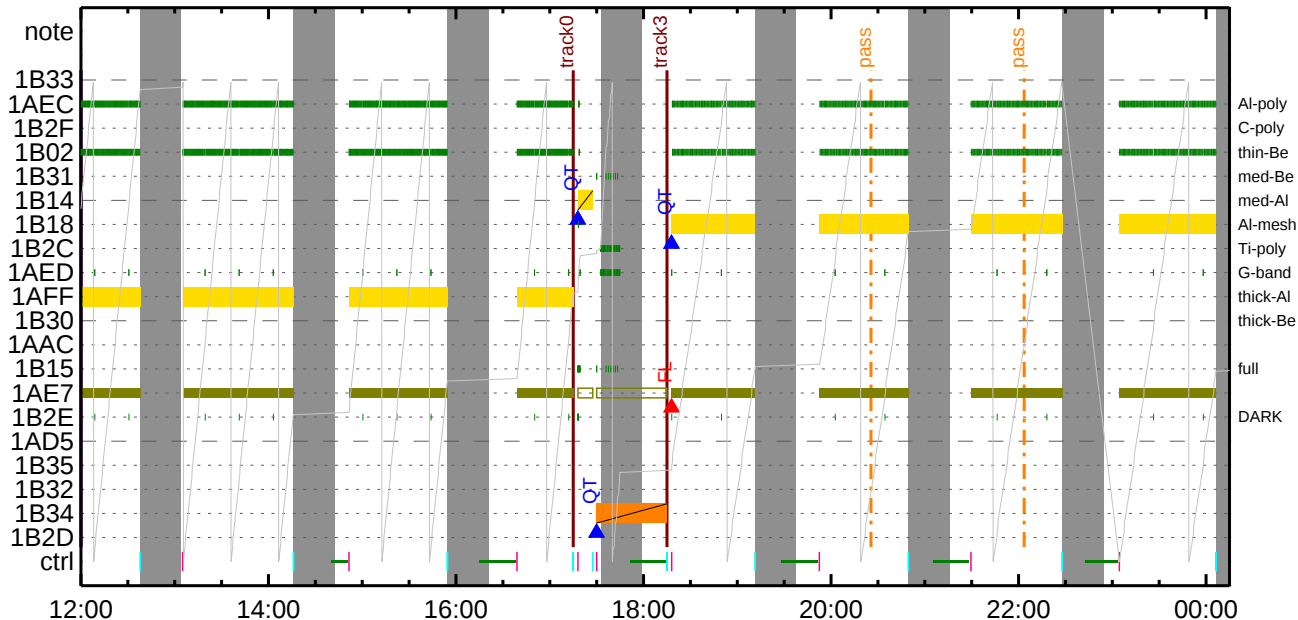
CMDI #0917 2016/05/12



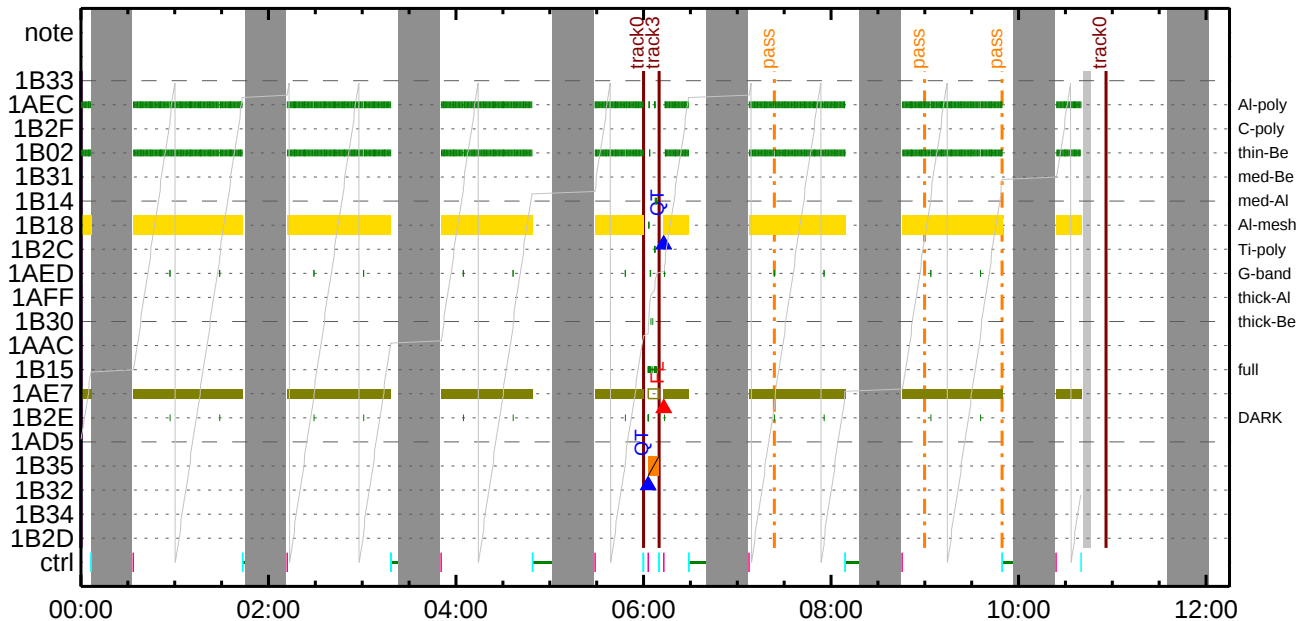
CMDI #0917 2016/05/13



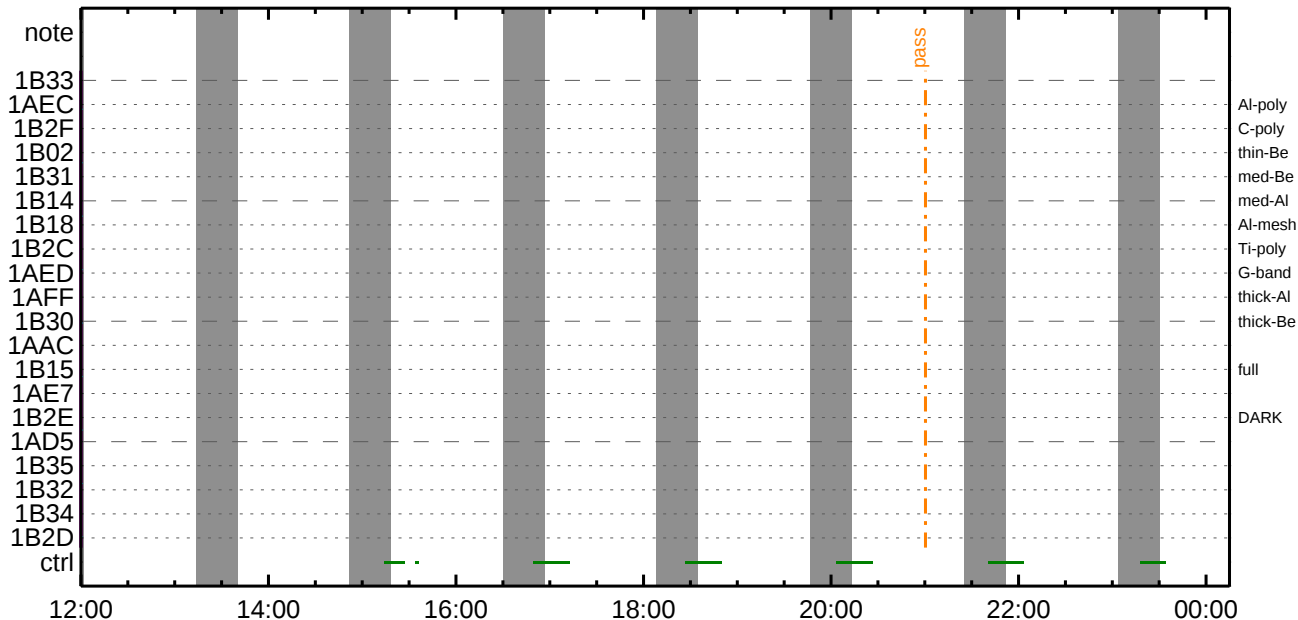
CMDI #0917 2016/05/13



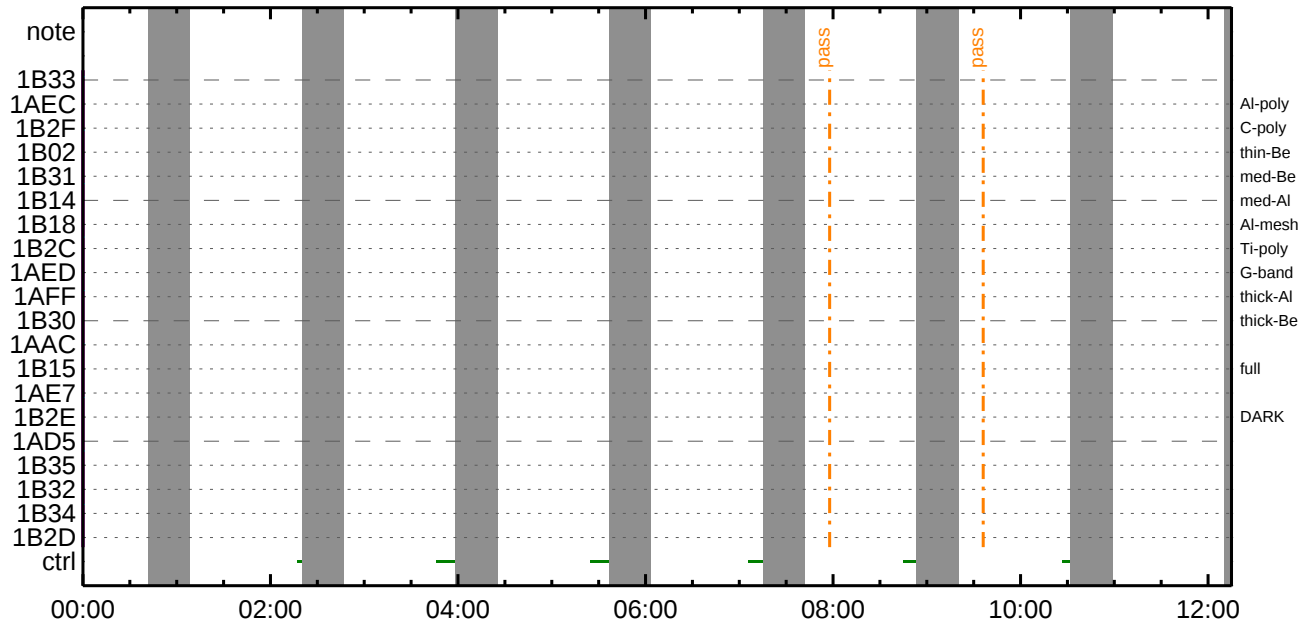
CMDI #0917 2016/05/14



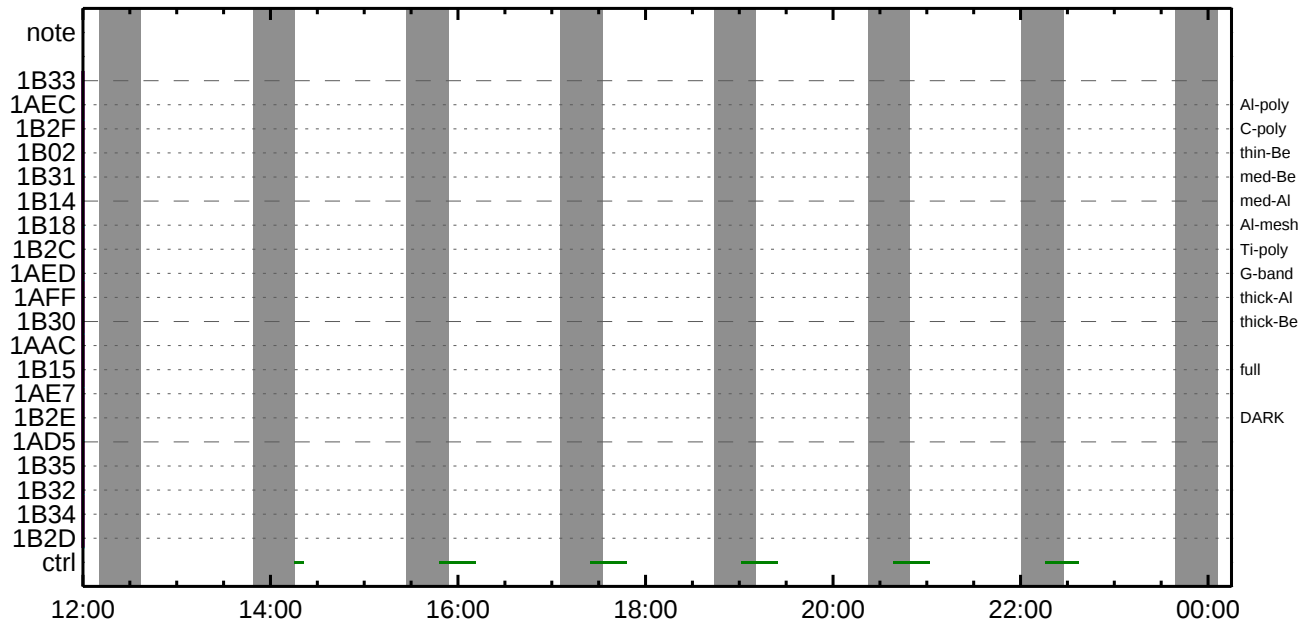
CMDI #0917 2016/05/14



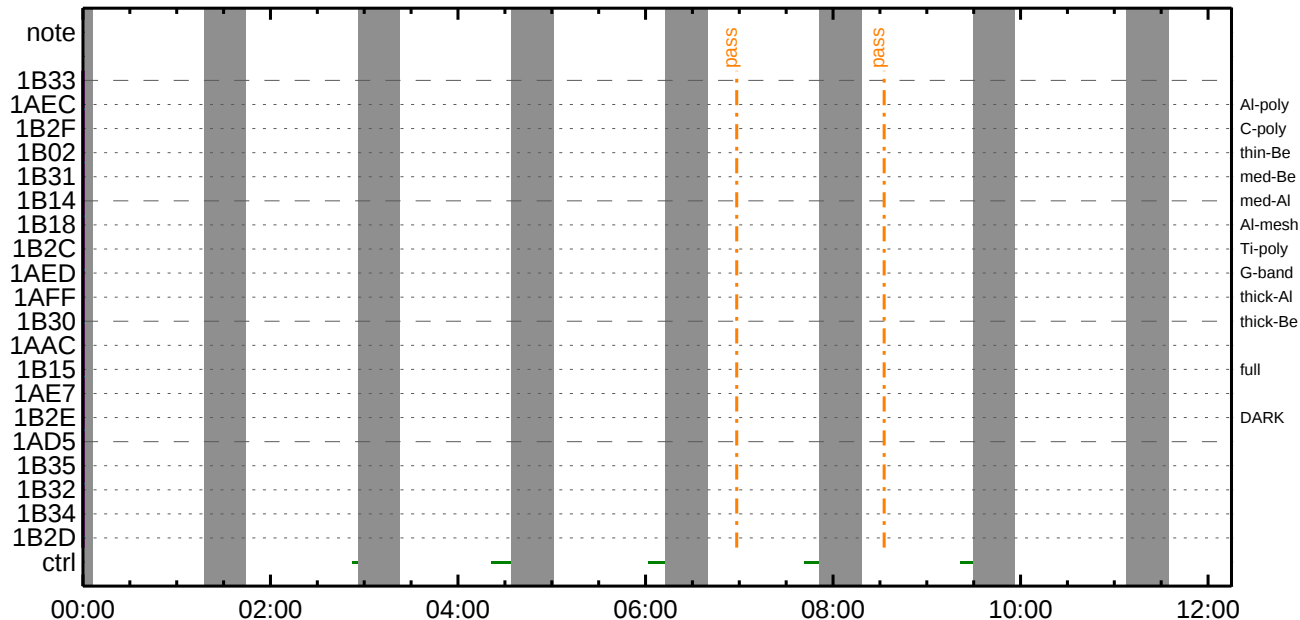
CMDI #0917 2016/05/15



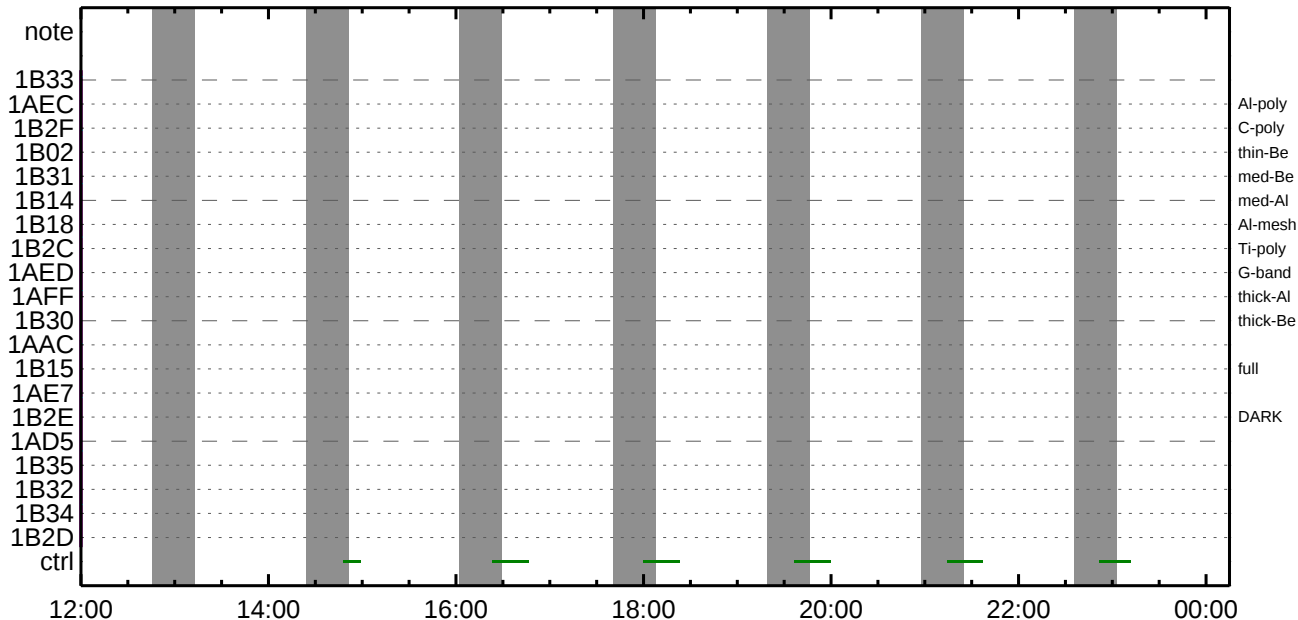
CMDI #0917 2016/05/15



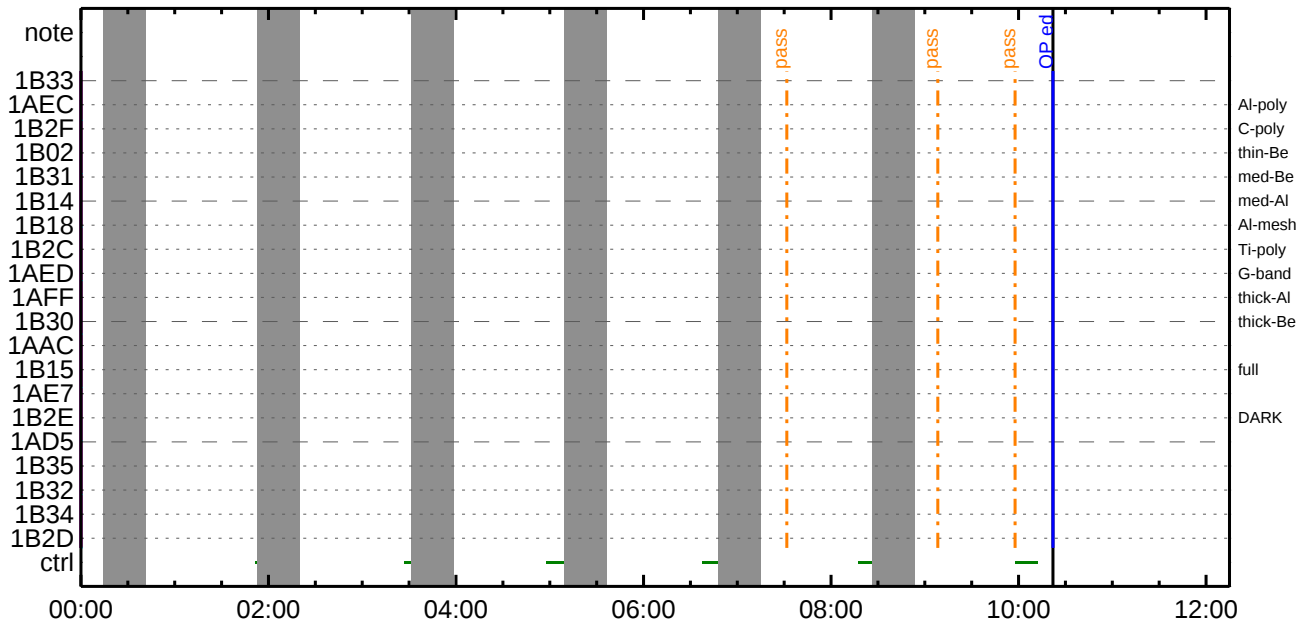
CMDI #0917 2016/05/16



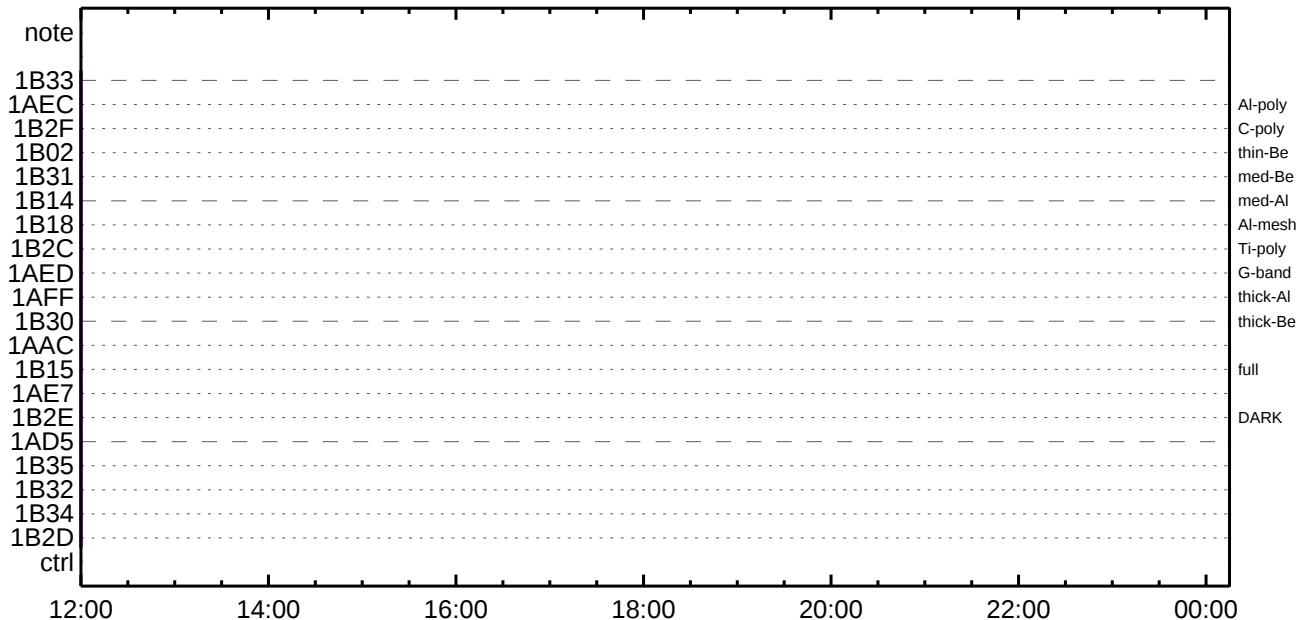
CMDI #0917 2016/05/16



CMDI #0917 2016/05/17



CMDI #0917 2016/05/17




```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OG%1j¼¥Éj¼¥Á%0¥x
0100 C. *****
0101 C.
0102 C. jãOP/OG%1j¼¥Éjã
0103 S. OP op-850:OP
0104 ( )
0105 S. OG og-850:OG
0106 ( )
0107 C.
0108 C. jãNMOG&OPîî°è¥Á%0¥xjã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0120 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. ¥Á%0¥x½ªî»ð³îÇ§
0125 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG¤î%É¹Ç•è²îOK¤ð³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0139 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. ¥Á%0¥x½ªî»ð³îÇ§
0144 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG¤î%É¹Ç•è²îOK¤ð³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0158 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. ¥Á%0¥x½ªî»ð³îÇ§
0163 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG,RAM ID=OP¤î%É¹Ç•è²îOK¤ð³îÇ§
0165 C.
0166 C. ***** °É²¼¤î¼Ã·¶Á°¤ÉÉ-¤°Á÷¿® (%ãµ-¥Á%0¥x½ªè½Ç¤ðÁÖÃ¤Ç½¤°·¤è¼î¹Ç¤Ç¤â) *****
0167 C. DHU¥ã¼¥É;É½¥½, ¥½¼¥É;É¤ðîã¤¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. ¢¢[HK1_PKT_FORM_NO] EQ 2
0171 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE j§ OPOG UPLOAD¤-Á÷¿®NG¤î¼¹Ç;¢°É²¼¤îîTI-CMDÁ÷¿®¤î¼¹Ô¤°¤É¤¤¤³¤Éj£
0180 C. ¢¤¤¿;¢SET¤ÉDUMP¤î¤±°î¥½¥½¤¹Ô¤°¤¹¤Éj£
0181 C.
0182 C. TI¥³¥¤¥ó¥É¤ðÁÖî¿(UT)
0183 +. TI 2016-05-12 10:36:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2016-05-12 10:36:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2016-05-12 10:36:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP

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```
0194 C.  
0195 +. TI 2016-05-12 10:40:59.5  
0196 DC 01-B2 DHU_OP_START EQ 1COUNTUP  
0197 C. ¢¢[HK1_TI_CMD_NUM]  
0198 C.  
0199 C. °Ê²¼¤İÄê%ıİŇ¤İ¥Á¥S¥Ã¥¹âİÜ  
0200 C. ¢¢[HK1_TI_CMD_ENA/DIS] EQ ENA  
0201 C. ¢¢[HK1_TI_CMD_NUM] EQ 4  
0202 C. ¢¢[HK1_NEXT_EXEC_PIM] EQ DHU  
0203 C. ¢¢[HK1_NEXT_EXEC_DC] EQ 0xB3  
0204 C.  
0205 C. *****  
0206 C. TIIÎ°è¥Ä¥Ó¥×  
0207 C. *****  
0208 C.  
0209 C. TI_TBL(0x03AB00-0x03AEFF;S 1024byte)  
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET  
0211 BC (03 ab 03 01 02)  
0212 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 07  
0213 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 2B  
0214 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 3  
0215 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0  
0216 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU  
0217 +. DC 01-22 DHU_MODE_CHNG  
0218 BC (07 0b f8)  
0219 C. ¢¢[HK1_PKT_FORM_NO] EQ 7  
0220 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s  
0221 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k  
0222 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M  
0223 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC  
0224 C.  
0225 C. ¥À¥Ó¥×½ªİ»¤ò³İÇŞ  
0226 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON  
0227 C.  
0228 C. RAM ID=TI_TBL¤İ¥Ê¹Ç•ë²İOK¤ò³İÇŞ  
0229 C.  
0230 C. DHU¥âı¼¥ÉıÊ¼Ý½, ¥İı¼¥ÈıË¤òİá¤¹  
0231 +. DC 01-22 DHU_MODE_CHNG  
0232 BC (02 0a f8)  
0233 C. ¢¢[HK1_PKT_FORM_NO] EQ 2  
0234 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.5S  
0235 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32K  
0236 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M  
0237 C.  
0238 C. Stop EIS observation and temporarily disable EIS mode changes  
0239 C.  
0240 C.  
0241 C. ***** Start EIS operation (TI set) *****  
0242 C. Execute, after the success of OP upload.  
0243 C. Set EIS TI-commands  
0244 +. TI 2016-05-12 10:40:30.0  
0245 DC 07-FC EIS_MODE_MANU  
0246 BC (21 02)  
0247 +. TI 2016-05-12 10:40:40.0  
0248 DC 07-FC EIS_MODE_CHG_DIS  
0249 BC (22)  
0250 C. [ ] [HK1_TI_CMD_NUM] EQ 2 COUNTUP  
0251 C. ***** End EIS operation (TI set) *****  
0252 C.  
0253 C.  
0254 C. *****  
0255 C. SOT TI command set  
0256 C. *****  
0257 C. Execute, after the success of OP upload.  
0258 +. TI 2016-05-12 10:40:16.0  
0259 DC 07-F0 MDP_SOT_MODE_STBY  
0260 BC (41)  
0261 C. -----  
0262 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]  
0263 C. -----  
0264 C. ***** SOT END *****  
0265 C.  
0266 C. ***** XRT START *****  
0267 C. Execute, after the success of OP upload.  
0268 +. TI 2016-05-12 10:40:00.0  
0269 DC 07-F0 MDP_XRT_MODE_STBY  
0270 BC (c3)  
0271 C. [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP  
0272 C.  
0273 C. ***** XRT END *****  
0274 C.  
0275 C. ***** MDP ´ÜÃİ¤İ»Ö¼Ý¤ĖÄ¤¤¹¤ēDCBC•×²è *****  
0276 C. (%ã°İ¥Ö¥Ä¥È¥¤¥È¥ä¥Ç¥ē¤Ė¼¤¤¼¥Ä»¤¤¹¤ē)  
0277 S. DC-BC dcbc-402:DCBC  
0278 (MDP_known_event)  
0279 C.  
0280 C.  
0281 C. ***** ¥Ð¥¹•İ Daily±çİŇ¤Ė´Ø¹¤ēDCBC•×²è *****  
0282 S. DC-BC dcbc-153:DCBC  
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)  
0284 C.  
0285 C.  
0286 C. ¡āLOS¥Ä¥S¥Ã¥¼Å»Ü¡ä  
0287 C.  
0288 C. ***** LOS *****  
0289 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-851 2016-05-12 13:06:35 263 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥S¥Ã¥¼Á»Üjã
0005 C.
0006 C. ¥À¥Bj¼¥³¥D¥Ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Āi;Ē¿¿A¤•µ°Æ»İ×AÇ¿İ¥Ç¥Á¥×¥İj¼¥ÉjĒĒ½µ•İĒĒjĒĒ½°Ç¿¤•¤¿¼¹Ç¿İjÇÀ®, Ü¤¹¤Ē¤¤¤ÇÁ÷¿®¤•¤Ē¤¤¤¤ĒjĒ
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷¿®µjON
0016 C. *****
0017 C. ¨ °ĒĒ, İ×ĒY¤¤ŁOS¤¤¤Ç¿İ»p´Ŏ¤Ŏ¹İİ, ¤•jÇĒŎİ×¤ĒXĒŎON¤İ¹Ŏ¤Ēİ¤Ē¤¤¤¤ĒjĒ
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 +. DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 +. DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ[HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ[HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. X¥Đ¥Ó¥É¥İ¥Ã¥¼ŎĀŎ¤¬°ĒĒĒ¤•¤¿¤ĒjÇ°Ē²¼¤İ°ĒĒ, ¼Ē½Ç¤Ŏ¼Ē¹Ŏ¤¹¤ĒjĒ
0030 C.
0031 . C. *****
0032 C. DR PT1 Āİ¼İ°ĒĒ
0033 C. *****
0034 C. ¨ ¨ RESTARTjĒPT1jĒ¤•¤¿¤¤¼¹Ç¿İjÇ°Ē²¼¤İ¼Ē¹Ŏ¤»¤°jÇDCBC-150¤Ŏ¿ĒĒ¤¤jĒ
0035 C.
0036 . C. jãPT1°ĒĒ, ³«»İjã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 +. DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 +. DC 06-B3 DR_REP_START
0041 +. DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ē¹Ŏ, j¼Ŏ)
0043 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ē¹Ŏ, j¼Ŏ)
0044 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ē¹Ŏ, j¼Ŏ)
0045 C.
0046 . C. jã¥Ç¥Ó¥Æ¥ĒĒŎĒjĒĒ•Ē°²ŎĒŎjĒ, Ē¤İ°ĒĒ, °Ē³«jã
0047 +. DC 06-B3 DR_REP_START
0048 +. DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ē¹Ŏ, j¼Ŏ)
0050 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ē¹Ŏ, j¼Ŏ)
0051 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ē¹Ŏ, j¼Ŏ)
0052 C.
0053 C.
0054 . C. PT1°ĒĒ, ¤¬¼«Æ°ĒĒ»İ¤•¤¿, ĒjÇ°Ē²¼¤Ŏ¼Ē¹Ŏ¤¹¤ĒjĒ
0055 C. ¥Ç¥Ó¥Æ¥ĒĒŎĒĒ•Ē°²ŎĒŎ¤¬ĒĒ¤¼¹Ç¿İ °İ»¤¹¤Ē¤¤ÇĒŎĒjĒ
0056 C.
0057 . C. *****
0058 C. DR PT2 Āİ¼İ°ĒĒ
0059 C. *****
0060 C. ¨ ¨ RESTARTjĒPT2jĒ¤•¤¿¤¤¼¹Ç¿İjÇ°Ē²¼¤İ¼Ē¹Ŏ¤»¤°jÇDCBC-151¤Ŏ¿ĒĒ¤¤jĒ
0061 C.
0062 . C. jãPT2°ĒĒ, ³«»İjã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 +. DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 +. DC 06-B3 DR_REP_START
0067 +. DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ē¹Ŏ, j¼Ŏ)
0069 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ē¹Ŏ, j¼Ŏ)
0070 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ē¹Ŏ, j¼Ŏ)
0071 C.
0072 . C. jã¥Ç¥Ó¥Æ¥ĒĒŎĒjĒĒ•Ē°²ŎĒŎjĒ, Ē¤İ°ĒĒ, °Ē³«jã
0073 +. DC 06-B3 DR_REP_START
0074 +. DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ē¹Ŏ, j¼Ŏ)
0076 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ē¹Ŏ, j¼Ŏ)
0077 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ē¹Ŏ, j¼Ŏ)
0078 C.
0079 . C. *****
0080 C. DR°ĒĒ, Ē¤»İjÇXÁ÷¿®µjOFF
0081 C. *****
0082 C.
0083 . C. jãDR°ĒĒ, Ē¤»İjã
0084 +. DC 06-B4 DR_REP_STOP
0085 +. DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ[HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. jãXÁ÷¿®µjOFFjã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 +. DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ[HK1_XPA_ON/OFF] EQ OFF
```

```

0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCS DUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 C.
0129 C.
0130 C. ***** XRT START *****
0131 C.
0132 +. DC 07-F0 MDP_XRT_CTRL_MANU
0133 BC (c1)
0134 +. DC 07-F0 MDP_XRT_CTRL_MANU
0135 BC (c1)
0136 +. DC 07-F0 MDP_XRT_MODE_STBY
0137 BC (c3)
0138 . C. ----- Success Verify ? OK / NG____
0139 C.
0140 C. XRT Obs. Table Upload
0141 . S. RAM ram-291:MDP_OBS_X
0142 ( )
0143 C.
0144 +. DC 07-F0 MDP_DUMP_XRTTBL
0145 BC (84 07 00 00 00 3a d4)
0146 . C. ----- Comparison Check ? OK / ERR ____
0147 C.
0148 C.
0149 +. DC 07-F0 MDP_XRT_ROI_SET
0150 BC (cd 01 b1 b1 04 04)
0151 +. DC 07-F0 MDP_XRT_ROI_SET
0152 BC (cd 02 b1 b1 08 08)
0153 +. DC 07-F0 MDP_XRT_ROI_SET
0154 BC (cd 03 b1 b1 08 08)
0155 +. DC 07-F0 MDP_XRT_ROI_SET
0156 BC (cd 04 b1 b1 06 06)
0157 +. DC 07-F0 MDP_XRT_ROI_SET
0158 BC (cd 06 85 83 06 06)
0159 +. DC 07-F0 MDP_XRT_ROI_SET
0160 BC (cd 07 80 80 20 20)
0161 +. DC 07-F0 MDP_XRT_ROI_SET
0162 BC (cd 08 80 96 08 08)
0163 +. DC 07-F0 MDP_XRT_ROI_SET
0164 BC (cd 09 80 80 20 08)
0165 +. DC 07-F0 MDP_XRT_ROI_SET
0166 BC (cd 0a 80 80 08 20)
0167 +. DC 07-F0 MDP_XRT_ROI_SET
0168 BC (cd 0f 80 80 06 06)
0169 +. DC 07-F0 MDP_XRT_ROI_SET
0170 BC (cd 10 80 80 08 08)
0171 +. DC 07-F0 MDP_XRT_FLD_ENA
0172 BC (d8)
0173 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0174 BC (c8)
0175 +. DC 07-F0 MDP_XRT_ARS_DIS
0176 BC (d5)
0177 +. DC 07-F0 MDP_XRT_AEC_RESET
0178 BC (d0)
0179 +. DC 07-F0 MDP_XRT_FLD_RESET
0180 BC (da)
0181 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0182 BC (c4 0b)
0183 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0184 BC (c5 07)
0185 . C. ----- Success Verify ? OK / NG ____
0186 C.
0187 C.
0188 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0189 C.
0190 +. DC 07-F0 MDP_XRT_MODE_OBSV
0191 BC (c2)
0192 +. TI 2016-05-12 10:40:02.0
0193 DC 07-F0 MDP_XRT_MODE_OBSV

```

```

0194 BC (c2)
0195 . C. ----- Success Verify ? OK / NG ____
0196 C.
0197 C. ***** XRT END *****
0198 . C. *****
0199 C. SOT table upload
0200 C. *****
0201 . C. < Stop FG table >
0202 +. DC 07-F0 MDP_FG_CTRL_MANU
0203 BC (51)
0204 . C. -----
0205 C. MDP_FG_CTRL_MODE = MANU [ ]
0206 C. -----
0207 C.
0208 . C. <Upload FG Observation Table>
0209 . S. RAM ram-265:MDP_OBS_F
0210 ( )
0211 C.
0212 . C. < Dump RAMID=MDP_OBS_F >
0213 +. DC 07-F0 MDP_DUMP_FGTBL
0214 BC (82 07 00 00 00 38 b8)
0215 C. -----
0216 C. MDP_OBS_F verify = OK/NG [ ]
0217 C. -----
0218 C.
0219 . C. < Stop SP table >
0220 +. DC 07-F0 MDP_SP_CTRL_MANU
0221 BC (61)
0222 C. -----
0223 C. MDP_SP_CTRL_MODE = MANU [ ]
0224 C. -----
0225 C.
0226 . C. <Upload SP Observation Table>
0227 . S. RAM ram-285:MDP_OBS_S
0228 ( )
0229 C.
0230 . C. < Dump RAMID=MDP_OBS_S >
0231 +. DC 07-F0 MDP_DUMP_SPTBL
0232 BC (83 07 00 00 00 38 b8)
0233 C. -----
0234 C. MDP_OBS_S verify = OK/NG [ ]
0235 C. -----
0236 C.
0237 C. *****
0238 C. SOT TI command set
0239 C. *****
0240 C. Execute, after the success of TBL upload.
0241 +. TI 2016-05-12 10:40:18.0
0242 DC 07-F0 MDP_SOT_MODE_OBSV
0243 BC (40)
0244 . C. -----
0245 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0246 C. -----
0247 C.
0248 C.
0249 . C. ***** MDP '0Ãi0i»0%Y0EÃ0010èDCBC•x2è *****
0250 C. (%ã°i¥0¥Ã¥E¥P¥E¥ã¥C¥è0E½¼00¥Ã»Ü010è)
0251 . S. DC-BC dcbc-402:DCBC
0252 (MDP_known_event)
0253 C.
0254 C.
0255 . C. ***** ¥0¥1•I Daily±¿IÑ0E'0010èDCBC•x2è *****
0256 . S. DC-BC dcbc-153:DCBC
0257 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0258 C.
0259 C.
0260 . C. jãLOS¥Ã¥S¥Ã¥¼Ã»Üjã
0261 C.
0262 . C. ***** LOS *****
0263 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-853 2016-05-12 13:06:35 54 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥S¥Ã¥¼Å»Üjã
0005 C.
0006 C. ¥À¥Bj¼¥³¥P¥Ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. ĀiĲ¿¿A¤•µ°Æ»İ×AÇ¤İ¥C¥A¥×¥i¼¥ÉiĒĒ¿µ•īĒiĒ¤Ē°ÇÖ¤•¤¿%i¹Ç¤İjÇÀ®, ũ¤¹¤Ē¤P¤ÇÁ÷¿®¤•¤Ē¤¤¤³¤Ēj£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0015 +. DC 07-FC EIS_MODE_CHG_ENA
0016 BC (20)
0017 . C. Verify EIS_MODE_CHG_FLG is ENA
0018 +. DC 07-FC EIS_MODE_MANU
0019 BC (21 02)
0020 . C. Verify EIS in MANUAL mode
0021 . C. Estimated OBSTBL upload time is 1m25s
0022 C. *****
0023 C. EIS START OBSTBL LOAD
0024 C. *****
0025 . S. RAM ram-820:EIS_OBSTBL
0026 ( )
0027 +. DC 07-FC EIS_DUMP_OBSTBL
0028 BC (07 07 07 00 00 70 00)
0029 C.
0030 C. Execute, after the success of OBSTBL upload.
0031 C. Set EIS TI-commands
0032 +. TI 2016-05-12 10:40:50.0
0033 DC 07-FC EIS_MODE_CHG_ENA
0034 BC (20)
0035 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0036 C. *****
0037 C. EIS END OBSTBL LOAD
0038 C. *****
0039 C.
0040 . C. ***** MDP ´ŮĀİ¤İ»Ö%Ÿ¤ĒĀ¤¤¹¤ĒDCBC•×²è *****
0041 C. (%Ā°İ¥Ů¥Ā¥Ē¥P¥Ē¥Ā¥C¥Ē¤Ē½%¤¤¼Ā»Ü¤¹¤Ē)
0042 . S. DC-BC dcbc-402:DCBC
0043 (MDP_known_event)
0044 C.
0045 C.
0046 . C. ***** ¥Đ¥¹•İ Daily±¿İÑ¤Ē´Ů¤¹¤ĒDCBC•×²è *****
0047 . S. DC-BC dcbc-153:DCBC
0048 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0049 C.
0050 C.
0051 . C. iãLOS¥Á¥S¥Ã¥¼Å»Üjã
0052 C.
0053 . C. ***** LOS *****
0054 C.
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*** OP Sequence for XRT ***

2016/05/12	10:50:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/12	10:50:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/12	10:50:58.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/05/12	10:51:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	00 58 72 01 58
2016/05/12	10:51:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2016/05/12	10:51:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/05/12	10:51:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2016/05/12	10:51:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/12	10:51:26.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/12	10:53:56.0	XRT_QT_PROG_SET_413_OG [0x19d]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2016/05/12	10:53:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/05/12	10:54:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/12	11:12:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/12	11:12:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/12	11:12:58.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/05/12	11:13:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 4f 8d 01 58
2016/05/12	11:13:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2016/05/12	11:13:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/05/12	11:13:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2016/05/12	11:13:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/12	11:13:26.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/12	11:15:56.0	XRT_QT_PROG_SET_413_OG [0x19d]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2016/05/12	11:15:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/05/12	11:16:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/12	11:34:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/12	11:34:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/12	11:34:58.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/05/12	11:35:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 46 a7 01 58
2016/05/12	11:35:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2016/05/12	11:35:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/05/12	11:35:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2016/05/12	11:35:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/12	11:35:26.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/12	11:37:56.0	XRT_QT_PROG_SET_413_OG [0x19d]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2016/05/12	11:37:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/05/12	11:38:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/12	12:02:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/12	12:02:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/12	12:02:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/12	12:02:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/12	12:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/12	12:29:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/12	12:29:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/12	12:29:28.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/05/12	12:29:30.0	AOCS_ORe-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	00 3d ca 01 58
2016/05/12	12:29:48.0	XRT_FLD_ENA_411_OG [0x19b]			

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2016/05/12	12:29:50.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/05/12	12:29:52.0	XRT_AEC_RESET_448_0G [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/05/12	12:29:54.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/05/12	12:29:56.0	XRT_FLD_RESET_433_0G [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/12	12:32:26.0	XRT_QT_PROG_SET_413_0G [0x19d]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/12	12:32:28.0	XRT_FL_PROG_SET_436_0G [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2016/05/12	12:32:30.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/05/12	12:51:24.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/12	12:51:26.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/12	12:51:28.0	XRT_FOCUS_POSITION_403_0G [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/12	12:51:30.0	AOCS_0Re-point_Start_5_0G [0x09b]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/05/12	12:51:48.0	XRT_FLD_ENA_411_0G [0x19b]	AOCU_NM	5	02-76	00 34 e5 01 58
2016/05/12	12:51:50.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/05/12	12:51:52.0	XRT_AEC_RESET_448_0G [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/05/12	12:51:54.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/05/12	12:51:56.0	XRT_FLD_RESET_433_0G [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/12	12:54:26.0	XRT_QT_PROG_SET_413_0G [0x19d]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/12	12:54:28.0	XRT_FL_PROG_SET_436_0G [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2016/05/12	12:54:30.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/05/12	13:13:24.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/12	13:13:26.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/12	13:13:28.0	XRT_FOCUS_POSITION_403_0G [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/12	13:13:30.0	AOCS_0Re-point_Start_6_0G [0x09c]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/05/12	13:13:48.0	XRT_FLD_ENA_411_0G [0x19b]	AOCU_NM	5	02-76	00 2c 00 01 58
2016/05/12	13:13:50.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/05/12	13:13:52.0	XRT_AEC_RESET_448_0G [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/05/12	13:13:54.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/05/12	13:13:56.0	XRT_FLD_RESET_433_0G [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/12	13:16:26.0	XRT_QT_PROG_SET_413_0G [0x19d]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/12	13:16:28.0	XRT_FL_PROG_SET_436_0G [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2016/05/12	13:16:30.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/05/12	13:40:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/12	13:40:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/12	13:40:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/12	13:40:36.0	XRT_PREFLR_STRT_432_0G [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/12	13:43:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/12	14:06:30.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/12	14:07:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CUSTOM_430_0G [0x1ae]	1	07-F0	c0
2016/05/12	14:09:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/12	14:09:02.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/12	14:09:04.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/12	14:09:06.0	XRT_PREFLR_STRT_432_0G [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/12	14:11:54.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/12	14:11:56.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/12	14:11:58.0	XRT_FOCUS_POSITION_403_0G [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/12	14:12:00.0	AOCS_0Re-point_Start_7_0G [0x09d]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/05/12	14:12:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	AOCU_NM	5	02-76	00 23 1a 01 58

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2016/05/12	14:12:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
			MDP_XRT_FLD_ENA	1	07-F0	d8
2016/05/12	14:12:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/05/12	14:12:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/05/12	14:12:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/12	14:12:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/12	14:14:56.0	XRT_QT_PROG_SET_413_OG [0x19d]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2016/05/12	14:14:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/05/12	14:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/12	14:33:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/12	14:33:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/12	14:33:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/05/12	14:34:00.0	AOCS_ORe-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00 1a 35 01 58
2016/05/12	14:34:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/05/12	14:34:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/05/12	14:34:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/05/12	14:34:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/12	14:34:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/12	14:36:56.0	XRT_QT_PROG_SET_413_OG [0x19d]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2016/05/12	14:36:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/05/12	14:37:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/12	15:19:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/12	15:19:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/12	15:19:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/12	15:19:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/12	15:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/12	16:03:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/12	16:03:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/12	16:03:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/05/12	16:04:00.0	AOCS_ORe-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	00 11 58 01 58
2016/05/12	16:04:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/05/12	16:04:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/05/12	16:04:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/05/12	16:04:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/12	16:04:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/12	16:06:56.0	XRT_QT_PROG_SET_413_OG [0x19d]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2016/05/12	16:06:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/05/12	16:07:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/12	16:25:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/12	16:25:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/12	16:25:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/05/12	16:26:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]	AOCU_NM	5	02-76	00 08 72 01 58
2016/05/12	16:26:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/05/12	16:26:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/05/12	16:26:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/05/12	16:26:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/12	16:26:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da

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2016/05/12	16:28:56.0	XRT_QT_PROG_SET_413_0G [0x19d]			
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 05	
2016/05/12	16:28:58.0	XRT_FL_PROG_SET_436_0G [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 07	
2016/05/12	16:29:00.0	XRT_CTRL_AUTO_408_0G [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2016/05/12	16:45:51.0	XRT_CTRL_MANU_402_0G [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2016/05/12	16:46:00.0	AOCS_ORe-point_Start_11_0G [0x0a1]			
		AOCU_NM	5	02-76 00 00 72 01 58	
2016/05/12	16:52:00.0	XRT_CTRL_MANU_402_0G [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2016/05/12	16:52:02.0	XRT_FOCUS_POSITION_403_0G [0x193]			
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00	
2016/05/12	16:52:22.0	XRT_FLD_DIS_407_0G [0x197]			
		MDP_XRT_FLD_DIS	1	07-F0 d9	
2016/05/12	16:54:24.0	XRT_FLRCTRL_DIS_405_0G [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9	
2016/05/12	16:54:26.0	XRT_ARS_DIS_423_0G [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0 d5	
2016/05/12	16:54:28.0	XRT_QT_PROG_SET_446_0G [0x1be]			
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 14	
2016/05/12	16:54:30.0	XRT_CTRL_AUTO_408_0G [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2016/05/12	17:46:54.0	XRT_CTRL_MANU_402_0G [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2016/05/12	17:46:56.0	XRT_CTRL_MANU_402_0G [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2016/05/12	17:46:58.0	XRT_FOCUS_POSITION_403_0G [0x193]			
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00	
2016/05/12	17:47:18.0	XRT_FLD_DIS_406_0G [0x196]			
		MDP_XRT_FLD_DIS	1	07-F0 d9	
2016/05/12	17:49:54.0	XRT_FLRCTRL_DIS_405_0G [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9	
2016/05/12	17:49:56.0	XRT_ARS_DIS_423_0G [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0 d5	
2016/05/12	17:49:58.0	XRT_QT_PROG_SET_434_0G [0x1b2]			
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 0f	
2016/05/12	17:50:00.0	XRT_CTRL_AUTO_408_0G [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2016/05/12	18:02:54.0	XRT_CTRL_MANU_402_0G [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2016/05/12	18:02:56.0	XRT_CTRL_MANU_402_0G [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2016/05/12	18:02:58.0	XRT_FOCUS_POSITION_403_0G [0x193]			
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00	
2016/05/12	18:03:00.0	AOCS_ORe-point_Start_12_0G [0x0a2]			
		AOCU_NM	5	02-76 00 f7 8e 01 58	
2016/05/12	18:03:18.0	XRT_FLD_ENA_411_0G [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0 d8	
2016/05/12	18:03:20.0	XRT_FLRCTRL_ENA_412_0G [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8	
2016/05/12	18:03:22.0	XRT_AEC_RESET_448_0G [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0 d0	
2016/05/12	18:03:24.0	XRT_ARS_DIS_423_0G [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0 d5	
2016/05/12	18:03:26.0	XRT_FLD_RESET_433_0G [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2016/05/12	18:05:56.0	XRT_QT_PROG_SET_413_0G [0x19d]			
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 05	
2016/05/12	18:05:58.0	XRT_FL_PROG_SET_436_0G [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 07	
2016/05/12	18:06:00.0	XRT_CTRL_AUTO_408_0G [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2016/05/12	18:36:00.0	XRT_CTRL_MANU_400_0G [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2016/05/12	18:36:02.0	XRT_CTRL_MANU_402_0G [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2016/05/12	18:36:04.0	XRT_FLD_RESET_415_0G [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2016/05/12	18:36:06.0	XRT_PREFLR_STRT_432_0G [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0 e8	
2016/05/12	18:39:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0 e9	
2016/05/12	19:17:24.0	XRT_CTRL_MANU_402_0G [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2016/05/12	19:17:26.0	XRT_CTRL_MANU_402_0G [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2016/05/12	19:17:28.0	XRT_FOCUS_POSITION_403_0G [0x193]			
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00	
2016/05/12	19:17:30.0	AOCS_ORe-point_Start_13_0G [0x0a3]			
		AOCU_NM	5	02-76 00 ee a8 01 58	
2016/05/12	19:17:48.0	XRT_FLD_ENA_411_0G [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0 d8	
2016/05/12	19:17:50.0	XRT_FLRCTRL_ENA_412_0G [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8	
2016/05/12	19:17:52.0	XRT_AEC_RESET_448_0G [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0 d0	
2016/05/12	19:17:54.0	XRT_ARS_DIS_423_0G [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0 d5	
2016/05/12	19:17:56.0	XRT_FLD_RESET_433_0G [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2016/05/12	19:20:26.0	XRT_QT_PROG_SET_413_0G [0x19d]			

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2016/05/12	19:20:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05
2016/05/12	19:20:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2016/05/12	19:39:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/12	19:39:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/12	19:39:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/12	19:39:30.0	AOCs_Or-point_Start_14_OG [0x0a4]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2016/05/12	19:39:48.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00	e5 cb 01 58
2016/05/12	19:39:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/05/12	19:39:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/05/12	19:39:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/05/12	19:39:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/05/12	19:42:26.0	XRT_QT_PROG_SET_413_OG [0x19d]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/12	19:42:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05
2016/05/12	19:42:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2016/05/12	20:14:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/12	20:14:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/12	20:14:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/12	20:14:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/12	20:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/05/12	20:54:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/12	20:54:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/12	20:54:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/12	20:55:00.0	AOCs_Or-point_Start_15_OG [0x0a5]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2016/05/12	20:55:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00	dc e6 01 58
2016/05/12	20:55:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/05/12	20:55:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/05/12	20:55:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/05/12	20:55:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/05/12	20:57:56.0	XRT_QT_PROG_SET_413_OG [0x19d]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/12	20:57:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05
2016/05/12	20:58:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2016/05/12	21:16:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/12	21:16:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/12	21:16:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/12	21:17:00.0	AOCs_Or-point_Start_16_OG [0x0a6]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2016/05/12	21:17:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00	d4 00 01 58
2016/05/12	21:17:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/05/12	21:17:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/05/12	21:17:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/05/12	21:17:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/05/12	21:19:56.0	XRT_QT_PROG_SET_413_OG [0x19d]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/12	21:19:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05
2016/05/12	21:20:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2016/05/12	21:52:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/12	21:52:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/12	21:52:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
			MDP_XRT_FLD_RESET	1	07-F0	da	

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2016/05/12	21:52:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2016/05/12	21:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2016/05/12	22:30:24.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/05/12	22:30:26.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/05/12	22:30:28.0	XRT_FOCUS_POSITION_403_OG [0x193]		
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2016/05/12	22:30:30.0	AOCS_ORe-point_Start_17_OG [0x0a7]		
		AOCU_NM	5	02-76 00 cb 1b 01 58
2016/05/12	22:30:48.0	XRT_FLD_ENA_411_OG [0x19b]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2016/05/12	22:30:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2016/05/12	22:30:52.0	XRT_AEC_RESET_448_OG [0x1c0]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2016/05/12	22:30:54.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2016/05/12	22:30:56.0	XRT_FLD_RESET_433_OG [0x1b1]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2016/05/12	22:33:26.0	XRT_QT_PROG_SET_413_OG [0x19d]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 05
2016/05/12	22:33:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 07
2016/05/12	22:33:30.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2016/05/12	22:52:24.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/05/12	22:52:26.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/05/12	22:52:28.0	XRT_FOCUS_POSITION_403_OG [0x193]		
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2016/05/12	22:52:30.0	AOCS_ORe-point_Start_18_OG [0x0a8]		
		AOCU_NM	5	02-76 00 c2 36 01 58
2016/05/12	22:52:48.0	XRT_FLD_ENA_411_OG [0x19b]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2016/05/12	22:52:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2016/05/12	22:52:52.0	XRT_AEC_RESET_448_OG [0x1c0]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2016/05/12	22:52:54.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2016/05/12	22:52:56.0	XRT_FLD_RESET_433_OG [0x1b1]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2016/05/12	22:55:26.0	XRT_QT_PROG_SET_413_OG [0x19d]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 05
2016/05/12	22:55:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 07
2016/05/12	22:55:30.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2016/05/12	23:31:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/05/12	23:31:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/05/12	23:31:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2016/05/12	23:31:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2016/05/12	23:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2016/05/12	23:57:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/05/12	23:57:56.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/05/12	23:57:58.0	XRT_FOCUS_POSITION_403_OG [0x193]		
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2016/05/12	23:58:00.0	AOCS_ORe-point_Start_19_OG [0x0a9]		
		AOCU_NM	5	02-76 00 b9 59 01 58
2016/05/12	23:58:18.0	XRT_FLD_ENA_411_OG [0x19b]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2016/05/12	23:58:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2016/05/12	23:58:22.0	XRT_AEC_RESET_448_OG [0x1c0]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2016/05/12	23:58:24.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2016/05/12	23:58:26.0	XRT_FLD_RESET_433_OG [0x1b1]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2016/05/13	00:00:56.0	XRT_QT_PROG_SET_413_OG [0x19d]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 05
2016/05/13	00:00:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 07
2016/05/13	00:01:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2016/05/13	00:19:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/05/13	00:19:56.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/05/13	00:19:58.0	XRT_FOCUS_POSITION_403_OG [0x193]		
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2016/05/13	00:20:00.0	AOCS_ORe-point_Start_20_OG [0x0aa]		

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2016/05/13	00:20:18.0	XRT_FLD_ENA_411_0G [0x19b]	5	02-76 00 b0 73 01 58
		MDP_XRT_FLD_ENA	1	07-F0 d8
2016/05/13	00:20:20.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	1	07-F0 c8
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2016/05/13	00:20:22.0	XRT_AEC_RESET_448_0G [0x1c0]	1	07-F0 d0
		MDP_XRT_AEC_RESET	1	07-F0 d0
2016/05/13	00:20:24.0	XRT_ARS_DIS_423_0G [0x1a7]	1	07-F0 d5
		MDP_XRT_ARS_DIS	1	07-F0 d5
2016/05/13	00:20:26.0	XRT_FLD_RESET_433_0G [0x1b1]	1	07-F0 da
		MDP_XRT_FLD_RESET	1	07-F0 da
2016/05/13	00:22:56.0	XRT_QT_PROG_SET_413_0G [0x19d]	2	07-F0 c4 05
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 05
2016/05/13	00:22:58.0	XRT_FL_PROG_SET_436_0G [0x1b4]	2	07-F0 c5 07
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 07
2016/05/13	00:23:00.0	XRT_CTRL_AUTO_408_0G [0x198]	1	07-F0 c0
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2016/05/13	00:39:54.0	XRT_CTRL_MANU_402_0G [0x192]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/05/13	00:39:56.0	XRT_CTRL_MANU_402_0G [0x192]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/05/13	00:39:58.0	XRT_FOCUS_POSITION_410_0G [0x19a]	4	07-F8 22 fe 97 00
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2016/05/13	00:40:00.0	AOCS_ORe-point_Start_21_0G [0x0ab]	5	02-76 03 00 00 00 00
		AOCU_NM	5	02-76 03 00 00 00 00
2016/05/13	00:40:18.0	XRT_FLD_ENA_411_0G [0x19b]	1	07-F0 d8
		MDP_XRT_FLD_ENA	1	07-F0 d8
2016/05/13	00:40:20.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	1	07-F0 c8
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2016/05/13	00:40:22.0	XRT_AEC_RESET_448_0G [0x1c0]	1	07-F0 d0
		MDP_XRT_AEC_RESET	1	07-F0 d0
2016/05/13	00:40:24.0	XRT_ARS_DIS_423_0G [0x1a7]	1	07-F0 d5
		MDP_XRT_ARS_DIS	1	07-F0 d5
2016/05/13	00:40:26.0	XRT_FLD_RESET_433_0G [0x1b1]	1	07-F0 da
		MDP_XRT_FLD_RESET	1	07-F0 da
2016/05/13	00:42:56.0	XRT_QT_PROG_SET_449_0G [0x1c1]	2	07-F0 c4 0b
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 0b
2016/05/13	00:42:58.0	XRT_FL_PROG_SET_436_0G [0x1b4]	2	07-F0 c5 07
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 07
2016/05/13	00:43:00.0	XRT_CTRL_AUTO_408_0G [0x198]	1	07-F0 c0
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2016/05/13	01:09:00.0	XRT_CTRL_MANU_400_0G [0x190]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/05/13	01:09:02.0	XRT_CTRL_MANU_402_0G [0x192]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/05/13	01:09:04.0	XRT_FLD_RESET_415_0G [0x19f]	1	07-F0 da
		MDP_XRT_FLD_RESET	1	07-F0 da
2016/05/13	01:09:06.0	XRT_PREFLR_STRT_432_0G [0x1b0]	1	07-F0 e8
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2016/05/13	01:12:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	1	07-F0 e9
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2016/05/13	01:35:30.0	XRT_Custom_430_0G [0x1ae]	1	07-F0 c0
2016/05/13	01:36:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]	1	07-F0 c0
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2016/05/13	02:43:30.0	XRT_CTRL_MANU_400_0G [0x190]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/05/13	02:43:32.0	XRT_CTRL_MANU_402_0G [0x192]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/05/13	02:43:34.0	XRT_FLD_RESET_415_0G [0x19f]	1	07-F0 da
		MDP_XRT_FLD_RESET	1	07-F0 da
2016/05/13	02:43:36.0	XRT_PREFLR_STRT_432_0G [0x1b0]	1	07-F0 e8
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2016/05/13	02:46:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	1	07-F0 e9
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2016/05/13	03:13:30.0	XRT_Custom_430_0G [0x1ae]	1	07-F0 c0
2016/05/13	03:14:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]	1	07-F0 c0
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2016/05/13	04:12:30.0	XRT_CTRL_MANU_400_0G [0x190]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/05/13	04:12:32.0	XRT_CTRL_MANU_402_0G [0x192]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/05/13	04:12:34.0	XRT_FLD_RESET_415_0G [0x19f]	1	07-F0 da
		MDP_XRT_FLD_RESET	1	07-F0 da
2016/05/13	04:12:36.0	XRT_PREFLR_STRT_432_0G [0x1b0]	1	07-F0 e8
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2016/05/13	04:15:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	1	07-F0 e9
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2016/05/13	04:52:00.0	XRT_Custom_430_0G [0x1ae]	1	07-F0 c0
2016/05/13	04:53:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]	1	07-F0 c0
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2016/05/13	05:53:00.0	XRT_CTRL_MANU_400_0G [0x190]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/05/13	05:53:02.0	XRT_CTRL_MANU_402_0G [0x192]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/05/13	05:53:04.0	XRT_FLD_RESET_415_0G [0x19f]	1	07-F0 da
		MDP_XRT_FLD_RESET	1	07-F0 da
2016/05/13	05:53:06.0	XRT_PREFLR_STRT_432_0G [0x1b0]	1	07-F0 e8
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2016/05/13	05:56:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	1	07-F0 e9
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2016/05/13	06:30:30.0	XRT_Custom_430_0G [0x1ae]	1	07-F0 c0
2016/05/13	06:31:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]	1	07-F0 c0
		MDP_XRT_CTRL_AUTO	1	07-F0 c0

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2016/05/13	06:33:24.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/13	06:33:26.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/13	06:33:28.0	XRT_FOCUS_POSITION_403_0G [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/05/13	06:33:30.0	AOCS_0Re-point_Start_22_0G [0x0ac]	AOCU_NM	5	02-76	00 00 00 00 00
2016/05/13	06:33:48.0	XRT_FLD_DIS_406_0G [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9
2016/05/13	06:36:24.0	XRT_FLRCTRL_DIS_405_0G [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/05/13	06:36:26.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/13	06:36:28.0	XRT_QT_PROG_SET_434_0G [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2016/05/13	06:36:30.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/13	06:43:24.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/13	06:43:26.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/13	06:43:28.0	XRT_FOCUS_POSITION_410_0G [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/05/13	06:43:30.0	AOCS_0Re-point_Start_21_0G [0x0ab]	AOCU_NM	5	02-76	03 00 00 00 00
2016/05/13	06:43:48.0	XRT_FLD_ENA_411_0G [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/05/13	06:43:50.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/05/13	06:43:52.0	XRT_AEC_RESET_448_0G [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/05/13	06:43:54.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/13	06:43:56.0	XRT_FLD_RESET_433_0G [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/13	06:46:26.0	XRT_QT_PROG_SET_449_0G [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2016/05/13	06:46:28.0	XRT_FL_PROG_SET_436_0G [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/05/13	06:46:30.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/13	07:33:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/13	07:33:02.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/13	07:33:04.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/13	07:33:06.0	XRT_PREFLR_STRT_432_0G [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/13	07:36:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/13	08:09:00.0	XRT_Custom_430_0G [0x1ae]				
2016/05/13	08:10:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/13	09:12:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/13	09:12:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/13	09:12:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/13	09:12:36.0	XRT_PREFLR_STRT_432_0G [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/13	09:15:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/13	09:47:30.0	XRT_Custom_430_0G [0x1ae]				
2016/05/13	09:48:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/13	10:55:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/13	10:55:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/13	10:55:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/13	10:55:36.0	XRT_PREFLR_STRT_432_0G [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/13	10:58:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/13	10:59:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/13	10:59:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/13	10:59:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/13	10:59:36.0	XRT_PREFLR_STRT_432_0G [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/13	11:02:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/13	11:25:30.0	XRT_Custom_430_0G [0x1ae]				
2016/05/13	11:26:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/13	12:38:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1

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2016/05/13	12:38:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/13	12:38:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/13	12:38:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/13	12:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/13	13:04:00.0	XRT_Custom_430_OG [0x1ae]				
2016/05/13	13:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/13	14:16:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/13	14:16:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/13	14:16:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/13	14:16:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/13	14:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/13	14:50:30.0	XRT_Custom_430_OG [0x1ae]				
2016/05/13	14:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/13	15:54:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/13	15:54:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/13	15:54:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/13	15:54:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/13	15:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/13	16:38:00.0	XRT_Custom_430_OG [0x1ae]				
2016/05/13	16:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/13	17:14:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/13	17:14:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/13	17:14:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/05/13	17:15:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	00 00 00 00 00
2016/05/13	17:15:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9
2016/05/13	17:17:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/05/13	17:17:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/13	17:17:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2016/05/13	17:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/13	17:27:30.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/13	17:27:32.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/05/13	17:27:52.0	XRT_FLD_DIS_407_OG [0x197]	MDP_XRT_FLD_DIS	1	07-F0	d9
2016/05/13	17:29:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/05/13	17:29:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/13	17:29:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2016/05/13	17:30:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/13	18:14:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/13	18:14:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/13	18:14:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/05/13	18:15:00.0	AOCS_ORe-point_Start_21_OG [0x0ab]	AOCU_NM	5	02-76	03 00 00 00 00
2016/05/13	18:15:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/05/13	18:15:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/05/13	18:15:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/05/13	18:15:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/13	18:15:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/13	18:17:56.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2016/05/13	18:17:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/05/13	18:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0

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2016/05/13	19:11:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/05/13	19:11:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/05/13	19:11:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2016/05/13	19:11:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2016/05/13	19:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2016/05/13	19:51:30.0	XRT_Custom_430_OG [0x1ae]		
2016/05/13	19:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2016/05/13	20:49:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/05/13	20:49:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/05/13	20:49:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2016/05/13	20:49:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2016/05/13	20:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2016/05/13	21:28:30.0	XRT_Custom_430_OG [0x1ae]		
2016/05/13	21:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2016/05/13	22:28:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/05/13	22:28:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/05/13	22:28:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2016/05/13	22:28:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2016/05/13	22:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2016/05/13	23:03:30.0	XRT_Custom_430_OG [0x1ae]		
2016/05/13	23:04:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2016/05/14	00:06:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/05/14	00:06:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/05/14	00:06:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2016/05/14	00:06:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2016/05/14	00:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2016/05/14	00:32:30.0	XRT_Custom_430_OG [0x1ae]		
2016/05/14	00:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2016/05/14	01:43:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/05/14	01:43:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/05/14	01:43:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2016/05/14	01:43:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2016/05/14	01:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2016/05/14	02:11:00.0	XRT_Custom_430_OG [0x1ae]		
2016/05/14	02:12:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2016/05/14	03:18:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/05/14	03:18:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/05/14	03:18:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2016/05/14	03:18:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2016/05/14	03:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2016/05/14	03:49:30.0	XRT_Custom_430_OG [0x1ae]		
2016/05/14	03:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2016/05/14	04:49:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/05/14	04:49:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/05/14	04:49:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2016/05/14	04:49:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2016/05/14	04:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2016/05/14	05:28:00.0	XRT_Custom_430_OG [0x1ae]		
2016/05/14	05:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2016/05/14	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1

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2016/05/14	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/14	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/05/14	06:00:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	00 00 00 00 00
2016/05/14	06:00:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9
2016/05/14	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/05/14	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/14	06:02:58.0	XRT_QT_PROG_SET_418_OG [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2016/05/14	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/14	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/14	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/14	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/05/14	06:10:00.0	AOCS_ORe-point_Start_21_OG [0x0ab]	AOCU_NM	5	02-76	03 00 00 00 00
2016/05/14	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/05/14	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/05/14	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/05/14	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/14	06:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/14	06:12:56.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2016/05/14	06:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/05/14	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/14	06:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/14	06:29:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/14	06:29:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/14	06:29:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/14	06:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/14	07:06:30.0	XRT_Custom_430_OG [0x1ae]				
2016/05/14	07:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/14	08:09:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/14	08:09:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/14	08:09:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/14	08:09:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/14	08:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/14	08:44:30.0	XRT_Custom_430_OG [0x1ae]				
2016/05/14	08:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/14	09:49:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/14	09:49:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/14	09:49:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/14	09:49:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/14	09:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/14	10:23:00.0	XRT_Custom_430_OG [0x1ae]				
2016/05/14	10:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/14	10:40:00.5	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/14	10:56:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	00 00 00 00 00