

XRT Timeline to be uploaded on 2016/05/19

Period: 2016/05/19 09:55:00 - 2016/05/24 09:37:00

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Normal mode

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XOB #1AFF: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
05/19 10:08:00 - 05/19 17:35:54	Track (766.8, 310.0) @ 05/19 10:05:00	# OP start + 10min, AR 12544
05/19 18:32:30 - 05/20 05:59:54	Track (-173.3, -138.1) @ 05/19 17:46:00	AR 12546
05/20 06:13:00 - 05/20 18:00:54	Track (-62.3, -138.6) @ 05/20 06:10:00	AR 12546 IHOP 267

PROG= 11 1-time(s) 2.0sec

Subr= 1 1-time(s) 2.0sec	Seqn= 56 1-time(s) 2.0sec	Seqn= 75 1-time(s) 2.0sec	Seqn= 96 4-time(s) 60.0sec
Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec	Open/G-band Open/G-band close Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec	Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec	Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec	thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec	thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec	Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec
			thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 15.0sec
			Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 2.0sec
			thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 15.0sec
			Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec
			thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #1B0A: Synoptic Q95 2x2 - Al/mesh(12/181/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(24/362/1443) + T

Term	Pointing (x, y)	Comment
05/19 17:39:00 - 05/19 17:45:54	Fixed (0.0, 0.0)	synoptic, shifted -24.0 min
05/20 06:03:00 - 05/20 06:09:54	Fixed (0.0, 0.0)	synoptic
05/21 06:28:30 - 05/21 06:35:24	Fixed (0.0, 0.0)	synoptic, shifted 25.5 min

PROG= 13 1-time(s) 2.0sec

Subr= 1 1-time(s) 2.0sec	Seqn= 5 1-time(s) 2.0sec	Seqn= 91 1-time(s) 2.0sec	Seqn= 93 1-time(s) 2.0sec	Seqn= 77 1-time(s) 2.0sec	Seqn= 54 1-time(s) 2.0sec
Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec	Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec	Open/Al-mesh Open/Al-mesh close Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	Al-poly/Open Al-poly/Open close Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	thin-Be/Open thin-Be/Open close Safe Norm 86ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec	Open/Al-mesh Open/Al-mesh close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	Al-poly/Open Al-poly/Open close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	Al-poly/Open Al-poly/thick-Al close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	thin-Be/Open thin-Be/Open close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	Open/G-band Open/G-band close Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec	Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	Al-poly/Open Al-poly/thick-Al close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec		thin-Be/Open thin-Be/Open close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec					

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #1ADF: Synoptic 7 Filter w/ Al-mesh(8/128/1024), Al-poly(16/362/1443), Thin-Be(88/1024/5795) - Thick-Be(65536), Al-poly+Ti-poly(256/2048), Med-Al(40

Term	Pointing (x, y)	Comment
05/20 18:04:00 - 05/20 18:10:54	Fixed (0.0, 0.0)	synoptic, shifted 1.0 min

PROG= 08 1-time(s) 2.0sec

Subr= 1 1-time(s) 2.0sec	Seqn= 5 1-time(s) 2.0sec	Seqn= 33 1-time(s) 2.0sec	Seqn= 49 1-time(s) 2.0sec
Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec	Open/Al-mesh Open/Al-mesh close Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec	Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec	Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec	
Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec		Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec	

Al-poly/Open	Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 77 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54 1-time(s) 4.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 72 2-time(s) 2.0sec												
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 59 2-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1AD5: CME watch - 4x4 - AEC 2 - Be-thin - G-band (2x2,1ms) - Leak (2x2,1ms) - 60s cad

Term	Pointing (x, y)	Comment
05/20 18:14:00 - 05/21 06:25:24	Fixed (0.0, 870.0)	HOP 279 N-pole
PROG= 05 Inf.-time(s)		
Subr= 1 60-time(s) 60.0sec		
Seqn= 29 1-time(s) 4.0sec		
thin-Be/Open	med-Be/Open	close Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 26 1-time(s) 2.0sec		
Open/G-band	Open/G-band	open Safe Norm 1ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band	close Safe Norm 1ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1B37: FLD test - 8x8 - AEC 0 - Be-thin (Med-Be) 45ms - G-band (2x2,1ms) - Leak (2x2,1ms) - 30s cad (G-band/Leak first)

Term	Pointing (x, y)	Comment
05/21 06:38:30 - 05/21 09:02:00	Fixed (0.0, 0.0)	synoptic, shifted 25.5 min
PROG= 06 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 26 1-time(s) 2.0sec		
Open/G-band	Open/G-band	open Safe Norm 1ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band	close Safe Norm 1ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Subr= 2 120-time(s) 30.0sec		
Seqn= 30 1-time(s) 2.0sec		
thin-Be/Open	med-Be/Open	close Safe Norm 44ms Obs 8x8 2048x2048 (1024, 1024) Q=50 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Flare mode

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XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
05/19 10:08:00 - 05/19 17:35:54	Track (766.8, 310.0) @ 05/19 10:05:00	# OP start + 10min, AR 12544
05/19 18:32:30 - 05/20 05:59:54	Track (-173.3, -138.1) @ 05/19 17:46:00	AR 12546
05/20 06:13:00 - 05/20 18:00:54	Track (-62.3, -138.6) @ 05/20 06:10:00	AR 12546 IHOP 267
05/20 18:14:00 - 05/21 06:25:24	Fixed (0.0, 870.0)	HOP 279 N-pole
05/21 06:38:30 - 05/21 09:02:00	Fixed (0.0, 0.0)	synoptic, shifted 25.5 min
PROG= 07 30-time(s)		
Subr= 1 20-time(s) 2.0sec		
Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al	close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn=100 1-time(s) 10.0sec		
thin-Be/Open	med-Be/Open	close Safe Norm 125ms Obs 1x1 384x384 (1024, 1024) Q=95 2 0 2.0sec
med-Be/Open	Open/thick-Al	close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Be	close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 10 1-time(s) 2.0sec		
med-Al/Open	med-Al/thick-Al	close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be	close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al	close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn= 84 1-time(s) 2.0sec		
Open/G-band	Open/G-band	open Safe Norm 3ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/G-band	Open/G-band	close Safe Norm 3ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al	close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al	close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Active Region Search

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NOT USED

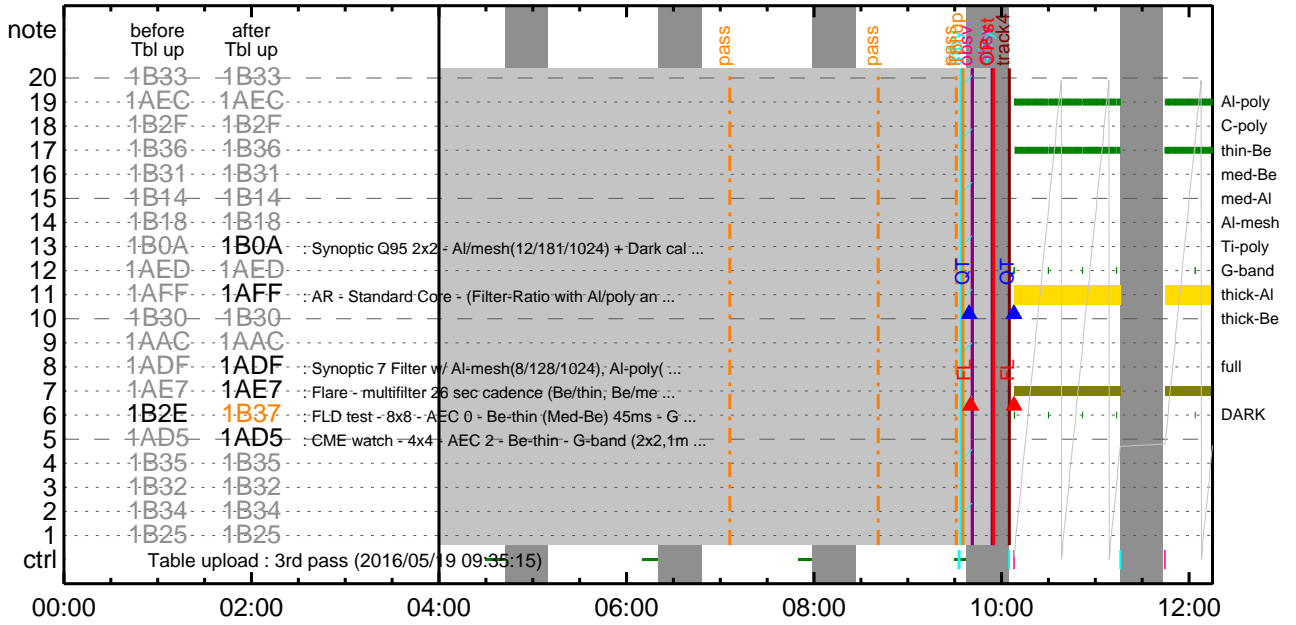
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Flare Detection

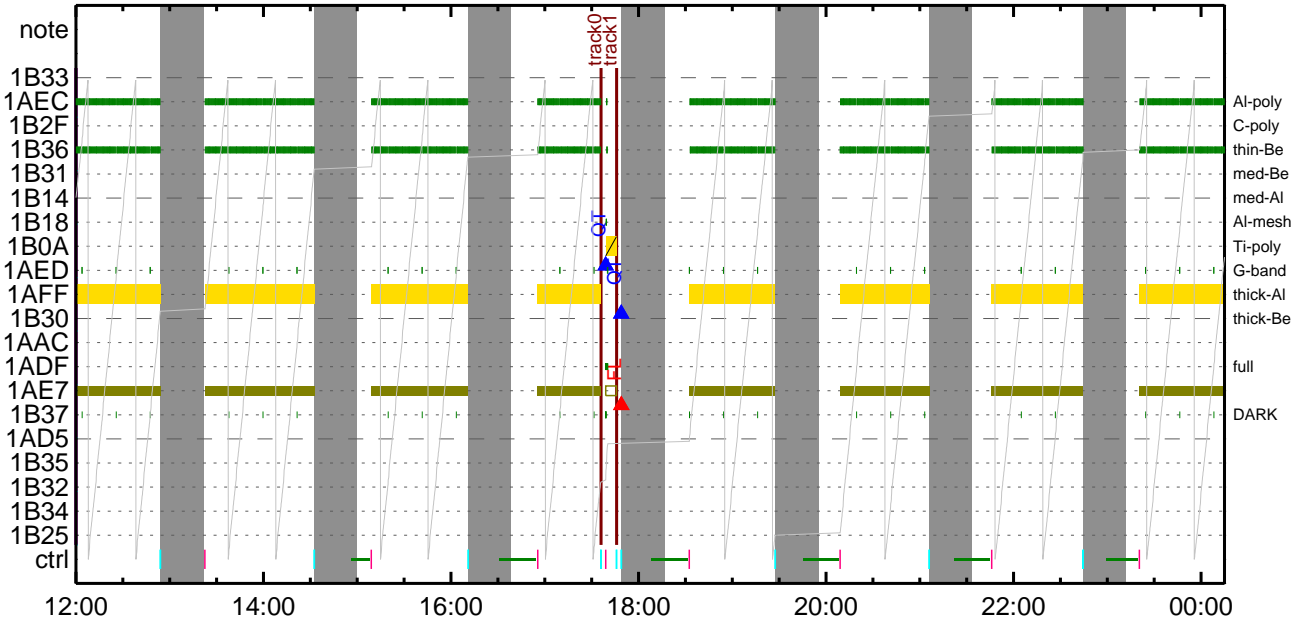
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FLD Patrol											
Term		Pointing (x, y)						Comment			
05/19 17:46:18 - 05/20 06:00:18		Track (-173.3, -138.1) @ 05/19 17:46:00						AR 12546			
05/20 06:10:18 - 05/20 18:01:18		Track (-62.3, -138.6) @ 05/20 06:10:00						AR 12546 IHOP 267			
05/20 18:11:18 - 05/21 06:25:48		Fixed (0.0, 870.0)						HOP 279 N-pole			
05/21 06:35:48 - 05/24 09:37:00		Fixed (0.0, 0.0)						synoptic, shifted 25.5 min			
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8			Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

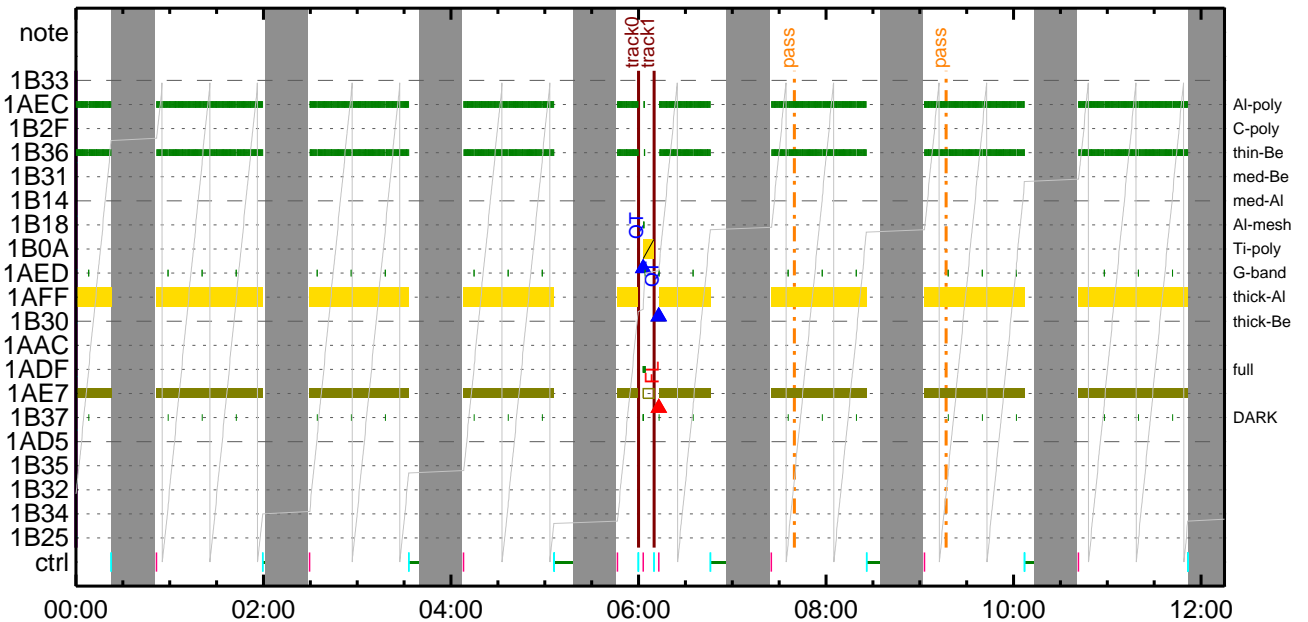
CMDI #0938 2016/05/19



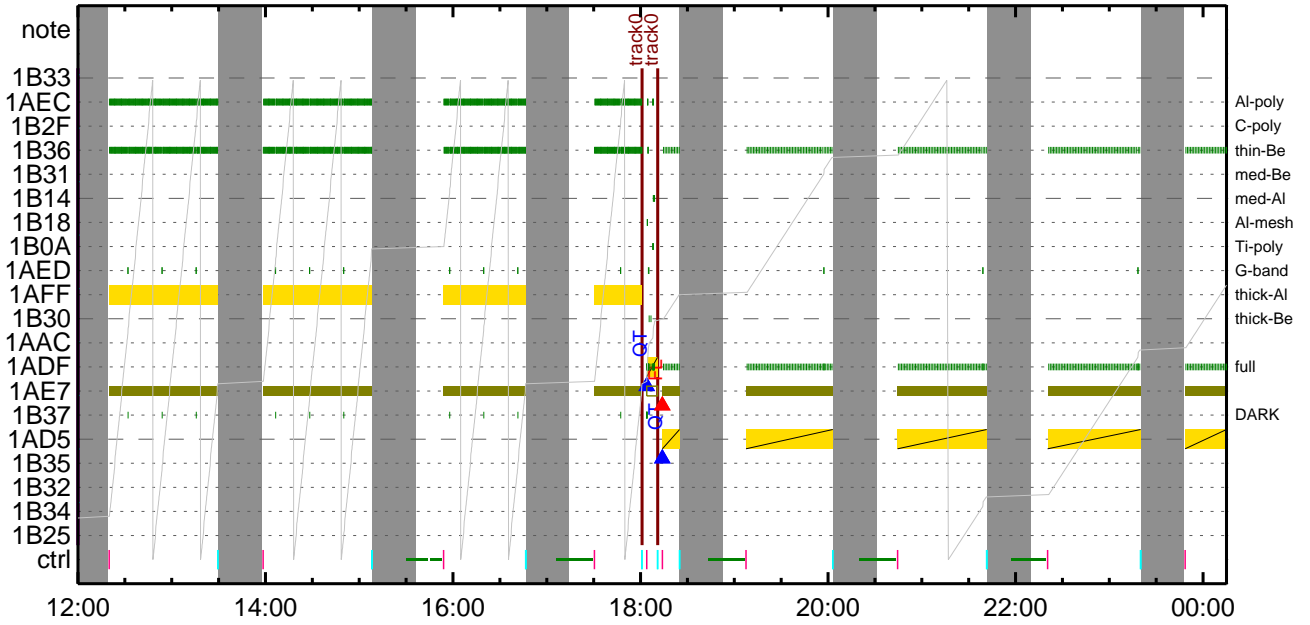
CMDI #0938 2016/05/19



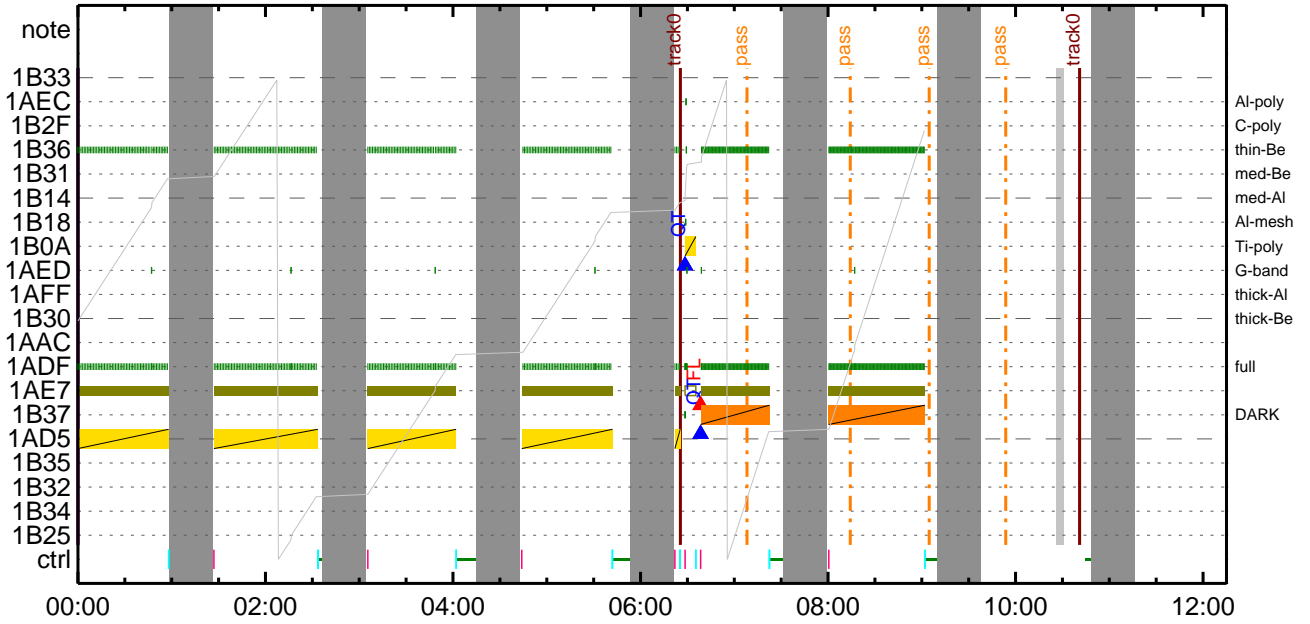
CMDI #0938 2016/05/20



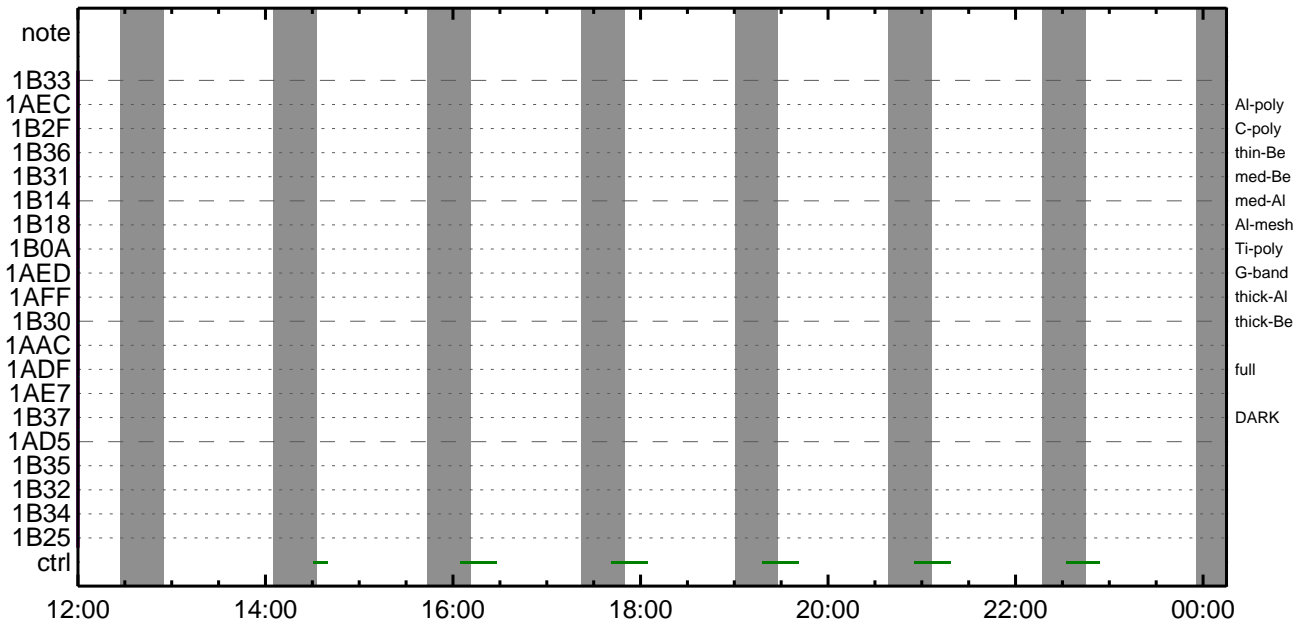
CMDI #0938 2016/05/20



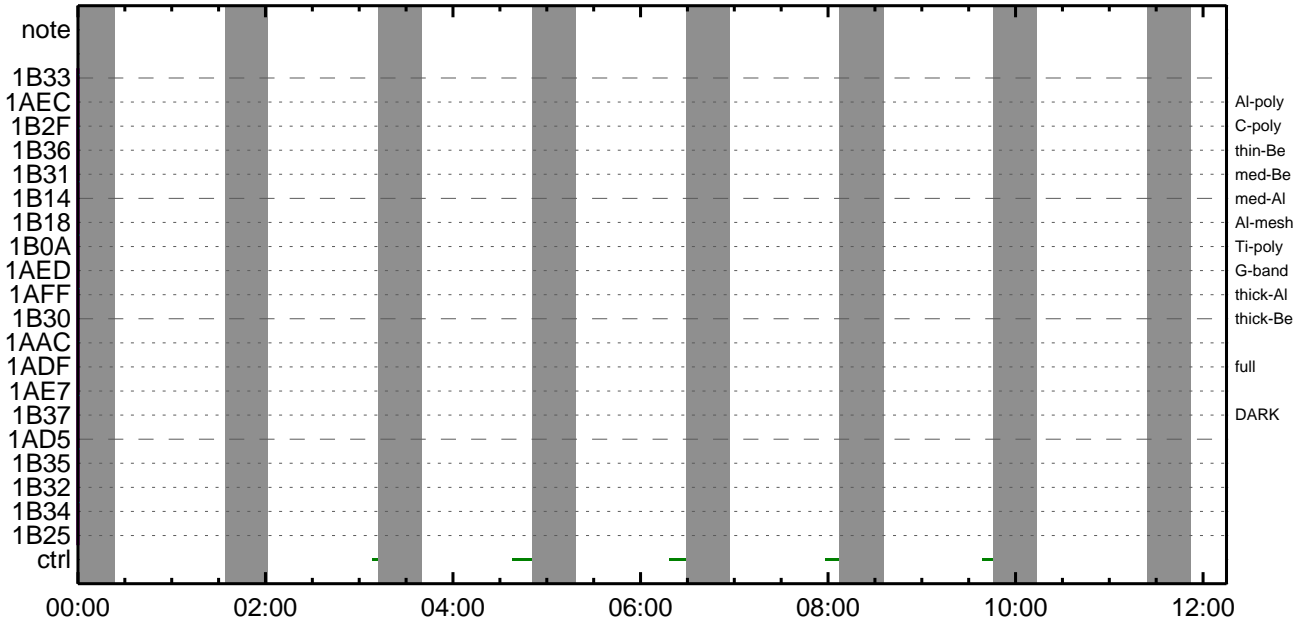
CMDI #0938 2016/05/21



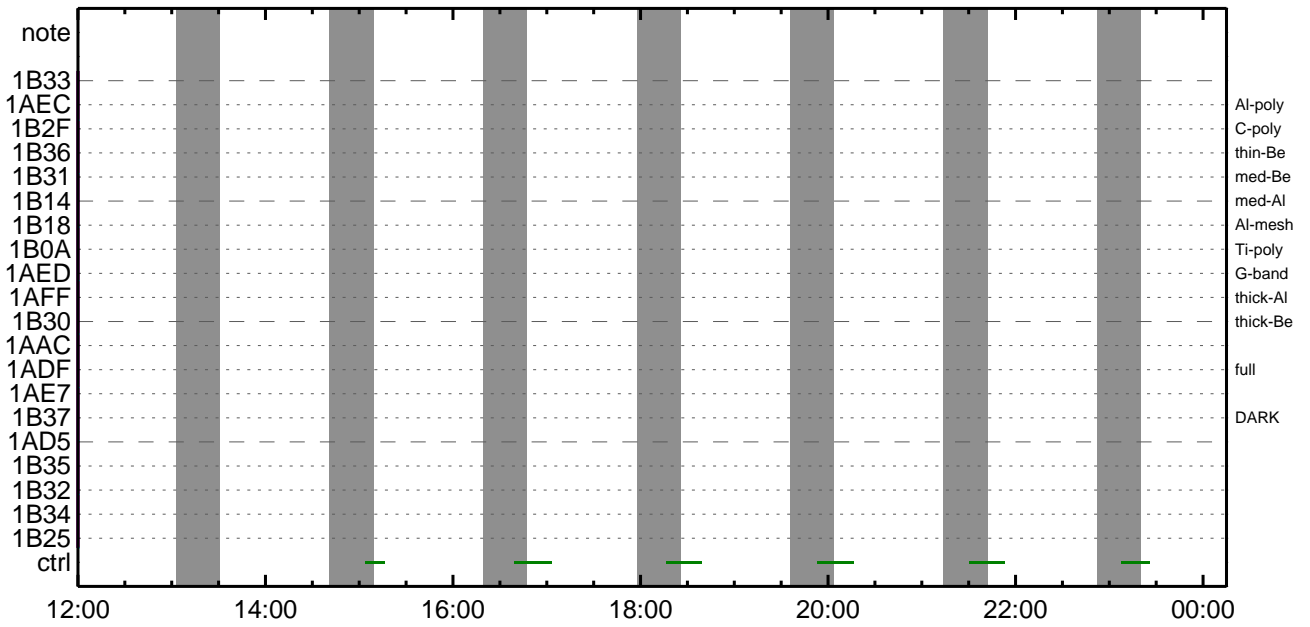
CMDI #0938 2016/05/21



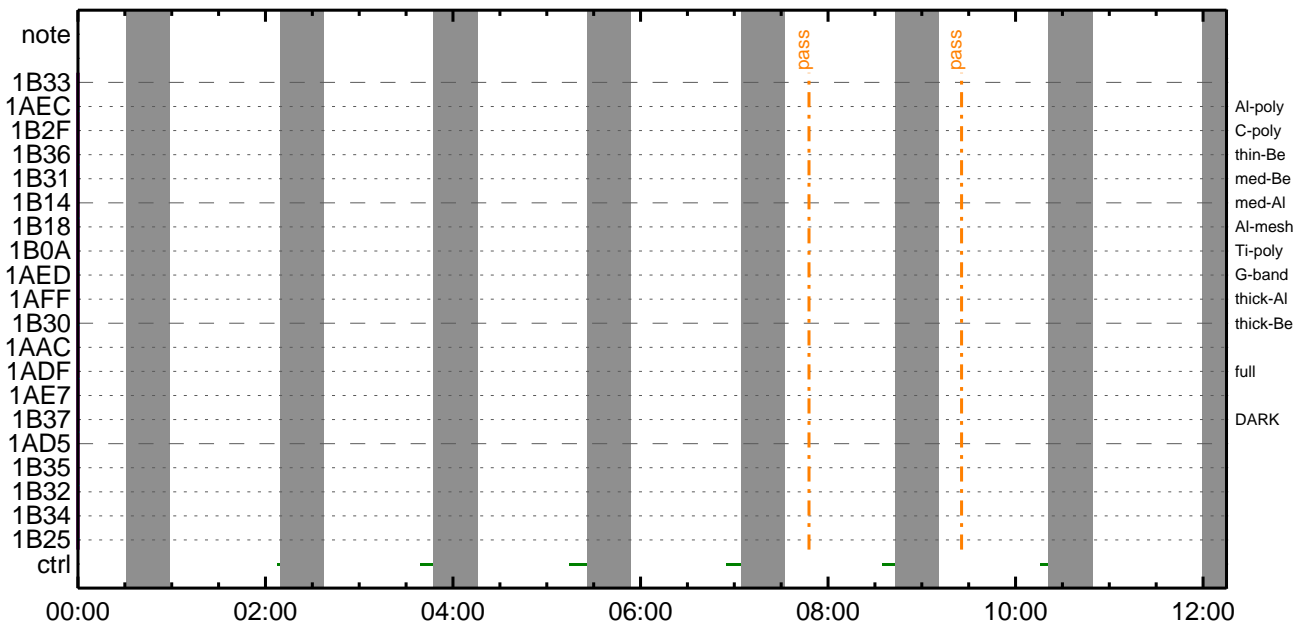
CMDI #0938 2016/05/22



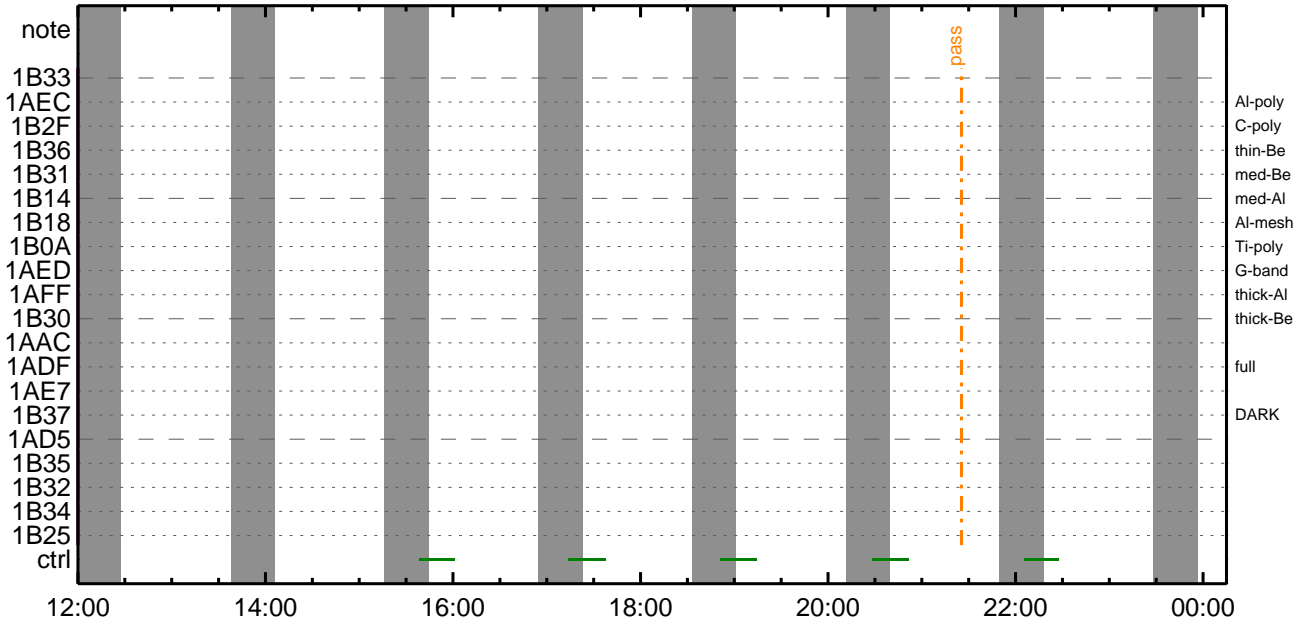
CMDI #0938 2016/05/22



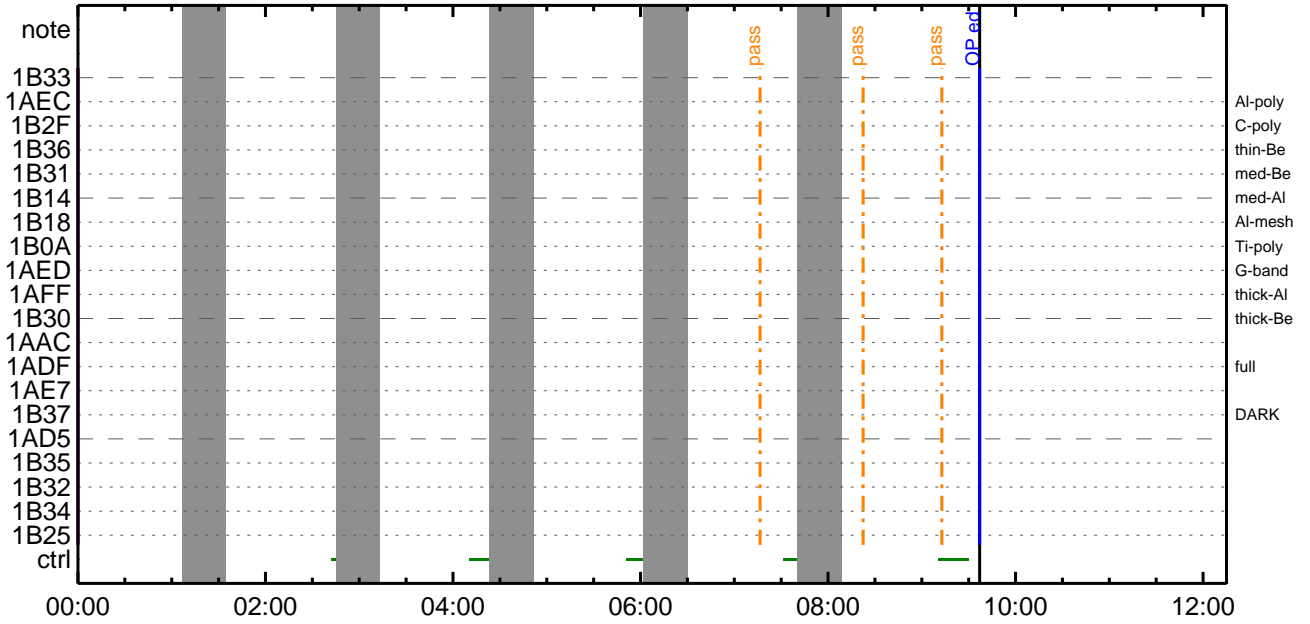
CMDI #0938 2016/05/23



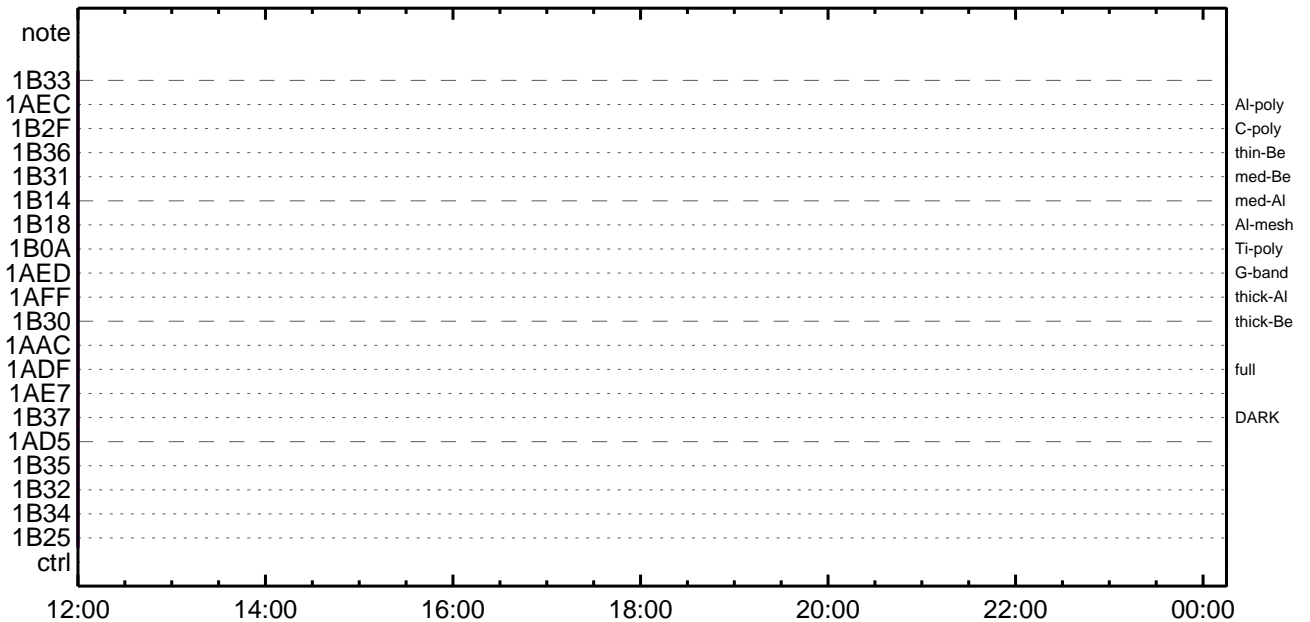
CMDI #0938 2016/05/23



CMDI #0938 2016/05/24



CMDI #0938 2016/05/24




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ä
0100 C. *****
0101 C.
0102 . C. ;äOP/OGY1;4YE;ä
0103 . S. OP op-865:OP
0104 ( )
0105 . S. OG og-865:OG
0106 ( )
0107 C.
0108 . C. ;änMOG&OPîî°èÄYÖYx;ä
0109 C. NMOG(0x200000-0x207FFF;s 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 . C. YÄYÖYx½ªî»òð³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 . C. RAM ID=NMOGqîîÉ¹ç•é²îOKòð³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;s 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 . C. YÄYÖYx½ªî»òð³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 . C. RAM ID=NMOGqîîÉ¹ç•é²îOKòð³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;s 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 . C. YÄYÖYx½ªî»òð³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 . C. RAM ID=NMOG, RAM ID=OPqîîÉ¹ç•é²îOKòð³îÇ§
0165 C.
0166 . C. ***** òÊ²¼òîîÄ´¶Á°òÊÊ¬ò°Á÷¿@ (¼âµ-ÄYÖYx½ªî»òð³îÇ§) *****
0167 C. DHUYâ;4YE;È½Y½;Yî;4YE;Ëòðîäª¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 . C. NOTICE ; § OPOG UPLOAD¬-Á÷¿@NGüîî¹¹ç;ç°Ê²¼òîîTI-CMDÁ÷¿@qîîÄ¹òª•qÊòð³òÊ²; f
0180 C. qð²¿;çSETòEDUMPAîî±°iYÑY¹ç¹òª|ò³òÊ²; f
0181 C.
0182 . C. TIY³YÞYÖYËòððÄDî¿¿(UT)
0183 +. TI 2016-05-19 09:50:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2016-05-19 09:50:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2016-05-19 09:50:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2016-05-19 09:54:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          €€[HK1_TI_CMD_NUM]                     EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼oïÄë%îíñoîŷÁŷ$ŷÄŷ⁻¹àîŮ
0200 C.          €€[HK1_TI_CMD_ENA/DIS]                   EQ      ENA
0201 C.          €€[HK1_TI_CMD_NUM]                       EQ      4
0202 C.          €€[HK1_NEXT_EXEC_PIM]                    EQ      DHU
0203 C.          €€[HK1_NEXT_EXEC_DC]                     EQ      0xB3
0204 C.
0205 . C. *****
0206 C. TIîŷ°èŷÄŷÓŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;$ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          €€[HK1_DMP_TOP_ADDR_1]                   EQ      07
0213 C.          €€[HK1_DMP_TOP_ADDR_0]                   EQ      2B
0214 C.          €€[HK1_DMP_BLOCK_NUM]                    EQ      3
0215 C.          €€[HK1_DMP_REPEAT_NUM]                   EQ      0
0216 C.          €€[HK1_DMA_DMP_PIM]                      EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          €€[HK1_PKT_FORM_NO]                       EQ      7
0220 C.          €€[HK1_PKT_GEN_TIME]                      EQ      0.25 s
0221 C.          €€[HK1_S_TLM_BIT_RATE]                   EQ      32k
0222 C.          €€[HK1_X_TLM_BIT_RATE]                   EQ      4M
0223 C.          €€[HK1_DMP_CHK_FLG]                      EQ      EXEC
0224 C.
0225 . C. ŷÄŷÓŷ×½ªî»oð³îÇ§
0226 C.          €€[HK1_DMP_CHK_FLG]                      EQ      NON
0227 C.
0228 . C. RAM ID=TI_TBLoîŷÈ¹çªè²îOKoð³îÇ§
0229 C.
0230 . C. DHUŷâ;¼ŷÈ;Êŷŷ¼. ŷî;¼ŷÈ;Ëoðîãª¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          €€[HK1_PKT_FORM_NO]                       EQ      2
0234 C.          €€[HK1_PKT_GEN_TIME]                      EQ      0.5S
0235 C.          €€[HK1_S_TLM_BIT_RATE]                   EQ      32K
0236 C.          €€[HK1_X_TLM_BIT_RATE]                   EQ      4M
0237 C.
0238 . C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2016-05-19 09:54:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2016-05-19 09:54:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 . C.          [ ] [HK1_TI_CMD_NUM]                     EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.
0255 C. ***** XRT START *****
0256 C. Execute, after the success of OP upload.
0257 +. TI 2016-05-19 09:54:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC      (c3)
0260 . C.          [ ] [HK1_TI_CMD_NUM]                     EQ      1COUNTUP
0261 C.
0262 C. ***** XRT END *****
0263 C.
0264 . C. ***** MDP ´ûÃîoî»ö¼ŷoÈÄðª¹oèDCBC•x²è *****
0265 C. (¼ª°îŷÓŷÄŷÈŷŷŷÈŷáŷçŷèoÈ¼o¼¼Ä»Ůª¹oè)
0266 . S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 . C. ***** ŷĐŷ¹.İ Daily±;îñoÈ´Øª¹oèDCBC•x²è *****
0271 . S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 . C. įãLOSŷÄŷ$ŷÄŷ⁻¼Ä»Ů;ã
0276 C.
0277 . C. ***** LOS *****
0278 C.

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(a) Spacecraft Operation Procedure (real-commands)

```
main-867 2016-05-19 11:58:26 96 33 SOLAR-B MAIN //  
0001  . C.  
0002  . C. ***** AOS *****  
0003  . C.  
0004  . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä  
0005  . C.  
0006  . C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®  
0007  +. DC 00-00 NULL_DUMMY_CMD  
0008  . C.  
0009  . C. ***** AOCS : Reload orbital element (send every contact) *****  
0010  . C. Áí;Èª¿ªÀª•µ°Æ»Í×ÁÇªíYçYÁY×¥í;¼YÉ;ÈÈªµ•ííÉ;ÈªÈ¼°ÇÓª•ª¿¼i¹çªí;çÀ®, ùª¹ªèªªªçÁ+¿ªª•ªÈªªªª³ªÈ;f  
0011  +. DC 02-8E AOCU_ORB_UPD  
0012  . C.  
0013  . C.  
0014  . C.  
0015  . C. ***** XRT START *****  
0016  . C.  
0017  +. DC 07-F0 MDP_XRT_CTRL_MANU  
0018  BC (c1)  
0019  +. DC 07-F0 MDP_XRT_CTRL_MANU  
0020  BC (c1)  
0021  +. DC 07-F0 MDP_XRT_MODE_STBY  
0022  BC (c3)  
0023  . C. ----- Success Verify ? OK / NG ____  
0024  . C.  
0025  . C. XRT Obs. Table Upload  
0026  . S. RAM ram-291:MDP_OBS_X  
0027  ( )  
0028  . C.  
0029  +. DC 07-F0 MDP_DUMP_XRTTBL  
0030  BC (84 07 00 00 00 3a d4)  
0031  . C. ----- Comparison Check ? OK / ERR ____  
0032  . C.  
0033  . C.  
0034  +. DC 07-F0 MDP_XRT_ROI_SET  
0035  BC (cd 01 b1 b1 04 04)  
0036  +. DC 07-F0 MDP_XRT_ROI_SET  
0037  BC (cd 02 b1 b1 08 08)  
0038  +. DC 07-F0 MDP_XRT_ROI_SET  
0039  BC (cd 03 b1 b1 08 08)  
0040  +. DC 07-F0 MDP_XRT_ROI_SET  
0041  BC (cd 04 b1 b1 06 06)  
0042  +. DC 07-F0 MDP_XRT_ROI_SET  
0043  BC (cd 06 85 83 06 06)  
0044  +. DC 07-F0 MDP_XRT_ROI_SET  
0045  BC (cd 07 80 80 20 20)  
0046  +. DC 07-F0 MDP_XRT_ROI_SET  
0047  BC (cd 08 80 80 20 08)  
0048  +. DC 07-F0 MDP_XRT_ROI_SET  
0049  BC (cd 09 80 80 08 20)  
0050  +. DC 07-F0 MDP_XRT_ROI_SET  
0051  BC (cd 0f 80 80 06 06)  
0052  +. DC 07-F0 MDP_XRT_ROI_SET  
0053  BC (cd 10 80 80 08 08)  
0054  +. DC 07-F0 MDP_XRT_FLD_ENA  
0055  BC (d8)  
0056  +. DC 07-F0 MDP_XRT_FLRCTRL_ENA  
0057  BC (c8)  
0058  +. DC 07-F0 MDP_XRT_ARS_DIS  
0059  BC (d5)  
0060  +. DC 07-F0 MDP_XRT_AEC_RESET  
0061  BC (d0)  
0062  +. DC 07-F0 MDP_XRT_FLD_RESET  
0063  BC (da)  
0064  +. DC 07-F0 MDP_XRT_QT_PROG_SET  
0065  BC (c4 0b)  
0066  +. DC 07-F0 MDP_XRT_FL_PROG_SET  
0067  BC (c5 07)  
0068  . C. ----- Success Verify ? OK / NG ____  
0069  . C.  
0070  . C.  
0071  . C. All OK? Yes--> Please Proceed. / No --> Stop here.  
0072  . C.  
0073  +. DC 07-F0 MDP_XRT_MODE_OBSV  
0074  BC (c2)  
0075  +. TI 2016-05-19 09:54:02.0  
0076  DC 07-F0 MDP_XRT_MODE_OBSV  
0077  BC (c2)  
0078  . C. ----- Success Verify ? OK / NG ____  
0079  . C.  
0080  . C. ***** XRT END *****  
0081  . C.  
0082  . C. ***** MDP ´ûÁíªí»ò¼YªÈÁªª¹ªèDCBC•x²è *****  
0083  . C. (¼á°íYÁYÈY¥YÈYÁYçYÈªÈ¼ªªª¼Á»Ûª¹ªè)  
0084  . S. DC-BC dcbc-402:DCBC  
0085  (MDP_known_event)  
0086  . C.  
0087  . C.  
0088  . C. ***** YDY¹•í Daily+¿íÑªÈ´Øª¹ªèDCBC•x²è *****  
0089  . S. DC-BC dcbc-153:DCBC  
0090  (SPECIAL-CMD_DAILY_OPERATIN_DCB)  
0091  . C.  
0092  . C.  
0093  . C. ;ãLOS¥ÁY$YÁY-¼Á»Û;ä  
0094  . C.  
0095  . C. ***** LOS *****
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*** OP Sequence for XRT ***

2016/05/19	10:04:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/19	10:04:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/19	10:04:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2016/05/19	10:05:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	04 00 00 00 00		
2016/05/19	10:05:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2016/05/19	10:05:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/05/19	10:05:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/05/19	10:05:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/05/19	10:05:26.0	XRT_FLD_RESET_433_OG [0x1b1]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2016/05/19	10:07:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b		
2016/05/19	10:07:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07		
2016/05/19	10:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/05/19	11:16:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/19	11:16:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/19	11:16:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2016/05/19	11:16:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/05/19	11:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/05/19	11:43:30.0	XRT_Custom_430_OG [0x1ae]					
2016/05/19	11:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/05/19	12:54:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/19	12:54:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/19	12:54:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2016/05/19	12:54:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/05/19	12:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/05/19	13:21:30.0	XRT_Custom_430_OG [0x1ae]					
2016/05/19	13:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/05/19	14:32:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/19	14:32:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/19	14:32:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2016/05/19	14:32:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/05/19	14:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/05/19	15:08:00.0	XRT_Custom_430_OG [0x1ae]					
2016/05/19	15:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/05/19	16:11:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/19	16:11:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/19	16:11:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2016/05/19	16:11:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/05/19	16:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/05/19	16:54:30.0	XRT_Custom_430_OG [0x1ae]					
2016/05/19	16:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/05/19	17:35:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/19	17:35:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/19	17:35:58.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2016/05/19	17:36:00.0	AOCS_Ore-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2016/05/19	17:36:18.0	XRT_FLD_DIS_406_OG [0x196]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2016/05/19	17:38:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2016/05/19	17:38:56.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/05/19	17:38:58.0	XRT_QT_PROG_SET_439_OG [0x1b7]					

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2016/05/19	17:39:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/19	17:45:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/19	17:45:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/19	17:45:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2016/05/19	17:46:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01	00 00 00 00
2016/05/19	17:46:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/05/19	17:46:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/05/19	17:46:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/05/19	17:46:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/05/19	17:46:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/19	17:48:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b
2016/05/19	17:48:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2016/05/19	17:49:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/19	17:49:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/19	17:49:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/19	17:49:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/05/19	17:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/19	18:31:30.5	XRT_Custom_430_OG [0x1ae]					
2016/05/19	18:32:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/19	19:27:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/19	19:27:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/19	19:27:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/19	19:27:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/05/19	19:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/19	20:08:00.0	XRT_Custom_430_OG [0x1ae]					
2016/05/19	20:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/19	21:06:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/19	21:06:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/19	21:06:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/19	21:06:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/05/19	21:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/19	21:45:00.0	XRT_Custom_430_OG [0x1ae]					
2016/05/19	21:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/19	22:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/19	22:44:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/19	22:44:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/19	22:44:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/05/19	22:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/19	23:19:30.0	XRT_Custom_430_OG [0x1ae]					
2016/05/19	23:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/20	00:22:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/20	00:22:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/20	00:22:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/20	00:22:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/05/20	00:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/20	00:50:30.0	XRT_Custom_430_OG [0x1ae]					
2016/05/20	00:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/20	01:59:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/20	01:59:32.0	XRT_CTRL_MANU_402_OG [0x192]					

2016/05/20	01:59:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/20	01:59:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/20	02:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/05/20	02:28:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/20	02:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2016/05/20	03:33:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/20	03:33:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/20	03:33:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/20	03:33:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/20	03:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/05/20	04:07:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/20	04:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2016/05/20	05:06:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/20	05:06:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/20	05:06:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/20	05:06:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/20	05:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/05/20	05:45:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/20	05:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2016/05/20	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/20	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/20	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/20	06:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2016/05/20	06:00:18.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00 00 00 00 00	
2016/05/20	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2016/05/20	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2016/05/20	06:02:58.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/05/20	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d	
2016/05/20	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/20	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/20	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/20	06:10:00.0	AOCS_ORe-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2016/05/20	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01 00 00 00 00	
2016/05/20	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/05/20	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/05/20	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/05/20	06:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/05/20	06:12:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/20	06:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b	
2016/05/20	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2016/05/20	06:46:00.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/20	06:46:02.5	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/20	06:46:04.5	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/20	06:46:06.5	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/20	06:49:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/05/20	07:24:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/20	07:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2016/05/20	08:26:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
			MDP_XRT_CTRL_MANU	1	07-F0	c1	

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2016/05/20	08:26:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/20	08:26:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/20	08:26:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/20	08:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/20	09:02:00.0	XRT_Custom_430_OG [0x1ae]			
2016/05/20	09:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/20	10:07:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/20	10:07:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/20	10:07:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/20	10:07:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/20	10:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/20	10:40:30.0	XRT_Custom_430_OG [0x1ae]			
2016/05/20	10:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/20	11:51:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/20	11:51:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/20	11:51:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/20	11:51:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/20	11:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/20	12:19:00.0	XRT_Custom_430_OG [0x1ae]			
2016/05/20	12:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/20	13:29:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/20	13:29:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/20	13:29:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/20	13:29:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/20	13:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/20	13:57:30.0	XRT_Custom_430_OG [0x1ae]			
2016/05/20	13:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/20	15:08:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/20	15:08:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/20	15:08:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/20	15:08:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/20	15:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/20	15:53:00.0	XRT_Custom_430_OG [0x1ae]			
2016/05/20	15:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/20	16:46:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/20	16:46:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/20	16:46:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/20	16:46:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/20	16:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/20	17:29:30.0	XRT_Custom_430_OG [0x1ae]			
2016/05/20	17:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/20	18:00:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/20	18:00:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/20	18:00:58.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/05/20	18:01:00.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2016/05/20	18:01:18.0	XRT_FLD_DIS_406_OG [0x196]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2016/05/20	18:03:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/05/20	18:03:56.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/20	18:03:58.0	XRT_QT_PROG_SET_431_OG [0x1af]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2016/05/20	18:04:00.0	XRT_CTRL_AUTO_408_OG [0x198]			

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2016/05/20	18:10:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/20	18:10:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/20	18:10:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/05/20	18:11:00.0	AOCS_OrE-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 b2 a8 00 00
2016/05/20	18:11:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/05/20	18:11:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/05/20	18:11:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/05/20	18:11:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/20	18:11:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/20	18:13:56.0	XRT_QT_PROG_SET_413_OG [0x19d]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2016/05/20	18:13:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/05/20	18:14:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/20	18:25:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/20	18:25:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/20	18:25:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/20	18:25:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/20	18:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/20	19:06:31.0	XRT_Custom_430_OG [0x1ae]				
2016/05/20	19:07:31.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/20	20:03:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/20	20:03:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/20	20:03:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/20	20:03:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/20	20:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/20	20:43:30.0	XRT_Custom_430_OG [0x1ae]				
2016/05/20	20:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/20	21:41:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/20	21:41:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/20	21:41:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/20	21:41:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/20	21:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/20	22:19:30.0	XRT_Custom_430_OG [0x1ae]				
2016/05/20	22:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/20	23:20:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/20	23:20:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/20	23:20:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/20	23:20:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/20	23:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/20	23:47:30.0	XRT_Custom_430_OG [0x1ae]				
2016/05/20	23:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/21	00:58:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/21	00:58:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/21	00:58:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/21	00:58:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/21	01:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/21	01:26:00.0	XRT_Custom_430_OG [0x1ae]				
2016/05/21	01:27:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/21	02:33:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/21	02:33:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1

2016/05/21	02:33:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/21	02:33:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/21	02:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/05/21	03:04:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/21	03:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/21	04:02:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/21	04:02:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/21	04:02:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/21	04:02:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/21	04:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/05/21	04:43:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/21	04:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/21	05:42:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/21	05:42:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/21	05:42:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/21	05:42:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/21	05:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/05/21	06:21:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/21	06:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/21	06:25:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/21	06:25:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/21	06:25:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/21	06:25:30.0	AOCS_OrE-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2016/05/21	06:25:48.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00 00 00 00 00	
2016/05/21	06:28:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2016/05/21	06:28:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2016/05/21	06:28:28.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/05/21	06:28:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d	
2016/05/21	06:35:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/21	06:35:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/21	06:35:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/21	06:35:48.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2016/05/21	06:35:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/05/21	06:35:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/05/21	06:35:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/05/21	06:35:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/05/21	06:38:26.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/21	06:38:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06	
2016/05/21	06:38:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2016/05/21	07:22:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/21	07:22:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/21	07:22:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/21	07:22:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/21	07:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/05/21	07:59:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/21	08:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/21	09:02:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/21	09:02:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

