

XRT Timeline to be uploaded on 2016/05/28

Period: 2016/05/28 10:20:00 - 2016/06/07 10:15:00

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Normal mode

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XOB #1AFF: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
05/28 10:33:00 - 05/28 15:59:54	Track (743.1, 177.0) @ 05/28 10:30:00	# OP start + 10min AR 12548, HOP304 (16UT-)
05/29 02:03:00 - 05/29 06:16:24	Track (818.6, 174.1) @ 05/29 02:00:00	AR 12548
05/29 06:29:30 - 05/29 17:55:54	Track (836.9, 173.3) @ 05/29 06:26:30	AR 12548
05/29 18:09:00 - 05/30 05:40:24	Fixed (889.0, 170.0)	AR 12548, EIS spectral Atlas for off-limb (2:00UT -)
05/30 06:03:30 - 05/30 07:29:54	Fixed (889.0, 170.0)	AR 12548, HOP300 (7:30 UT-)
05/30 12:03:00 - 05/30 14:30:00	Fixed (-939.0, -114.1)	EIS AR temperature diagnostics
05/30 18:30:30 - 05/30 23:59:54	Fixed (-939.0, -114.1)	new AR @ E-limb
05/31 00:03:00 - 05/31 05:59:54	Track (97.9, -56.3) @ 05/31 00:00:00	AR near CM observation
05/31 06:13:00 - 05/31 07:29:54	Fixed (-939.0, -114.1)	New AR @ E-limb

PROG= 11 Inf.-time(s)

Subr=	1-time(s)	2.0sec										
Subr= 1	1-time(s)	2.0sec										
Seqn= 56	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2	5-time(s)	2.0sec										
Seqn= 75	1-time(s)	2.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 96	4-time(s)	60.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	15.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	15.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1AF0: AR (Filter-Ratio with Al/poly and thin-Be), 384x384 at 1064, 1048 with G-band (3ms/3ms leak) 30s cad - AEC3- HOP268

Term	Pointing (x, y)	Comment
05/28 16:03:00 - 05/28 17:59:24	Track (743.1, 177.0) @ 05/28 10:30:00	# OP start + 10min AR 12548, HOP304 (16UT-)
05/28 18:12:30 - 05/28 21:30:30	Track (782.6, 175.6) @ 05/28 18:09:30	HOP304

PROG= 02 Inf.-time(s)

Subr=	1-time(s)	2.0sec										
Subr= 1	1-time(s)	2.0sec										
Seqn= 56	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2	1-time(s)	2.0sec										
Seqn= 37	120-time(s)	30.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1B35: Synoptic 7 Filter w/ Al-mesh(24/256/2897), Al-poly(45/512/4096), Thin-Be(181/2048/11571) - Thick-Be(65536), Al-poly+Ti-poly(362/4096), Med-Al

Term	Pointing (x, y)	Comment
05/28 18:02:30 - 05/28 18:09:24	Fixed (0.0, 0.0)	synoptic, shifted -0.5 min

PROG= 04 1-time(s)

Subr=	1-time(s)	2.0sec										
Subr= 1	1-time(s)	2.0sec										
Seqn= 5	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 1	1-time(s)	2.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 99	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 67	1-time(s)	2.0sec										
thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54	1-time(s)	4.0sec										

	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2	1-time(s)	2.0sec											
Seqn= 46	2-time(s)	2.0sec											
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 94	2-time(s)	2.0sec											
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 68	2-time(s)	2.0sec											
	med-Al/Open	med-Al/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1AEC: G-Band Alignment with North Pole Q90 2x2 (G-band and VLS=CLS) - 1msec (Al/poly) - 4096msec - 5min cadence - Partial Sun-wNGT

Term	Pointing (x, y)	Comment											
05/28 22:15:00 - 05/28 23:59:54	Fixed (0.0, 930.0)	Co-alignment N-limb											
PROG= 19	1-time(s)												
Subr= 1	24-time(s)	300.0sec											
Seqn= 98	1-time(s)	2.0sec											
	Open/G-band	Open/G-band open	Safe	Norm	1ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec	
Seqn= 63	1-time(s)	2.0sec											
	Open/G-band	Open/G-band close	Safe	Norm	1ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec	
Seqn= 45	1-time(s)	2.0sec											
	Al-poly/Open	med-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x1536 (1024, 768)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1AED: G-Band Alignment with East limb Q90 2x2 (G-band and VLS=CLS) - 1msec - (Al/poly) 1443msec - 8 min cadence-wNGT

Term	Pointing (x, y)	Comment											
05/29 00:15:00 - 05/29 01:59:54	Fixed (-970.0, 0.0)	Co-alignment E limb											
PROG= 12	1-time(s)												
Subr= 1	15-time(s)	480.0sec											
Seqn= 19	1-time(s)	2.0sec											
	Open/G-band	Open/G-band open	Safe	Norm	1ms	Obs	2x2	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec	
Seqn= 43	1-time(s)	2.0sec											
	Open/G-band	Open/G-band close	Safe	Norm	1ms	Obs	2x2	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec	
Seqn= 70	1-time(s)	2.0sec											
	Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	1536x2048 (1280, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1B0A: Synoptic Q95 2x2 - Al/mesh(12/181/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(24/362/1443) + T

Term	Pointing (x, y)	Comment											
05/29 06:19:30 - 05/29 06:26:24	Fixed (0.0, 0.0)	synoptic, shifted 16.5 min											
05/29 17:59:00 - 05/29 18:05:54	Fixed (0.0, 0.0)	synoptic, shifted -4.0 min											
05/31 06:03:00 - 05/31 06:09:54	Fixed (0.0, 0.0)	synoptic											
PROG= 13	1-time(s)												
Subr= 1	1-time(s)	2.0sec											
Seqn= 5	1-time(s)	2.0sec											
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 91	1-time(s)	2.0sec											
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 93	1-time(s)	2.0sec											
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 77	1-time(s)	2.0sec											
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54	1-time(s)	2.0sec											
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1B39: HOP301 Synoptic Q95 2x2 - 7 Filters+WL+MultiExpo set

Term	Pointing (x, y)	Comment
05/30 05:43:30 - 05/30 06:00:24	Fixed (0.0, 0.0)	synoptic, shifted -19.5 min
05/30 17:25:30 - 05/30 17:46:30	Fixed (0.0, 0.0)	synoptic, shifted -42.5 min
PROG= 15	2-time(s)	
Subr= 1	1-time(s)	2.0sec
Seqn= 87	1-time(s)	2.0sec

Al-poly/Open	Al-poly/Open	close	Safe	Dark	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Dark	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Dark	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 77		1-time(s)	2.0sec									
thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 90		1-time(s)	2.0sec									
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 21		1-time(s)	2.0sec									
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 46		1-time(s)	2.0sec									
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 51		1-time(s)	2.0sec									
Open/thick-Al	Open/thick-Al	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 2		1-time(s)	2.0sec									
Seqn= 93		1-time(s)	2.0sec									
Al-poly/Open	Al-poly/Open	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 91		1-time(s)	2.0sec									
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54		1-time(s)	2.0sec									
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1B21: AR - Standard Core - (Filter-Ratio with thin-Be and Med-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Med-Be context, with

Term	Pointing (x, y)	Comment
05/30 07:33:00 - 05/30 11:59:54	Fixed (889.0, 170.0)	AR 12548, HOP300 (7:30 UT-)
05/30 15:07:00 - 05/30 17:22:24	Fixed (889.0, 170.0)	HOP300
05/31 07:33:00 - 05/31 11:55:54	Fixed (889.0, 170.0)	HOP300

PROG= 01 Inf.-time(s)

Subr= 1		1-time(s)	2.0sec									
Seqn= 56		1-time(s)	2.0sec									
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2		5-time(s)	2.0sec									
Seqn= 48		1-time(s)	2.0sec									
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 97		4-time(s)	90.0sec									
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * *

Flare mode

* * * * *

XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
05/28 10:33:00 - 05/28 15:59:54	Track (743.1, 177.0) @ 05/28 10:30:00	# OP start + 10min AR 12548, HOP304 (16UT-)
05/28 16:03:00 - 05/28 17:59:24	Track (743.1, 177.0) @ 05/28 10:30:00	# OP start + 10min AR 12548, HOP304 (16UT-)
05/28 18:12:30 - 05/28 21:30:30	Track (782.6, 175.6) @ 05/28 18:09:30	HOP304
05/29 02:03:00 - 05/29 06:16:24	Track (818.6, 174.1) @ 05/29 02:00:00	AR 12548
05/29 06:29:30 - 05/29 17:55:54	Track (836.9, 173.3) @ 05/29 06:26:30	AR 12548
05/29 18:09:00 - 05/30 01:59:54	Fixed (889.0, 170.0)	AR 12548, EIS spectral Atlas for off-limb (2:00UT -)
05/30 02:03:00 - 05/30 05:40:24	Fixed (889.0, 170.0)	AR 12548, EIS spectral Atlas for off-limb (2:00UT -)
05/30 06:03:30 - 05/30 07:29:54	Fixed (889.0, 170.0)	AR 12548, HOP300 (7:30 UT-)
05/30 07:33:00 - 05/30 11:59:54	Fixed (889.0, 170.0)	AR 12548, HOP300 (7:30 UT-)
05/30 12:03:00 - 05/30 14:30:00	Fixed (-939.0, -114.1)	EIS AR temperature diagnostics
05/30 15:07:00 - 05/30 17:22:24	Fixed (889.0, 170.0)	HOP300
05/30 18:30:30 - 05/30 23:59:54	Fixed (-939.0, -114.1)	new AR @ E-limb
05/31 00:03:00 - 05/31 05:59:54	Track (97.9, -56.3) @ 05/31 00:00:00	AR near CM observation
05/31 06:13:00 - 05/31 07:29:54	Fixed (-939.0, -114.1)	New AR @ E-limb

PROG= 07 30-time(s)												
Subr= 1 20-time(s) 2.0sec												
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn=100 1-time(s) 10.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 10 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 84 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * *

Active Region Search

* * * * *

NOT USED

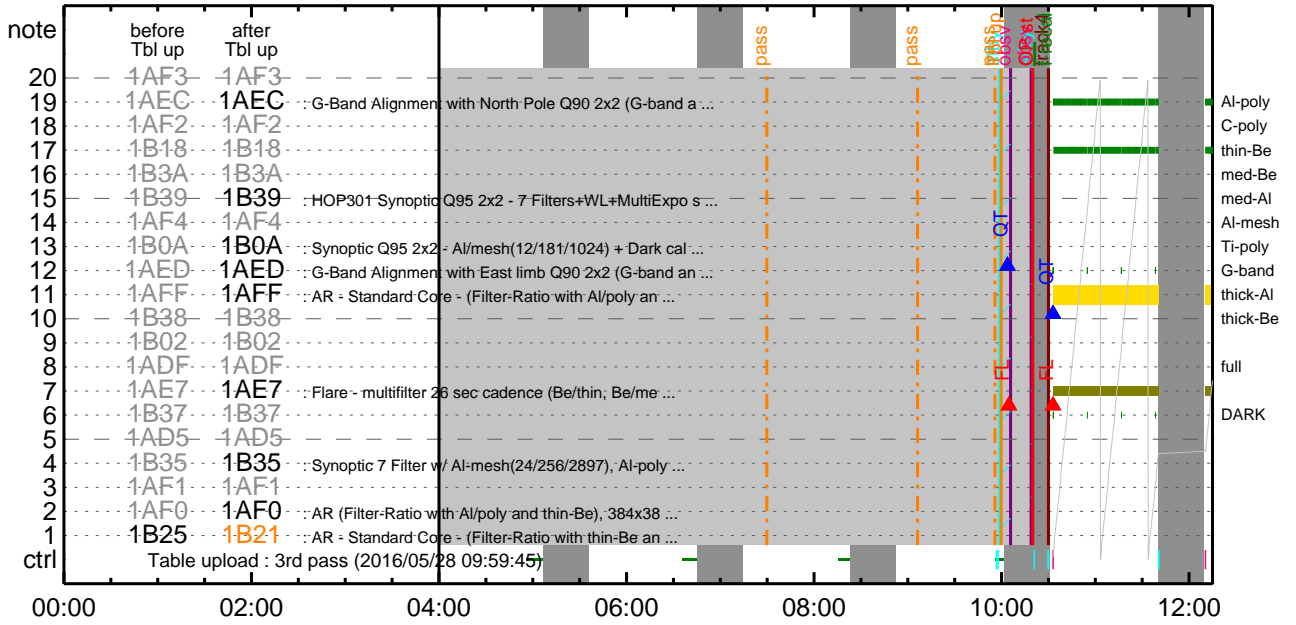
* * * * *

Flare Detection

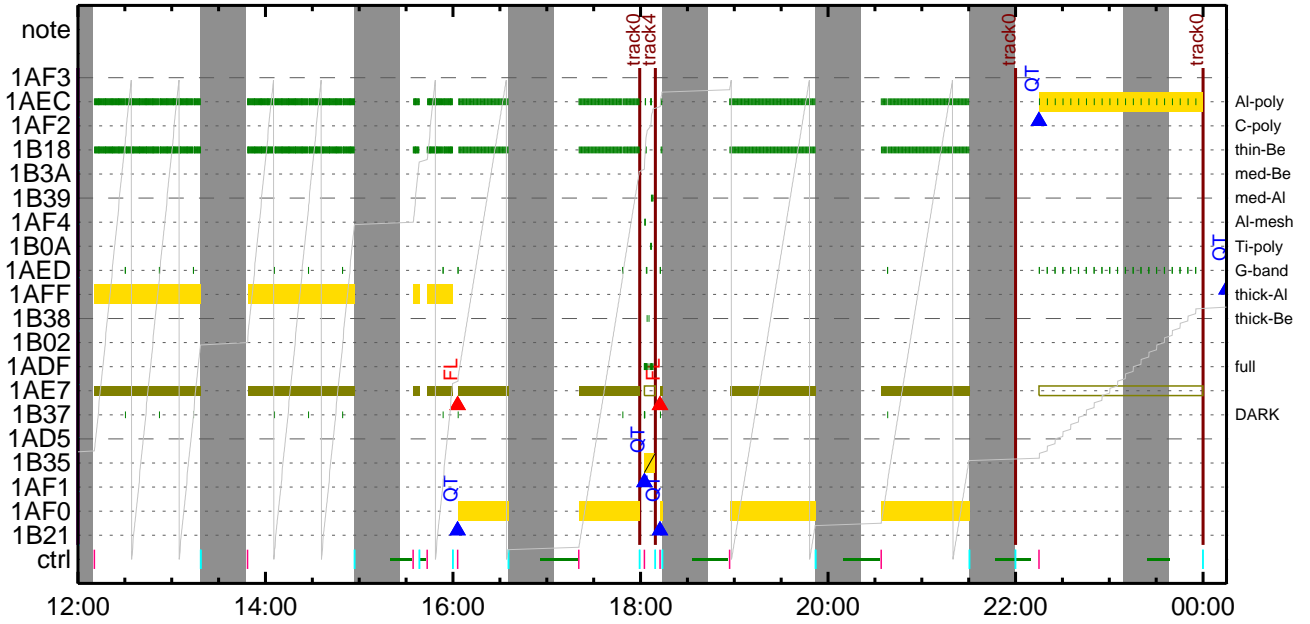
* * * * *

FLD Patrol											
Term		Pointing (x, y)				Comment					
05/28 18:09:48 - 05/28 22:00:18	Track (782.6, 175.6)	⑥ 05/28 18:09:30				HOP304					
05/29 02:00:18 - 05/29 06:16:48	Track (818.6, 174.1)	⑥ 05/29 02:00:00				AR 12548					
05/29 06:26:48 - 05/29 17:56:18	Track (836.9, 173.3)	⑥ 05/29 06:26:30				AR 12548					
05/29 18:06:18 - 05/30 05:40:48	Fixed (889.0, 170.0)					AR 12548, EIS spectral Atlas for off-limb (2:00UT -)					
05/30 06:00:48 - 05/30 17:22:48	Fixed (889.0, 170.0)					AR 12548, HOP300 (7:30 UT-)					
05/30 17:52:48 - 05/31 06:00:18	Fixed (-939.0, -114.1)					new AR @ E-limb					
05/31 06:10:18 - 06/07 10:15:00	Fixed (-939.0, -114.1)					New AR @ E-limb					
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50	30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

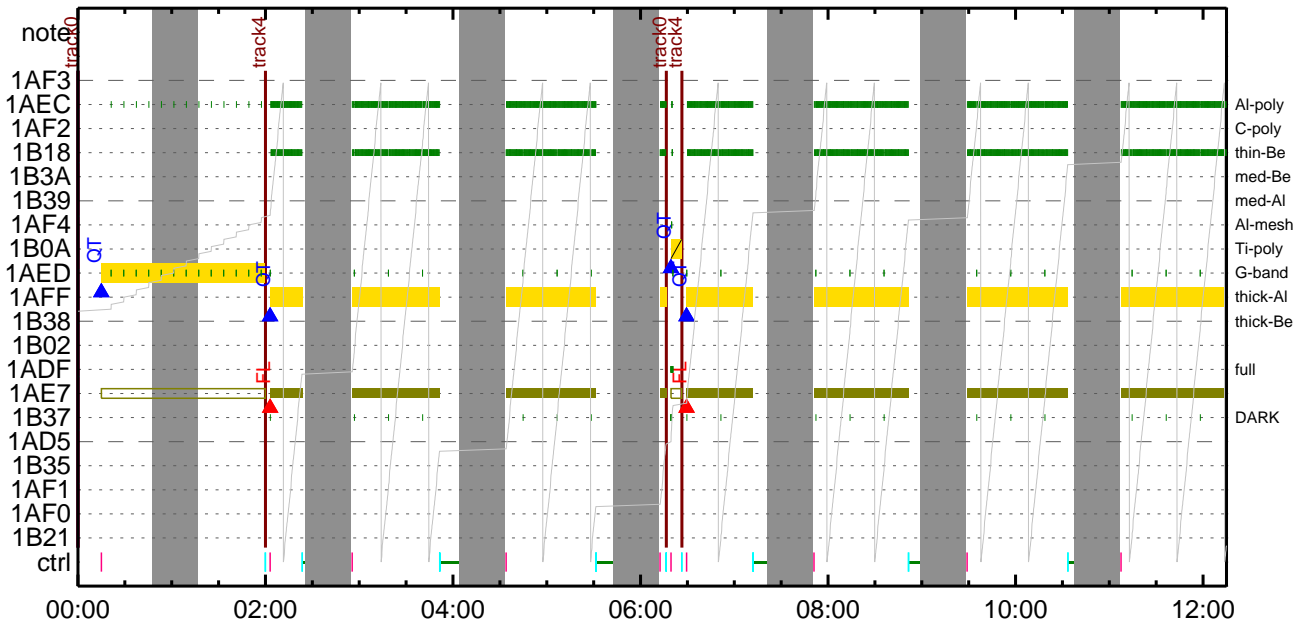
CMDI #0960 2016/05/28



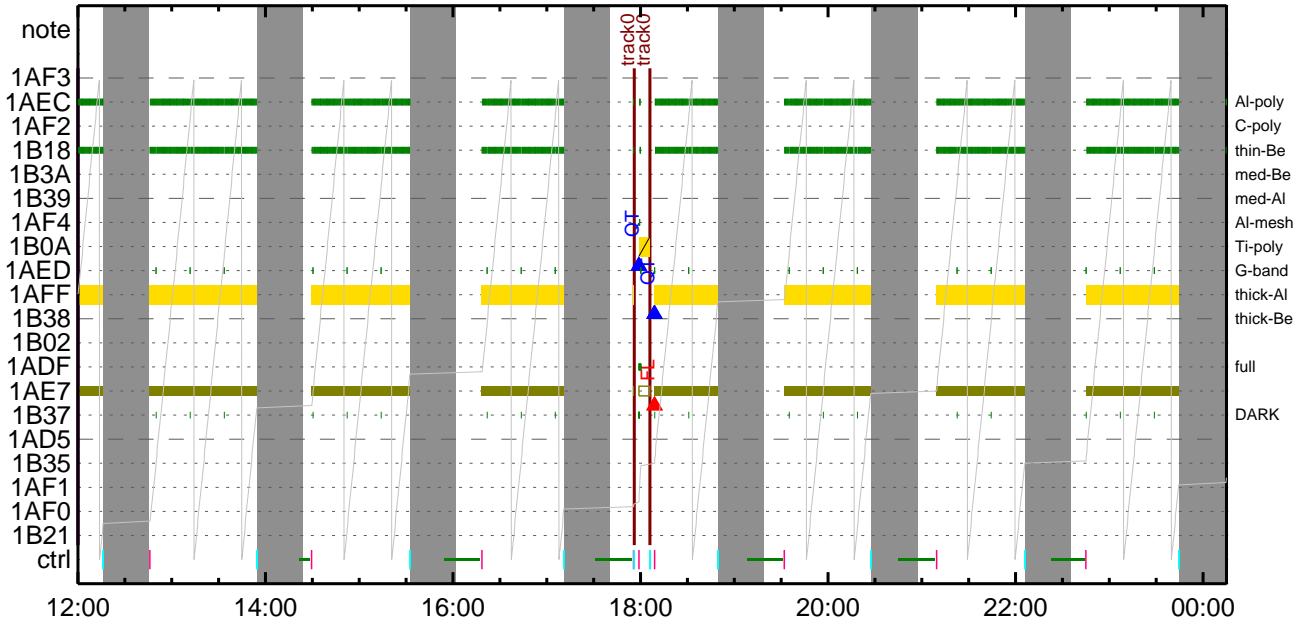
CMDI #0960 2016/05/28



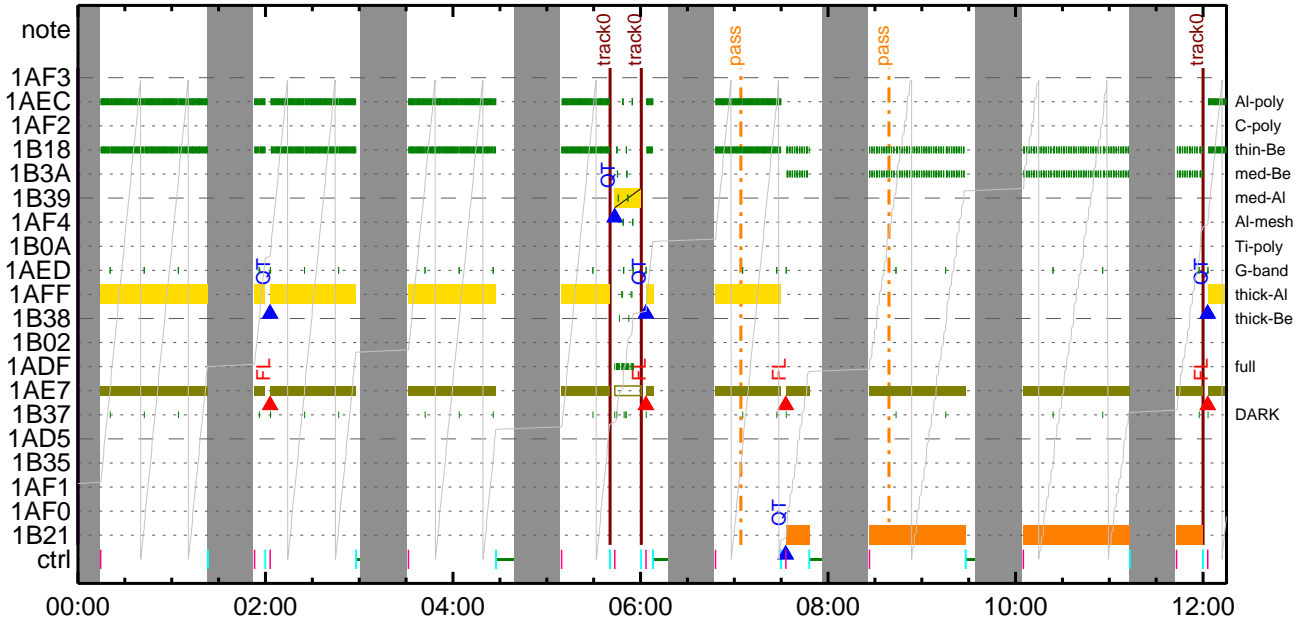
CMDI #0960 2016/05/29



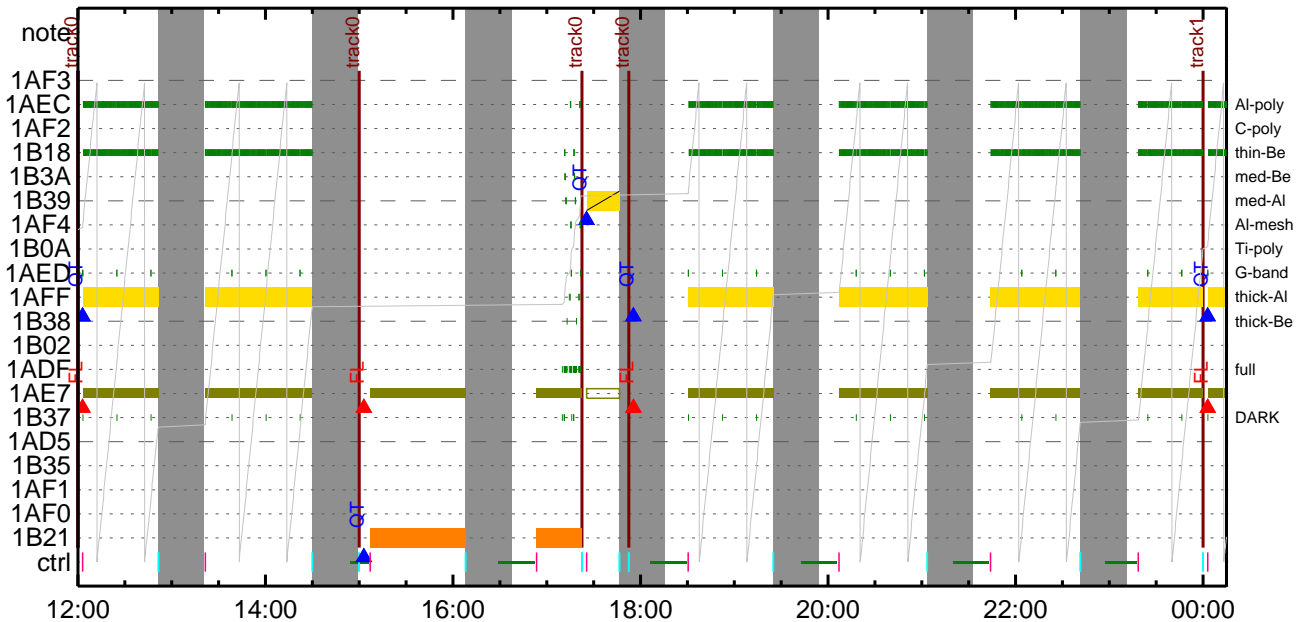
CMDI #0960 2016/05/29



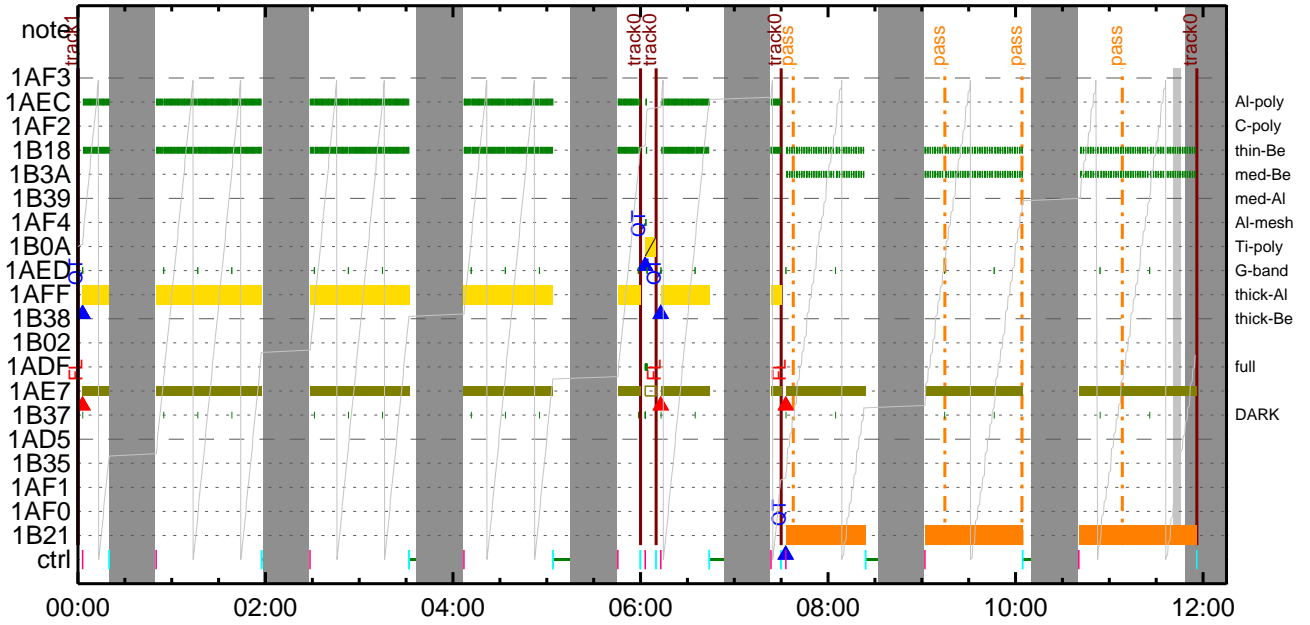
CMDI #0960 2016/05/30



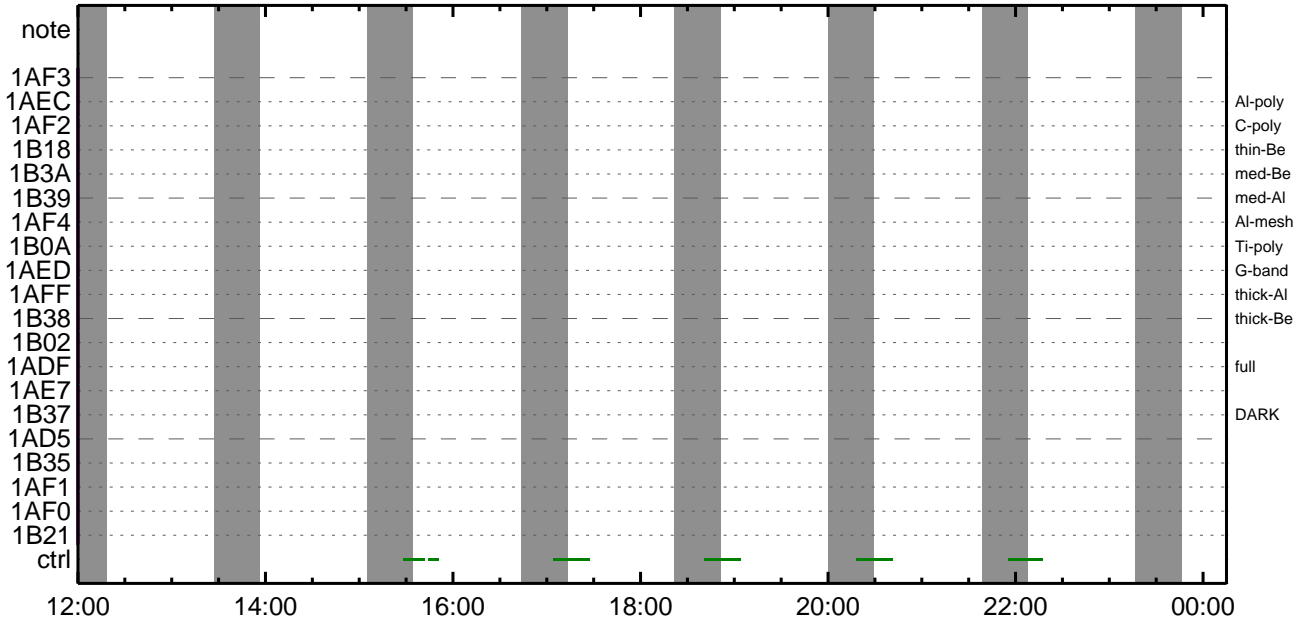
CMDI #0960 2016/05/30



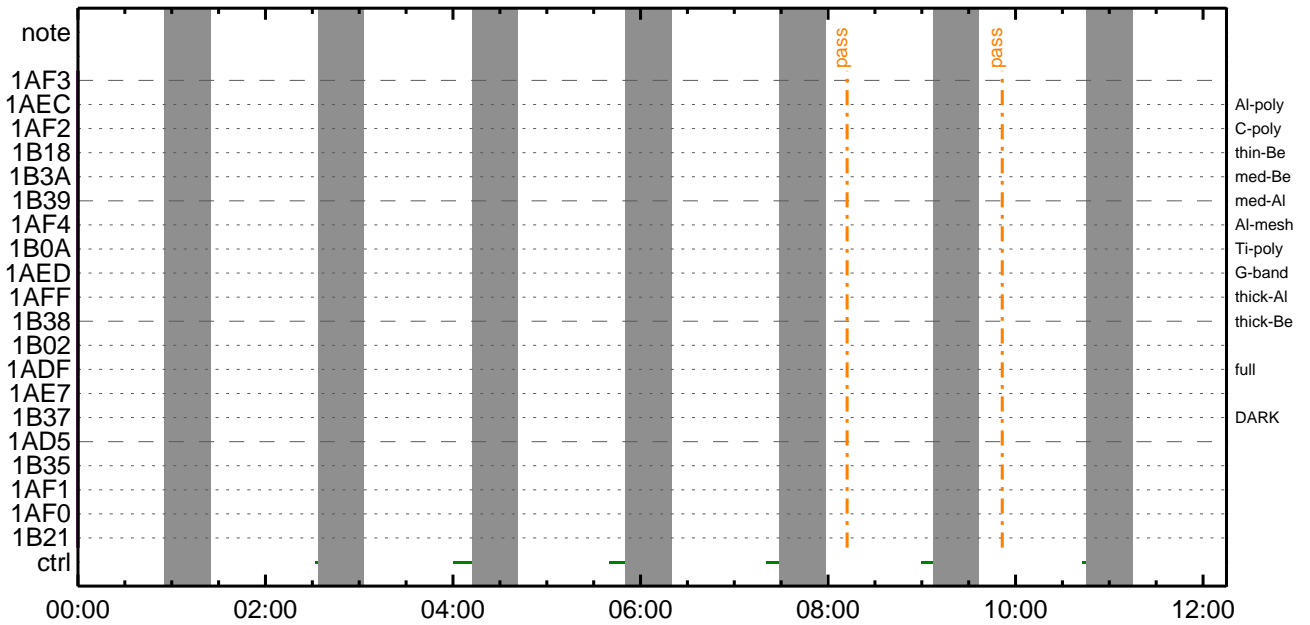
CMDI #0960 2016/05/31



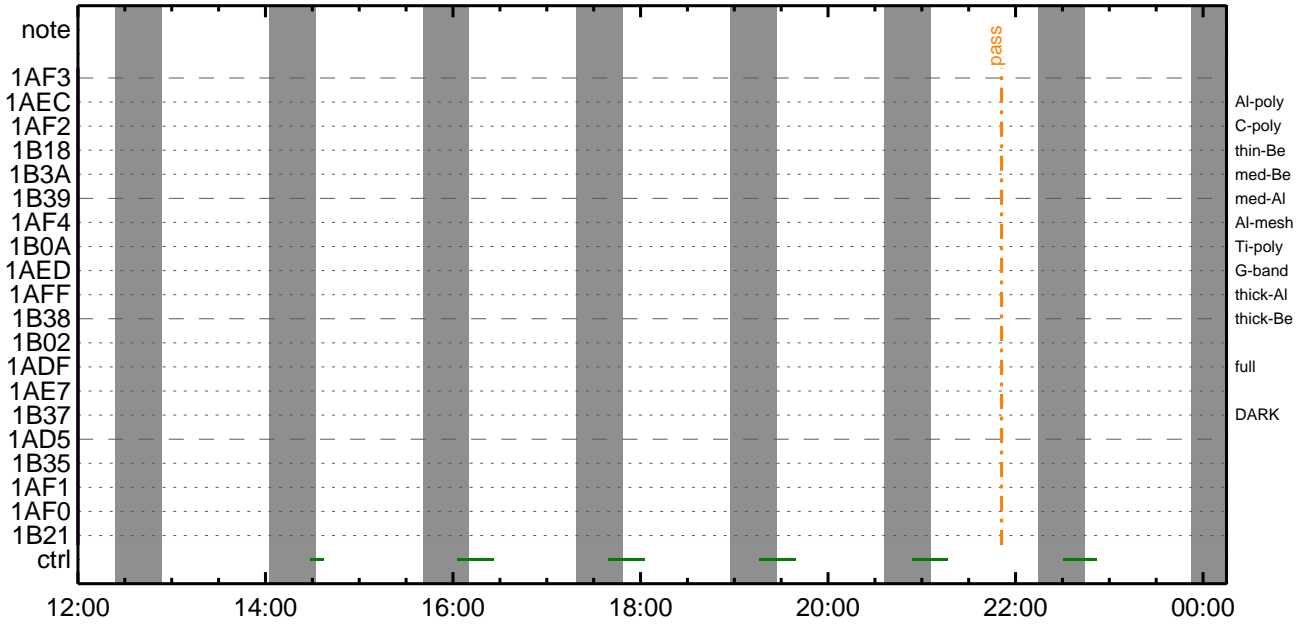
CMDI #0960 2016/05/31



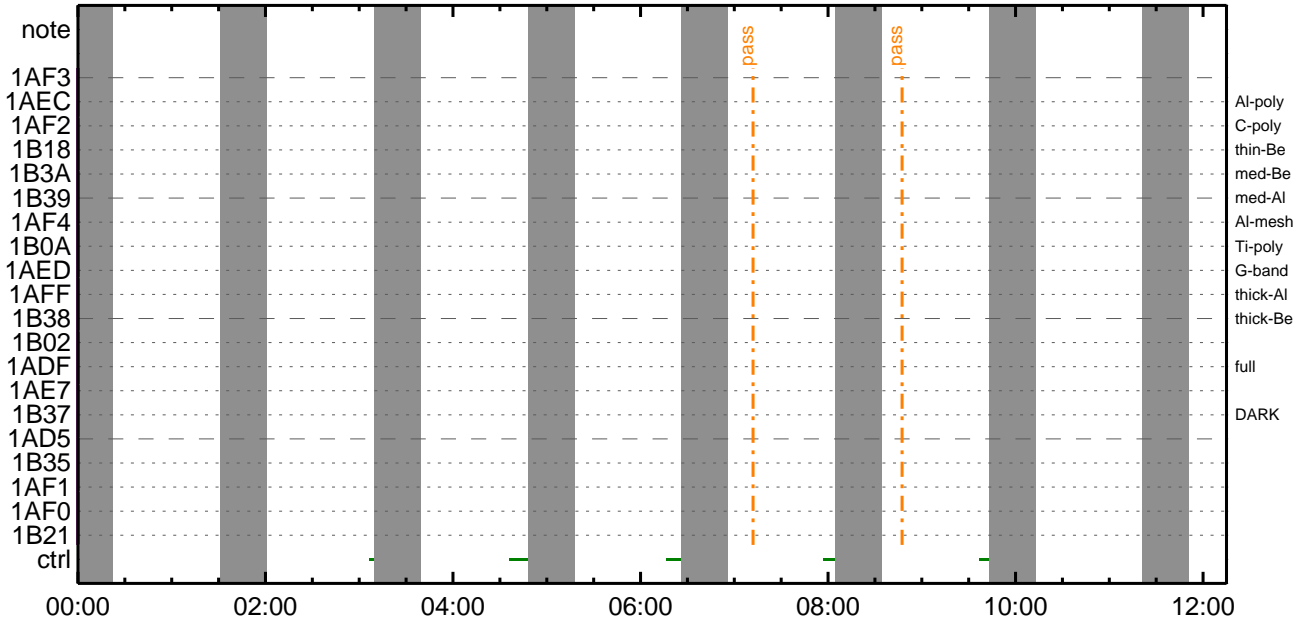
CMDI #0960 2016/06/01



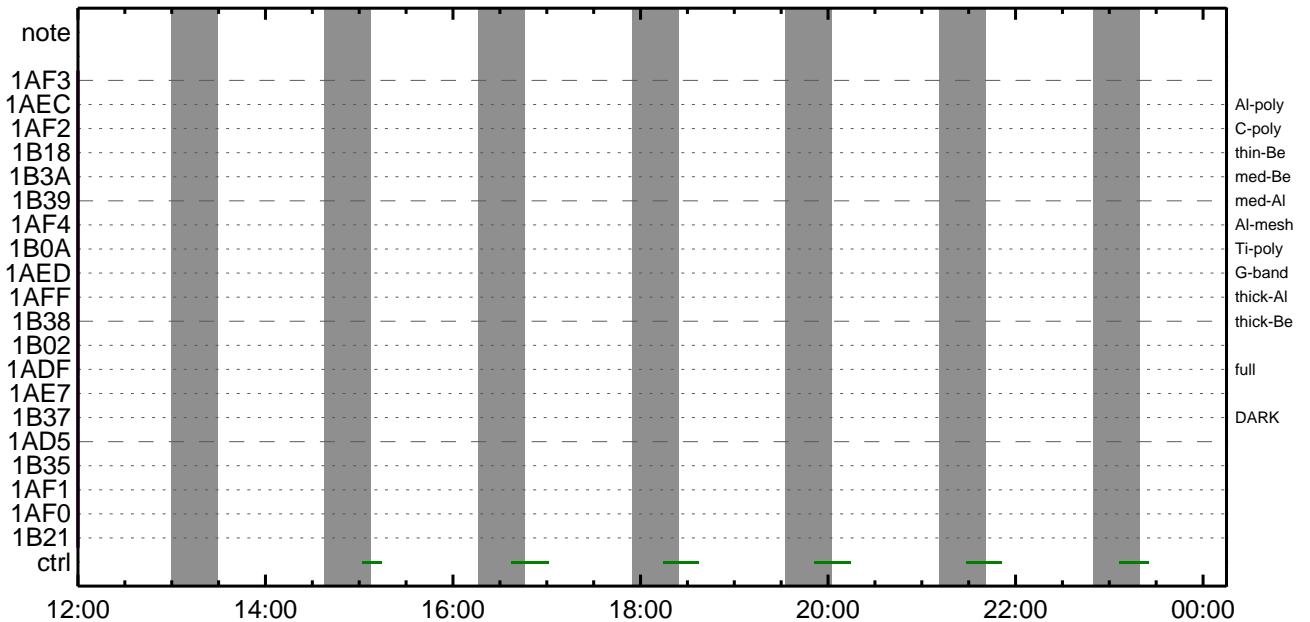
CMDI #0960 2016/06/01



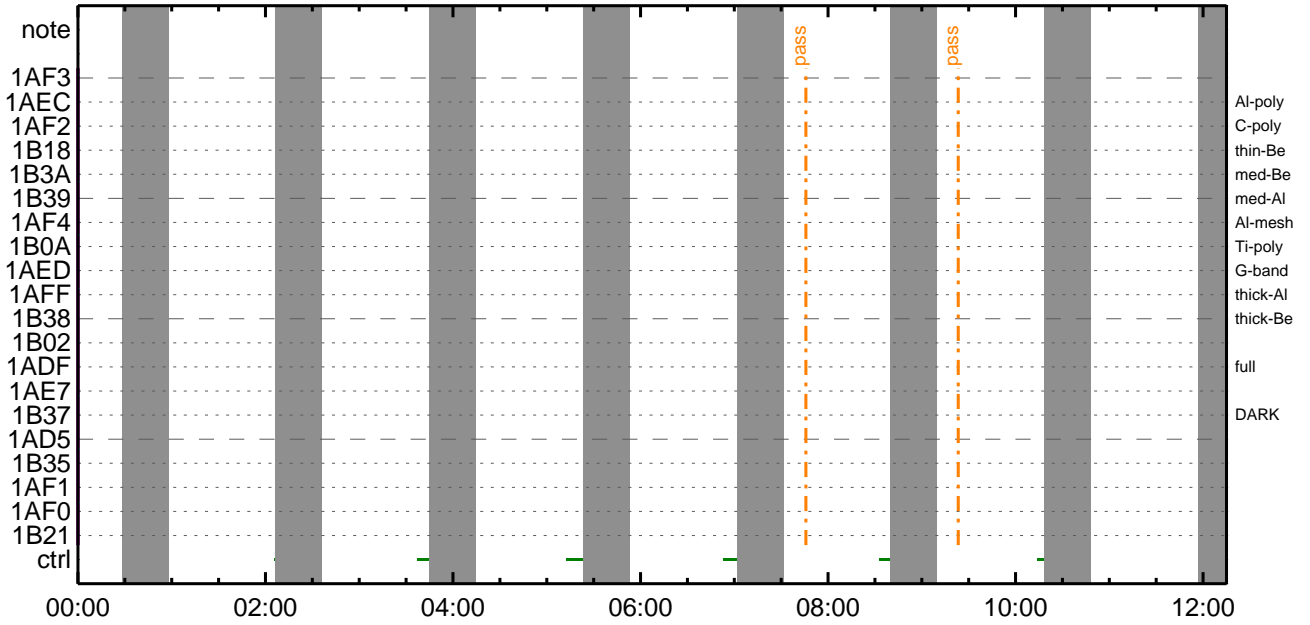
CMDI #0960 2016/06/02



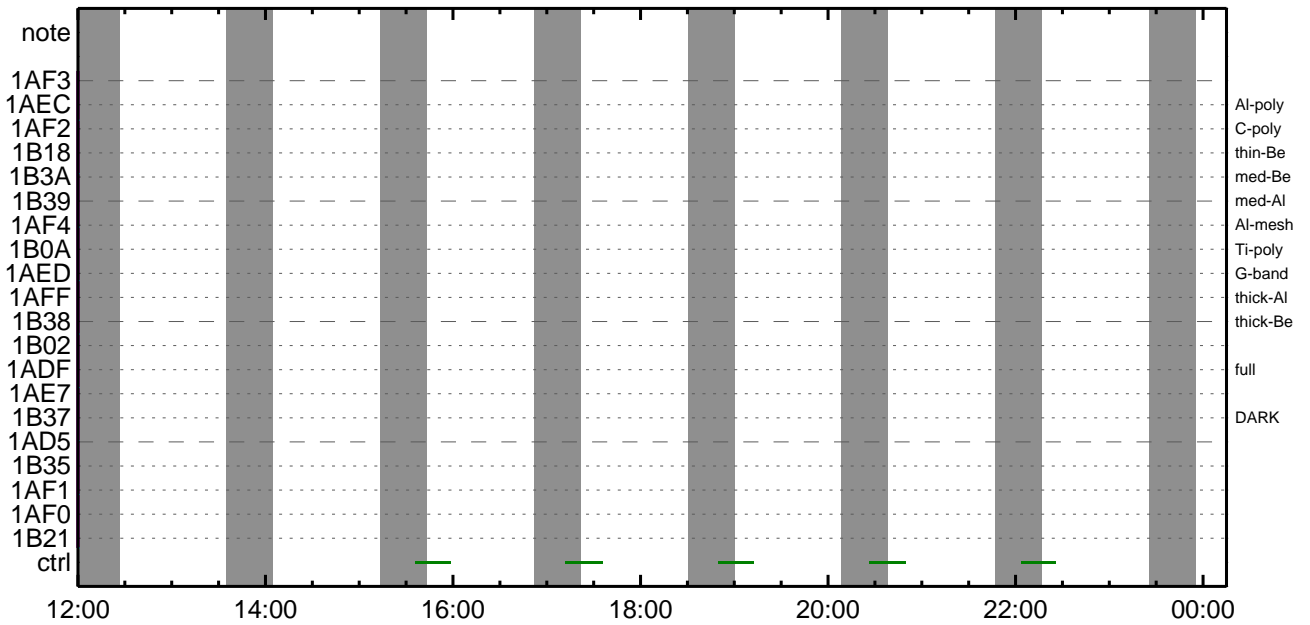
CMDI #0960 2016/06/02



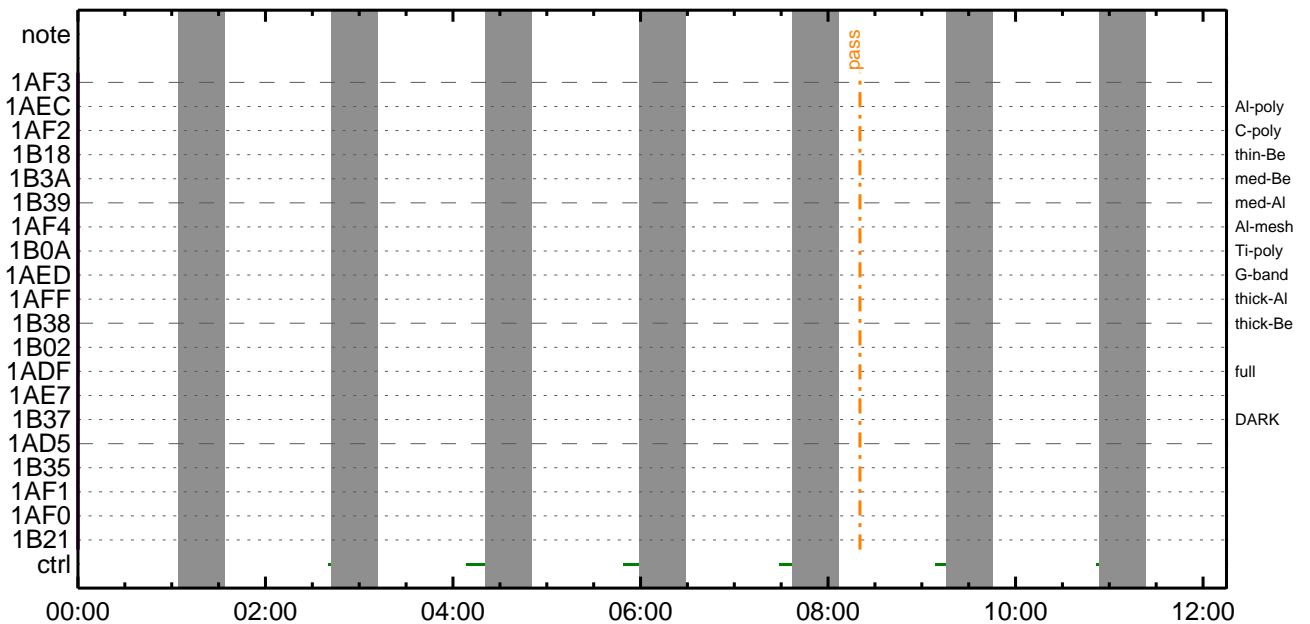
CMDI #0960 2016/06/03



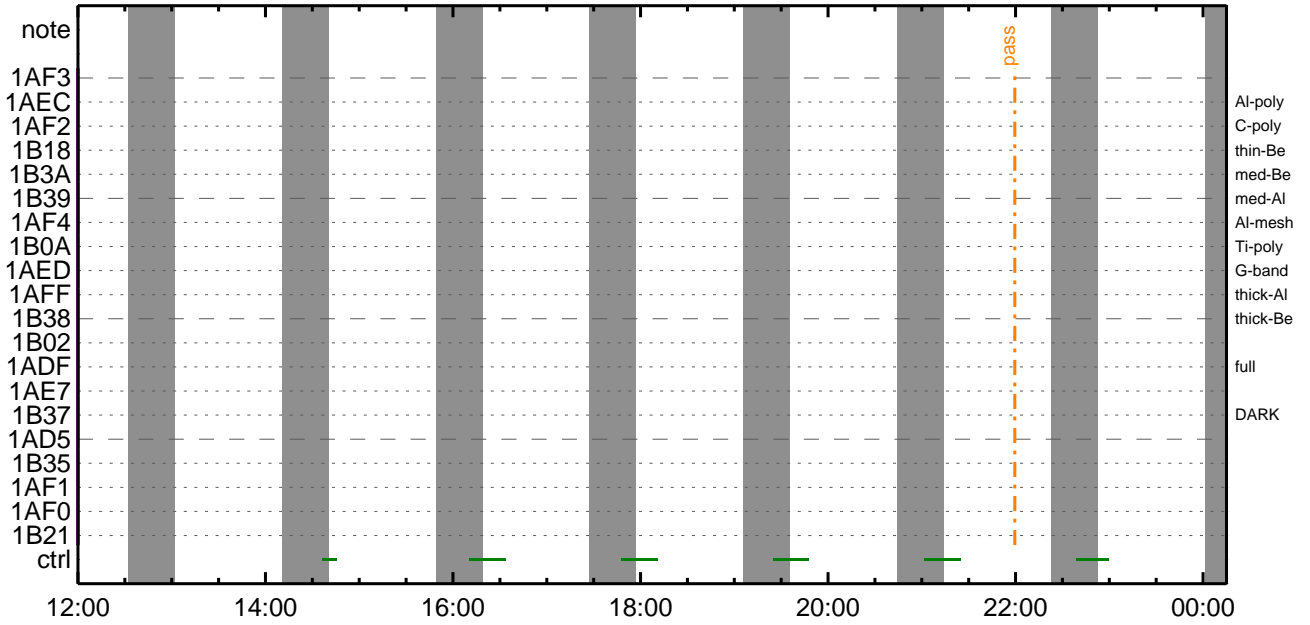
CMDI #0960 2016/06/03



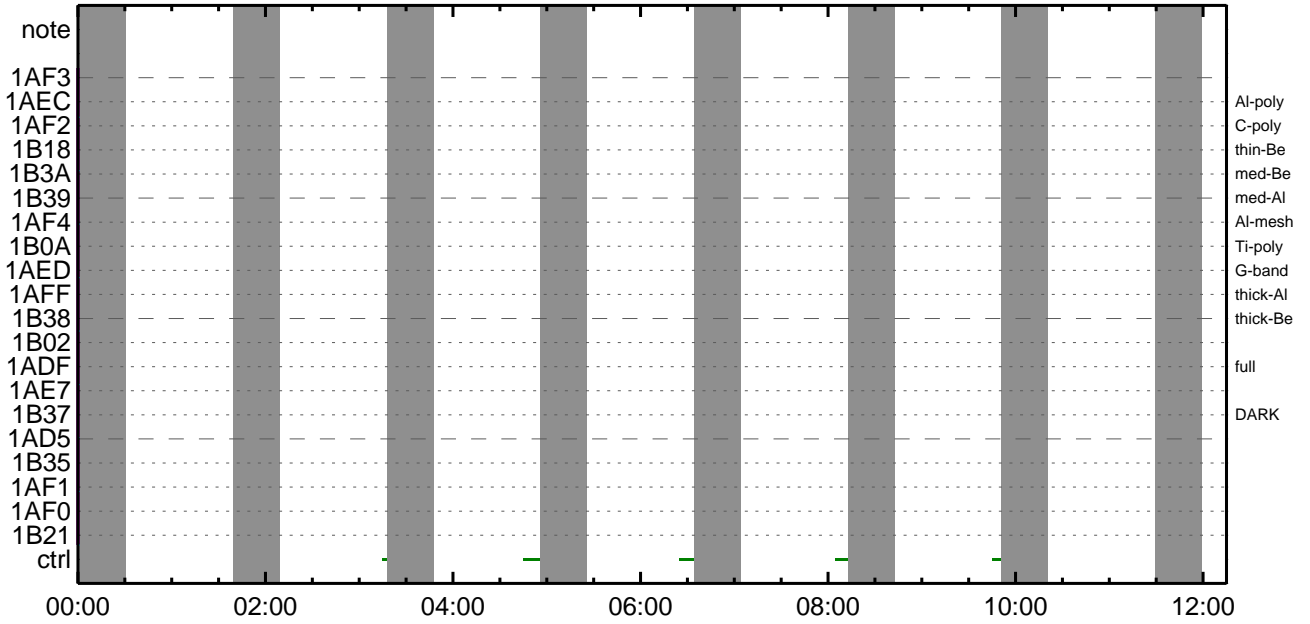
CMDI #0960 2016/06/04



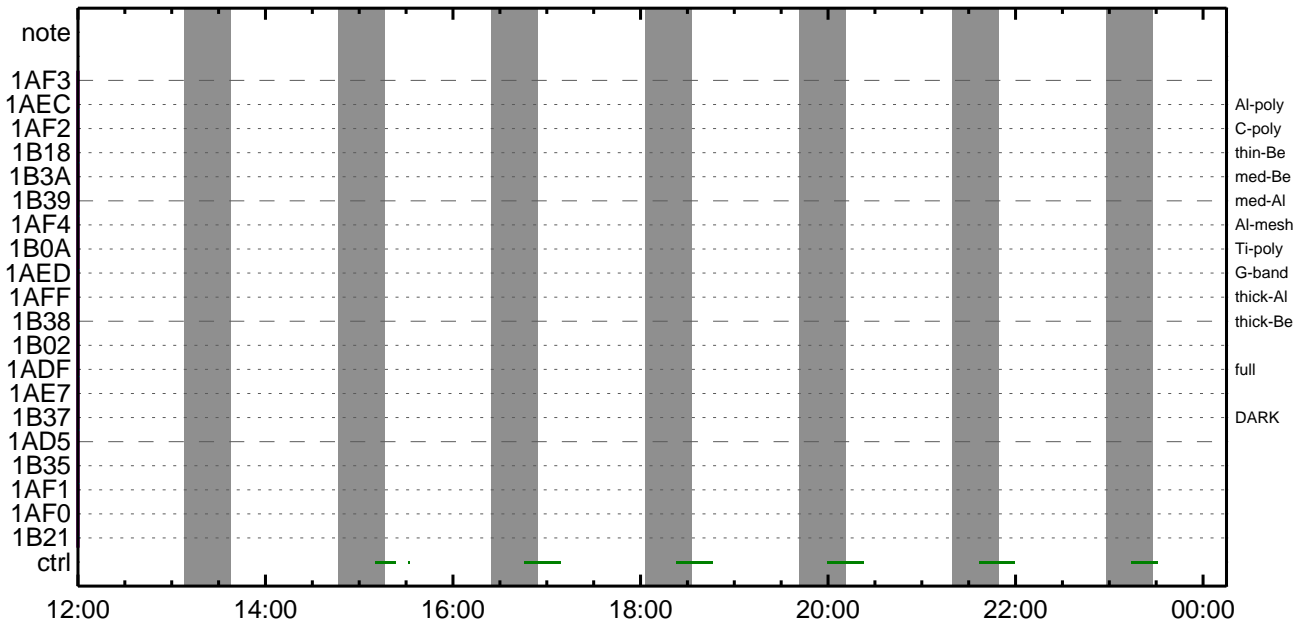
CMDI #0960 2016/06/04



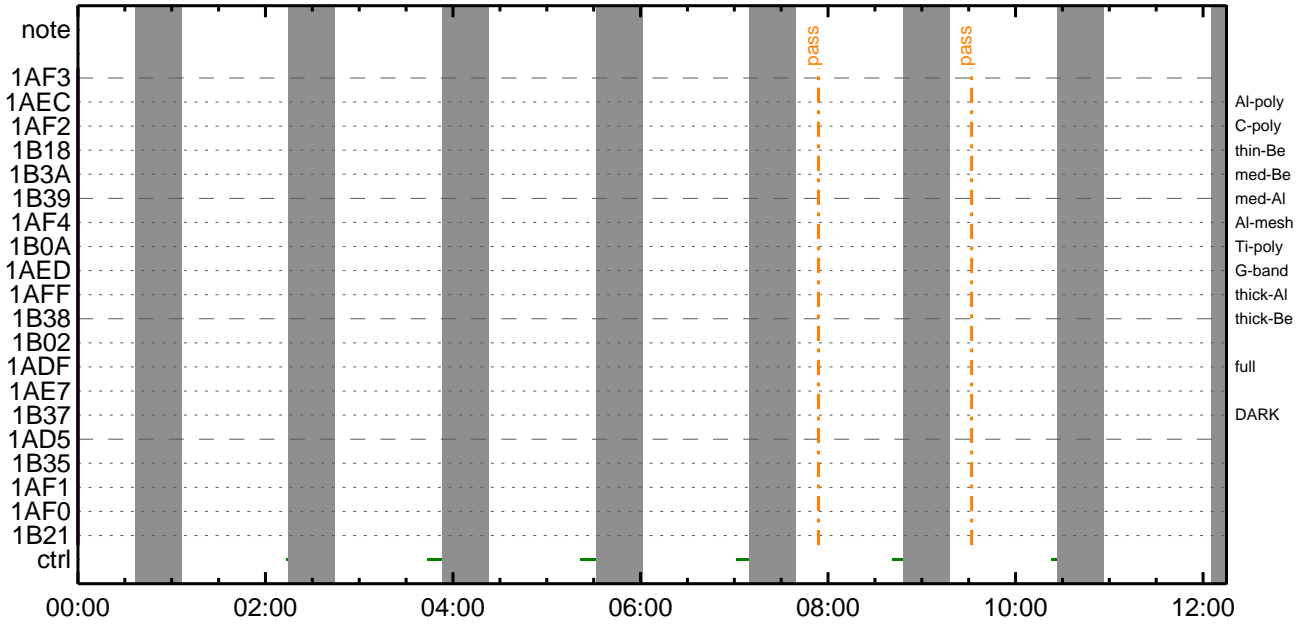
CMDI #0960 2016/06/05



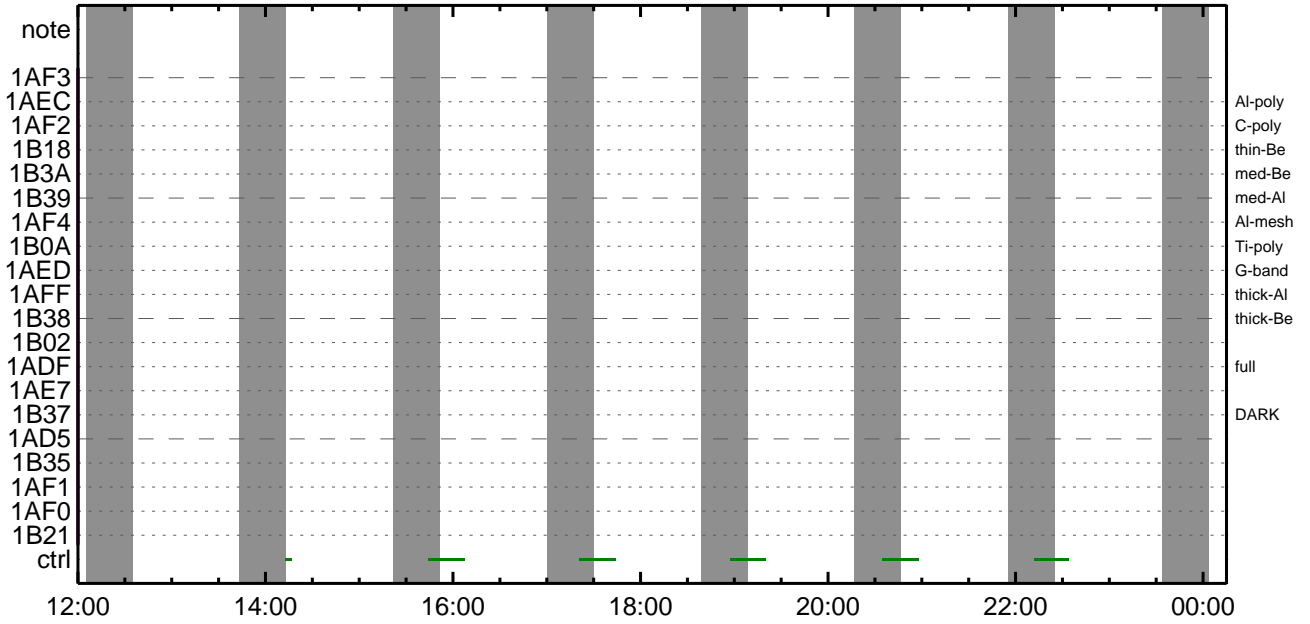
CMDI #0960 2016/06/05



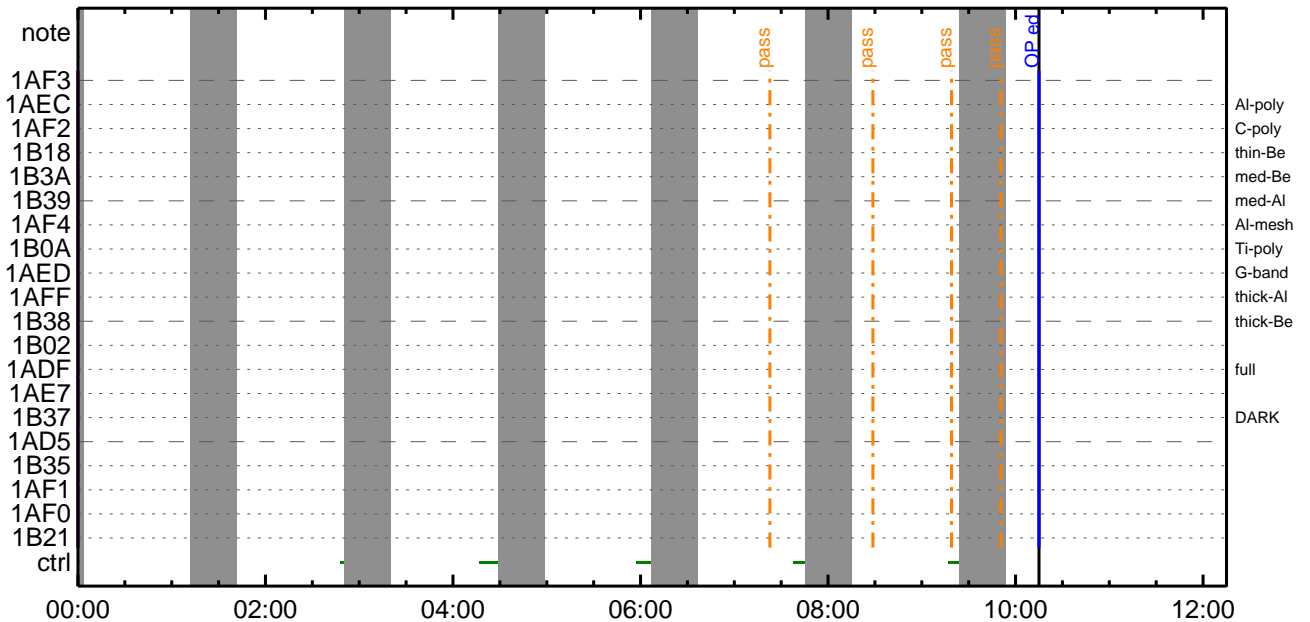
CMDI #0960 2016/06/06



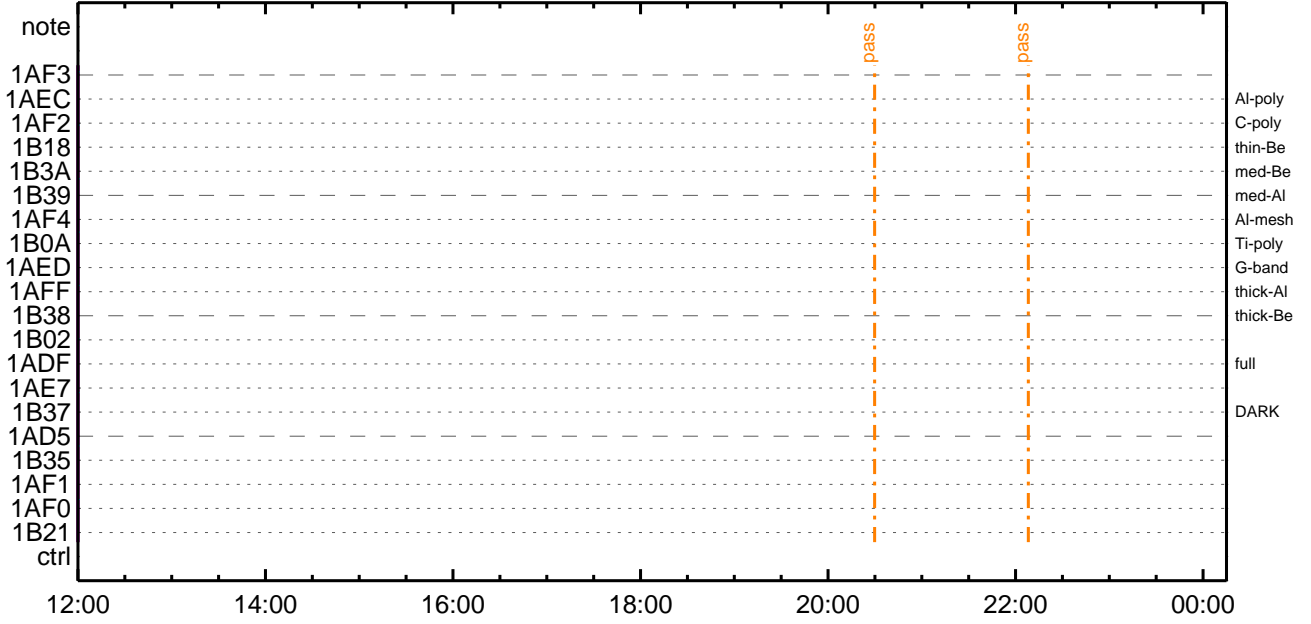
CMDI #0960 2016/06/06



CMDI #0960 2016/06/07



CMDI #0960 2016/06/07




```
0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-884:OP
0104 ( )
0105 S. OG og-884:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGãî¼E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGãî¼E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPãî¼E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** òE²¼òî¼Ã´¶Á°òEÈ-ò°Á÷¿® (¼ãµ-YAYOXx½ªî»ò³îÇ§) *****
0167 C. DHUYã;¼YE;È¼Y½, Y;¼YE;Èòîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOADã-Á÷¿®NGãî¼E¹ç;ç°E²¼òî¼TI-CMDÁ÷¿®ãî¼E¹ò°•òEòòò³òE;f
0180 C. òEò¿;çSETòEDUMPAîE±°iYNY¹ç¹òã|ò³òE;f
0181 C.
0182 C. TIY³YpYóYEòòÁDî¿¿(UT)
0183 +. TI 2016-05-28 10:15:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2016-05-28 10:15:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2016-05-28 10:15:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
```

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0194 C.
0195 +. TI 2016-05-28 10:19:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.      çç[HK1_TI_CMD_NUM]      EQ      1COUNTUP
0198 C.
0199 C.      °Ê²¼õîÄë%îíñõîî¥Ä¥§¥Ä¥-¹àîü
0200 C.      çç[HK1_TI_CMD_ENA/DIS]    EQ      ENA
0201 C.      çç[HK1_TI_CMD_NUM]      EQ      4
0202 C.      çç[HK1_NEXT_EXEC_PIM]    EQ      DHU
0203 C.      çç[HK1_NEXT_EXEC_DC]    EQ      0xB3
0204 C.
0205 C.      *****
0206 C.      TIîî°è¥Ä¥Ö¥×
0207 C.      *****
0208 C.
0209 C.      TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.      çç[HK1_DMP_TOP_ADRS_1]    EQ      07
0213 C.      çç[HK1_DMP_TOP_ADRS_0]    EQ      2B
0214 C.      çç[HK1_DMP_BLOCK_NUM]    EQ      3
0215 C.      çç[HK1_DMP_REPEAT_NUM]   EQ      0
0216 C.      çç[HK1_DMA_DMP_PIM]      EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.      çç[HK1_PKT_FORM_NO]      EQ      7
0220 C.      çç[HK1_PKT_GEN_TIME]     EQ      0.25 s
0221 C.      çç[HK1_S_TLM_BIT_RATE]   EQ      32k
0222 C.      çç[HK1_X_TLM_BIT_RATE]   EQ      4M
0223 C.      çç[HK1_DMP_CHK_FLG]     EQ      EXEC
0224 C.
0225 C.      ¥Ä¥Ö¥×½ªî»õ³îÇ§
0226 C.      çç[HK1_DMP_CHK_FLG]     EQ      NON
0227 C.
0228 C.      RAM ID=TI_TBLõîî³Ê¹ç•ë²îOKõ³îÇ§
0229 C.
0230 C.      DHU¥â;¼¥É;Ê¼¥½. ¥î;¼¥É;Êõõîãõ¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.      çç[HK1_PKT_FORM_NO]      EQ      2
0234 C.      çç[HK1_PKT_GEN_TIME]     EQ      0.5S
0235 C.      çç[HK1_S_TLM_BIT_RATE]   EQ      32K
0236 C.      çç[HK1_X_TLM_BIT_RATE]   EQ      4M
0237 C.
0238 C.      *****
0239 C.      SOT TI command set
0240 C.      *****
0241 C.      Execute, after the success of OP upload.
0242 +. TI 2016-05-28 10:19:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C.      -----
0246 C.      HK1_TI_CMD_NUM      = 1 CNTUP [ ]
0247 C.      -----
0248 C.      ***** SOT END *****
0249 C.      Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C.      ***** Start EIS operation (TI set) *****
0253 C.      Execute, after the success of OP upload.
0254 C.      Set EIS TI-commands
0255 +. TI 2016-05-28 10:19:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2016-05-28 10:19:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.      [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0262 C.      ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C.      ***** XRT START *****
0267 C.      Execute, after the success of OP upload.
0268 +. TI 2016-05-28 10:19:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.      [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0272 C.
0273 C.      ***** XRT END *****
0274 C.
0275 C.      ***** MDP ´ûÃîãî»õ¼ÝõÊÄõ¹õêDCBC•x²è *****
0276 C.      (¼ã°î¥Ö¥Ä¥É¥½¥É¥á¥ç¥èõÊ¼õõ¼Ã»Ûõ¹õè)
0277 C.      S. DC-BC dcbc-402:DCBC
0278 C.      (MDP_known_event)
0279 C.
0280 C.
0281 C.      ***** ¥Ð¥¹•î Daily±çîñõÊ´õõ¹õêDCBC•x²è *****
0282 C.      S. DC-BC dcbc-153:DCBC
0283 C.      (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C.      ;ãLOS¥Ä¥§¥Ä¥-¼Ã»Û;ã
0287 C.
0288 C.      ***** LOS *****
0289 C.

```



```
0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_CHG_ENA
0131 BC (20)
0132 . C. Verify EIS_MODE_CHG_FLG is ENA
0133 +. DC 07-FC EIS_MODE_MANU
0134 BC (21 02)
0135 . C. Verify EIS in MANUAL mode
0136 . C. Estimated OBSTBL upload time is 1m36s
0137 C. *****
0138 C. EIS START OBSTBL LOAD
0139 C. *****
0140 . S. RAM ram-820:EIS_OBSTBL
0141 ( )
0142 +. DC 07-FC EIS_DUMP_OBSTBL
0143 BC (07 07 07 00 00 70 00)
0144 C.
0145 C. Execute, after the success of OBSTBL upload.
0146 C. Set EIS TI-commands
0147 +. TI 2016-05-28 10:19:50.0
0148 DC 07-FC EIS_MODE_CHG_ENA
0149 BC (20)
0150 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0151 C. *****
0152 C. EIS END OBSTBL LOAD
0153 C. *****
0154 C.
0155 . C. ***** MDP 'ûÃîâî»ö¼ÝðËÄð¹ñèDCBC•x²è *****
0156 C. (¼ã°îÝÖÝÄÝËÝÞÝËÝáÝçÝèñË¼ã¼Ä»Û¹ñè)
0157 . S. DC-BC dcbc-402:DCBC
0158 (MDP_known_event)
0159 C.
0160 C.
0161 . C. ***** ÝÐÝ¹•Ï Daily±¿ÎÑñË'Ø¹ñèDCBC•x²è *****
0162 . S. DC-BC dcbc-153:DCBC
0163 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164 C.
0165 C.
0166 . C. ;ãLOSÝÄÝ§ÝÄÝ¹¼Ä»Û;ã
0167 C.
0168 . C. ***** LOS *****
0169 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-886 2016-05-28 13:27:48 132 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;äAOSYÁYSYÄY-¼Ä»Ü;ä
0005 C.
0006 C. YÄYB;¼Y³YFYÖYÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. ÁíÈ¿αΔβ•μ°ξ»Í×ΑÇρîYçYÄY×Yí;¼YÉ;ÈÈε%μ•íÉ;ÈεÈ¼°ÇÔα•α¿%î¹çαí;çÄ®, ùα¹αèαβαÇÄ+¿®α•αÈααα³αÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop SP table >
0018 +. DC 07-F0 MDP_SP_CTRL_MANU
0019 BC (61)
0020 C. -----
0021 C. MDP_SP_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload SP Observation Table>
0025 . S. RAM ram-281:MDP_OBS_S
0026 ()
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_S >
0029 +. DC 07-F0 MDP_DUMP_SPTBL
0030 BC (83 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_S verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2016-05-28 10:19:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 +. DC 07-F0 MDP_XRT_CTRL_MANU
0052 BC (c1)
0053 +. DC 07-F0 MDP_XRT_MODE_STBY
0054 BC (c3)
0055 . C. ----- Success Verify ? OK / NG ____
0056 C.
0057 C. XRT Obs. Table Upload
0058 . S. RAM ram-291:MDP_OBS_X
0059 ()
0060 C.
0061 +. DC 07-F0 MDP_DUMP_XRTTBL
0062 BC (84 07 00 00 00 3a d4)
0063 . C. ----- Comparison Check ? OK / ERR ____
0064 C.
0065 C.
0066 +. DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 01 b1 b1 04 04)
0068 +. DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 02 b1 b1 08 08)
0070 +. DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 03 b1 b1 08 08)
0072 +. DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 04 b1 b1 06 06)
0074 +. DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 06 80 80 20 20)
0076 +. DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 07 80 80 20 08)
0078 +. DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 08 80 80 08 20)
0080 +. DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 09 85 83 06 06)
0082 +. DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 0a 80 60 20 18)
0084 +. DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0b a0 80 18 20)
0086 +. DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 0f 80 80 06 06)
0088 +. DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 10 80 80 08 08)
0090 +. DC 07-F0 MDP_XRT_FLD_ENA
0091 BC (d8)
0092 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0093 BC (c8)
0094 +. DC 07-F0 MDP_XRT_ARS_DIS
0095 BC (d5)
```

0096 +. DC 07-F0 MDP_XRT_AEC_RESET
0097 BC (d0)
0098 +. DC 07-F0 MDP_XRT_FLD_RESET
0099 BC (da)
0100 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0101 BC (c4 0d)
0102 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0103 BC (c5 07)
0104 . C. ----- Success Verify ? OK / NG ____
0105 C.
0106 C.
0107 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0108 C.
0109 +. DC 07-F0 MDP_XRT_MODE_OBSV
0110 BC (c2)
0111 +. TI 2016-05-28 10:19:02.0
0112 DC 07-F0 MDP_XRT_MODE_OBSV
0113 BC (c2)
0114 . C. ----- Success Verify ? OK / NG ____
0115 C.
0116 C. ***** XRT END *****
0117 C.
0118 . C. ***** MDP `ûÃîñî»ö¼ÿñÊÂðñ¹ñèDCBC•x²è *****
0119 C. (¼ã°îÿÓÿÃÿËÿPÿËÿãÿçÿèñ¼ññ¼Ã»Ûñ¹ñè)
0120 . S. DC-BC dcbc-402:DCBC
0121 (MDP_known_event)
0122 C.
0123 C.
0124 . C. ***** ÿDÿ¹•î Daily±;îññè'øñ¹ñèDCBC•x²è *****
0125 . S. DC-BC dcbc-153:DCBC
0126 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0127 C.
0128 C.
0129 . C. ;ãLOSÿÃÿSÿÿÃÿ-¼Ã»Û;ã
0130 C.
0131 . C. ***** LOS *****
0132 C.

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*** OP Sequence for XRT ***

2016/05/28	10:21:00.5	XRT_CTRL_MANU_416_OG [0x1a0]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/28	10:21:10.5	XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]							
		XRT_FOCUS_RECAL	2	07-F8	78	00			
2016/05/28	10:25:10.5	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2016/05/28	10:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/28	10:29:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/28	10:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2016/05/28	10:30:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04	00	00	00	00
2016/05/28	10:30:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/05/28	10:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/05/28	10:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/05/28	10:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/05/28	10:30:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/05/28	10:32:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2016/05/28	10:32:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2016/05/28	10:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/05/28	11:40:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/28	11:40:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/28	11:40:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/05/28	11:40:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/05/28	11:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/05/28	12:09:30.0	XRT_Custom_430_OG [0x1ae]							
2016/05/28	12:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/05/28	13:18:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/28	13:18:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/28	13:18:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/05/28	13:18:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/05/28	13:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/05/28	13:47:30.0	XRT_Custom_430_OG [0x1ae]							
2016/05/28	13:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/05/28	14:57:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/28	14:57:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/28	14:57:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/05/28	14:57:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/05/28	15:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/05/28	15:33:30.0	XRT_Custom_430_OG [0x1ae]							
2016/05/28	15:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/05/28	15:38:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/28	15:38:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/28	15:38:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/05/28	15:38:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/05/28	15:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/05/28	15:42:30.0	XRT_Custom_430_OG [0x1ae]							
2016/05/28	15:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/05/28	15:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/28	15:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/28	15:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2016/05/28	16:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/05/28	16:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							

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2016/05/28	16:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/05/28	16:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/05/28	16:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/05/28	16:02:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02			
2016/05/28	16:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2016/05/28	16:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/05/28	16:35:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/28	16:35:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/28	16:35:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/05/28	16:35:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/05/28	16:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/05/28	17:19:30.0	XRT_Custom_430_OG [0x1ae]								
2016/05/28	17:20:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/05/28	17:59:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/28	17:59:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/28	17:59:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2016/05/28	17:59:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00	00
2016/05/28	17:59:48.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/05/28	18:02:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/05/28	18:02:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/05/28	18:02:28.0	XRT_QT_PROG_SET_418_OG [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2016/05/28	18:02:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/05/28	18:09:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/28	18:09:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/28	18:09:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2016/05/28	18:09:30.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04	00	00	00	00
2016/05/28	18:09:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/05/28	18:09:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/05/28	18:09:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/05/28	18:09:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/05/28	18:09:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/05/28	18:12:26.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02			
2016/05/28	18:12:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2016/05/28	18:12:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/05/28	18:14:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/28	18:14:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/28	18:14:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/05/28	18:14:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/05/28	18:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/05/28	18:56:00.0	XRT_Custom_430_OG [0x1ae]								
2016/05/28	18:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/05/28	19:52:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/28	19:52:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/05/28	19:52:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/05/28	19:52:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/05/28	19:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/05/28	20:33:01.0	XRT_Custom_430_OG [0x1ae]								
2016/05/28	20:34:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]								

2016/05/28	21:30:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/28	21:30:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/28	21:30:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/05/28	21:30:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/05/28	21:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/05/28	21:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/28	21:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/28	21:59:58.0	XRT_FOCUS_POSITION_438_OG [0x1b6]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2016/05/28	22:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 ad 59 00 00		
2016/05/28	22:00:18.0	XRT_FLD_DIS_440_OG [0x1b8]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2016/05/28	22:14:54.0	XRT_FLRCTRL_DIS_441_OG [0x1b9]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2016/05/28	22:14:56.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/05/28	22:14:58.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13		
2016/05/28	22:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/05/28	23:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/28	23:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/28	23:59:58.0	XRT_FOCUS_POSITION_438_OG [0x1b6]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2016/05/29	00:00:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 00 00 56 35		
2016/05/29	00:00:18.0	XRT_FLD_DIS_440_OG [0x1b8]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2016/05/29	00:14:54.0	XRT_FLRCTRL_DIS_441_OG [0x1b9]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2016/05/29	00:14:56.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/05/29	00:14:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c		
2016/05/29	00:15:00.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/05/29	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/29	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/29	01:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2016/05/29	02:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04 00 00 00 00		
2016/05/29	02:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2016/05/29	02:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/05/29	02:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/05/29	02:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/05/29	02:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/05/29	02:02:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b		
2016/05/29	02:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07		
2016/05/29	02:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/05/29	02:23:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/29	02:23:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/29	02:23:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/05/29	02:23:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/05/29	02:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/05/29	02:54:30.0	XRT_Custom_430_OG [0x1ae]						
2016/05/29	02:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/05/29	03:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/29	03:51:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/29	03:51:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/05/29	03:51:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/05/29	03:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						

2016/05/29	04:33:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/29	04:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2016/05/29	05:31:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/29	05:31:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/29	05:31:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/29	05:31:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/29	05:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/05/29	06:11:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/29	06:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2016/05/29	06:16:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/29	06:16:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/29	06:16:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/29	06:16:30.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2016/05/29	06:16:48.0	XRT_FLD_DIS_406_OG [0x196]	AOCS_Ore-point_Start_2_OG [0x098]	5	02-76	00 00 00 00 00	
2016/05/29	06:19:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	AOCU_NM	1	07-F0	d9	
2016/05/29	06:19:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2016/05/29	06:19:28.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2016/05/29	06:19:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/05/29	06:26:24.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_QT_PROG_SET_439_OG [0x1b7]	2	07-F0	c4 0d	
2016/05/29	06:26:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d	
2016/05/29	06:26:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/29	06:26:30.0	AOCS_Ore-point_Start_1_OG [0x097]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/29	06:26:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/29	06:26:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	XRT_FOCUS_POSITION_410_OG [0x19a]	4	07-F8	22 fe 97 00	
2016/05/29	06:26:52.0	XRT_AEC_RESET_448_OG [0x1c0]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2016/05/29	06:26:54.0	XRT_ARS_DIS_423_OG [0x1a7]	AOCS_Ore-point_Start_1_OG [0x097]	5	02-76	04 00 00 00 00	
2016/05/29	06:26:56.0	XRT_FLD_RESET_433_OG [0x1b1]	AOCU_NM	1	07-F0	d8	
2016/05/29	06:29:26.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/05/29	06:29:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/05/29	06:29:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/05/29	07:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/05/29	07:12:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/29	07:12:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b	
2016/05/29	07:12:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2016/05/29	07:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/29	07:50:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/29	07:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/29	08:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/29	08:51:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/29	08:51:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/29	08:51:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/29	08:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/29	09:28:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/05/29	09:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/29	10:33:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/29	10:33:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/29	10:33:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/29	10:33:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/29	10:33:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/29	10:33:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	

2016/05/29	10:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/29	11:06:30.0	XRT_Custom_430_OG [0x1ae]				
2016/05/29	11:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/29	12:16:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/29	12:16:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/29	12:16:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/29	12:16:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/29	12:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/29	12:45:00.0	XRT_Custom_430_OG [0x1ae]				
2016/05/29	12:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/29	13:54:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/29	13:54:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/29	13:54:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/29	13:54:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/29	13:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/29	14:28:30.0	XRT_Custom_430_OG [0x1ae]				
2016/05/29	14:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/29	15:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/29	15:32:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/29	15:32:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/29	15:32:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/29	15:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/29	16:17:30.0	XRT_Custom_430_OG [0x1ae]				
2016/05/29	16:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/29	17:11:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/29	17:11:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/29	17:11:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/29	17:11:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/29	17:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/29	17:54:30.5	XRT_Custom_430_OG [0x1ae]				
2016/05/29	17:55:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/29	17:55:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/29	17:55:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/29	17:55:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/05/29	17:56:00.0	AOCS_Orе-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00
2016/05/29	17:56:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9
2016/05/29	17:58:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/05/29	17:58:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/29	17:58:58.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2016/05/29	17:59:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/29	18:05:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/29	18:05:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/29	18:05:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/05/29	18:06:00.0	AOCS_Orе-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00 f0 e6 b0 fe
2016/05/29	18:06:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/05/29	18:06:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/05/29	18:06:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/05/29	18:06:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/29	18:06:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da

2016/05/29	18:08:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b
2016/05/29	18:08:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2016/05/29	18:09:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/29	18:49:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/29	18:49:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/29	18:49:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/29	18:49:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/05/29	18:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/29	19:31:00.5	XRT_Custom_430_OG [0x1ae]					
2016/05/29	19:32:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/29	20:27:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/29	20:27:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/29	20:27:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/29	20:27:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/05/29	20:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/29	21:08:30.0	XRT_Custom_430_OG [0x1ae]					
2016/05/29	21:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/29	22:06:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/29	22:06:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/29	22:06:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/29	22:06:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/05/29	22:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/29	22:44:00.0	XRT_Custom_430_OG [0x1ae]					
2016/05/29	22:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/29	23:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/29	23:44:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/29	23:44:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/29	23:44:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/05/29	23:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/30	00:13:30.0	XRT_Custom_430_OG [0x1ae]					
2016/05/30	00:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/30	01:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/30	01:23:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/30	01:23:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/30	01:23:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/05/30	01:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/30	01:52:00.0	XRT_Custom_430_OG [0x1ae]					
2016/05/30	01:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/30	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/30	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/30	01:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2016/05/30	02:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/05/30	02:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/05/30	02:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/05/30	02:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/05/30	02:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/30	02:02:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b
2016/05/30	02:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2016/05/30	02:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	

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2016/05/30	02:58:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/30	02:58:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/30	02:58:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/05/30	02:58:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/05/30	03:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/05/30	03:30:30.0	XRT_Custom_430_OG [0x1ae]							
2016/05/30	03:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/05/30	04:27:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/30	04:27:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/30	04:27:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/05/30	04:27:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/05/30	04:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/05/30	05:08:30.0	XRT_Custom_430_OG [0x1ae]							
2016/05/30	05:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/05/30	05:40:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/30	05:40:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/30	05:40:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2016/05/30	05:40:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00			
2016/05/30	05:40:48.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2016/05/30	05:43:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2016/05/30	05:43:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/05/30	05:43:28.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f			
2016/05/30	05:43:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/05/30	06:00:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/30	06:00:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/30	06:00:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2016/05/30	06:00:30.0	AOCS_Ore-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00 f0 e6 b0 fe			
2016/05/30	06:00:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2016/05/30	06:00:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2016/05/30	06:00:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2016/05/30	06:00:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/05/30	06:00:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/05/30	06:03:26.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b			
2016/05/30	06:03:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07			
2016/05/30	06:03:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/05/30	06:08:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/30	06:08:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/30	06:08:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/05/30	06:08:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/05/30	06:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/05/30	06:47:00.0	XRT_Custom_430_OG [0x1ae]							
2016/05/30	06:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/05/30	07:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/30	07:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/30	07:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2016/05/30	07:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2016/05/30	07:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2016/05/30	07:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			

2016/05/30	07:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/30	07:30:26.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/30	07:32:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2016/05/30	07:32:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/05/30	07:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/30	07:48:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/30	07:48:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/30	07:48:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/30	07:48:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/30	07:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/30	08:25:30.0	XRT_Custom_430_OG [0x1ae]			
2016/05/30	08:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/30	09:28:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/30	09:28:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/30	09:28:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/30	09:28:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/30	09:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/30	10:04:00.0	XRT_Custom_430_OG [0x1ae]			
2016/05/30	10:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/30	11:13:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/30	11:13:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/30	11:13:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/30	11:13:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/30	11:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/30	11:42:00.0	XRT_Custom_430_OG [0x1ae]			
2016/05/30	11:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/30	11:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/30	11:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/30	11:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/05/30	12:00:00.0	AOCS_OrE-point_Start_6_OG [0x09c]			
		AOCU_NM	5	02-76	00 0a 24 53 74
2016/05/30	12:00:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2016/05/30	12:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/05/30	12:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2016/05/30	12:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2016/05/30	12:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/30	12:02:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2016/05/30	12:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/05/30	12:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/30	12:51:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/30	12:51:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/30	12:51:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/30	12:51:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/30	12:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/30	13:20:30.0	XRT_Custom_430_OG [0x1ae]			
2016/05/30	13:21:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/30	14:30:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/30	14:30:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/30	14:30:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/30	14:30:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]			

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2016/05/30	14:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e8			
2016/05/30	14:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/05/30	14:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/30	14:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/30	15:00:00.0	AOCS_Ore-point_Start_5_OG [0x09b]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2016/05/30	15:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00	f0	e6	b0 fe
2016/05/30	15:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2016/05/30	15:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2016/05/30	15:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2016/05/30	15:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/05/30	15:02:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/05/30	15:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01		
2016/05/30	15:06:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07		
2016/05/30	15:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]						
2016/05/30	16:08:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/05/30	16:08:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/30	16:08:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/30	16:08:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/05/30	16:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/05/30	16:52:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/05/30	16:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]						
2016/05/30	17:22:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/05/30	17:22:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/30	17:22:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/30	17:22:30.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2016/05/30	17:22:48.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00	00	00	00 00
2016/05/30	17:25:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2016/05/30	17:25:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2016/05/30	17:25:28.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/05/30	17:25:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f		
2016/05/30	17:46:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/05/30	17:46:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/30	17:46:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/30	17:46:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/05/30	17:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/05/30	17:52:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/05/30	17:52:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/30	17:52:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/05/30	17:52:30.0	AOCS_Ore-point_Start_6_OG [0x09c]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2016/05/30	17:52:48.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00	0a	24	53 74
2016/05/30	17:52:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2016/05/30	17:52:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2016/05/30	17:52:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2016/05/30	17:52:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/05/30	17:55:26.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/05/30	17:55:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b		
2016/05/30	18:29:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07		
2016/05/30	18:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]						

2016/05/30	19:25:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/30	19:25:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/30	19:25:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/30	19:25:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/05/30	19:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/30	20:06:00.0	XRT_Custom_430_OG [0x1ae]					
2016/05/30	20:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/30	21:03:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/30	21:03:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/30	21:03:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/30	21:03:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/05/30	21:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/30	21:43:00.0	XRT_Custom_430_OG [0x1ae]					
2016/05/30	21:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/30	22:41:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/30	22:41:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/30	22:41:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/30	22:41:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/05/30	22:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/30	23:17:30.0	XRT_Custom_430_OG [0x1ae]					
2016/05/30	23:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/30	23:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/30	23:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/30	23:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2016/05/31	00:00:00.0	AOCS_OrE-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	01 00 00 00 00	
2016/05/31	00:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/05/31	00:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/05/31	00:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/05/31	00:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/05/31	00:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/31	00:02:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b	
2016/05/31	00:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2016/05/31	00:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/31	00:20:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/31	00:20:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/31	00:20:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/31	00:20:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/05/31	00:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/31	00:49:00.0	XRT_Custom_430_OG [0x1ae]					
2016/05/31	00:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/31	01:57:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/31	01:57:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/31	01:57:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/05/31	01:57:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/05/31	02:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/05/31	02:27:30.0	XRT_Custom_430_OG [0x1ae]					
2016/05/31	02:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/05/31	03:32:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/05/31	03:32:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

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2016/05/31	03:32:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/31	03:32:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/05/31	03:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/05/31	04:06:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/05/31	04:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/05/31	05:04:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/31	05:04:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/31	05:04:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/31	05:04:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/05/31	05:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/05/31	05:44:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/05/31	05:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/05/31	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/31	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/31	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/31	06:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2016/05/31	06:00:18.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00 00 00 00 00		
2016/05/31	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2016/05/31	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2016/05/31	06:02:58.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/05/31	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d		
2016/05/31	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/05/31	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/31	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/31	06:10:00.0	AOCS_Ore-point_Start_6_OG [0x09c]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2016/05/31	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 0a 24 53 74		
2016/05/31	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2016/05/31	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/05/31	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/05/31	06:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/05/31	06:12:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/05/31	06:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b		
2016/05/31	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07		
2016/05/31	06:44:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/05/31	06:44:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/31	06:44:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/31	06:44:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/05/31	06:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/05/31	07:22:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/05/31	07:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/05/31	07:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/31	07:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/31	07:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/05/31	07:30:00.0	AOCS_Ore-point_Start_5_OG [0x09b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2016/05/31	07:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 f0 e6 b0 fe		
2016/05/31	07:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2016/05/31	07:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/05/31	07:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		

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2016/05/31	07:30:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
			MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/31	07:32:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2016/05/31	07:32:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/05/31	07:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/31	08:24:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/31	08:24:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/31	08:24:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/31	08:24:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/31	08:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/31	09:01:00.0	XRT_Custom_430_OG [0x1ae]				
2016/05/31	09:02:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/31	10:04:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/31	10:04:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/31	10:04:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/05/31	10:04:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/05/31	10:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/05/31	10:39:30.0	XRT_Custom_430_OG [0x1ae]				
2016/05/31	10:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/05/31	11:55:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/05/31	11:56:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00