

# XRT Timeline to be uploaded on 2016/06/07

Period: 2016/06/07 10:15:00 - 2016/06/15 09:00:00

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## Normal mode

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### XOB #1AFF: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
06/07 10:28:00 - 06/07 13:58:54	Track ( 29.9, -115.0) <sup>Ⓜ 06/07 10:25:00</sup>	# OP start + 10min, EIS sensitivity monitoring, then HOP 311 from (14-17UT) on QS.
06/07 14:02:00 - 06/07 17:35:30	Track ( 116.0, 184.1) <sup>Ⓜ 06/07 13:59:00</sup>	# HOP 311. IRIS target.
06/07 18:32:00 - 06/08 01:47:30	Track ( 340.3, 176.0) <sup>Ⓜ 06/07 18:29:00</sup>	# AR observations.
06/08 14:03:00 - 06/08 17:56:24	Track ( 324.6, 182.3) <sup>Ⓜ 06/08 14:00:00</sup>	# HOP 311 (active area).
06/08 18:09:30 - 06/08 19:49:30	Track ( 312.8, -117.3) <sup>Ⓜ 06/08 18:06:30</sup>	# EIS spectral atlas.
06/09 14:27:01 - 06/09 17:53:54	Track ( 515.5, 181.0) <sup>Ⓜ 06/09 14:00:00</sup>	# HOP 311 (active area).
06/10 14:03:00 - 06/10 17:44:00	Track ( 678.7, 180.5) <sup>Ⓜ 06/10 14:00:00</sup>	# HOP 311 (active area).

**PROG= 11 Inf.-time(s)**

Subr= 1	1-time(s)	2.0sec
└─ Seqn= 56 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
└─ Open/G-band	Open/G-band close	Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
└─ Subr= 2 5-time(s) 2.0sec		
└─ Seqn= 75 1-time(s) 2.0sec		
└─ Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec
└─ Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec
└─ thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ Seqn= 96 4-time(s) 60.0sec		
└─ Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec
└─ thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 15.0sec
└─ Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 2.0sec
└─ thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 15.0sec
└─ Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec
└─ thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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### XOB #1B35: Synoptic 7 Filter w/ Al-mesh(24/256/2897), Al-poly(45/512/4096), Thin-Be(181/2048/11571) - Thick-Be(65536), Al-poly+Ti-poly(362/4096), Med-Al

Term	Pointing (x, y)	Comment
06/07 18:22:00 - 06/07 18:28:54	Fixed ( 0.0, 0.0)	synoptic, shifted 19.0 min

**PROG= 04 1-time(s)**

Subr= 1	1-time(s)	2.0sec
└─ Seqn= 5 1-time(s) 2.0sec		
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ Seqn= 1 1-time(s) 2.0sec		
└─ Open/Al-mesh	Open/Al-mesh close	Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Al-mesh	Open/Al-mesh close	Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Al-mesh	Open/Al-mesh close	Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 99 1-time(s) 2.0sec		
└─ Al-poly/Open	Al-poly/Open close	Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Al-poly/Open	Al-poly/Open close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Al-poly/Open	Al-poly/thick-Al close	Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 67 1-time(s) 2.0sec		
└─ thin-Be/Open	thin-Be/Open close	Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ thin-Be/Open	thin-Be/Open close	Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ thin-Be/Open	thin-Be/Open close	Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 54 1-time(s) 4.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band close	Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 46 2-time(s) 2.0sec		
└─ Open/thick-Be	Open/thick-Be close	Safe Norm 64.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Seqn= 94 2-time(s) 2.0sec		
└─ Al-poly/Ti-poly	Al-poly/thick-Al close	Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Al-poly/Ti-poly	Al-poly/thick-Al close	Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Seqn= 68 2-time(s) 2.0sec		
└─ med-Al/Open	med-Al/Open close	Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ med-Al/Open	med-Al/Open close	Safe Norm 32.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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### XOB #1B3C: HOP314 Synoptic Loop Q95 2x2 - Al/Poly(45/512/4096) - Al/mesh(24/256/2897) - G-Band

Term	Pointing (x, y)	Comment
06/08 02:18:30 - 06/08 07:29:54	Fixed ( -970.0, 0.0)	# HOP 314 (Akatsuki).

**PROG= 03 Inf.-time(s)**

Subr= 1	1-time(s)	1800.0sec
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<b>Seqn= 99</b>		<b>1-time(s)</b>		<b>2.0sec</b>											
Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
<b>Seqn= 1</b>		<b>1-time(s)</b>		<b>2.0sec</b>											
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	24ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
<b>Seqn= 54</b>		<b>1-time(s)</b>		<b>2.0sec</b>											
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec		
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval			

<b>XOB #1B0A: Synoptic Q95 2x2 - Al/mesh(12/181/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(24/362/1443) + T</b>													
Term	Pointing (x, y)			Comment									
06/08 07:33:00 - 06/08 07:39:54	Fixed (	0.0,	0.0)	synoptic, shifted.									
06/08 17:59:30 - 06/08 18:06:24	Fixed (	0.0,	0.0)	synoptic, shifted -3.5 min									
06/09 06:18:30 - 06/09 06:25:24	Fixed (	0.0,	0.0)	synoptic, shifted 15.5 min									
06/09 17:57:00 - 06/09 18:03:54	Fixed (	0.0,	0.0)	synoptic, shifted -6.0 min									
06/10 05:41:30 - 06/10 05:48:24	Fixed (	0.0,	0.0)	synoptic, shifted -21.5 min									
06/10 18:30:30 - 06/10 18:37:24	Fixed (	0.0,	0.0)	synoptic, shifted 27.5 min									
06/11 06:01:00 - 06/11 06:07:54	Fixed (	0.0,	0.0)	synoptic, shifted -2.0 min									
06/11 18:03:00 - 06/11 18:09:54	Fixed (	0.0,	0.0)	synoptic									
06/12 06:27:00 - 06/12 06:33:54	Fixed (	0.0,	0.0)	synoptic, shifted 24.0 min. HOP 313.									

<b>PROG= 13</b>		<b>1-time(s)</b>		<b>2.0sec</b>											
<b>Subr= 1</b>		<b>1-time(s)</b>		<b>2.0sec</b>											
<b>Seqn= 5</b>		<b>1-time(s)</b>		<b>2.0sec</b>											
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec		
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec		
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec		
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512	(1024, 1024)	DPCM	0	0	2.0sec		
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048	(1024, 1024)	DPCM	0	0	2.0sec		
<b>Seqn= 91</b>		<b>1-time(s)</b>		<b>2.0sec</b>											
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
<b>Seqn= 93</b>		<b>1-time(s)</b>		<b>2.0sec</b>											
Al-poly/Open	Al-poly/Open	close	Safe	Norm	24ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Al-poly/Open	Al-poly/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
<b>Seqn= 77</b>		<b>1-time(s)</b>		<b>2.0sec</b>											
thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
<b>Seqn= 54</b>		<b>1-time(s)</b>		<b>2.0sec</b>											
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec		
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval			

<b>XOB #1B21: AR - Standard Core - (Filter-Ratio with thin-Be and Med-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Med-Be context, with</b>													
Term	Pointing (x, y)			Comment									
06/08 07:43:00 - 06/08 09:54:00	Track (	-293.1,	357.3)	# HOP 300 (Eastern activity).									
06/08 10:30:00 - 06/08 13:59:54	Track (	465.7,	175.0)	# HOP 300 (AR near disk center).									
06/09 06:28:30 - 06/09 09:59:54	Track (	-109.9,	355.4)	# HOP 300 (Eastern activity).									
06/09 10:03:00 - 06/09 13:51:30	Track (	637.8,	174.1)	# HOP 300 (AR near disk center).									
06/10 05:51:30 - 06/10 09:26:00	Track (	83.5,	353.5)	# HOP 300 (Eastern activity).									
06/10 10:03:00 - 06/10 13:59:54	Track (	775.6,	173.9)	# HOP 300 (AR near disk center).									
06/11 06:11:00 - 06/11 09:59:54	Track (	280.2,	352.0)	# HOP 300 (Eastern activity).									
06/11 10:38:30 - 06/11 14:59:54	Track (	871.7,	174.7)	# HOP 300 (AR near disk center). Also 15-17UT.									
06/11 15:41:00 - 06/11 17:59:54	Fixed (	886.0,	175.0)	# HOP 300 AR at the limb (change to fixed pointing).									

<b>PROG= 01</b>		<b>Inf.-time(s)</b>		<b>2.0sec</b>											
<b>Subr= 1</b>		<b>1-time(s)</b>		<b>2.0sec</b>											
<b>Seqn= 56</b>		<b>1-time(s)</b>		<b>2.0sec</b>											
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384	(1064, 1048)	DPCM	0	0	2.0sec		
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384	(1064, 1048)	DPCM	0	0	2.0sec		
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384	(1064, 1048)	Q=98	0	0	2.0sec		
<b>Subr= 2</b>		<b>5-time(s)</b>		<b>2.0sec</b>											
<b>Seqn= 48</b>		<b>1-time(s)</b>		<b>2.0sec</b>											
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	2	0	2.0sec		
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	2.0sec		
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1064, 1048)	Q=95	2	0	2.0sec		
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	2.0sec		
<b>Seqn= 97</b>		<b>4-time(s)</b>		<b>90.0sec</b>											
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	1	0	2.0sec		
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1064, 1048)	Q=95	1	0	2.0sec		
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	1	1	2.0sec		
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1064, 1048)	Q=95	1	1	2.0sec		
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	1	2	2.0sec		

med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1B25: AR - Standard Core - (Filter-Ratio with thin-Be and Med-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Med-Be context, with**

Term	Pointing (x, y)	Comment
06/08 20:32:00 - 06/09 06:15:24	Track ( 541.8, 174.5) @ 06/08 20:06:30	# AR observations.
06/09 18:07:00 - 06/10 05:38:24	Track ( 688.4, 173.9) @ 06/09 18:04:00	# AR observations.
06/10 18:40:30 - 06/11 05:57:54	Track ( 815.3, 174.1) @ 06/10 18:37:30	# AR observations.
06/11 18:13:00 - 06/12 06:23:54	Fixed ( 886.0, 175.0)	# AR observations.

**PROG= 10 Inf.-time(s)**

<b>Subr= 1</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
<b>Seqn= 56</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
<b>Subr= 2</b>		<b>5-time(s)</b>	<b>2.0sec</b>									
<b>Seqn= 48</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
<b>Seqn= 97</b>		<b>4-time(s)</b>	<b>30.0sec</b>									
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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**Flare mode**

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**XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512**

Term	Pointing (x, y)	Comment
06/07 10:28:00 - 06/07 13:58:54	Track ( 29.9, -115.0) @ 06/07 10:25:00	# OP start + 10min, EIS sensitivity monitoring, then HOP 311 from (14-17UT) on QS.
06/07 14:02:00 - 06/07 17:35:30	Track ( 116.0, 184.1) @ 06/07 13:59:00	# HOP 311. IRIS target.
06/07 18:32:00 - 06/08 01:47:30	Track ( 340.3, 176.0) @ 06/07 18:29:00	# AR observations.
06/08 07:43:00 - 06/08 09:54:00	Track ( -293.1, 357.3) @ 06/08 07:40:00	# HOP 300 (Eastern activity).
06/08 10:30:00 - 06/08 13:59:54	Track ( 465.7, 175.0) @ 06/08 10:00:00	# HOP 300 (AR near disk center).
06/08 14:03:00 - 06/08 17:56:24	Track ( 324.6, 182.3) @ 06/08 14:00:00	# HOP 311 (active area).
06/08 18:09:30 - 06/08 19:49:30	Track ( 312.8, -117.3) @ 06/08 18:06:30	# EIS spectral atlas.
06/08 20:32:00 - 06/09 06:15:24	Track ( 541.8, 174.5) @ 06/08 20:06:30	# AR observations.
06/09 06:28:30 - 06/09 09:59:54	Track ( -109.9, 355.4) @ 06/09 06:25:30	# HOP 300 (Eastern activity).
06/09 10:03:00 - 06/09 13:51:30	Track ( 637.8, 174.1) @ 06/09 10:00:00	# HOP 300 (AR near disk center).
06/09 14:27:01 - 06/09 17:53:54	Track ( 515.5, 181.0) @ 06/09 14:00:00	# HOP 311 (active area).
06/09 18:07:00 - 06/10 05:38:24	Track ( 688.4, 173.9) @ 06/09 18:04:00	# AR observations.
06/10 05:51:30 - 06/10 09:26:00	Track ( 83.5, 353.5) @ 06/10 05:48:30	# HOP 300 (Eastern activity).
06/10 10:03:00 - 06/10 13:59:54	Track ( 775.6, 173.9) @ 06/10 10:00:00	# HOP 300 (AR near disk center).
06/10 14:03:00 - 06/10 17:44:00	Track ( 678.7, 180.5) @ 06/10 14:00:00	# HOP 311 (active area).
06/10 18:40:30 - 06/11 05:57:54	Track ( 815.3, 174.1) @ 06/10 18:37:30	# AR observations.
06/11 06:11:00 - 06/11 09:59:54	Track ( 280.2, 352.0) @ 06/11 06:08:00	# HOP 300 (Eastern activity).
06/11 10:38:30 - 06/11 14:59:54	Track ( 871.7, 174.7) @ 06/11 10:00:00	# HOP 300 (AR near disk center). Also 15-17UT.
06/11 15:41:00 - 06/11 17:59:54	Fixed ( 886.0, 175.0)	# HOP 300 AR at the limb (change to fixed pointing).
06/11 18:13:00 - 06/12 06:23:54	Fixed ( 886.0, 175.0)	# AR observations.

**PROG= 07 30-time(s)**

<b>Subr= 1</b>		<b>20-time(s)</b>	<b>2.0sec</b>									
<b>Seqn= 11</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Seqn=100</b>		<b>1-time(s)</b>	<b>10.0sec</b>									
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Subr= 2</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
<b>Seqn= 10</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 11</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Seqn= 84</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

\* \* \* \* \*

**Active Region Search**

\* \* \* \* \*

NOT USED

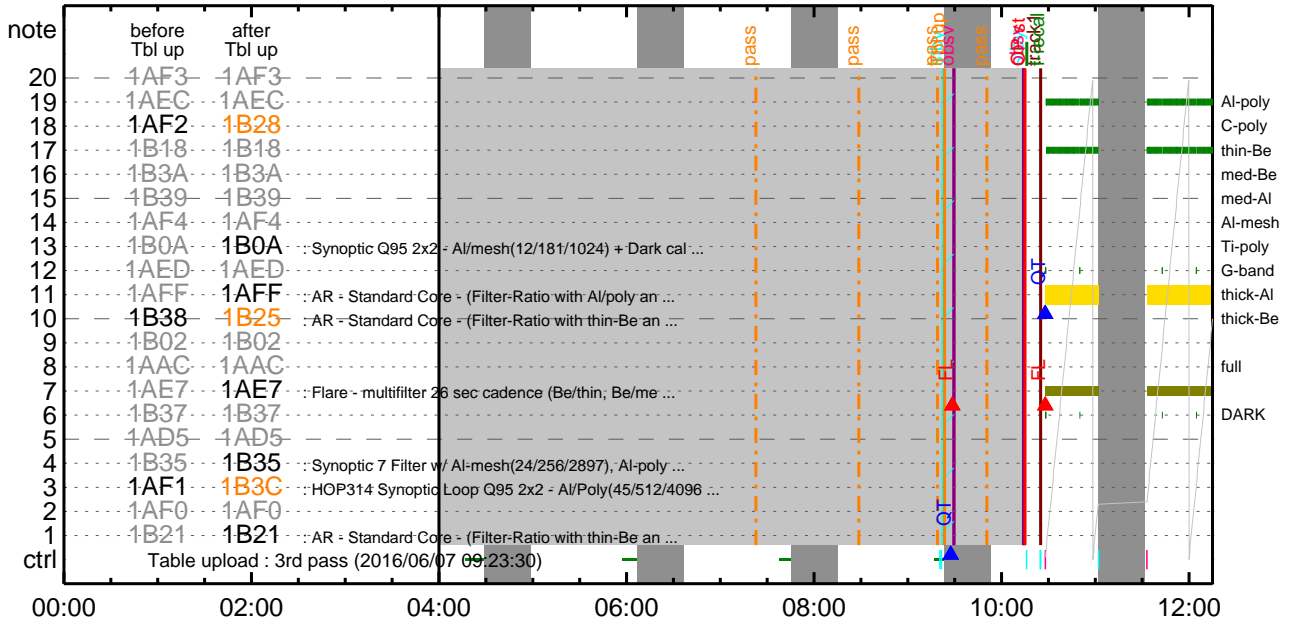
\* \* \* \* \*

### Flare Detection

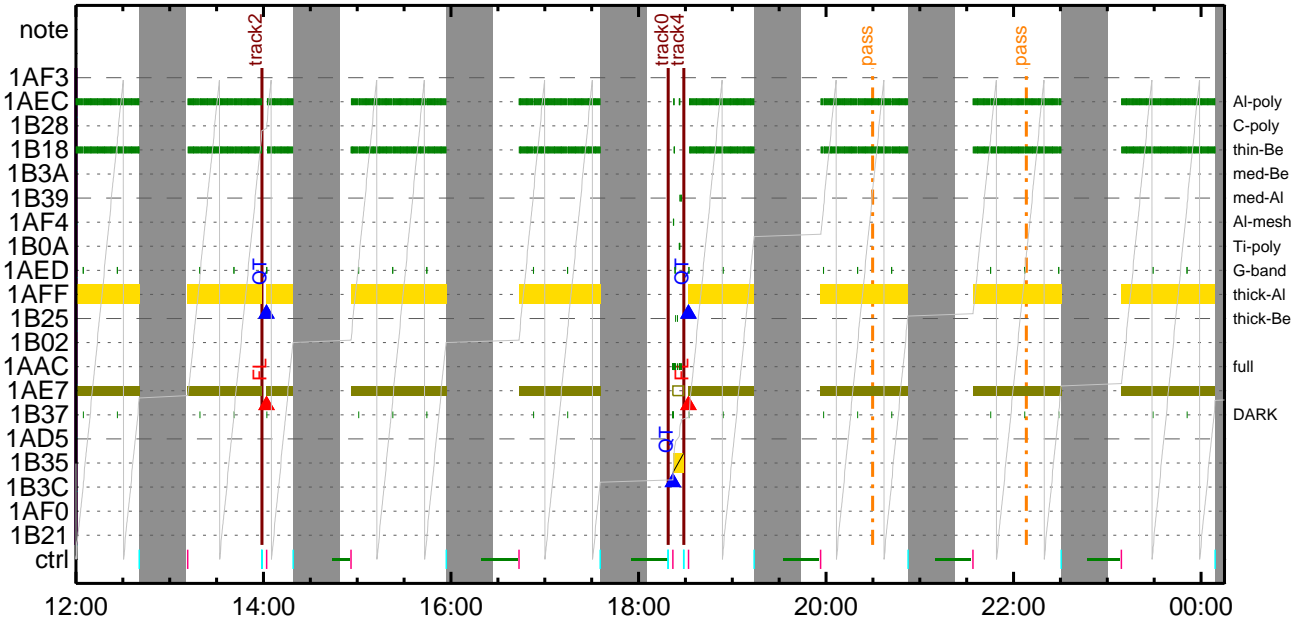
\* \* \* \* \*

FLD Patrol									
Term		Pointing (x, y)				Comment			
06/07 18:29:18 - 06/08 02:00:18		Track ( 340.3, 176.0)		⑥ 06/07 18:29:00		# AR observations.			
06/08 07:40:18 - 06/08 17:56:48		Track ( -293.1, 357.3)		⑥ 06/08 07:40:00		# HOP 300 (Eastern activity).			
06/08 18:06:48 - 06/09 06:15:48		Track ( 312.8, -117.3)		⑥ 06/08 18:06:30		# EIS spectral atlas.			
06/09 06:25:48 - 06/09 17:54:18		Track ( -109.9, 355.4)		⑥ 06/09 06:25:30		# HOP 300 (Eastern activity).			
06/09 18:04:18 - 06/10 05:38:48		Track ( 688.4, 173.9)		⑥ 06/09 18:04:00		# AR observations.			
06/10 05:48:48 - 06/10 18:27:48		Track ( 83.5, 353.5)		⑥ 06/10 05:48:30		# HOP 300 (Eastern activity).			
06/10 18:37:48 - 06/11 05:58:18		Track ( 815.3, 174.1)		⑥ 06/10 18:37:30		# AR observations.			
06/11 06:08:18 - 06/11 18:00:18		Track ( 280.2, 352.0)		⑥ 06/11 06:08:00		# HOP 300 (Eastern activity).			
06/11 18:10:18 - 06/12 06:24:18		Fixed ( 886.0, 175.0)				# AR observations.			
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp. AEC Buffer Interval

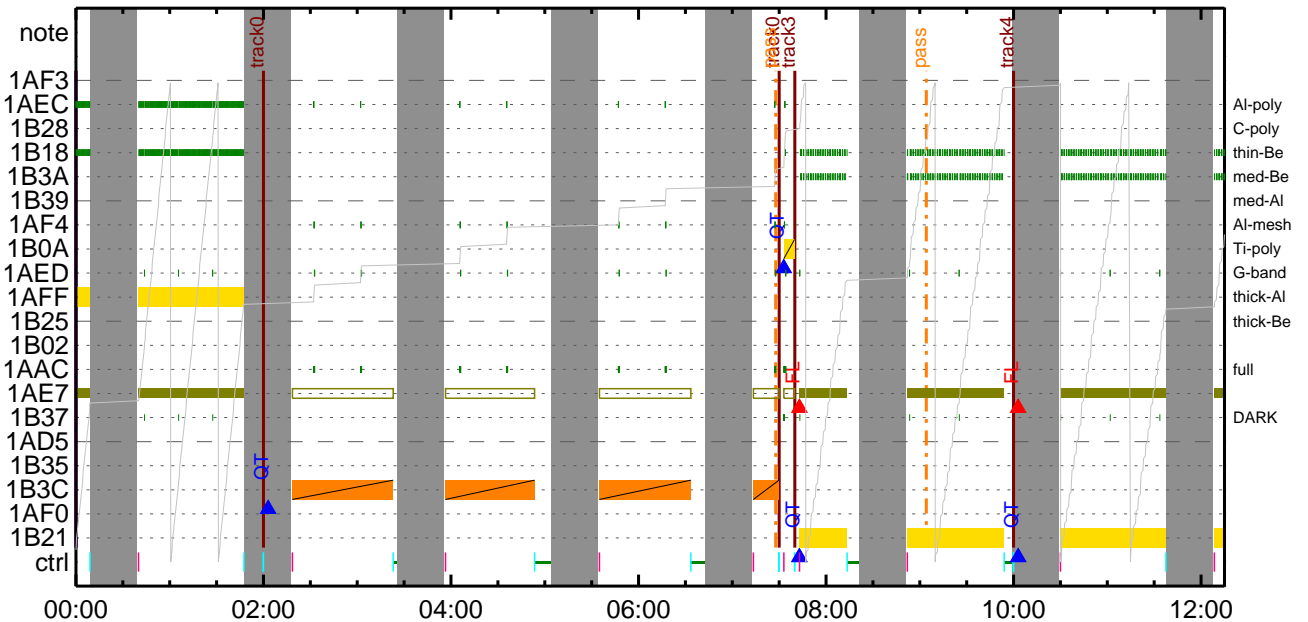
### CMDI #0978 2016/06/07



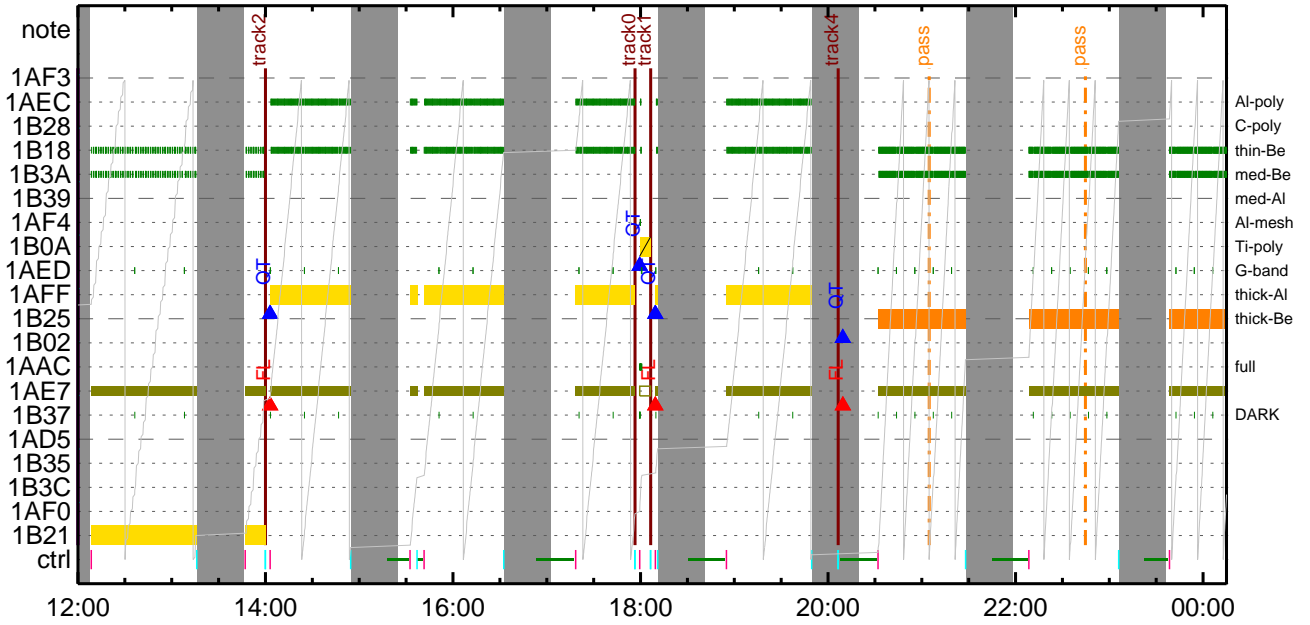
### CMDI #0978 2016/06/07



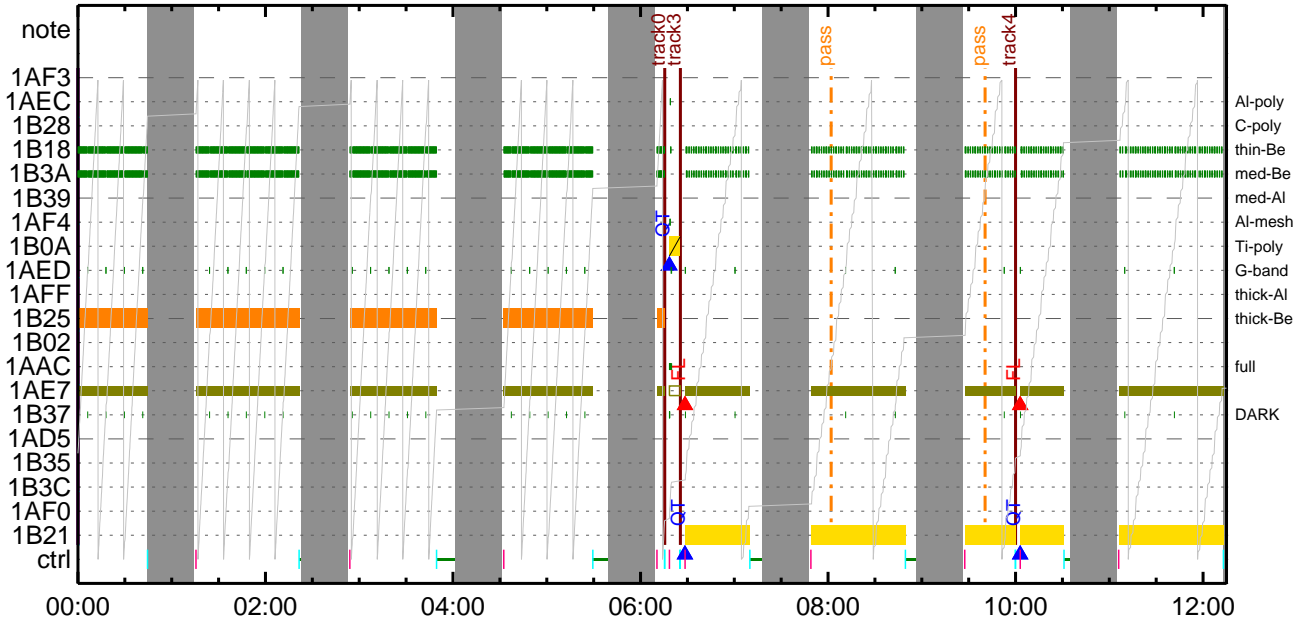
### CMDI #0978 2016/06/08



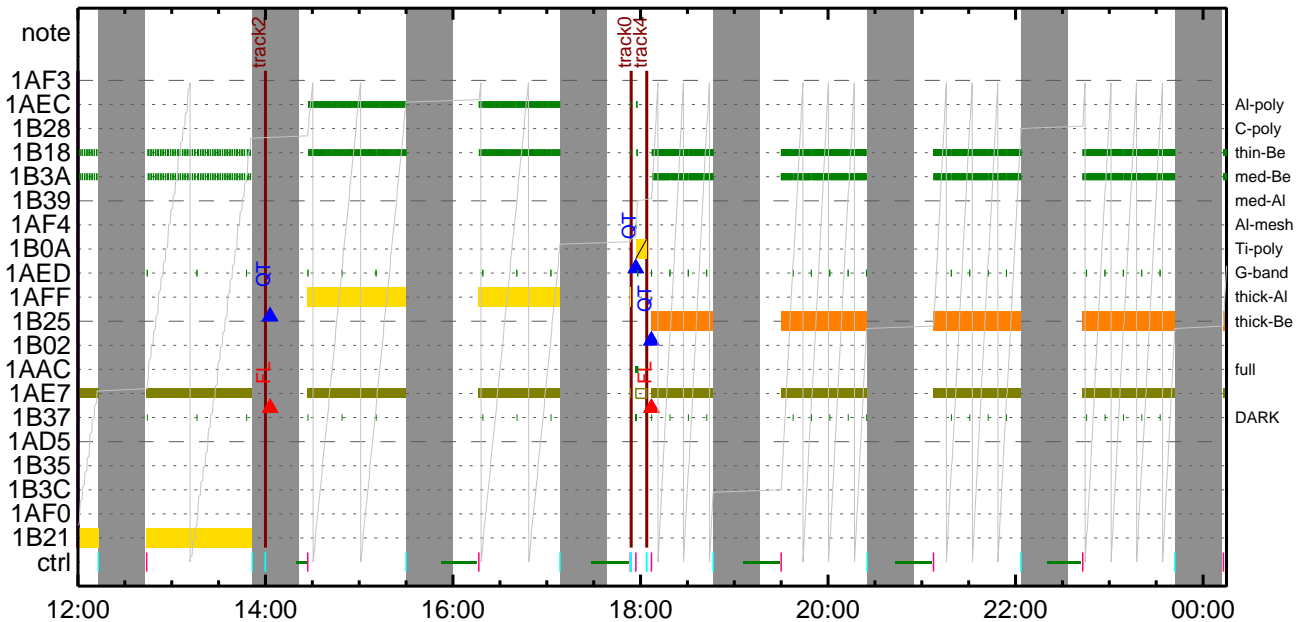
CMDI #0978 2016/06/08



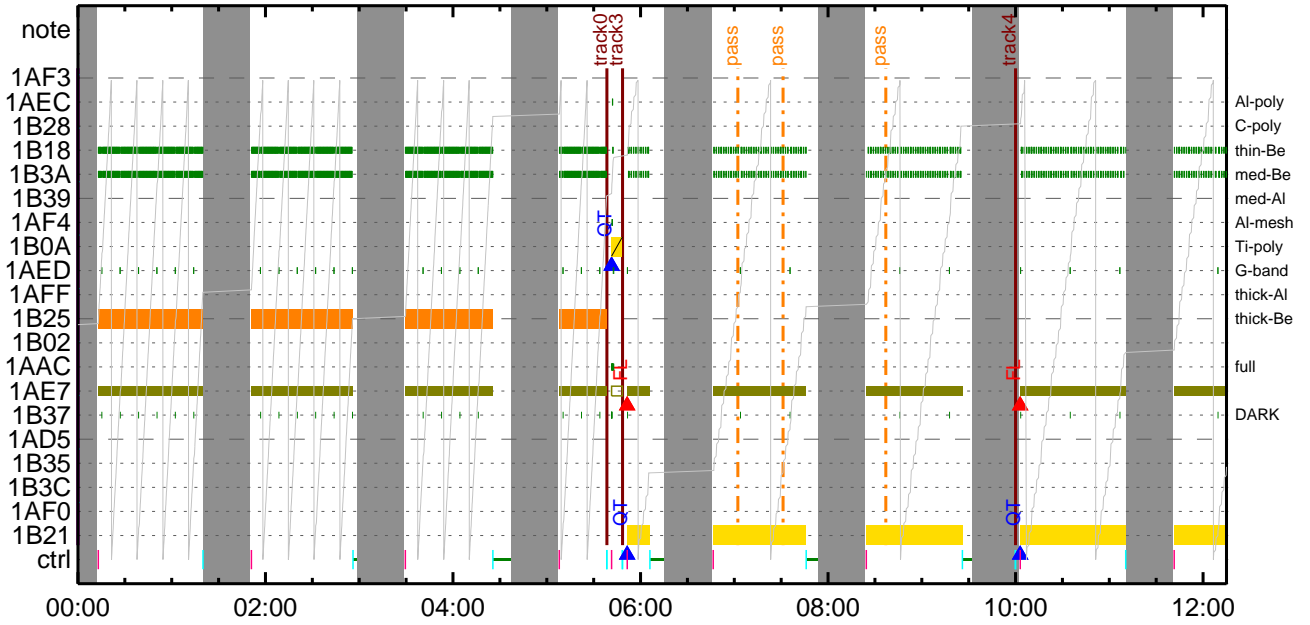
CMDI #0978 2016/06/09



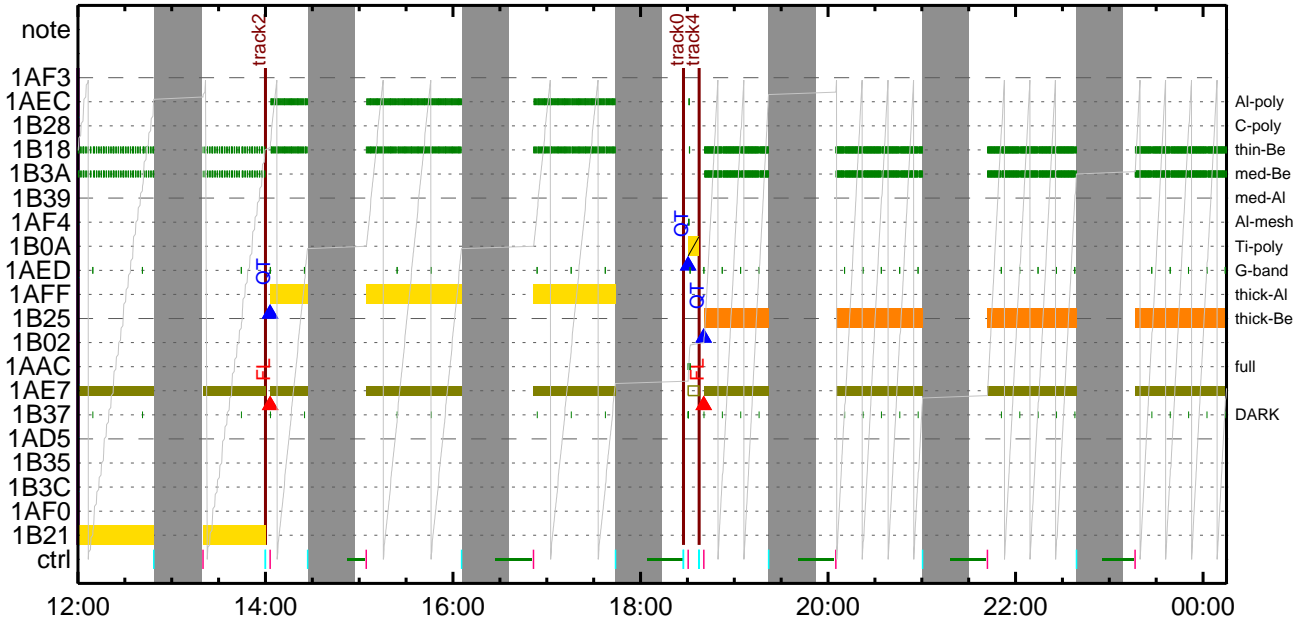
CMDI #0978 2016/06/09



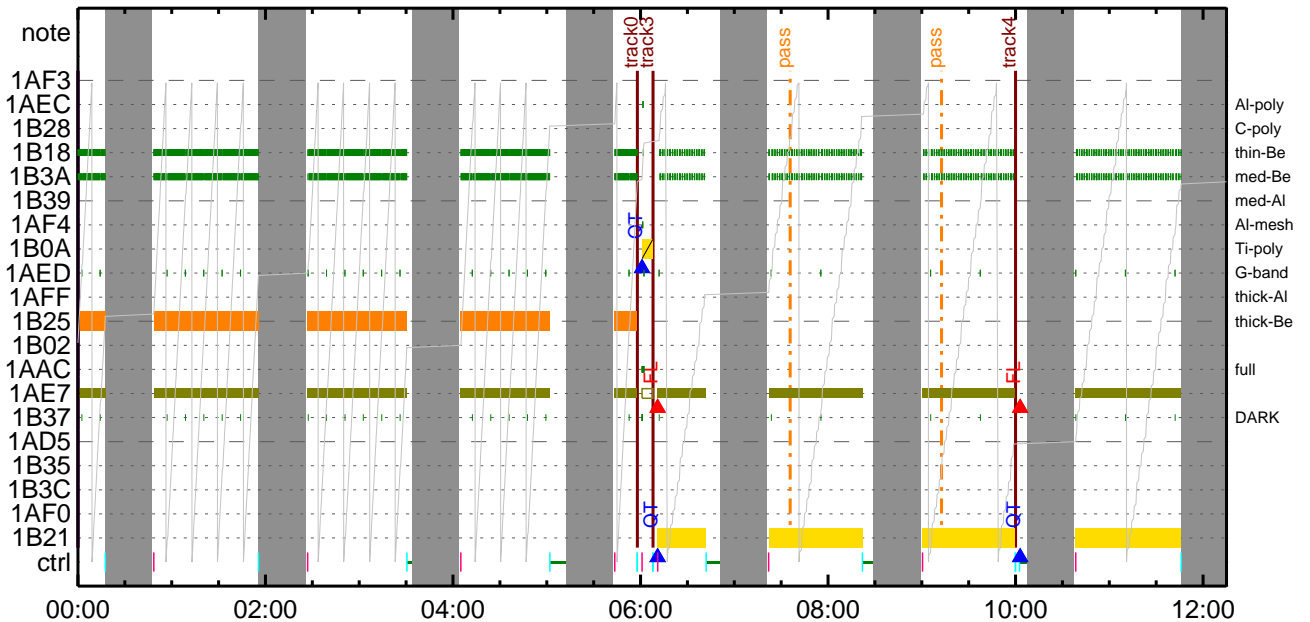
### CMDI #0978 2016/06/10



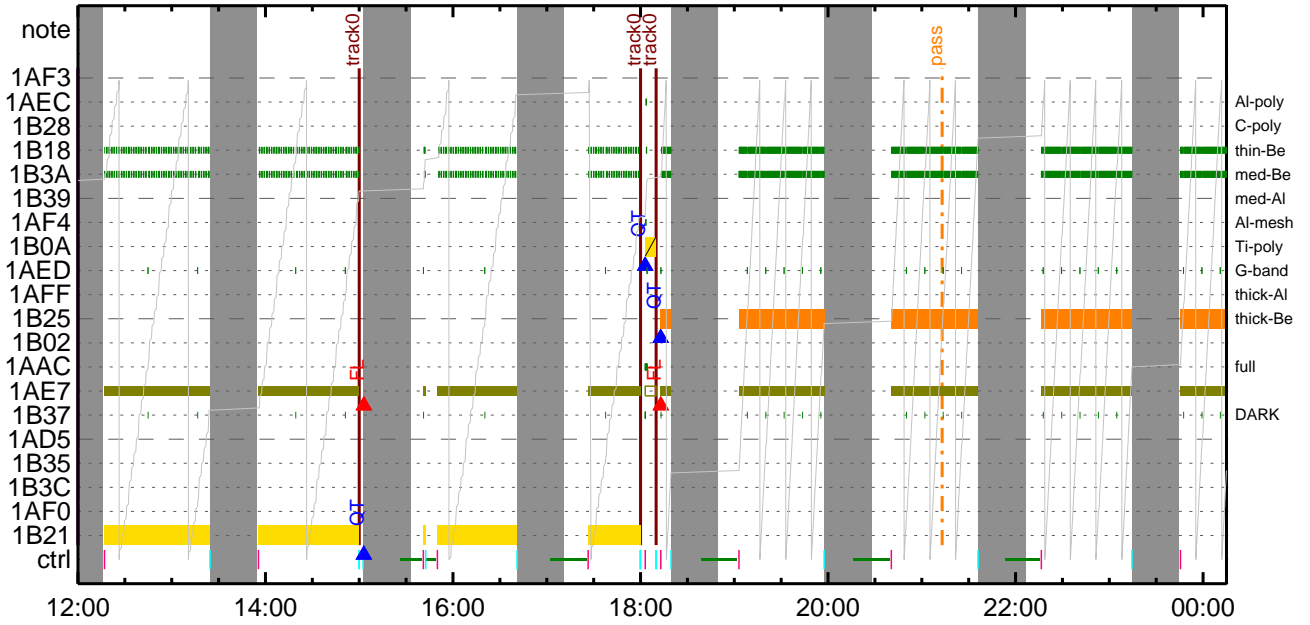
### CMDI #0978 2016/06/10



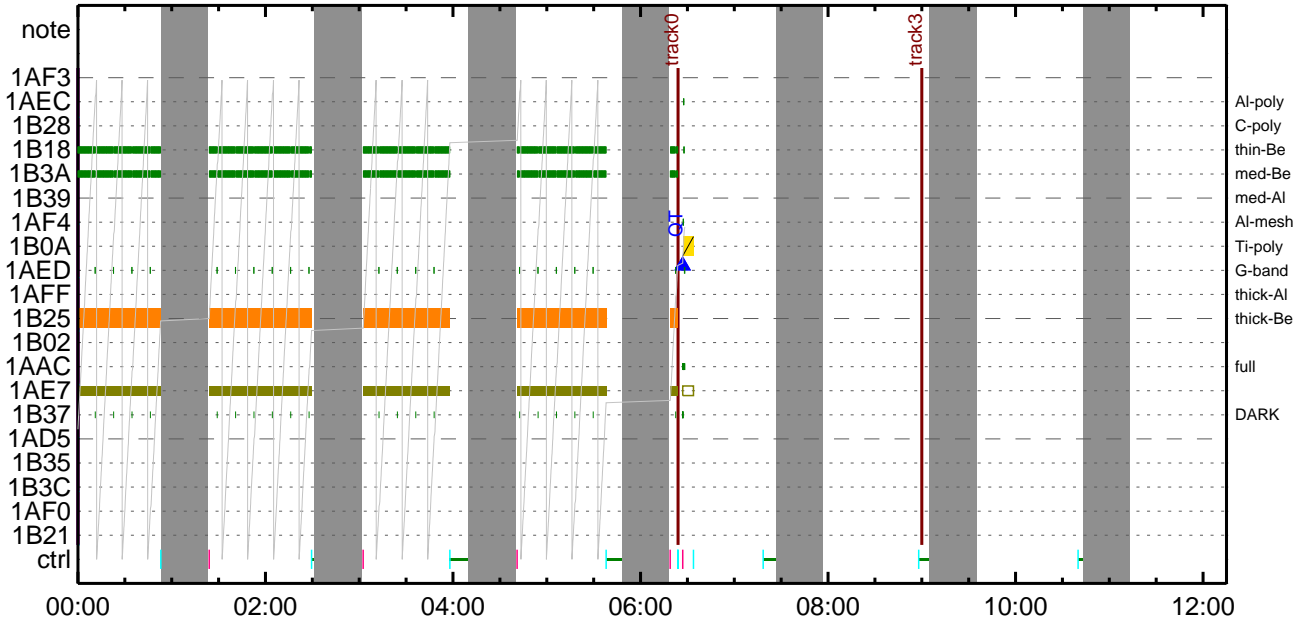
### CMDI #0978 2016/06/11



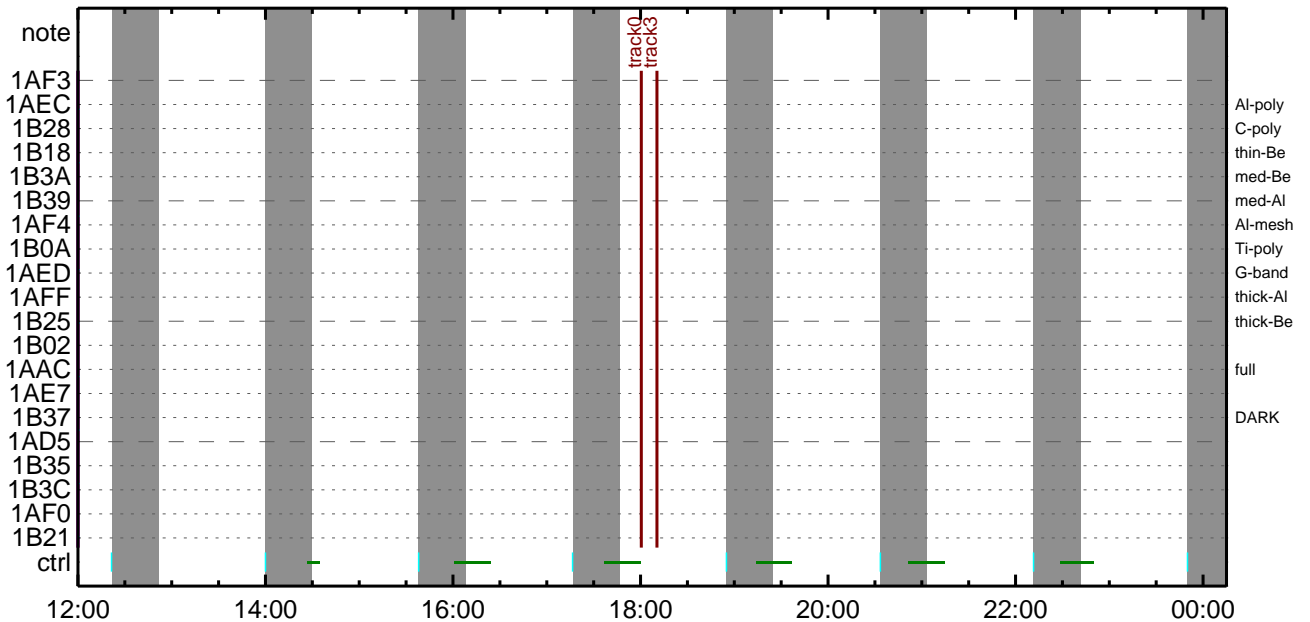
CMDI #0978 2016/06/11



CMDI #0978 2016/06/12

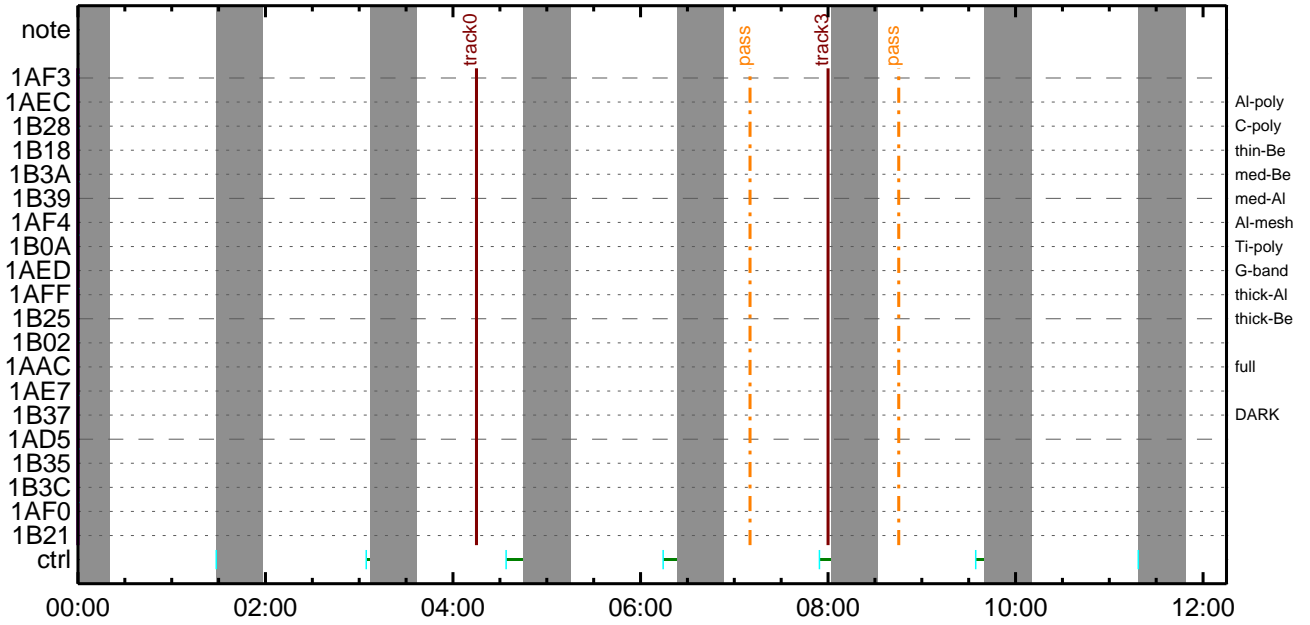


CMDI #0978 2016/06/12

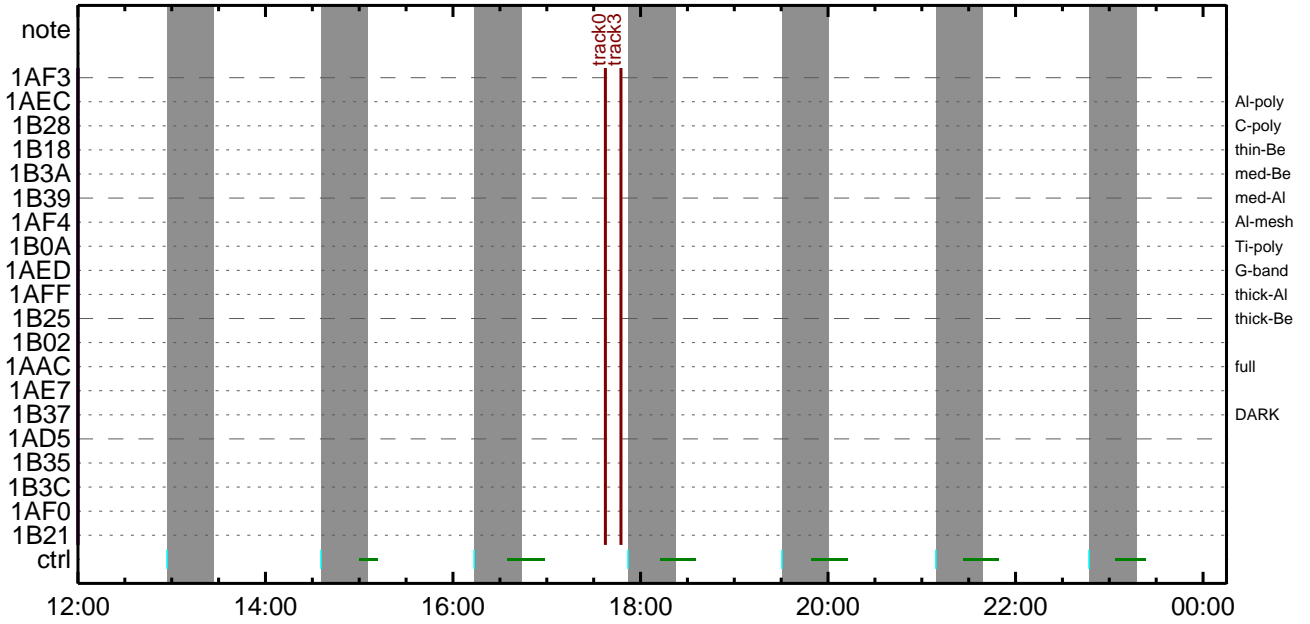




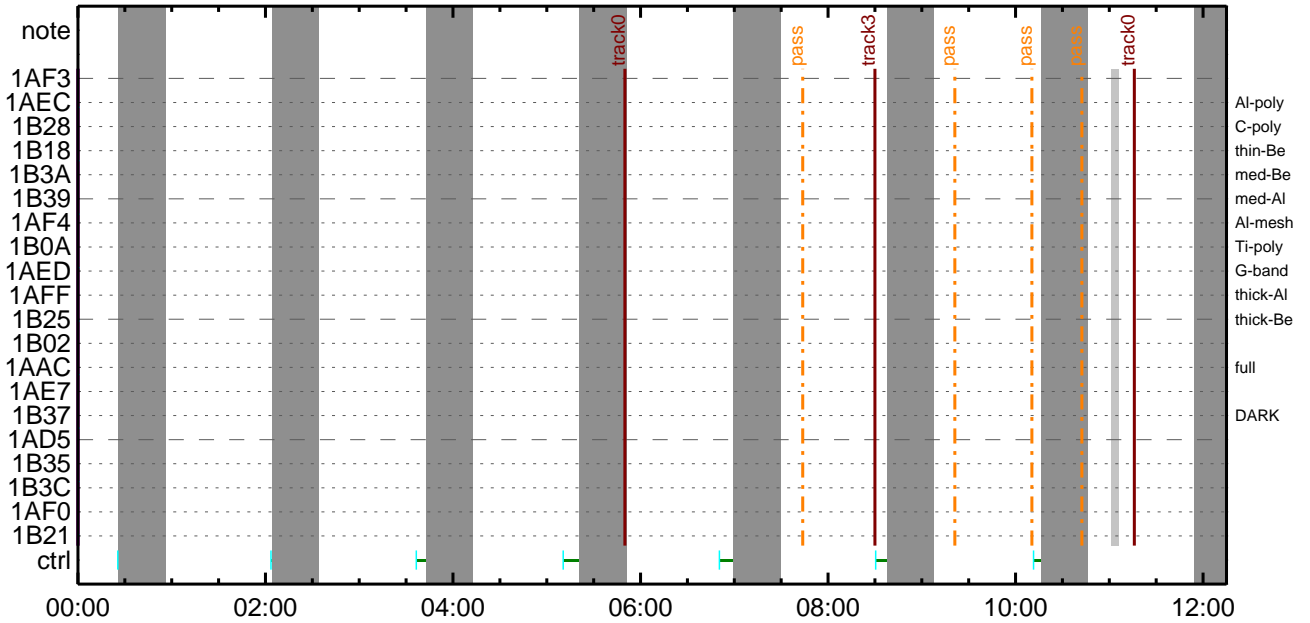
CMDI #0978 2016/06/13



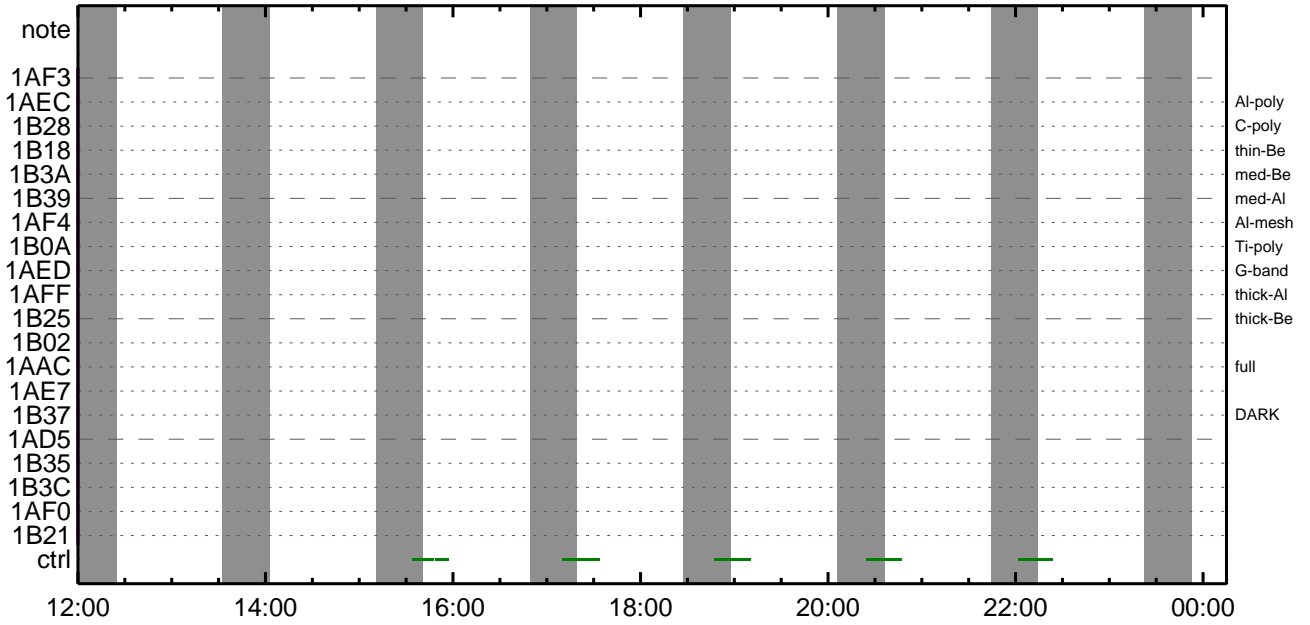
CMDI #0978 2016/06/13



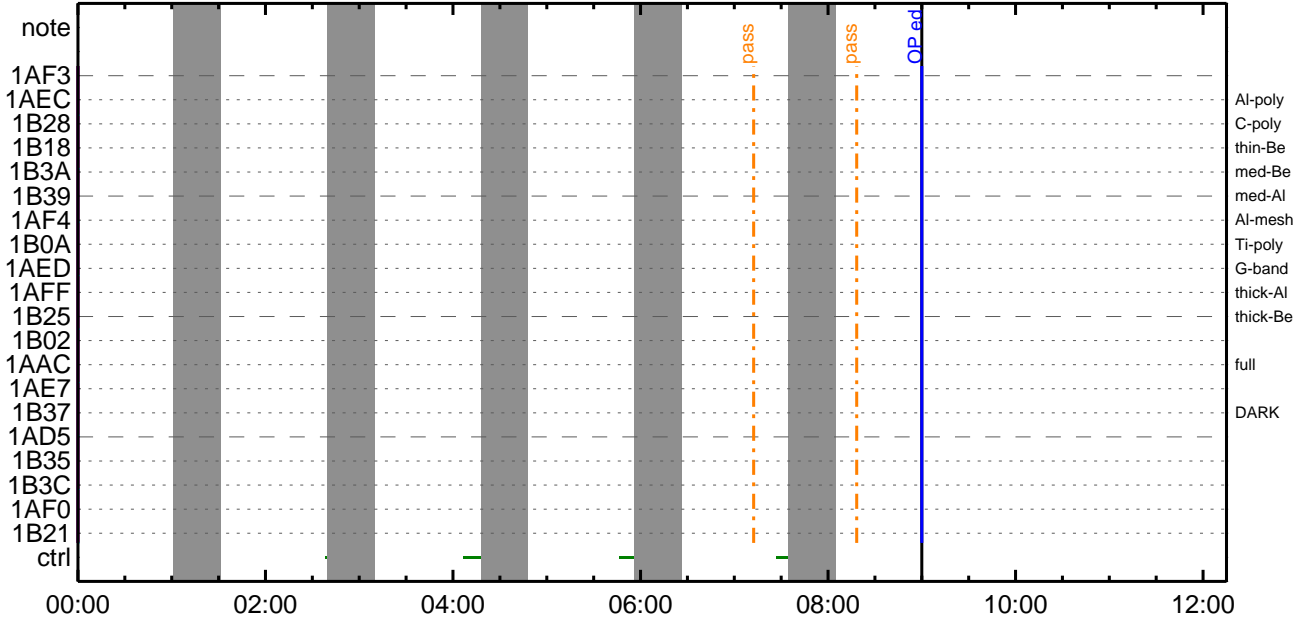
CMDI #0978 2016/06/14



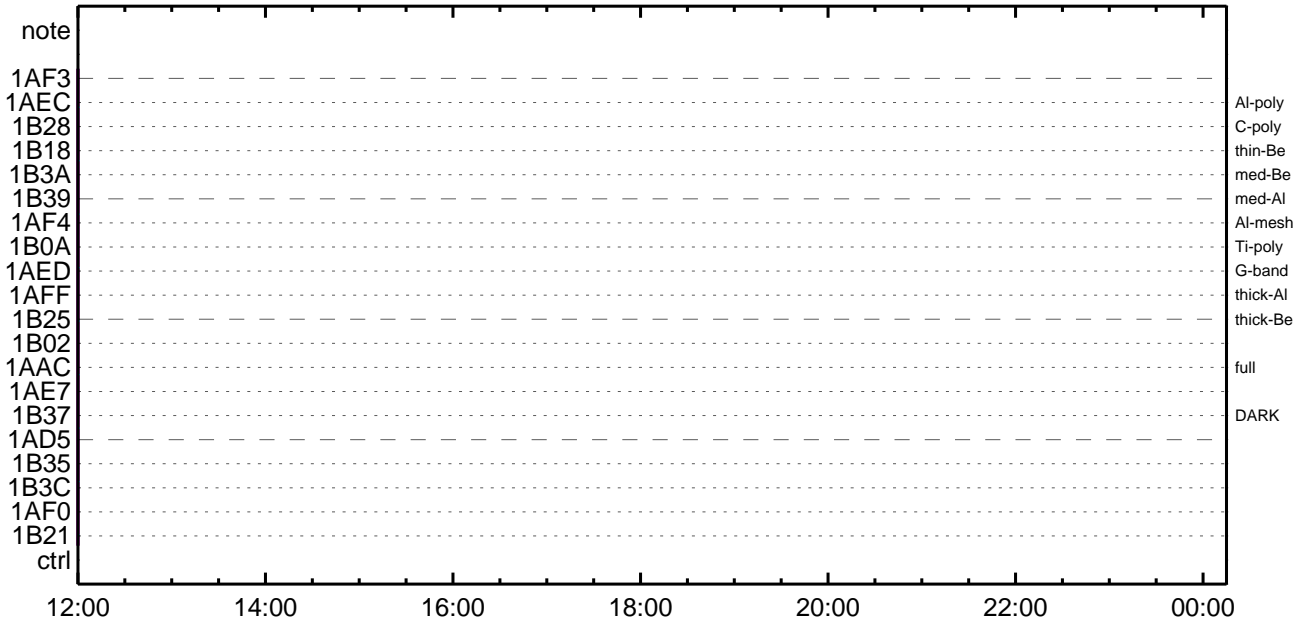
CMDI #0978 2016/06/14



CMDI #0978 2016/06/15



CMDI #0978 2016/06/15



(a) Spacecraft Operation Procedure (real-commands)

```

main-902 2016-06-07 13:27:29 205 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Ä»Û;ä
0005 C.
0006 C. YÀYB;¼Y³YF¥ÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿ðÁð•μ°È»Í×ÁÇ¿íYçYÁY×Yí;¼YÉ;ÈÈè¿μ•ííÉ;ÈðÈ¼°ÇÓð•α¿¼í¹Ç¿Í;çÄ®, ùα¹αèððÇÁ+¿®ð•ðÈððð³ðÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGYí;¼YÉ;|YÁYÖY×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGYí;¼YÉ;ä
0019 . S. OP op-902:OP
0020 ( )
0021 . S. OG og-902:OG
0022 ( )
0023 C.
0024 . C. ;ãNMOG&OPíî°èYÁYÖY×;ä
0025 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C.         çç[HK1_DMP_TOP_ADRS_1]           EQ      40
0029 C.         çç[HK1_DMP_TOP_ADRS_0]           EQ      0
0030 C.         çç[HK1_DMP_BLOCK_NUM]            EQ     127
0031 C.         çç[HK1_DMP_REPEAT_NUM]          EQ      0
0032 C.         çç[HK1_DMA_DMP_PIM]             EQ     DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C.         çç[HK1_PKT_FORM_NO]              EQ      7
0036 C.         çç[HK1_PKT_GEN_TIME]             EQ     0.25 s
0037 C.         çç[HK1_S_TLM_BIT_RATE]          EQ     32k
0038 C.         çç[HK1_X_TLM_BIT_RATE]          EQ      4M
0039 C.         çç[HK1_DMP_CHK_FLG]             EQ     EXEC
0040 . C. YÀYÖY×¼¾ª î»ðð³ îÇ§
0041 C.         çç[HK1_DMP_CHK_FLG]             EQ     NON
0042 . C. RAM ID=NMOGðí¾È¹ç•è²îOKðð³ îÇ§
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C.         çç[HK1_DMP_TOP_ADRS_1]           EQ     41
0048 C.         çç[HK1_DMP_TOP_ADRS_0]           EQ      0
0049 C.         çç[HK1_DMP_BLOCK_NUM]            EQ     127
0050 C.         çç[HK1_DMP_REPEAT_NUM]          EQ      0
0051 C.         çç[HK1_DMA_DMP_PIM]             EQ     DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C.         çç[HK1_PKT_FORM_NO]              EQ      7
0055 C.         çç[HK1_PKT_GEN_TIME]             EQ     0.25 s
0056 C.         çç[HK1_S_TLM_BIT_RATE]          EQ     32k
0057 C.         çç[HK1_X_TLM_BIT_RATE]          EQ      4M
0058 C.         çç[HK1_DMP_CHK_FLG]             EQ     EXEC
0059 . C. YÀYÖY×¼¾ª î»ðð³ îÇ§
0060 C.         çç[HK1_DMP_CHK_FLG]             EQ     NON
0061 . C. RAM ID=NMOGðí¾È¹ç•è²îOKðð³ îÇ§
0062 C.
0063 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C.         çç[HK1_DMP_TOP_ADRS_1]           EQ     42
0067 C.         çç[HK1_DMP_TOP_ADRS_0]           EQ      0
0068 C.         çç[HK1_DMP_BLOCK_NUM]            EQ     65
0069 C.         çç[HK1_DMP_REPEAT_NUM]          EQ      0
0070 C.         çç[HK1_DMA_DMP_PIM]             EQ     DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C.         çç[HK1_PKT_FORM_NO]              EQ      7
0074 C.         çç[HK1_PKT_GEN_TIME]             EQ     0.25 s
0075 C.         çç[HK1_S_TLM_BIT_RATE]          EQ     32k
0076 C.         çç[HK1_X_TLM_BIT_RATE]          EQ      4M
0077 C.         çç[HK1_DMP_CHK_FLG]             EQ     EXEC
0078 . C. YÀYÖY×¼¾ª î»ðð³ îÇ§
0079 C.         çç[HK1_DMP_CHK_FLG]             EQ     NON
0080 . C. RAM ID=NMOG,RAM ID=OPðí¾È¹ç•è²îOKðð³ îÇ§
0081 C.
0082 . C. ***** òÈ²¼òí¾Ä´¶¼°òÈÈ-ð°Ä+¿® (¼åμ-YÀYÖY×¼¾È¼ÇððÄÖÄæç¾ª°α°ð¾¼í¹Ç¿Ç¿ðâ) *****
0083 C. DHUYâ;¼YÉ;È¼Y¼; Yí;¼YÉ;Èððíáð¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C.         çç[HK1_PKT_FORM_NO]              EQ      2
0087 C.         çç[HK1_PKT_GEN_TIME]             EQ     0.5S
0088 C.         çç[HK1_S_TLM_BIT_RATE]          EQ     32k
0089 C.         çç[HK1_X_TLM_BIT_RATE]          EQ      4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE |§ OPOG UPLOADð-Á+¿¿®NGðí¾È¹ç; ç°È²¼òí¾Ä-¿¿®í¾Ä¹Ôð•ðÈððð³ðÈ;f

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0096 C.          0303; SET EDUMP 0100 0100 0100 0100 0100 0100
0097 C.
0098 C. TI 2016-06-07 10:10:00.0
0099 +. TI 2016-06-07 10:10:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.          0303; SET EDUMP 0100 0100 0100 0100 0100 0100
0102 C.
0103 +. TI 2016-06-07 10:10:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.          0303; SET EDUMP 0100 0100 0100 0100 0100 0100
0106 C.
0107 +. TI 2016-06-07 10:10:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.          0303; SET EDUMP 0100 0100 0100 0100 0100 0100
0110 C.
0111 +. TI 2016-06-07 10:14:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.          0303; SET EDUMP 0100 0100 0100 0100 0100 0100
0114 C.
0115 C.          0303; SET EDUMP 0100 0100 0100 0100 0100 0100
0116 C.          0303; SET EDUMP 0100 0100 0100 0100 0100 0100
0117 C.          0303; SET EDUMP 0100 0100 0100 0100 0100 0100
0118 C.          0303; SET EDUMP 0100 0100 0100 0100 0100 0100
0119 C.          0303; SET EDUMP 0100 0100 0100 0100 0100 0100
0120 C.
0121 C. *****
0122 C. TI 2016-06-07 10:14:59.5
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF; 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.          0303; SET EDUMP 0100 0100 0100 0100 0100 0100
0129 C.          0303; SET EDUMP 0100 0100 0100 0100 0100 0100
0130 C.          0303; SET EDUMP 0100 0100 0100 0100 0100 0100
0131 C.          0303; SET EDUMP 0100 0100 0100 0100 0100 0100
0132 C.          0303; SET EDUMP 0100 0100 0100 0100 0100 0100
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.          0303; SET EDUMP 0100 0100 0100 0100 0100 0100
0136 C.          0303; SET EDUMP 0100 0100 0100 0100 0100 0100
0137 C.          0303; SET EDUMP 0100 0100 0100 0100 0100 0100
0138 C.          0303; SET EDUMP 0100 0100 0100 0100 0100 0100
0139 C.          0303; SET EDUMP 0100 0100 0100 0100 0100 0100
0140 C.
0141 C.          0303; SET EDUMP 0100 0100 0100 0100 0100 0100
0142 C.          0303; SET EDUMP 0100 0100 0100 0100 0100 0100
0143 C.
0144 C. RAM ID=TI_TBL 0100 0100 0100 0100 0100 0100
0145 C.
0146 C. DHU 0100 0100 0100 0100 0100 0100
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.          0303; SET EDUMP 0100 0100 0100 0100 0100 0100
0150 C.          0303; SET EDUMP 0100 0100 0100 0100 0100 0100
0151 C.          0303; SET EDUMP 0100 0100 0100 0100 0100 0100
0152 C.          0303; SET EDUMP 0100 0100 0100 0100 0100 0100
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2016-06-07 10:14:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC (41)
0161 C.
0162 C.          0303; SET EDUMP 0100 0100 0100 0100 0100 0100
0163 C.
0164 C. ***** SOT END *****
0165 C.
0166 C. ***** XRT START *****
0167 C. Execute, after the success of OP upload.
0168 +. TI 2016-06-07 10:14:00.0
0169 DC 07-F0 MDP_XRT_MODE_STBY
0170 BC (c3)
0171 C.          0303; SET EDUMP 0100 0100 0100 0100 0100 0100
0172 C.
0173 C. ***** XRT END *****
0174 C. Stop EIS observation and temporarily disable EIS mode changes
0175 C.
0176 C.
0177 C. ***** Start EIS operation (TI set) *****
0178 C. Execute, after the success of OP upload.
0179 C. Set EIS TI-commands
0180 +. TI 2016-06-07 10:14:30.0
0181 DC 07-FC EIS_MODE_MANU
0182 BC (21 02)
0183 +. TI 2016-06-07 10:14:40.0
0184 DC 07-FC EIS_MODE_CHG_DIS
0185 BC (22)
0186 C.          0303; SET EDUMP 0100 0100 0100 0100 0100 0100
0187 C.          0303; SET EDUMP 0100 0100 0100 0100 0100 0100
0188 C. ***** End EIS operation (TI set) *****
0189 C.
0190 C.
0191 C. ***** MDP 0100 0100 0100 0100 0100 0100 *****
0192 C. (0100 0100 0100 0100 0100 0100)
0193 S. DC-BC dcbc-402:DCBC

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```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```





(a) Spacecraft Operation Procedure (real-commands)

```
main-904 2016-06-07 13:27:29 128 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY~¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. ÁíË¿µ°Æ»Í×ÁÇóÍYÇYÁY×Yí;¼YÉ;ÉÈµ•íÉ;ÈB¼°ÇÔµ•µ¿i¹ÇµÍ;çÀ®, ù¹µèµµÇÁ+¿®µ•µÈµµ³µÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop SP table >
0018 +. DC 07-F0 MDP_SP_CTRL_MANU
0019 BC (61)
0020 C. -----
0021 C. MDP_SP_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload SP Observation Table>
0025 . S. RAM ram-283:MDP_OBS_S
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_S >
0029 +. DC 07-F0 MDP_DUMP_SPTBL
0030 BC (83 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_S verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2016-06-07 10:14:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 +. DC 07-F0 MDP_XRT_CTRL_MANU
0052 BC (c1)
0053 +. DC 07-F0 MDP_XRT_MODE_STBY
0054 BC (c3)
0055 . C. ----- Success Verify ? OK / NG____
0056 C.
0057 C. XRT Obs. Table Upload
0058 . S. RAM ram-291:MDP_OBS_X
0059 ( )
0060 C.
0061 +. DC 07-F0 MDP_DUMP_XRTTBL
0062 BC (84 07 00 00 00 3a d4)
0063 . C. ----- Comparison Check ? OK / ERR ____
0064 C.
0065 C.
0066 +. DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 01 b1 b1 04 04)
0068 +. DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 02 b1 b1 08 08)
0070 +. DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 03 b1 b1 08 08)
0072 +. DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 04 b1 b1 06 06)
0074 +. DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 06 85 83 06 06)
0076 +. DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 07 80 80 20 20)
0078 +. DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 08 80 80 20 08)
0080 +. DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 09 80 80 08 20)
0082 +. DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 0f 80 80 06 06)
0084 +. DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 10 80 80 08 08)
0086 +. DC 07-F0 MDP_XRT_FLD_ENA
0087 BC (d8)
0088 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0089 BC (c8)
0090 +. DC 07-F0 MDP_XRT_ARS_DIS
0091 BC (d5)
0092 +. DC 07-F0 MDP_XRT_AEC_RESET
0093 BC (d0)
0094 +. DC 07-F0 MDP_XRT_FLD_RESET
0095 BC (da)
```



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0096 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0097 BC (c4 01)
0098 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0099 BC (c5 07)
0100 . C. ----- Success Verify ? OK / NG ____
0101 C.
0102 C.
0103 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0104 C.
0105 +. DC 07-F0 MDP_XRT_MODE_OBSV
0106 BC (c2)
0107 +. TI 2016-06-07 10:14:02.0
0108 DC 07-F0 MDP_XRT_MODE_OBSV
0109 BC (c2)
0110 . C. ----- Success Verify ? OK / NG ____
0111 C.
0112 C. ***** XRT END *****
0113 C.
0114 . C. ***** MDP `úÃîñî»ò¼ŷñÊÂðñ¹ñèDCBC•x²è *****
0115 C. (¼á°îŷÓŷÃŷÈŷŲŷËŷáŷçŷèñÊ¼ññ¼Â»Ûñ¹ñè)
0116 . S. DC-BC dcbc-402:DCBC
0117 (MDP_known_event)
0118 C.
0119 C.
0120 . C. ***** ŷDŷ¹•İ Daily±¿İÑñÊ´Øñ¹ñèDCBC•x²è *****
0121 . S. DC-BC dcbc-153:DCBC
0122 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0123 C.
0124 C.
0125 . C. ¡ãLOSŷÁŷŲŷÃŷ-¼Â»Ûñ¹ñè
0126 C.
0127 . C. ***** LOS *****
0128 C.
```



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\*\*\* OP Sequence for XRT \*\*\*

2016/06/07	10:16:00.5	XRT_CTRL_MANU_416_OG [0x1a0]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/06/07	10:16:10.5	XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]			
		XRT_FOCUS_RECAL	2	07-F8	78 00
2016/06/07	10:20:10.5	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/06/07	10:24:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/06/07	10:24:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/06/07	10:24:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/06/07	10:25:00.0	AOCS_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 00 00 00 00
2016/06/07	10:25:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2016/06/07	10:25:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/06/07	10:25:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2016/06/07	10:25:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2016/06/07	10:25:26.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/06/07	10:27:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2016/06/07	10:27:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/06/07	10:28:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/06/07	11:02:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/06/07	11:02:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/06/07	11:02:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/06/07	11:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/06/07	11:32:00.0	XRT_Custom_430_OG [0x1ae]			
2016/06/07	11:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/06/07	12:40:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/06/07	12:40:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/06/07	12:40:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/06/07	12:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/06/07	13:10:30.0	XRT_Custom_430_OG [0x1ae]			
2016/06/07	13:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/06/07	13:58:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/06/07	13:58:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/06/07	13:58:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/06/07	13:59:00.0	AOCS_Ore-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	02 00 00 00 00
2016/06/07	13:59:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2016/06/07	13:59:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/06/07	13:59:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2016/06/07	13:59:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2016/06/07	13:59:26.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/06/07	14:01:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2016/06/07	14:01:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/06/07	14:02:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/06/07	14:19:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/06/07	14:19:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/06/07	14:19:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/06/07	14:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/06/07	14:55:00.0	XRT_Custom_430_OG [0x1ae]			
2016/06/07	14:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/06/07	15:57:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/06/07	15:57:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da

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2016/06/07	15:57:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/07	16:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/07	16:42:31.0	XRT_Custom_430_OG [0x1ae]							
2016/06/07	16:43:31.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/07	17:35:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/07	17:35:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/07	17:35:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/07	17:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/07	18:18:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/07	18:18:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/07	18:18:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/06/07	18:19:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2016/06/07	18:19:18.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/06/07	18:21:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/06/07	18:21:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/06/07	18:21:58.0	XRT_QT_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2016/06/07	18:22:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/07	18:28:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/07	18:28:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/07	18:28:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/06/07	18:29:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2016/06/07	18:29:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/06/07	18:29:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/06/07	18:29:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/06/07	18:29:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/06/07	18:29:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/07	18:31:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2016/06/07	18:31:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/06/07	18:32:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/07	19:14:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/07	19:14:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/07	19:14:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/07	19:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/07	19:55:30.0	XRT_Custom_430_OG [0x1ae]							
2016/06/07	19:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/07	20:52:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/07	20:52:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/07	20:52:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/07	20:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/07	21:33:00.0	XRT_Custom_430_OG [0x1ae]							
2016/06/07	21:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/07	22:30:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/07	22:30:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/07	22:30:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/07	22:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/07	23:08:00.0	XRT_Custom_430_OG [0x1ae]							
2016/06/07	23:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/08	00:09:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/08	00:09:02.0	XRT_FLD_RESET_415_OG [0x19f]							

2016/06/08	00:09:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/06/08	00:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/06/08	00:39:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/06/08	00:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/06/08	01:47:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/06/08	01:47:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/06/08	01:47:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/06/08	01:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/06/08	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/06/08	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/06/08	01:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2016/06/08	02:00:00.0	AOCS_ORe-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00 00 00 56 35	
2016/06/08	02:00:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2016/06/08	02:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2016/06/08	02:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/06/08	02:02:58.0	XRT_QT_PROG_SET_443_OG [0x1bb]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2016/06/08	02:17:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/06/08	02:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/06/08	03:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/06/08	03:23:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/06/08	03:23:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/06/08	03:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/06/08	03:55:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/06/08	03:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/06/08	04:53:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/06/08	04:53:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/06/08	04:53:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/06/08	04:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/06/08	05:34:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/06/08	05:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/06/08	06:33:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/06/08	06:33:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/06/08	06:33:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/06/08	06:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/06/08	06:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/06/08	07:12:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/06/08	07:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/06/08	07:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/06/08	07:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/06/08	07:29:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/06/08	07:30:00.0	AOCS_ORe-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2016/06/08	07:30:18.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00 00 00 00 00	
2016/06/08	07:32:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2016/06/08	07:32:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2016/06/08	07:32:58.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/06/08	07:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d	
2016/06/08	07:39:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/06/08	07:39:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/06/08	07:39:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/06/08	07:40:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	

2016/06/08	07:40:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03	00	00	00	00
		MDP_XRT_FLD_ENA		1	07-F0	d8				
2016/06/08	07:40:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/06/08	07:40:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/06/08	07:40:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/06/08	07:40:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/08	07:42:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01			
2016/06/08	07:42:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2016/06/08	07:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/08	08:13:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/08	08:13:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/08	08:13:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/08	08:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/08	08:51:00.0	XRT_Custom_430_OG [0x1ae]								
2016/06/08	08:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/08	09:54:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/08	09:54:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/08	09:54:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/08	09:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/08	09:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/08	09:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/08	09:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2016/06/08	10:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	04	00	00	00	00
2016/06/08	10:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/06/08	10:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/06/08	10:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/06/08	10:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/06/08	10:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/08	10:02:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01			
2016/06/08	10:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2016/06/08	10:29:00.0	XRT_Custom_430_OG [0x1ae]								
2016/06/08	10:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/08	11:37:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/08	11:37:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/08	11:37:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/08	11:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/08	12:07:30.0	XRT_Custom_430_OG [0x1ae]								
2016/06/08	12:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/08	13:16:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/08	13:16:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/08	13:16:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/08	13:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/08	13:46:00.0	XRT_Custom_430_OG [0x1ae]								
2016/06/08	13:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/08	13:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/08	13:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/08	13:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2016/06/08	14:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	02	00	00	00	00
2016/06/08	14:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				

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2016/06/08	14:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/06/08	14:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/06/08	14:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/06/08	14:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/06/08	14:02:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b	
2016/06/08	14:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07	
2016/06/08	14:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/06/08	14:54:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/06/08	14:54:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/06/08	14:54:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/06/08	14:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/06/08	15:31:31.0	XRT_Custom_430_OG [0x1ae]						
2016/06/08	15:32:31.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/06/08	15:37:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/06/08	15:37:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/06/08	15:37:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/06/08	15:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/06/08	15:40:30.0	XRT_Custom_430_OG [0x1ae]						
2016/06/08	15:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/06/08	16:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/06/08	16:32:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/06/08	16:32:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/06/08	16:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/06/08	17:17:30.0	XRT_Custom_430_OG [0x1ae]						
2016/06/08	17:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/06/08	17:56:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/06/08	17:56:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/06/08	17:56:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2016/06/08	17:56:30.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00 00 00 00	
2016/06/08	17:56:48.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2016/06/08	17:59:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2016/06/08	17:59:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/06/08	17:59:28.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d	
2016/06/08	17:59:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/06/08	18:06:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/06/08	18:06:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/06/08	18:06:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2016/06/08	18:06:30.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01	00 00 00 00	
2016/06/08	18:06:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2016/06/08	18:06:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/06/08	18:06:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/06/08	18:06:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/06/08	18:06:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/06/08	18:09:26.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b	
2016/06/08	18:09:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07	
2016/06/08	18:09:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/06/08	18:11:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/06/08	18:11:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		

2016/06/08	18:11:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/08	18:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/08	18:54:00.0	XRT_Custom_430_OG [0x1ae]							
2016/06/08	18:55:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/08	19:49:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/08	19:49:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/08	19:49:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/08	19:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/08	20:06:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/08	20:06:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/08	20:06:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/06/08	20:06:30.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2016/06/08	20:06:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/06/08	20:06:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/06/08	20:06:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/06/08	20:06:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/06/08	20:06:56.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/08	20:09:26.0	XRT_QT_PROG_SET_442_OG [0x1ba]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2016/06/08	20:09:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/06/08	20:31:00.5	XRT_Custom_430_OG [0x1ae]							
2016/06/08	20:32:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/08	21:28:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/08	21:28:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/08	21:28:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/08	21:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/08	22:07:30.0	XRT_Custom_430_OG [0x1ae]							
2016/06/08	22:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/08	23:06:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/08	23:06:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/08	23:06:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/08	23:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/08	23:37:30.0	XRT_Custom_430_OG [0x1ae]							
2016/06/08	23:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/09	00:44:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/09	00:44:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/09	00:44:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/09	00:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/09	01:14:30.0	XRT_Custom_430_OG [0x1ae]							
2016/06/09	01:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/09	02:21:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/09	02:21:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/09	02:21:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/09	02:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/09	02:53:00.0	XRT_Custom_430_OG [0x1ae]							
2016/06/09	02:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/09	03:49:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/09	03:49:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/09	03:49:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/09	03:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/09	04:31:30.0	XRT_Custom_430_OG [0x1ae]							



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2016/06/09	04:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/09	05:29:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/09	05:29:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/09	05:29:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/09	05:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/09	06:09:30.0	XRT_Custom_430_OG [0x1ae]							
2016/06/09	06:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/09	06:15:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/09	06:15:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/09	06:15:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/06/09	06:15:30.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2016/06/09	06:15:48.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/06/09	06:18:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/06/09	06:18:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/06/09	06:18:28.0	XRT_QT_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2016/06/09	06:18:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/09	06:25:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/09	06:25:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/09	06:25:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/06/09	06:25:30.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2016/06/09	06:25:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/06/09	06:25:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/06/09	06:25:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/06/09	06:25:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/06/09	06:25:56.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/09	06:28:26.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2016/06/09	06:28:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/06/09	06:28:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/09	07:10:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/09	07:10:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/09	07:10:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/09	07:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/09	07:48:00.0	XRT_Custom_430_OG [0x1ae]							
2016/06/09	07:49:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/09	08:49:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/09	08:49:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/09	08:49:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/09	08:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/09	09:26:30.0	XRT_Custom_430_OG [0x1ae]							
2016/06/09	09:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/09	09:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/09	09:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/09	09:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/06/09	10:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2016/06/09	10:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/06/09	10:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/06/09	10:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/06/09	10:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				

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2016/06/09	10:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/09	10:02:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01			
2016/06/09	10:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2016/06/09	10:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/09	10:31:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/09	10:31:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/09	10:31:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/09	10:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/09	11:05:00.0	XRT_Custom_430_OG [0x1ae]								
2016/06/09	11:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/09	12:13:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/09	12:13:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/09	12:13:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/09	12:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/09	12:43:00.0	XRT_Custom_430_OG [0x1ae]								
2016/06/09	12:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/09	13:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/09	13:51:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/09	13:51:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/09	13:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/09	13:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/09	13:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/09	13:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00			
2016/06/09	14:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	02	00 00 00 00			
2016/06/09	14:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/06/09	14:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/06/09	14:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/06/09	14:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/06/09	14:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/09	14:02:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2016/06/09	14:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2016/06/09	14:26:01.0	XRT_Custom_430_OG [0x1ae]								
2016/06/09	14:27:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/09	15:30:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/09	15:30:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/09	15:30:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/09	15:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/09	16:15:30.0	XRT_Custom_430_OG [0x1ae]								
2016/06/09	16:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/09	17:08:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/09	17:08:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/09	17:08:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/09	17:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/09	17:52:30.5	XRT_Custom_430_OG [0x1ae]								
2016/06/09	17:53:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/09	17:53:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/09	17:53:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/09	17:53:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00			
2016/06/09	17:54:00.0	AOCS_ORE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00 00 00 00			

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2016/06/09	17:54:18.0	XRT_FLD_DIS_406_OG [0x196]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2016/06/09	17:56:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/06/09	17:56:56.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2016/06/09	17:56:58.0	XRT_QT_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2016/06/09	17:57:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/06/09	18:03:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/06/09	18:03:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/06/09	18:03:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/06/09	18:04:00.0	AOCS_OrE-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	04 00 00 00 00
2016/06/09	18:04:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2016/06/09	18:04:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/06/09	18:04:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2016/06/09	18:04:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2016/06/09	18:04:26.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/06/09	18:06:56.0	XRT_QT_PROG_SET_442_OG [0x1ba]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2016/06/09	18:06:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/06/09	18:07:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/06/09	18:46:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/06/09	18:46:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/06/09	18:46:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/06/09	18:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/06/09	19:29:00.0	XRT_Custom_430_OG [0x1ae]			
2016/06/09	19:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/06/09	20:25:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/06/09	20:25:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/06/09	20:25:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/06/09	20:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/06/09	21:06:30.0	XRT_Custom_430_OG [0x1ae]			
2016/06/09	21:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/06/09	22:03:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/06/09	22:03:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/06/09	22:03:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/06/09	22:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/06/09	22:42:01.0	XRT_Custom_430_OG [0x1ae]			
2016/06/09	22:43:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/06/09	23:42:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/06/09	23:42:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/06/09	23:42:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/06/09	23:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/06/10	00:12:00.0	XRT_Custom_430_OG [0x1ae]			
2016/06/10	00:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/06/10	01:20:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/06/10	01:20:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/06/10	01:20:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/06/10	01:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/06/10	01:50:00.0	XRT_Custom_430_OG [0x1ae]			
2016/06/10	01:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/06/10	02:56:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/06/10	02:56:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da

2016/06/10	02:56:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/10	02:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/10	03:28:30.0	XRT_Custom_430_OG [0x1ae]							
2016/06/10	03:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/10	04:25:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/10	04:25:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/10	04:25:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/10	04:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/10	05:07:00.0	XRT_Custom_430_OG [0x1ae]							
2016/06/10	05:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/10	05:38:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/10	05:38:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/10	05:38:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/06/10	05:38:30.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2016/06/10	05:38:48.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/06/10	05:41:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/06/10	05:41:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/06/10	05:41:28.0	XRT_QT_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2016/06/10	05:41:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/10	05:48:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/10	05:48:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/10	05:48:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/06/10	05:48:30.0	AOCS_Ore-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2016/06/10	05:48:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/06/10	05:48:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/06/10	05:48:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/06/10	05:48:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/06/10	05:48:56.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/10	05:51:26.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2016/06/10	05:51:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/06/10	05:51:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/10	06:06:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/10	06:06:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/10	06:06:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/10	06:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/10	06:45:30.0	XRT_Custom_430_OG [0x1ae]							
2016/06/10	06:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/10	07:46:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/10	07:46:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/10	07:46:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/10	07:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/10	08:23:30.0	XRT_Custom_430_OG [0x1ae]							
2016/06/10	08:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/10	09:26:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/10	09:26:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/10	09:26:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/10	09:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/10	09:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/10	09:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							

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2016/06/10	09:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/06/10	10:00:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2016/06/10	10:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	04 00 00 00 00	
2016/06/10	10:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/06/10	10:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/06/10	10:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/06/10	10:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/06/10	10:02:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/06/10	10:02:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]					
2016/06/10	10:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01	
2016/06/10	10:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2016/06/10	11:10:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/06/10	11:10:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/06/10	11:10:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/06/10	11:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/06/10	11:40:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/06/10	11:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2016/06/10	12:48:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/06/10	12:48:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/06/10	12:48:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/06/10	12:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/06/10	13:19:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/06/10	13:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2016/06/10	13:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/06/10	13:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/06/10	13:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/06/10	14:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2016/06/10	14:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02 00 00 00 00	
2016/06/10	14:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/06/10	14:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/06/10	14:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/06/10	14:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/06/10	14:02:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/06/10	14:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b	
2016/06/10	14:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2016/06/10	14:27:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/06/10	14:27:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/06/10	14:27:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/06/10	14:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/06/10	15:03:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/06/10	15:04:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2016/06/10	16:05:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/06/10	16:05:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/06/10	16:05:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/06/10	16:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/06/10	16:50:31.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/06/10	16:51:31.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2016/06/10	17:44:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/06/10	17:44:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

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2016/06/10	17:44:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/06/10	17:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/06/10	18:27:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/06/10	18:27:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/06/10	18:27:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/06/10	18:27:30.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00
2016/06/10	18:27:48.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9
2016/06/10	18:30:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/06/10	18:30:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/06/10	18:30:28.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2016/06/10	18:30:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/06/10	18:37:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/06/10	18:37:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/06/10	18:37:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/06/10	18:37:30.0	AOCS_Ore-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	04 00 00 00 00
2016/06/10	18:37:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/06/10	18:37:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/06/10	18:37:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/06/10	18:37:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/06/10	18:37:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/06/10	18:40:26.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2016/06/10	18:40:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/06/10	18:40:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/06/10	19:22:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/06/10	19:22:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/06/10	19:22:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/06/10	19:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/06/10	20:04:00.0	XRT_Custom_430_OG [0x1ae]				
2016/06/10	20:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/06/10	21:00:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/06/10	21:00:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/06/10	21:00:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/06/10	21:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/06/10	21:41:00.0	XRT_Custom_430_OG [0x1ae]				
2016/06/10	21:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/06/10	22:39:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/06/10	22:39:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/06/10	22:39:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/06/10	22:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/06/10	23:15:30.0	XRT_Custom_430_OG [0x1ae]				
2016/06/10	23:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/06/11	00:17:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/06/11	00:17:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/06/11	00:17:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/06/11	00:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/06/11	00:47:30.0	XRT_Custom_430_OG [0x1ae]				
2016/06/11	00:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/06/11	01:55:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1

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2016/06/11	01:55:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/11	01:55:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/11	01:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/11	02:26:00.0	XRT_Custom_430_OG [0x1ae]							
2016/06/11	02:27:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/11	03:30:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/11	03:30:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/11	03:30:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/11	03:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/11	04:04:00.0	XRT_Custom_430_OG [0x1ae]							
2016/06/11	04:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/11	05:02:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/11	05:02:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/11	05:02:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/11	05:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/11	05:42:30.0	XRT_Custom_430_OG [0x1ae]							
2016/06/11	05:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/11	05:57:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/11	05:57:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/11	05:57:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/06/11	05:58:00.0	AOCS_OrE-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2016/06/11	05:58:18.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/06/11	06:00:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/06/11	06:00:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/06/11	06:00:58.0	XRT_QT_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2016/06/11	06:01:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/11	06:07:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/11	06:07:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/11	06:07:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/06/11	06:08:00.0	AOCS_OrE-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2016/06/11	06:08:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/06/11	06:08:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/06/11	06:08:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/06/11	06:08:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/06/11	06:08:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/11	06:10:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2016/06/11	06:10:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/06/11	06:11:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/11	06:42:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/11	06:42:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/11	06:42:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/11	06:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/11	07:21:00.0	XRT_Custom_430_OG [0x1ae]							
2016/06/11	07:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/11	08:22:00.5	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/11	08:22:03.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/11	08:22:05.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/11	08:25:15.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/11	08:59:30.0	XRT_Custom_430_OG [0x1ae]							

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2016/06/11	09:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/11	09:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/11	09:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/11	09:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/06/11	10:00:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2016/06/11	10:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/06/11	10:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/06/11	10:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/06/11	10:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/06/11	10:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/11	10:02:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/11	10:02:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/11	10:02:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/11	10:02:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2016/06/11	10:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/06/11	10:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/11	10:37:30.0	XRT_Custom_430_OG [0x1ae]							
2016/06/11	10:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/11	11:46:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/11	11:46:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/11	11:46:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/11	11:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/11	12:16:00.0	XRT_Custom_430_OG [0x1ae]							
2016/06/11	12:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/11	13:24:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/11	13:24:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/11	13:24:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/11	13:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/11	13:54:30.0	XRT_Custom_430_OG [0x1ae]							
2016/06/11	13:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/11	14:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/11	14:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/11	14:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/06/11	15:00:00.0	AOCS_Ore-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00 f0 73 b1 40				
2016/06/11	15:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/06/11	15:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/06/11	15:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/06/11	15:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/06/11	15:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/11	15:02:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/11	15:02:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/11	15:02:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/11	15:02:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2016/06/11	15:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/06/11	15:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/11	15:40:00.0	XRT_Custom_430_OG [0x1ae]							
2016/06/11	15:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/11	15:42:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/11	15:42:32.0	XRT_FLD_RESET_415_OG [0x19f]							



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2016/06/11	15:42:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/06/11	15:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/06/11	15:49:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/06/11	15:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/06/11	16:41:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/06/11	16:41:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/06/11	16:41:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/06/11	16:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/06/11	17:25:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/06/11	17:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/06/11	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/06/11	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/06/11	17:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/06/11	18:00:00.0	AOCS_Or-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00
2016/06/11	18:00:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9
2016/06/11	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/06/11	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/06/11	18:02:58.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2016/06/11	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/06/11	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/06/11	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/06/11	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/06/11	18:10:00.0	AOCS_Or-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 f0 73 b1 40
2016/06/11	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/06/11	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/06/11	18:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/06/11	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/06/11	18:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/06/11	18:12:56.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2016/06/11	18:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/06/11	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/06/11	18:19:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/06/11	18:19:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/06/11	18:19:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/06/11	18:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/06/11	19:02:01.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/06/11	19:03:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/06/11	19:57:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/06/11	19:57:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/06/11	19:57:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/06/11	20:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/06/11	20:39:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/06/11	20:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/06/11	21:36:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/06/11	21:36:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/06/11	21:36:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/06/11	21:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/06/11	22:15:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/06/11	22:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1

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2016/06/11	23:14:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/11	23:14:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/11	23:14:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/11	23:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/11	23:44:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/11	23:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/12	00:53:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/12	00:53:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/12	00:53:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/12	00:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/12	01:23:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/12	01:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/12	02:29:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/12	02:29:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/12	02:29:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/12	02:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/12	03:01:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/12	03:02:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/12	03:58:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/12	03:58:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/12	03:58:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/12	04:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/12	04:40:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/12	04:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/12	05:38:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/12	05:38:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/12	05:38:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/12	05:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/12	06:18:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/12	06:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/12	06:23:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/12	06:23:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/12	06:23:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/12	06:24:00.0	AOCS_Ore-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/06/12	06:24:18.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00 00 00 00 00				
2016/06/12	06:26:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/06/12	06:26:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/06/12	06:26:58.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/06/12	06:27:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2016/06/12	06:33:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/06/12	06:40:00.0	XRT_TCIB_XRT_S_HTR_A_ENA_417_OG [0x1a1]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/12	07:18:30.0	XRT_CTRL_MANU_400_OG [0x190]	TCIB_XRT_S_HTR_A_ENA	0	04-BC					
2016/06/12	07:18:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/12	07:18:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/12	07:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/12	08:58:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/12	08:58:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/12	08:58:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/12	09:00:00.5	AOCS_Ore-point_Start_6_OG [0x09c]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				

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2016/06/12	09:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	AOCU_NM	5	02-76	03	00	00	00	00
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2016/06/12	10:40:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/06/12	10:40:02.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET		1	07-F0	da				
2016/06/12	10:40:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]								
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2016/06/12	10:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2016/06/12	12:21:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/06/12	12:21:32.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET		1	07-F0	da				
2016/06/12	12:21:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]								
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2016/06/12	12:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2016/06/12	14:00:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/06/12	14:00:02.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET		1	07-F0	da				
2016/06/12	14:00:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]								
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2016/06/12	14:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2016/06/12	15:38:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/06/12	15:38:02.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET		1	07-F0	da				
2016/06/12	15:38:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]								
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2016/06/12	15:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2016/06/12	17:16:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/06/12	17:16:32.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET		1	07-F0	da				
2016/06/12	17:16:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]								
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2016/06/12	17:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2016/06/12	18:00:30.0	AOCs_OrE-point_Start_3_OG [0x099]								
2016/06/12	18:10:30.0	AOCs_OrE-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00	00	00	00	00
			AOCU_NM	5	02-76	03	00	00	00	00
2016/06/12	18:55:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/06/12	18:55:02.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET		1	07-F0	da				
2016/06/12	18:55:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]								
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2016/06/12	18:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2016/06/12	20:33:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/06/12	20:33:32.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET		1	07-F0	da				
2016/06/12	20:33:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]								
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2016/06/12	20:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2016/06/12	22:11:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/06/12	22:11:32.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET		1	07-F0	da				
2016/06/12	22:11:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]								
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2016/06/12	22:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2016/06/12	23:50:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/06/12	23:50:02.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET		1	07-F0	da				
2016/06/12	23:50:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]								
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2016/06/12	23:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2016/06/13	01:28:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/06/13	01:28:32.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET		1	07-F0	da				
2016/06/13	01:28:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]								
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2016/06/13	01:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2016/06/13	03:04:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/06/13	03:04:32.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET		1	07-F0	da				
2016/06/13	03:04:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]								
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				

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2016/06/13	03:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/13	04:15:00.0	AOCs_OrE-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2016/06/13	04:34:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/13	04:34:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/13	04:34:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/13	04:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/13	06:14:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/13	06:14:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/13	06:14:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/13	06:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/13	07:54:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/13	07:54:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/13	07:54:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/13	07:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/13	08:00:00.0	AOCs_OrE-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2016/06/13	09:34:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/13	09:34:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/13	09:34:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/13	09:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/13	11:18:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/13	11:18:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/13	11:18:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/13	11:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/13	12:57:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/13	12:57:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/13	12:57:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/13	13:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/13	14:35:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/13	14:35:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/13	14:35:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/13	14:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/13	16:13:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/13	16:13:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/13	16:13:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/13	16:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/13	17:37:30.0	AOCs_OrE-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2016/06/13	17:47:30.0	AOCs_OrE-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2016/06/13	17:52:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/13	17:52:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/13	17:52:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/13	17:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/13	19:30:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/13	19:30:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/13	19:30:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/13	19:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/13	21:09:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/13	21:09:02.0	XRT_FLD_RESET_415_OG [0x19f]							

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2016/06/13	21:09:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/13	21:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/13	22:47:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/13	22:47:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/13	22:47:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/13	22:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/14	00:25:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/14	00:25:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/14	00:25:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/14	00:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/14	02:03:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/14	02:03:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/14	02:03:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/14	02:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/14	03:36:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/14	03:36:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/14	03:36:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/14	03:36:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/14	03:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/14	03:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/14	05:10:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/14	05:10:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/14	05:10:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/14	05:10:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/14	05:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/14	05:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/14	05:50:00.0	AOCS_ORe-point_Start_3_OG [0x099]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/14	06:50:30.0	XRT_CTRL_MANU_400_OG [0x190]	AOCU_NM	5	02-76	00 00 00 00 00				
2016/06/14	06:50:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/14	06:50:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/14	06:50:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/14	06:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/14	06:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/14	08:30:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/14	08:30:30.0	XRT_CTRL_MANU_400_OG [0x190]	AOCU_NM	5	02-76	03 00 00 00 00				
2016/06/14	08:30:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/14	08:30:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/14	08:30:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/14	08:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/14	08:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/14	10:11:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/14	10:11:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/06/14	10:11:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/06/14	10:11:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/14	10:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/06/14	10:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/14	11:16:00.0	AOCS_ORe-point_Start_3_OG [0x099]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/06/14	11:16:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00				