

# XRT Timeline to be uploaded on 2016/07/23

Period: 2016/07/23 10:45:00 - 2016/08/02 09:34:00

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## Normal mode

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XOB #1AD5: CME watch - 4x4 - AEC 2 - Be-thin - G-band (2x2,1ms) - Leak (2x2,1ms) - 60s cad												
Term	Pointing (x, y)	Comment										
07/23 10:58:00 - 07/23 11:34:24	Fixed ( -17.0, -1006.0)	# OP start + 10min HOP 79 (1/20)										
07/23 11:37:30 - 07/23 12:04:00	Fixed ( -17.0, -906.0)	2/20										
07/23 12:56:00 - 07/23 13:12:54	Fixed ( -17.0, -706.0)	4/20										
07/23 13:16:00 - 07/23 13:42:30	Fixed ( -17.0, -606.0)	5/20										
07/23 14:19:30 - 07/23 14:36:24	Fixed ( -17.0, -506.0)	6/20										
07/23 14:39:30 - 07/23 14:56:24	Fixed ( -17.0, -406.0)	7/20										
07/23 16:11:00 - 07/23 16:27:54	Fixed ( -17.0, -206.0)	9/20										
07/23 16:31:00 - 07/23 16:59:00	Fixed ( -17.0, -106.0)	10/20										
07/23 18:08:00 - 07/23 18:37:30	Fixed ( -17.0, 84.0)	12/20										
07/23 19:44:30 - 07/23 20:16:00	Fixed ( -17.0, 284.0)	14/20										
07/23 21:01:30 - 07/23 21:18:24	Fixed ( -17.0, 384.0)	15/20										
07/23 21:21:30 - 07/23 21:54:00	Fixed ( -17.0, 484.0)	16/20										
07/23 22:37:30 - 07/23 22:54:24	Fixed ( -17.0, 584.0)	17/20										
07/24 00:04:30 - 07/24 00:21:24	Fixed ( -17.0, 784.0)	19/20										
07/24 00:24:30 - 07/24 00:41:24	Fixed ( -17.0, 884.0)	# HOP79 (20/20)										
<b>PROG= 17 Inf.-time(s)</b>												
└─ Subr= 1 60-time(s) 60.0sec												
└─ Seqn= 29 1-time(s) 4.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 26 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1B3D: CME watch - 4x4 - AEC 2 - Be-thin - G-band (2x2,1ms) - Leak (2x2,1ms) - 90s cad (G-band/Leak first)												
Term	Pointing (x, y)	Comment										
07/23 12:36:00 - 07/23 12:52:54	Fixed ( -17.0, -806.0)	3/20										
07/23 14:59:30 - 07/23 15:20:30	Fixed ( -17.0, -306.0)	8/20										
07/23 19:24:30 - 07/23 19:41:24	Fixed ( -17.0, 184.0)	13/20										
07/23 22:57:30 - 07/23 23:32:30	Fixed ( -17.0, 684.0)	18/20										
<b>PROG= 02 Inf.-time(s)</b>												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 26 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Subr= 2 40-time(s) 90.0sec												
└─ Seqn= 29 1-time(s) 2.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1B0A: Synoptic Q95 2x2 - Al/mesh(12/181/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(24/362/1443) + T												
Term	Pointing (x, y)	Comment										
07/23 17:52:00 - 07/23 18:04:54	Fixed ( -17.0, -16.0)	11/20 (synoptic)										
<b>PROG= 13 1-time(s)</b>												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 5 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec												
└─ Seqn= 91 1-time(s) 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 93 1-time(s) 2.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Al-poly/Open Al-poly/thick-Al close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 77 1-time(s) 2.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 86ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 54 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

**XOB #1B25: AR - Standard Core - (Filter-Ratio with thin-Be and Med-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Med-Be context, with**

Term	Pointing (x, y)	Comment
07/24 00:44:30 - 07/24 03:59:54	Fixed ( 899.1, -40.0)	AR 12567 obs. (HOP 173)
07/24 04:13:00 - 07/24 05:57:00	Fixed ( 899.1, -40.0)	# AR 12567 obs. (HOP 173)
<b>PROG= 10 Inf.-time(s)</b>		
<b>Subr= 1 1-time(s) 2.0sec</b>		
<b>Seqn= 56 1-time(s) 2.0sec</b>		
Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec		
Open/G-band Open/G-band close Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec		
Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec		
<b>Subr= 2 5-time(s) 2.0sec</b>		
<b>Seqn= 48 1-time(s) 2.0sec</b>		
thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec		
thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec		
med-Be/Open Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec		
med-Be/Open Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec		
<b>Seqn= 97 4-time(s) 30.0sec</b>		
thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec		
med-Be/Open Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec		
thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 2.0sec		
med-Be/Open Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 1 1 2.0sec		
thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec		
med-Be/Open Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec		
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval		

**XOB #1B35: Synoptic 7 Filter w/ Al-mesh(24/256/2897), Al-poly(45/512/4096), Thin-Be(181/2048/11571) - Thick-Be(65536), Al-poly+Ti-poly(362/4096), Med-Be**

Term	Pointing (x, y)	Comment
07/24 04:03:00 - 07/24 04:09:54	Fixed ( 0.0, 0.0)	synoptic, shifted manually
<b>PROG= 04 1-time(s)</b>		
<b>Subr= 1 1-time(s) 2.0sec</b>		
<b>Seqn= 5 1-time(s) 2.0sec</b>		
Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec		
Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec		
Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec		
Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec		
Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec		
<b>Seqn= 1 1-time(s) 2.0sec</b>		
Open/Al-mesh Open/Al-mesh close Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec		
Open/Al-mesh Open/Al-mesh close Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec		
Open/Al-mesh Open/Al-mesh close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec		
<b>Seqn= 99 1-time(s) 2.0sec</b>		
Al-poly/Open Al-poly/Open close Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec		
Al-poly/Open Al-poly/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec		
Al-poly/Open Al-poly/thick-Al close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec		
<b>Seqn= 67 1-time(s) 2.0sec</b>		
thin-Be/Open thin-Be/Open close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec		
thin-Be/Open thin-Be/Open close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec		
thin-Be/Open thin-Be/Open close Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec		
<b>Seqn= 54 1-time(s) 4.0sec</b>		
Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec		
Open/G-band Open/G-band close Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec		
<b>Subr= 2 1-time(s) 2.0sec</b>		
<b>Seqn= 46 2-time(s) 2.0sec</b>		
Open/thick-Be Open/thick-Be close Safe Norm 64.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec		
<b>Seqn= 94 2-time(s) 2.0sec</b>		
Al-poly/Ti-poly Al-poly/thick-Al close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec		
Al-poly/Ti-poly Al-poly/thick-Al close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec		
<b>Seqn= 68 2-time(s) 2.0sec</b>		
med-Al/Open med-Al/Open close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec		
med-Al/Open med-Al/Open close Safe Norm 32.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec		
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval		

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**Flare mode**

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**XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512**

Term	Pointing (x, y)	Comment
07/23 10:58:00 - 07/23 11:34:24	Fixed ( -17.0, -1006.0)	# OP start + 10min HOP 79 (1/20)
07/23 11:37:30 - 07/23 12:04:00	Fixed ( -17.0, -906.0)	2/20
07/23 12:36:00 - 07/23 12:52:54	Fixed ( -17.0, -806.0)	3/20
07/23 12:56:00 - 07/23 13:12:54	Fixed ( -17.0, -706.0)	4/20
07/23 13:16:00 - 07/23 13:42:30	Fixed ( -17.0, -606.0)	5/20
07/23 14:19:30 - 07/23 14:36:24	Fixed ( -17.0, -506.0)	6/20
07/23 14:39:30 - 07/23 14:56:24	Fixed ( -17.0, -406.0)	7/20
07/23 14:59:30 - 07/23 15:20:30	Fixed ( -17.0, -306.0)	8/20
07/23 16:11:00 - 07/23 16:27:54	Fixed ( -17.0, -206.0)	9/20
07/23 16:31:00 - 07/23 16:59:00	Fixed ( -17.0, -106.0)	10/20
07/23 18:08:00 - 07/23 18:37:30	Fixed ( -17.0, 84.0)	12/20
07/23 19:24:30 - 07/23 19:41:24	Fixed ( -17.0, 184.0)	13/20

07/23 19:44:30 - 07/23 20:16:00	Fixed ( -17.0, 284.0)	14/20
07/23 21:01:30 - 07/23 21:18:24	Fixed ( -17.0, 384.0)	15/20
07/23 21:21:30 - 07/23 21:54:00	Fixed ( -17.0, 484.0)	16/20
07/23 22:37:30 - 07/23 22:54:24	Fixed ( -17.0, 584.0)	17/20
07/23 22:57:30 - 07/23 23:32:30	Fixed ( -17.0, 684.0)	18/20
07/24 00:04:30 - 07/24 00:21:24	Fixed ( -17.0, 784.0)	19/20
07/24 00:24:30 - 07/24 00:41:24	Fixed ( -17.0, 884.0)	# HOP79 (20/20)
07/24 00:44:30 - 07/24 03:59:54	Fixed ( 899.1, -40.0)	AR 12567 obs. (HOP 173)
07/24 04:13:00 - 07/24 05:57:00	Fixed ( 899.1, -40.0)	# AR 12567 obs. (HOP 173)

**PROG= 07 30-time(s)**

<b>Subr= 1 20-time(s) 2.0sec</b>												
<b>Seqn= 11 1-time(s) 2.0sec</b>												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Seqn=100 1-time(s) 10.0sec</b>												
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>												
<b>Seqn= 10 1-time(s) 2.0sec</b>												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 11 1-time(s) 2.0sec</b>												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Seqn= 84 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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**Active Region Search**

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NOT USED

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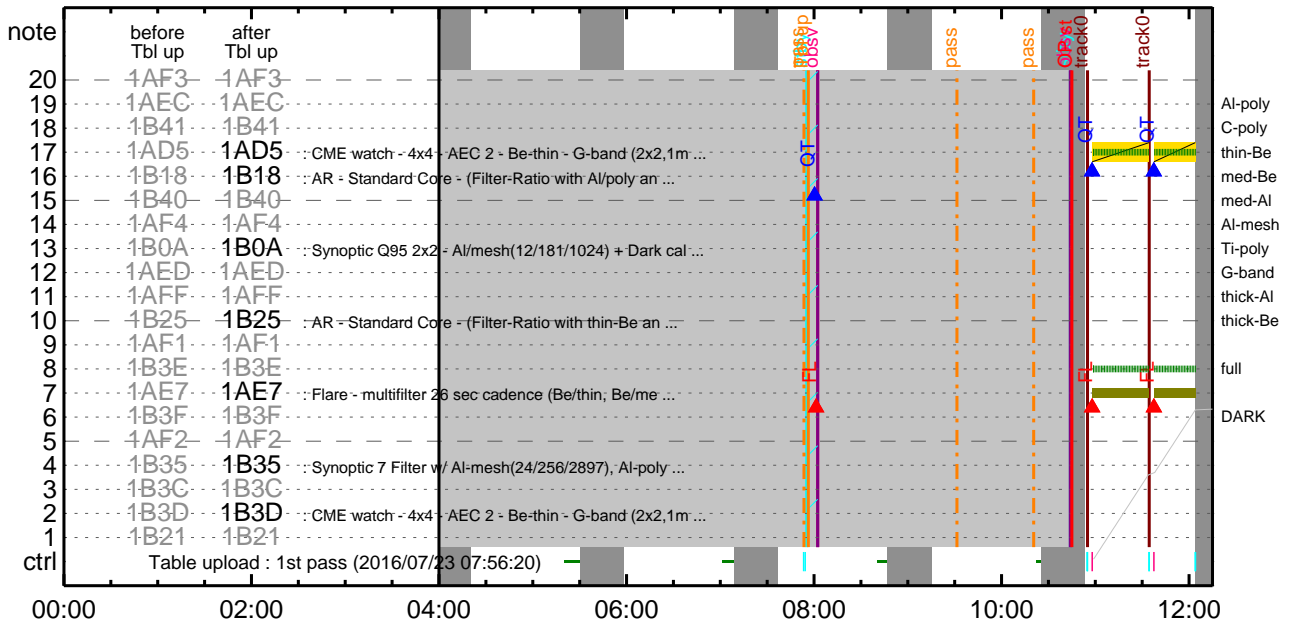
**Flare Detection**

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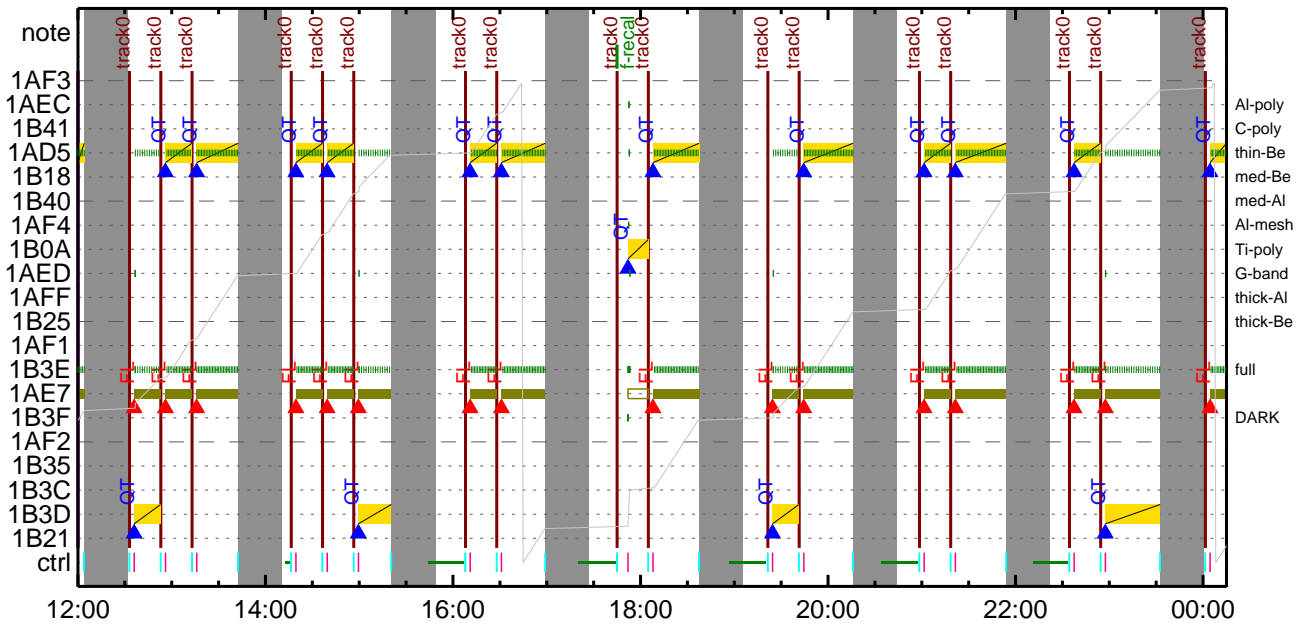
**FLD Patrol**

Term	Pointing (x, y)	Comment
07/23 18:05:18 - 07/24 04:00:18	Fixed ( -17.0, 84.0)	12/20
07/24 04:10:18 - 08/02 09:34:00	Fixed ( 899.1, -40.0)	# AR 12567 obs. (HOP 173)
Open/Ti-poly	Open/thick-Al	close Safe Norm 8ms Obs 8x8 Q=50 30sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

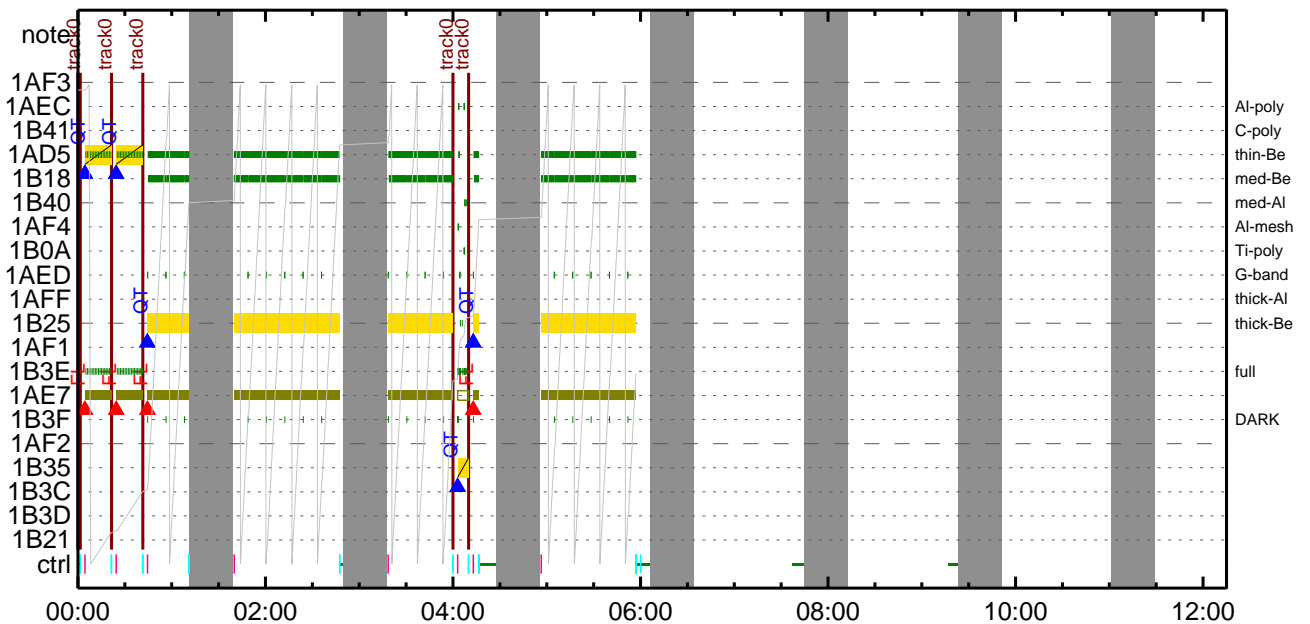
### CMDI #0069 2016/07/23



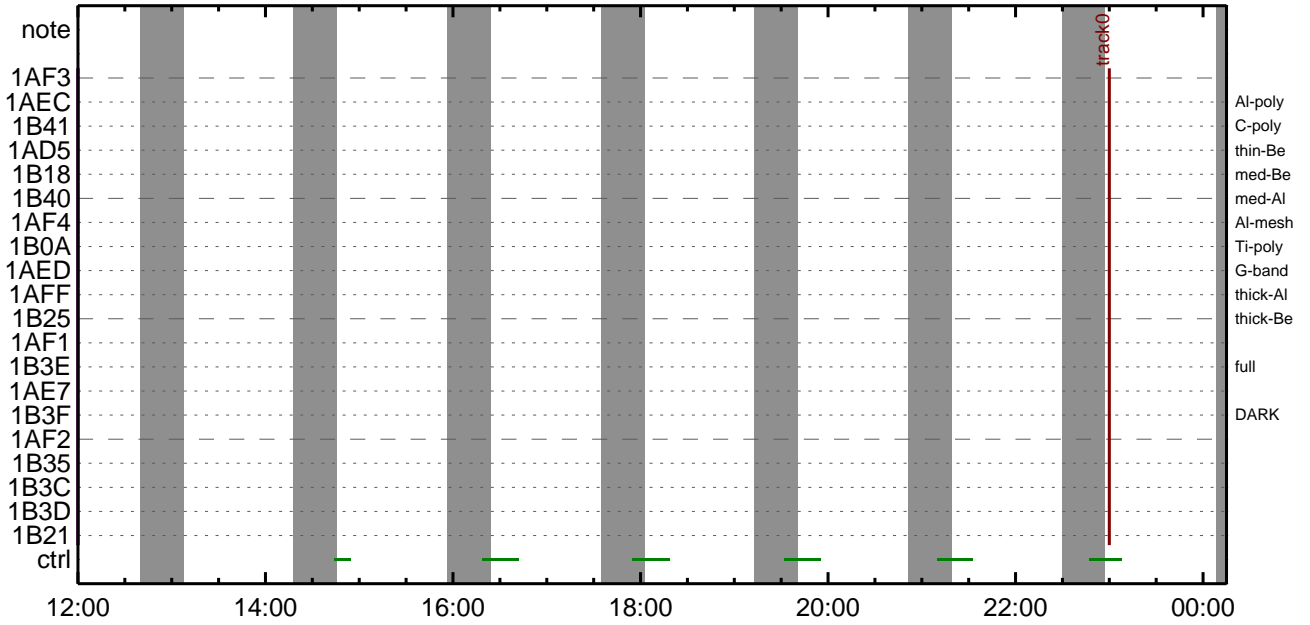
### CMDI #0069 2016/07/23



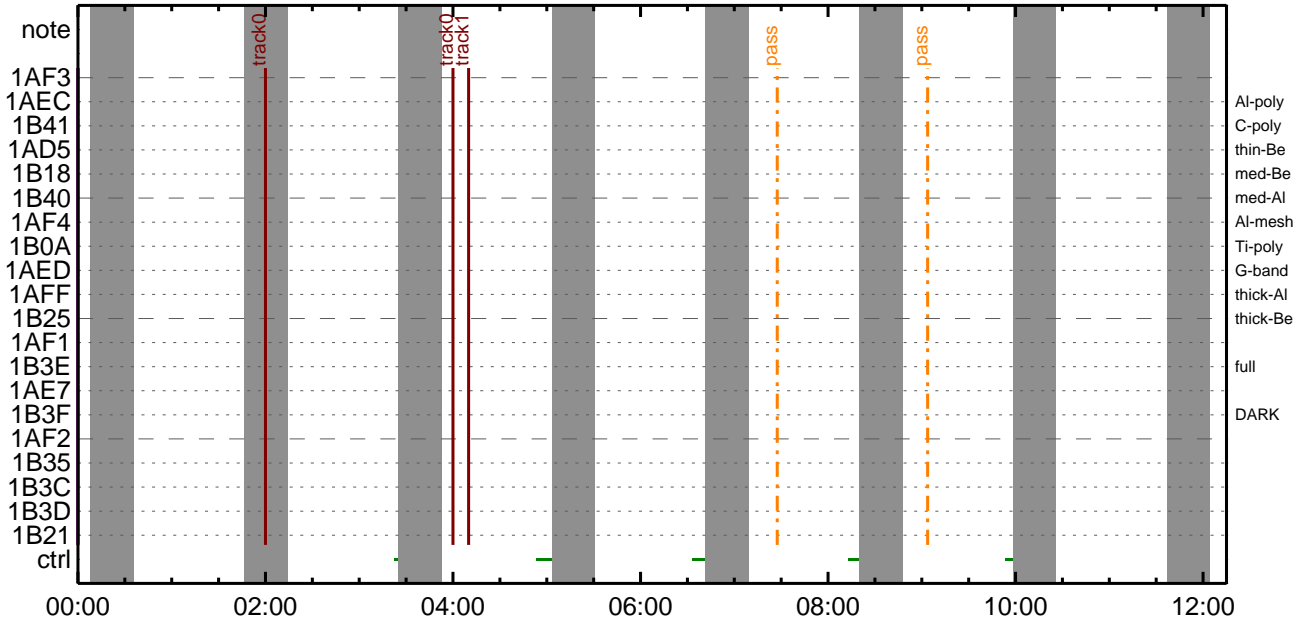
### CMDI #0069 2016/07/24



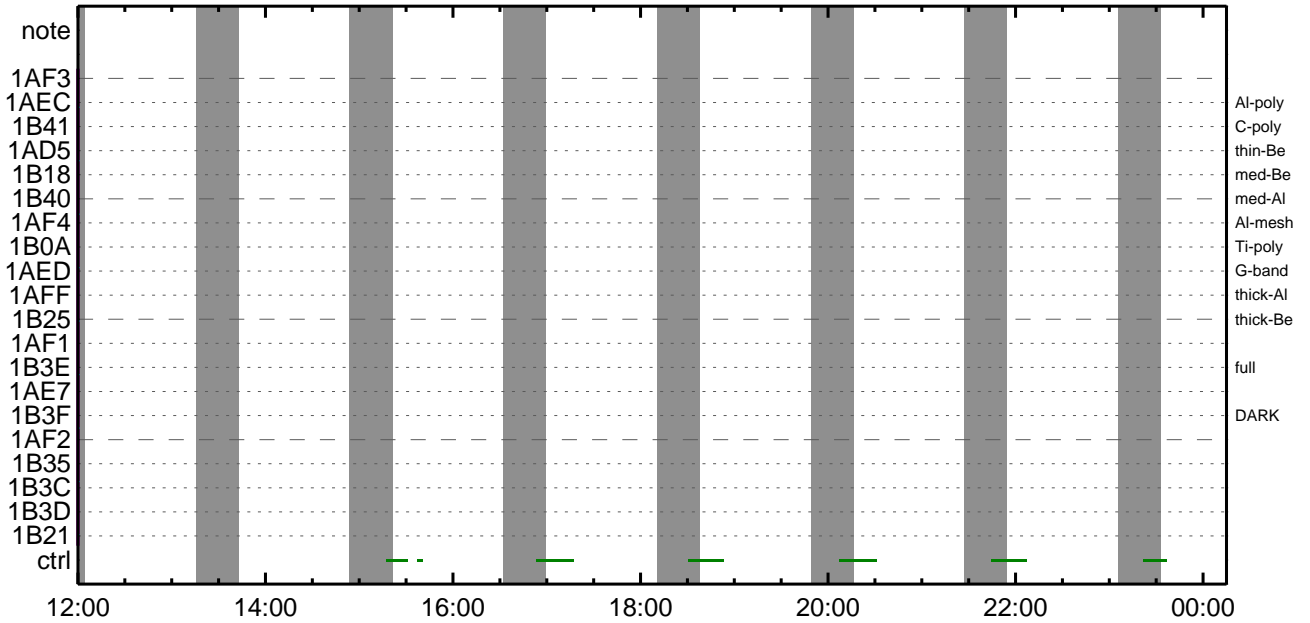
CMDI #0069 2016/07/24



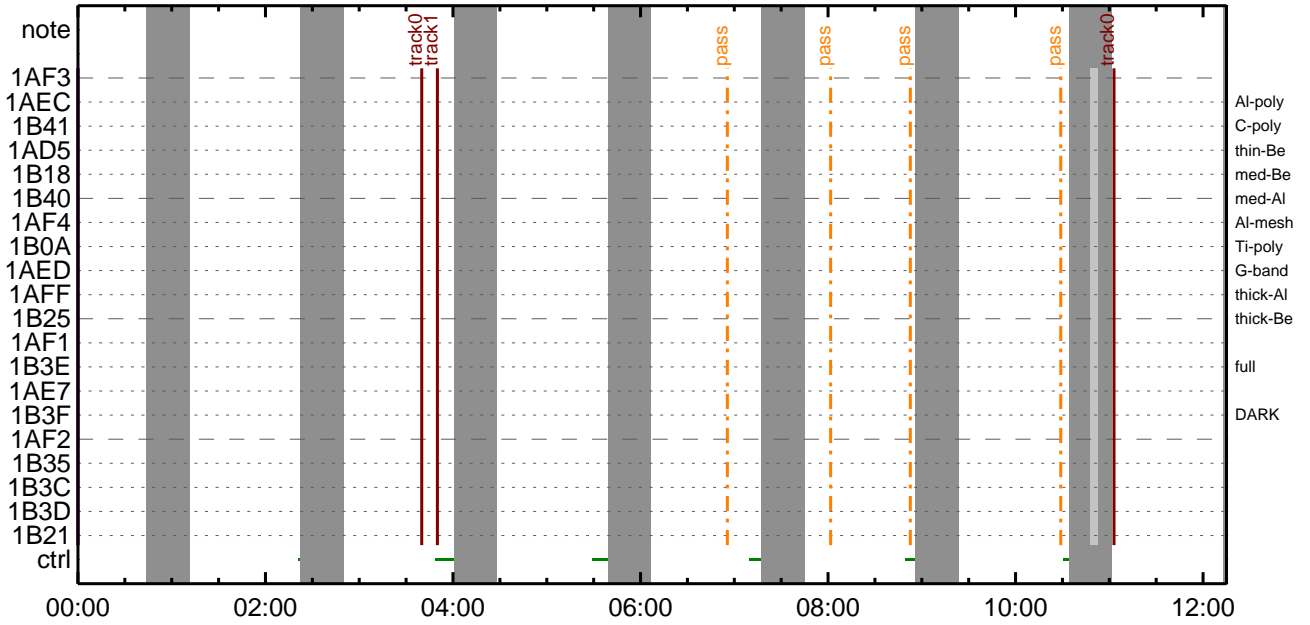
CMDI #0069 2016/07/25



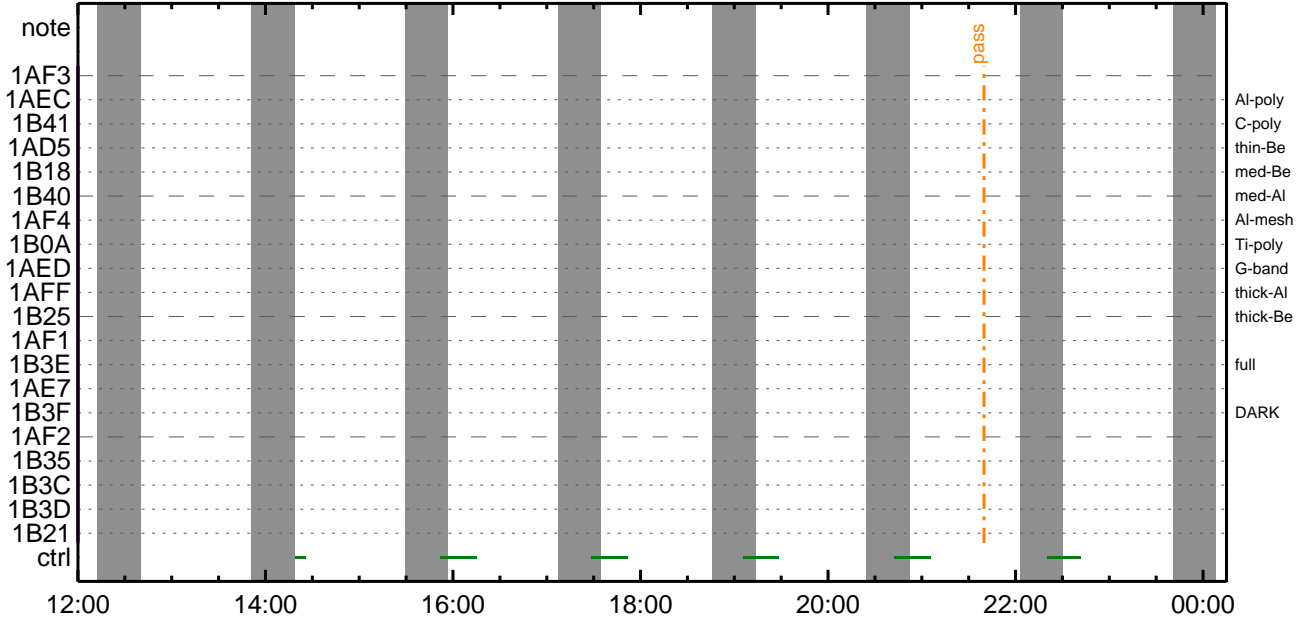
CMDI #0069 2016/07/25



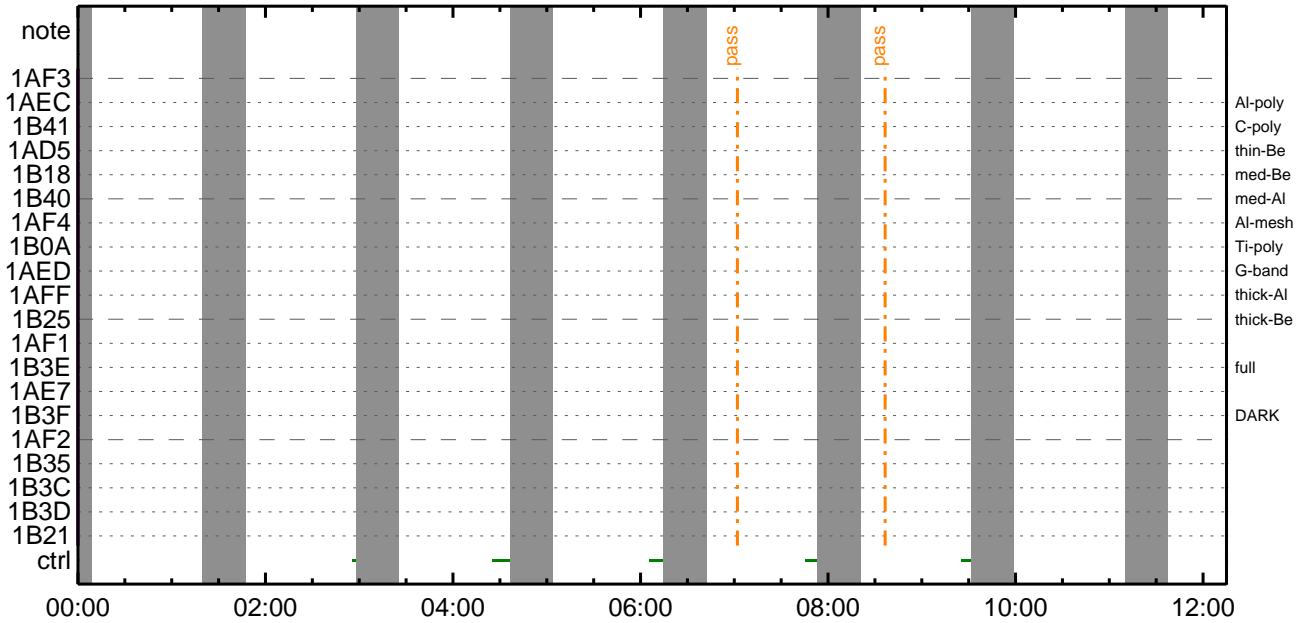
CMDI #0069 2016/07/26



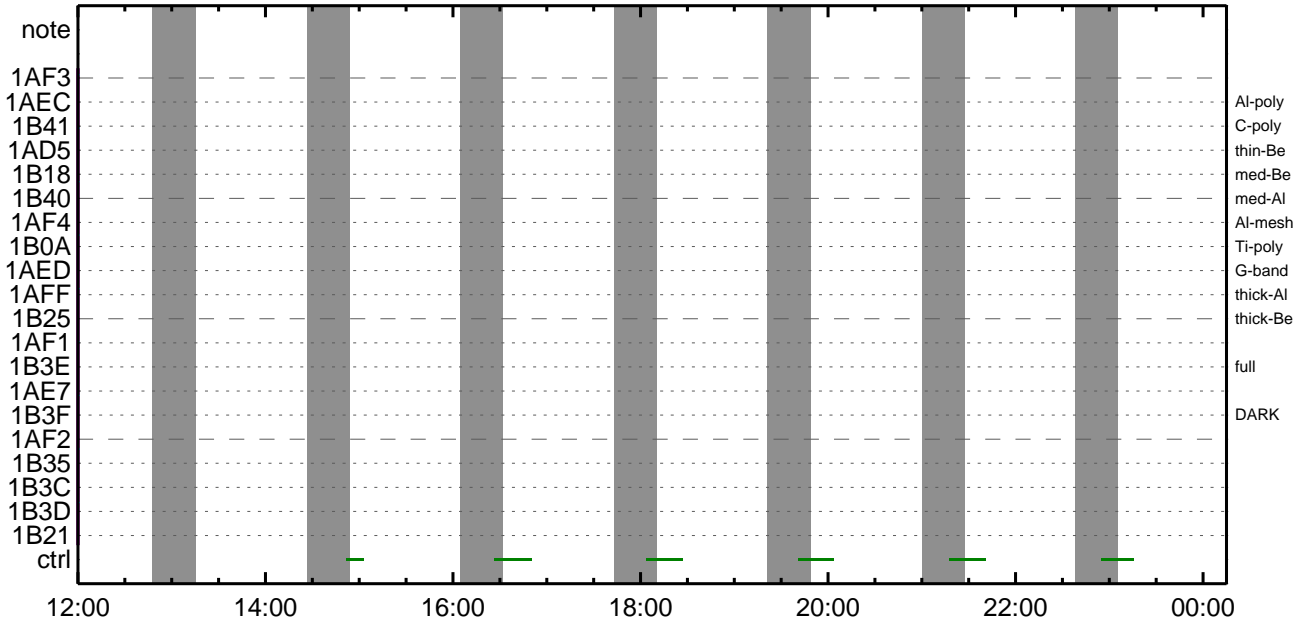
CMDI #0069 2016/07/26



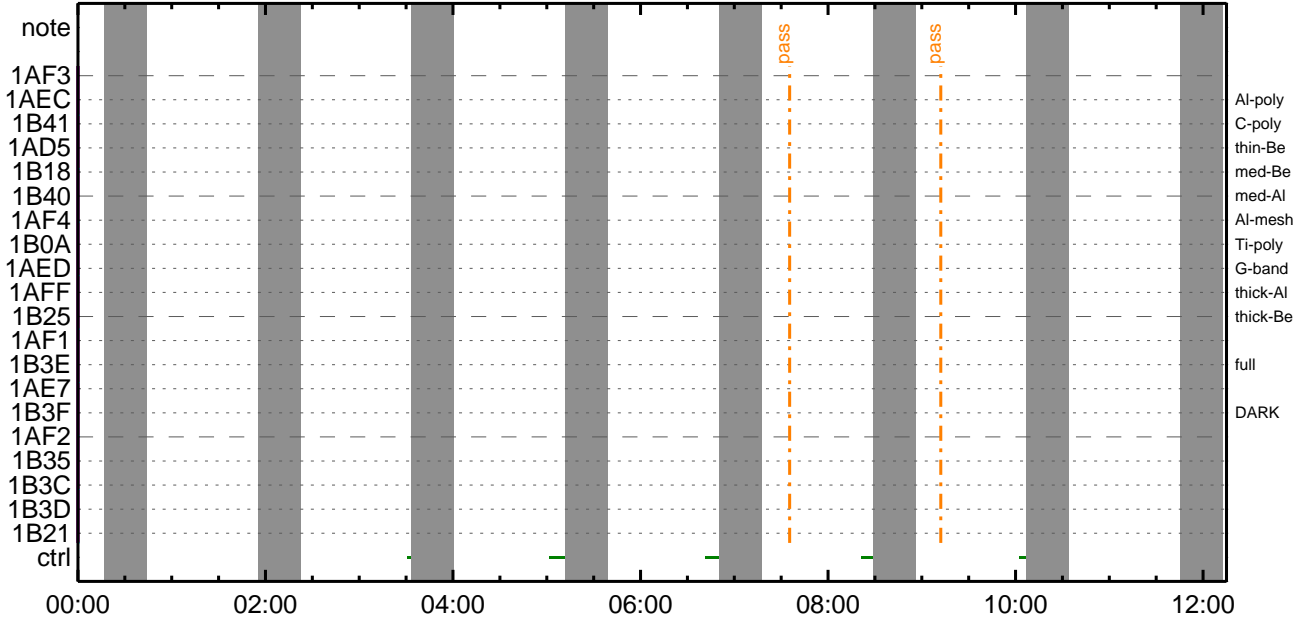
CMDI #0069 2016/07/27



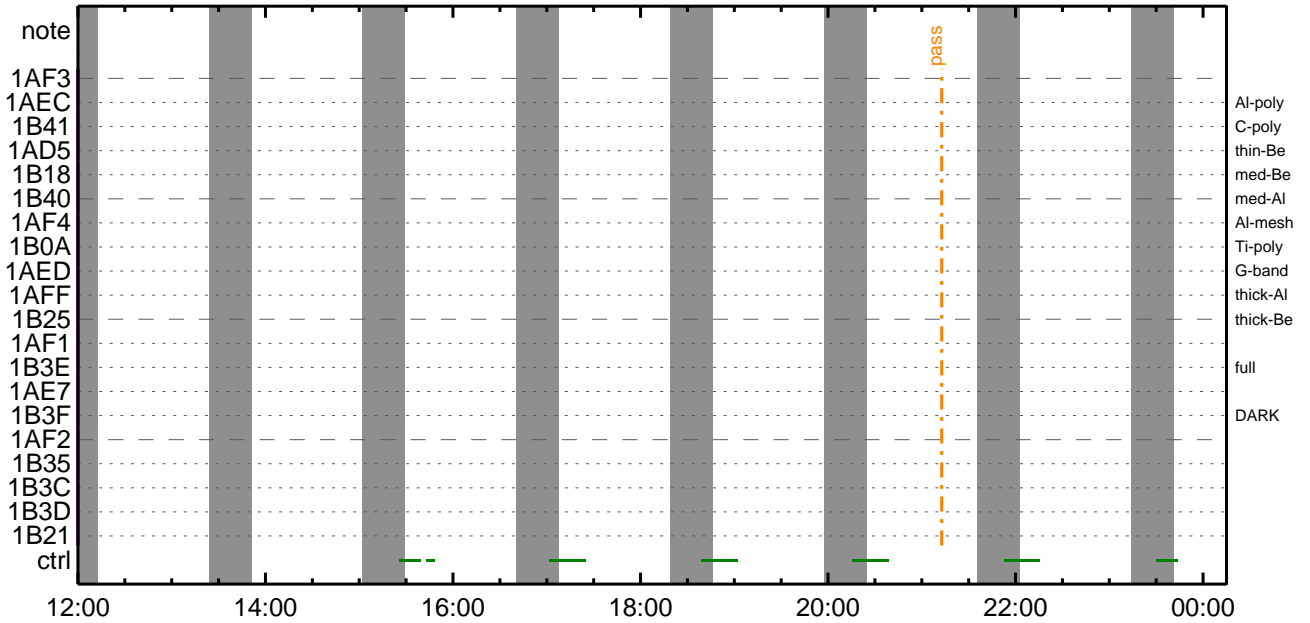
CMDI #0069 2016/07/27



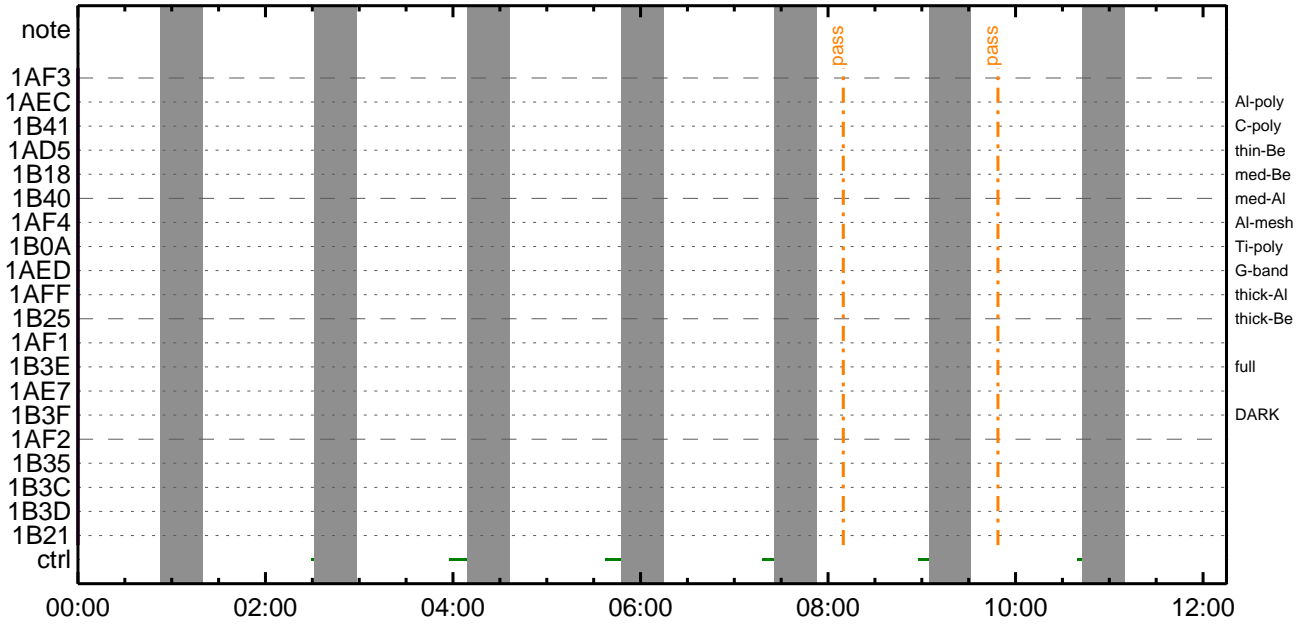
CMDI #0069 2016/07/28



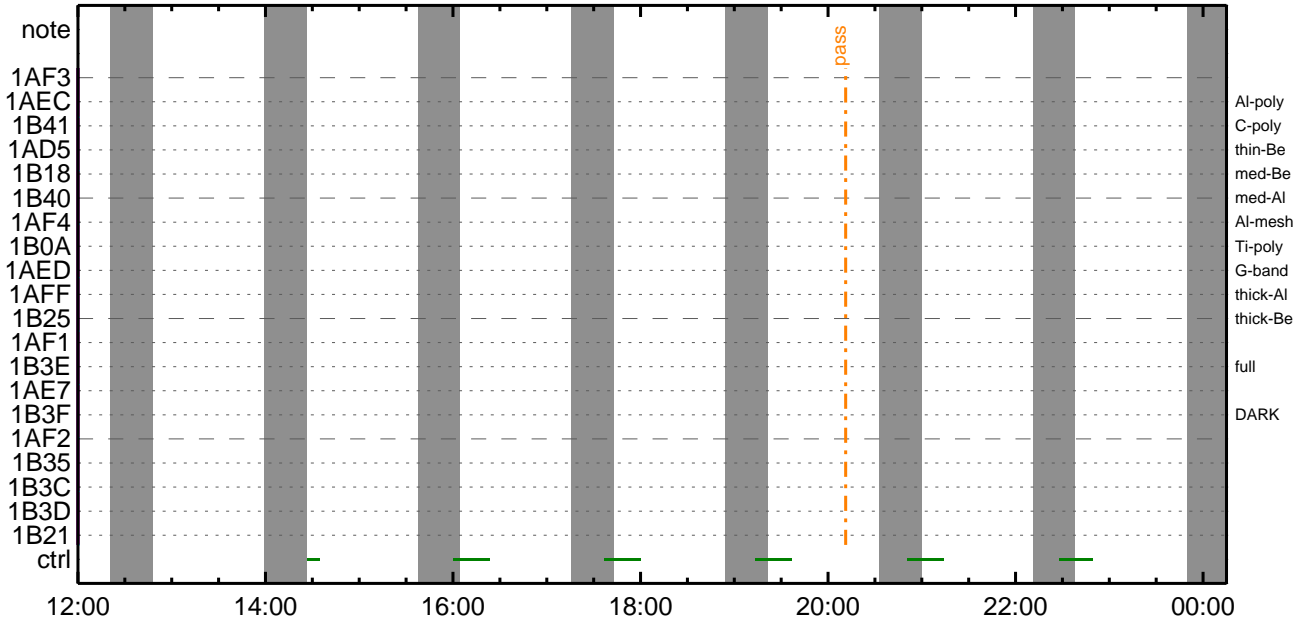
CMDI #0069 2016/07/28



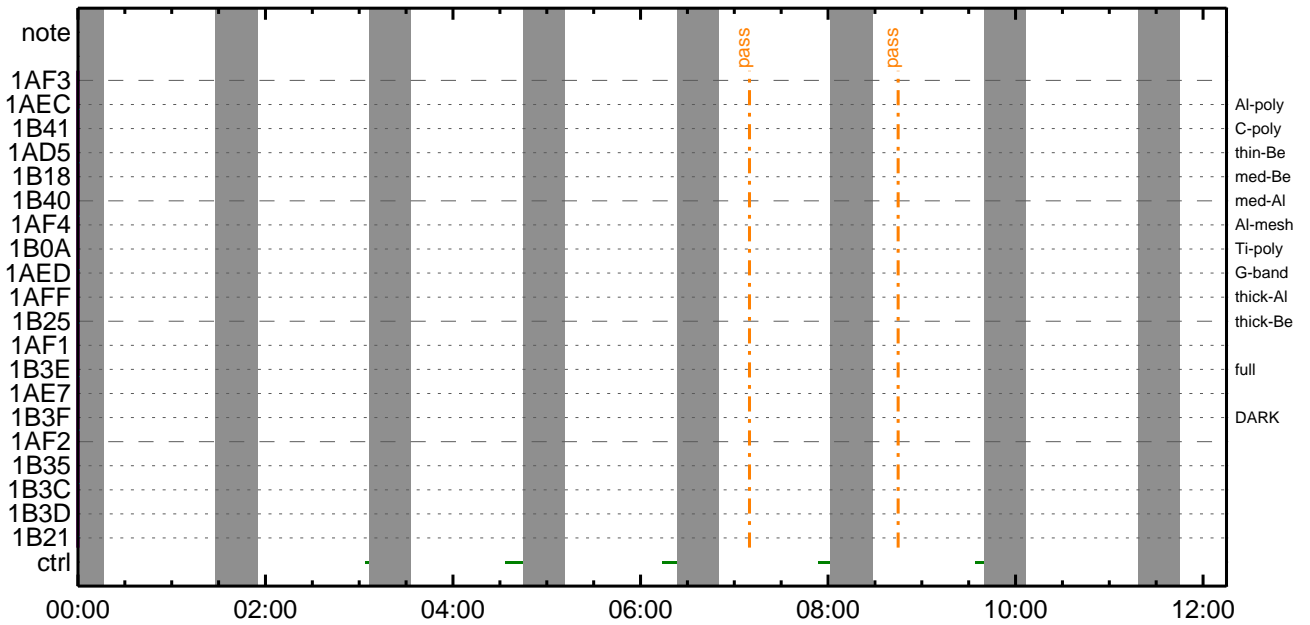
CMDI #0069 2016/07/29



CMDI #0069 2016/07/29

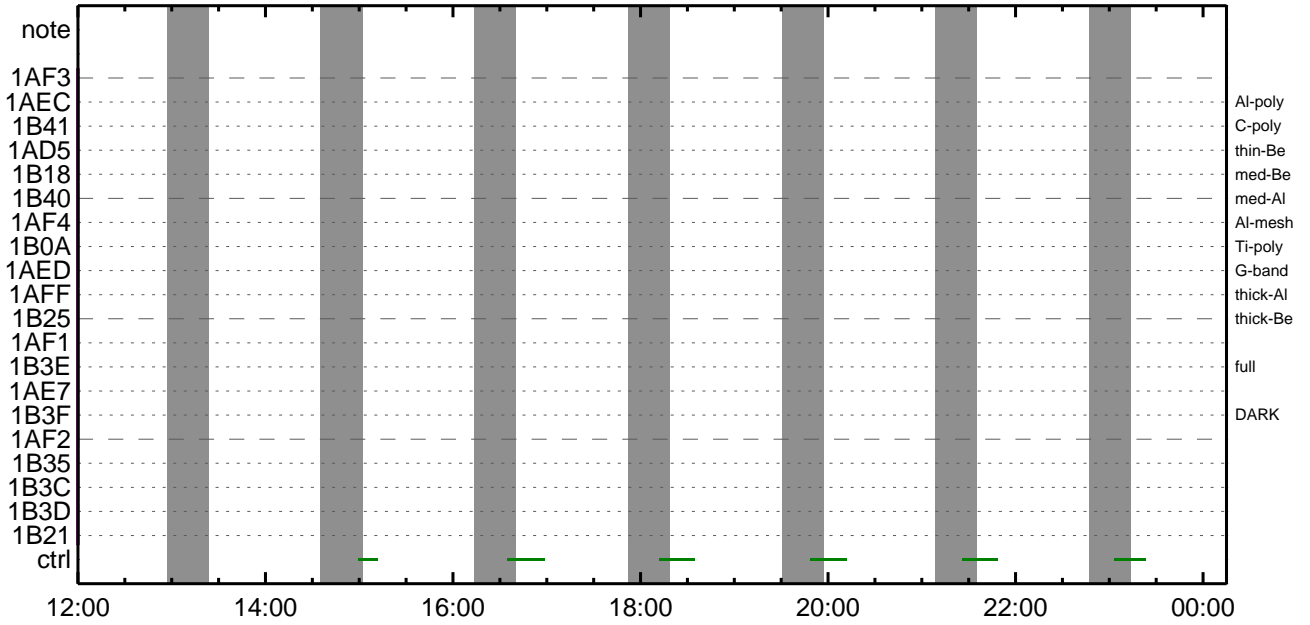


CMDI #0069 2016/07/30

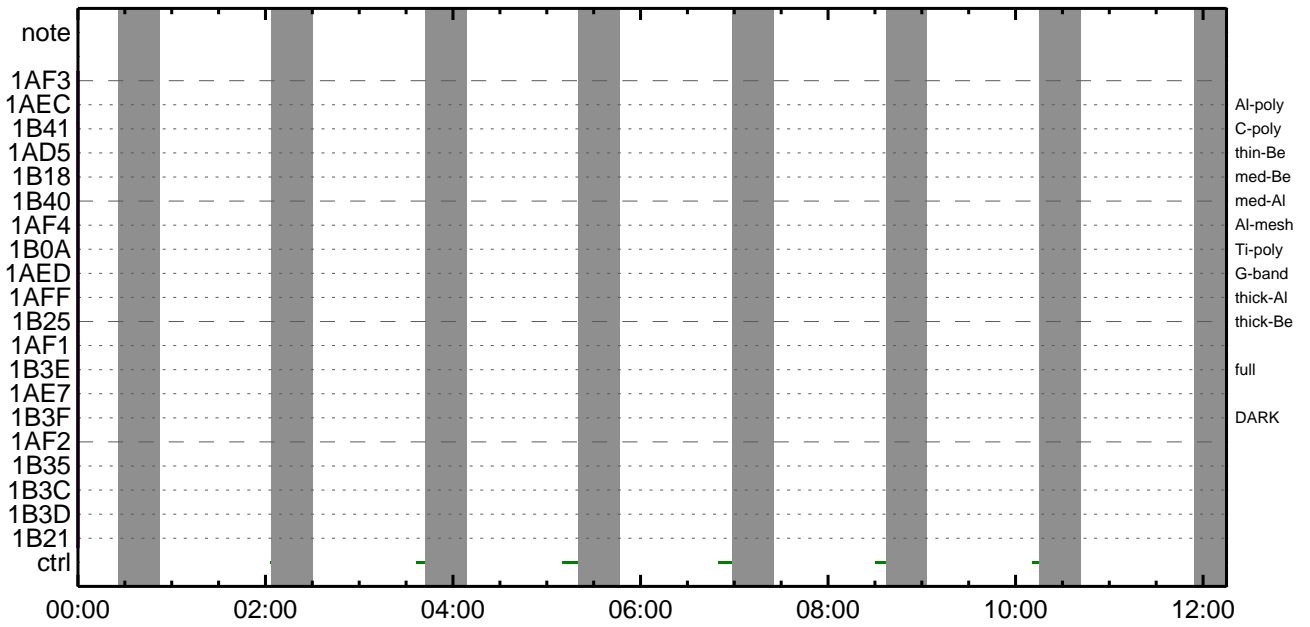




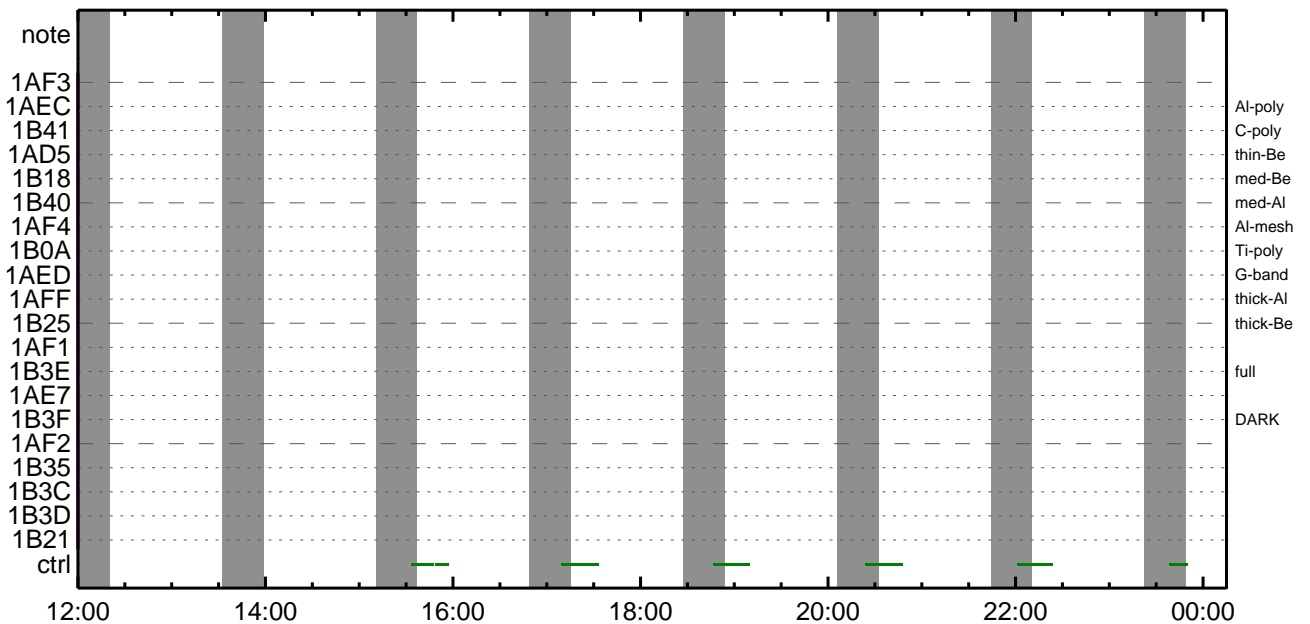
CMDI #0069 2016/07/30



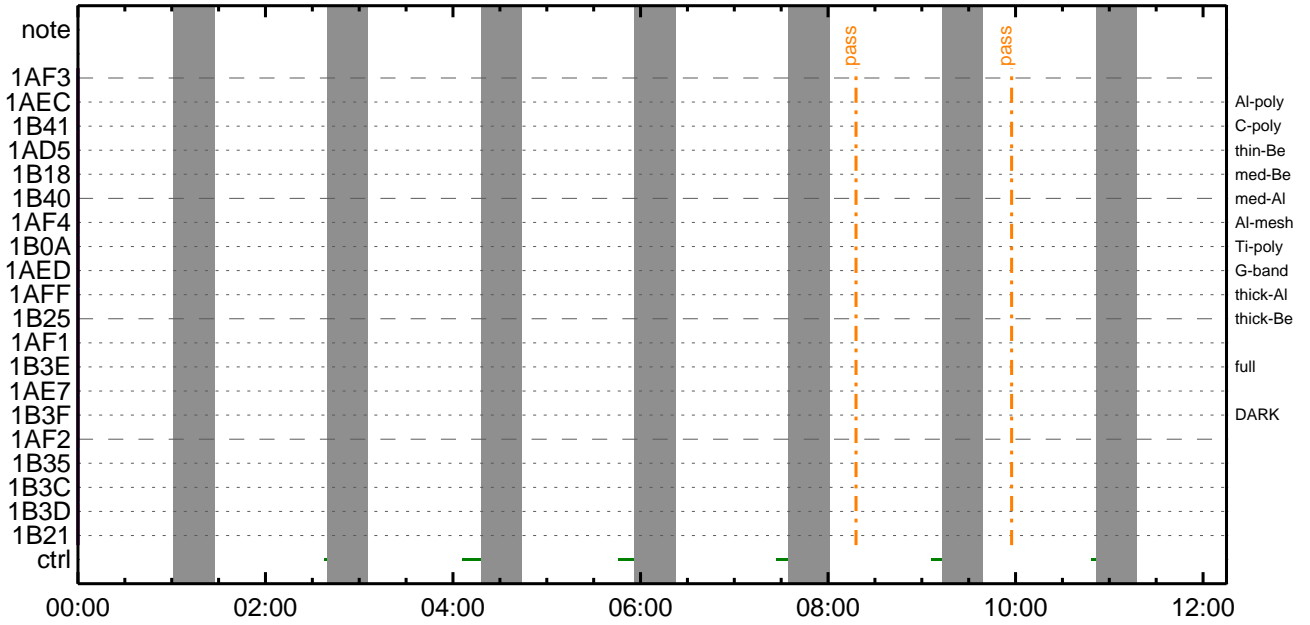
CMDI #0069 2016/07/31



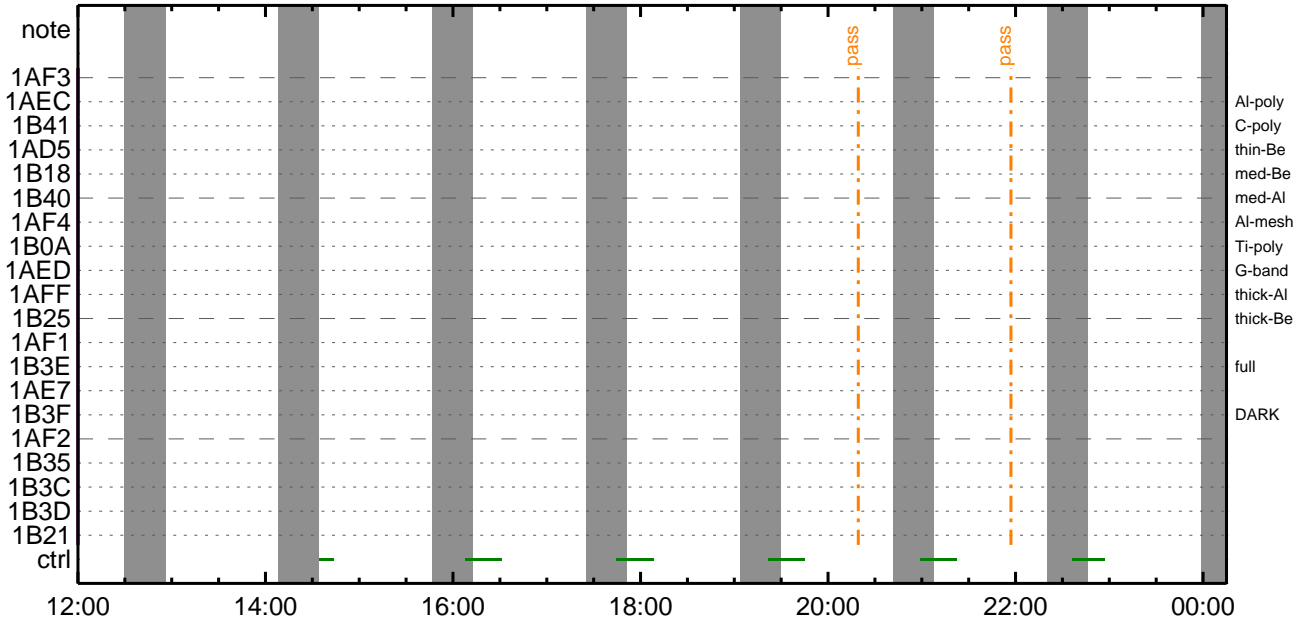
CMDI #0069 2016/07/31



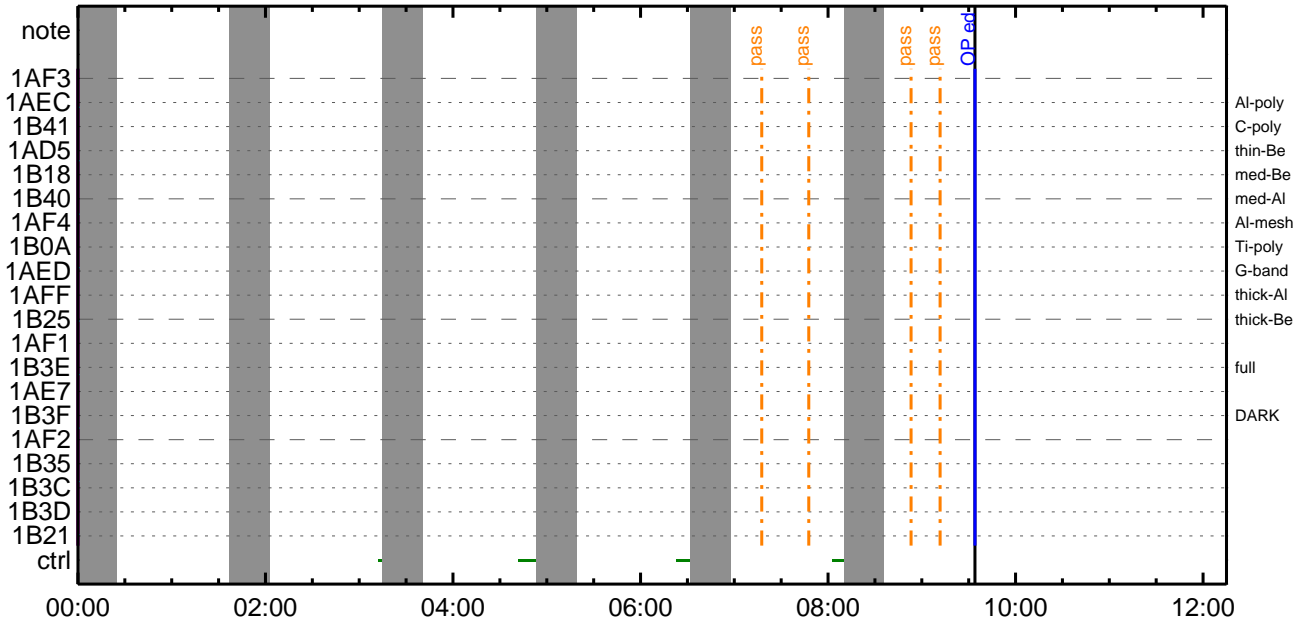
CMDI #0069 2016/08/01



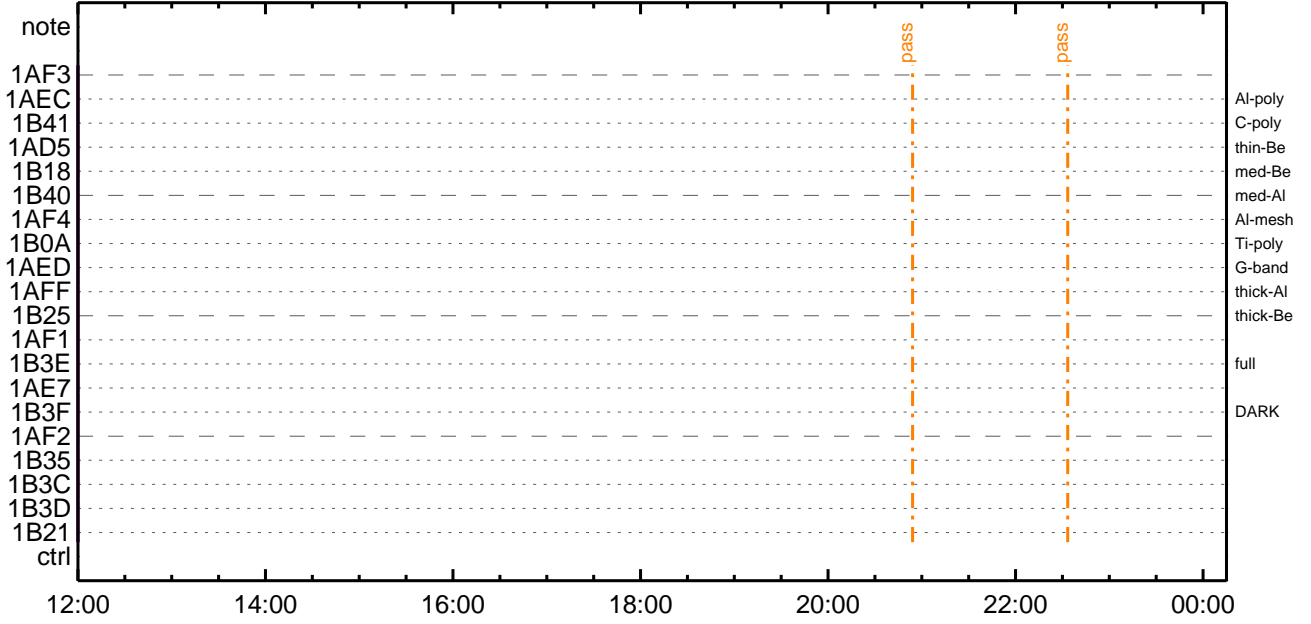
CMDI #0069 2016/08/01



CMDI #0069 2016/08/02



CMDI #0069 2016/08/02



(a) Spacecraft Operation Procedure (real-commands)

```
main-010 2016-07-23 12:10:21 96 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÁYB;¼Y³YF¥ÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È□¿□Á□•µ°È×Í×ÁÇ□íYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÈ;È□È¼°ÇÖ□•□¿¼í¹ç□í;çÁ®, ù□¹□è□È□çÁ+¿®□•□È□□□³□È;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 +. DC 07-F0 MDP_XRT_CTRL_MANU
0020 BC (c1)
0021 +. DC 07-F0 MDP_XRT_MODE_STBY
0022 BC (c3)
0023 . C. ----- Success Verify ? OK / NG____
0024 C.
0025 C. XRT Obs. Table Upload
0026 . S. RAM ram-291:MDP_OBS_X
0027 ( )
0028 C.
0029 +. DC 07-F0 MDP_DUMP_XRTTBL
0030 BC (84 07 00 00 00 3a d4)
0031 . C. ----- Comparison Check ? OK / ERR ____
0032 C.
0033 C.
0034 +. DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 01 b1 b1 04 04)
0036 +. DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 02 b1 b1 08 08)
0038 +. DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 03 b1 b1 08 08)
0040 +. DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 04 b1 b1 06 06)
0042 +. DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 06 06)
0044 +. DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 80 80 20 20)
0046 +. DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 20 08)
0048 +. DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 08 20)
0050 +. DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0f 80 80 06 06)
0052 +. DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 10 80 80 08 08)
0054 +. DC 07-F0 MDP_XRT_FLD_ENA
0055 BC (d8)
0056 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0057 BC (c8)
0058 +. DC 07-F0 MDP_XRT_ARS_DIS
0059 BC (d5)
0060 +. DC 07-F0 MDP_XRT_AEC_RESET
0061 BC (d0)
0062 +. DC 07-F0 MDP_XRT_FLD_RESET
0063 BC (da)
0064 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0065 BC (c4 10)
0066 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0067 BC (c5 07)
0068 . C. ----- Success Verify ? OK / NG ____
0069 C.
0070 C.
0071 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0072 C.
0073 +. DC 07-F0 MDP_XRT_MODE_OBSV
0074 BC (c2)
0075 +. TI 2016-07-23 10:44:02.0
0076 DC 07-F0 MDP_XRT_MODE_OBSV
0077 BC (c2)
0078 . C. ----- Success Verify ? OK / NG ____
0079 C.
0080 C. ***** XRT END *****
0081 C.
0082 . C. ***** MDP `úÁí□í»ö¼Y□ÉÁ□□¹□èDCBC•x²è *****
0083 C. (¼á°íYÁYÉY¥YÉYÁYçYè□È%¼□□¼Á»Û□¹□è)
0084 . S. DC-BC dcbc-402:DCBC
0085 (MDP_known_event)
0086 C.
0087 C.
0088 . C. ***** YÐY¹.İ Daily+¿ÍÑ□È´Ø□¹□èDCBC•x²è *****
0089 . S. DC-BC dcbc-153:DCBC
0090 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0091 C.
0092 C.
0093 . C. ;ãLOS¥ÁY$YÁY-¼Á»Û;ä
0094 C.
0095 . C. ***** LOS *****
```





0096 C.  
0097 C.  
0098 C. \*\*\*\*\*  
0099 C. OP/OGY1;YEE;ã  
0100 C. \*\*\*\*\*  
0101 C.  
0102 . C. ;ãOP/OGY1;YEE;ã  
0103 . S. OP op-998:OP  
0104 ()  
0105 . S. OG og-998:OG  
0106 ()  
0107 C.  
0108 . C. ;ãNMOG&OPÍî°èYAYóYx;ã  
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)  
0110 +. DC 01-23 DHU\_DMA\_DMP\_PRM\_SET  
0111 BC (20 00 7f 01 02)  
0112 C. çç[HK1\_DMP\_TOP\_ADRS\_1] EQ 40  
0113 C. çç[HK1\_DMP\_TOP\_ADRS\_0] EQ 0  
0114 C. çç[HK1\_DMP\_BLOCK\_NUM] EQ 127  
0115 C. çç[HK1\_DMP\_REPEAT\_NUM] EQ 0  
0116 C. çç[HK1\_DMA\_DMP\_PIM] EQ DHU  
0117 +. DC 01-22 DHU\_MODE\_CHNG  
0118 BC (07 0b f8)  
0119 C. çç[HK1\_PKT\_FORM\_NO] EQ 7  
0120 C. çç[HK1\_PKT\_GEN\_TIME] EQ 0.25 s  
0121 C. çç[HK1\_S\_TLM\_BIT\_RATE] EQ 32k  
0122 C. çç[HK1\_X\_TLM\_BIT\_RATE] EQ 4M  
0123 C. çç[HK1\_DMP\_CHK\_FLG] EQ EXEC  
0124 . C. YAYóYx½ªî»òð³îçš  
0125 C. çç[HK1\_DMP\_CHK\_FLG] EQ NON  
0126 . C. RAM ID=NMOGãî%Ê¹ç•è²îOKòð³îçš  
0127 C.  
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)  
0129 +. DC 01-23 DHU\_DMA\_DMP\_PRM\_SET  
0130 BC (20 80 7f 01 02)  
0131 C. çç[HK1\_DMP\_TOP\_ADRS\_1] EQ 41  
0132 C. çç[HK1\_DMP\_TOP\_ADRS\_0] EQ 0  
0133 C. çç[HK1\_DMP\_BLOCK\_NUM] EQ 127  
0134 C. çç[HK1\_DMP\_REPEAT\_NUM] EQ 0  
0135 C. çç[HK1\_DMA\_DMP\_PIM] EQ DHU  
0136 +. DC 01-22 DHU\_MODE\_CHNG  
0137 BC (07 0b f8)  
0138 C. çç[HK1\_PKT\_FORM\_NO] EQ 7  
0139 C. çç[HK1\_PKT\_GEN\_TIME] EQ 0.25 s  
0140 C. çç[HK1\_S\_TLM\_BIT\_RATE] EQ 32k  
0141 C. çç[HK1\_X\_TLM\_BIT\_RATE] EQ 4M  
0142 C. çç[HK1\_DMP\_CHK\_FLG] EQ EXEC  
0143 . C. YAYóYx½ªî»òð³îçš  
0144 C. çç[HK1\_DMP\_CHK\_FLG] EQ NON  
0145 . C. RAM ID=NMOGãî%Ê¹ç•è²îOKòð³îçš  
0146 C.  
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)  
0148 +. DC 01-23 DHU\_DMA\_DMP\_PRM\_SET  
0149 BC (21 00 41 01 02)  
0150 C. çç[HK1\_DMP\_TOP\_ADRS\_1] EQ 42  
0151 C. çç[HK1\_DMP\_TOP\_ADRS\_0] EQ 0  
0152 C. çç[HK1\_DMP\_BLOCK\_NUM] EQ 65  
0153 C. çç[HK1\_DMP\_REPEAT\_NUM] EQ 0  
0154 C. çç[HK1\_DMA\_DMP\_PIM] EQ DHU  
0155 +. DC 01-22 DHU\_MODE\_CHNG  
0156 BC (07 0b f8)  
0157 C. çç[HK1\_PKT\_FORM\_NO] EQ 7  
0158 C. çç[HK1\_PKT\_GEN\_TIME] EQ 0.25 s  
0159 C. çç[HK1\_S\_TLM\_BIT\_RATE] EQ 32k  
0160 C. çç[HK1\_X\_TLM\_BIT\_RATE] EQ 4M  
0161 C. çç[HK1\_DMP\_CHK\_FLG] EQ EXEC  
0162 . C. YAYóYx½ªî»òð³îçš  
0163 C. çç[HK1\_DMP\_CHK\_FLG] EQ NON  
0164 . C. RAM ID=NMOG, RAM ID=OPãî%Ê¹ç•è²îOKòð³îçš  
0165 C.  
0166 . C. \*\*\*\*\* õÊ²¼õî%Ä´¶Ä°õÊÊ¬õª÷¿@ (%ãµ-YAYóYx½ªê½çõðÄÖÃæõç½ª°ãðè%î¹çõçãä) \*\*\*\*\*  
0167 C. DHUYâ;½YEE;Ê%Y½;Yî;½YEE;Êòðîã¹  
0168 +. DC 01-22 DHU\_MODE\_CHNG  
0169 BC (02 0a f8)  
0170 C. çç[HK1\_PKT\_FORM\_NO] EQ 2  
0171 C. çç[HK1\_PKT\_GEN\_TIME] EQ 0.5S  
0172 C. çç[HK1\_S\_TLM\_BIT\_RATE] EQ 32K  
0173 C. çç[HK1\_X\_TLM\_BIT\_RATE] EQ 4M  
0174 C.  
0175 C. \*\*\*\*\*  
0176 C. TI-CMD SET (OPOG STOP/COPY/START)  
0177 C. \*\*\*\*\*  
0178 C.  
0179 . C. NOTICE ;§ OPOG UPLOADã¬Á÷¿@NGõî%î¹ç;ç°Ê²¼õîTIT-CMDÁ÷¿@ãî%Ä¹Ôõ•õÊõõõ³õÊ;f  
0180 C. õÊõ¿;çSETõÊDUMPAîÆ±õîYÑY¹õç¹Ôõ|õ³õÊ;f  
0181 C.  
0182 . C. TIY³YÑYóYEEòðÄDî¿(UT)  
0183 +. TI 2016-07-23 10:40:00.0  
0184 DC 01-B3 DHU\_OP\_STOP  
0185 C. çç[HK1\_TI\_CMD\_NUM] EQ 1COUNTUP  
0186 C.  
0187 +. TI 2016-07-23 10:40:01.0  
0188 DC 01-B4 DHU\_OP\_COPY  
0189 C. çç[HK1\_TI\_CMD\_NUM] EQ 1COUNTUP  
0190 C.  
0191 +. TI 2016-07-23 10:40:01.0  
0192 DC 01-B5 DHU\_OPOG\_COPY  
0193 C. çç[HK1\_TI\_CMD\_NUM] EQ 1COUNTUP

```
0194 C.
0195 +. TI 2016-07-23 10:44:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.             €€[HK1_TI_CMD_NUM]              EQ      1COUNTUP
0198 C.
0199 C. °È²¼ºãîÄë%îíñòîî¥Ä¥§¥Ä¥~¹àîÛ
0200 C.             €€[HK1_TI_CMD_ENA/DIS]          EQ      ENA
0201 C.             €€[HK1_TI_CMD_NUM]              EQ      4
0202 C.             €€[HK1_NEXT_EXEC_PIM]           EQ      DHU
0203 C.             €€[HK1_NEXT_EXEC_DC]            EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîî°ë¥Ä¥Ö¥×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.             €€[HK1_DMP_TOP_ADRS_1]         EQ      07
0213 C.             €€[HK1_DMP_TOP_ADRS_0]         EQ      2B
0214 C.             €€[HK1_DMP_BLOCK_NUM]          EQ      3
0215 C.             €€[HK1_DMP_REPEAT_NUM]         EQ      0
0216 C.             €€[HK1_DMA_DMP_PIM]           EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.             €€[HK1_PKT_FORM_NO]            EQ      7
0220 C.             €€[HK1_PKT_GEN_TIME]           EQ      0.25 s
0221 C.             €€[HK1_S_TLM_BIT_RATE]        EQ      32k
0222 C.             €€[HK1_X_TLM_BIT_RATE]        EQ      4M
0223 C.             €€[HK1_DMP_CHK_FLG]           EQ      EXEC
0224 C.
0225 C. ¥Ä¥Ö¥×½ªî»ò³îÇ§
0226 C.             €€[HK1_DMP_CHK_FLG]           EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLîî°è¹ç•ë²îOKò³îÇ§
0229 C.
0230 C. DHU¥ä;¼¥È;È¼¥½. ¥î;¼¥È;Èòðîäº¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.             €€[HK1_PKT_FORM_NO]            EQ      2
0234 C.             €€[HK1_PKT_GEN_TIME]           EQ      0.5S
0235 C.             €€[HK1_S_TLM_BIT_RATE]        EQ      32K
0236 C.             €€[HK1_X_TLM_BIT_RATE]        EQ      4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2016-07-23 10:44:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2016-07-23 10:44:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.             [ ] [HK1_TI_CMD_NUM]          EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.
0255 C. ***** XRT START *****
0256 C. Execute, after the success of OP upload.
0257 +. TI 2016-07-23 10:44:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC      (c3)
0260 C.             [ ] [HK1_TI_CMD_NUM]          EQ      1COUNTUP
0261 C.
0262 C. ***** XRT END *****
0263 C.
0264 C. ***** MDP `ûÄîòî»ò¼¥ºÈÄò¹òèDCBC•×²è *****
0265 C. (¼äºî¥Ö¥Ä¥È¥Þ¥È¥â¥ç¥ëòÈ¼ºã¼Ä»Ûº¹òè)
0266 S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 C. ***** ¥Ð¥¹•İ Daily±;îñòÈ¹øº¹òèDCBC•×²è *****
0271 S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 C. ;äLOS¥Ä¥§¥Ä¥~¼Ä»Û;ä
0276 C.
0277 C. ***** LOS *****
0278 C.
```







\*\*\* OP Sequence for XRT \*\*\*

```

2016/07/23 10:54:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/07/23 10:54:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/07/23 10:54:58.0 XRT_FOCUS_POSITION_403_OG [0x193]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2016/07/23 10:55:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 00 59 68 01 81
2016/07/23 10:55:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2016/07/23 10:55:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2016/07/23 10:55:22.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2016/07/23 10:55:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2016/07/23 10:55:26.0 XRT_FLD_RESET_433_OG [0x1b1]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2016/07/23 10:57:56.0 XRT_QT_PROG_SET_428_OG [0x1ac]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 11
2016/07/23 10:57:58.0 XRT_FL_PROG_SET_436_OG [0x1b4]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 07
2016/07/23 10:58:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2016/07/23 11:34:24.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/07/23 11:34:26.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/07/23 11:34:28.0 XRT_FOCUS_POSITION_403_OG [0x193]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2016/07/23 11:34:30.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 00 50 8b 01 81
2016/07/23 11:34:48.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2016/07/23 11:34:50.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2016/07/23 11:34:52.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2016/07/23 11:34:54.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2016/07/23 11:34:56.0 XRT_FLD_RESET_433_OG [0x1b1]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2016/07/23 11:37:26.0 XRT_QT_PROG_SET_428_OG [0x1ac]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 11
2016/07/23 11:37:28.0 XRT_FL_PROG_SET_436_OG [0x1b4]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 07
2016/07/23 11:37:30.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2016/07/23 12:04:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/07/23 12:04:02.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/07/23 12:04:04.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2016/07/23 12:04:06.0 XRT_PREFLR_STRT_432_OG [0x1b0]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2016/07/23 12:07:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2016/07/23 12:32:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/07/23 12:32:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/07/23 12:32:58.0 XRT_FOCUS_POSITION_403_OG [0x193]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2016/07/23 12:33:00.0 AOCs_OrE-point_Start_3_OG [0x099]
                        AOCU_NM 5 02-76 00 47 a5 01 81
2016/07/23 12:33:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2016/07/23 12:33:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2016/07/23 12:33:22.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2016/07/23 12:33:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2016/07/23 12:33:26.0 XRT_FLD_RESET_433_OG [0x1b1]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2016/07/23 12:35:56.0 XRT_QT_PROG_SET_427_OG [0x1ab]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 02
2016/07/23 12:35:58.0 XRT_FL_PROG_SET_436_OG [0x1b4]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 07
2016/07/23 12:36:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2016/07/23 12:52:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/07/23 12:52:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/07/23 12:52:58.0 XRT_FOCUS_POSITION_403_OG [0x193]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2016/07/23 12:53:00.0 AOCs_OrE-point_Start_4_OG [0x09a]
                        AOCU_NM 5 02-76 00 3e c0 01 81
2016/07/23 12:53:18.0 XRT_FLD_ENA_411_OG [0x19b]

```

Jul 23, 16 12:10

## XRT\_OGLIST\_0069.chk

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2016/07/23	12:53:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/07/23	12:53:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/07/23	12:53:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/07/23	12:53:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/07/23	12:55:56.0	XRT_QT_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11	
2016/07/23	12:55:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07	
2016/07/23	12:56:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/07/23	13:12:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/07/23	13:12:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/07/23	13:12:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa 00
2016/07/23	13:13:00.0	AOCS_OrE-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00	35	db 01 81
2016/07/23	13:13:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2016/07/23	13:13:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/07/23	13:13:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/07/23	13:13:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/07/23	13:13:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/07/23	13:15:56.0	XRT_QT_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11	
2016/07/23	13:15:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07	
2016/07/23	13:16:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/07/23	13:42:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/07/23	13:42:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/07/23	13:42:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/07/23	13:42:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/07/23	13:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/07/23	14:16:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/07/23	14:16:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/07/23	14:16:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa 00
2016/07/23	14:16:30.0	AOCS_OrE-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00	2c	fd 01 81
2016/07/23	14:16:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2016/07/23	14:16:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/07/23	14:16:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/07/23	14:16:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/07/23	14:16:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/07/23	14:19:26.0	XRT_QT_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11	
2016/07/23	14:19:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07	
2016/07/23	14:19:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/07/23	14:36:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/07/23	14:36:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/07/23	14:36:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa 00
2016/07/23	14:36:30.0	AOCS_OrE-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00	24	18 01 81
2016/07/23	14:36:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2016/07/23	14:36:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/07/23	14:36:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/07/23	14:36:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/07/23	14:36:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/07/23	14:39:26.0	XRT_QT_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11	

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2016/07/23	14:39:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/07/23	14:39:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/07/23	14:56:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/07/23	14:56:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/07/23	14:56:28.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/07/23	14:56:30.0	AOCS_ORe-point_Start_8_OG [0x09e]			
		AOCU_NM	5	02-76	00 1b 33 01 81
2016/07/23	14:56:48.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2016/07/23	14:56:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/07/23	14:56:52.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2016/07/23	14:56:54.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2016/07/23	14:56:56.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/07/23	14:59:26.0	XRT_QT_PROG_SET_427_OG [0x1ab]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2016/07/23	14:59:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/07/23	14:59:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/07/23	15:20:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/07/23	15:20:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/07/23	15:20:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/07/23	15:20:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/07/23	15:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/07/23	16:07:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/07/23	16:07:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/07/23	16:07:58.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/07/23	16:08:00.0	AOCS_ORe-point_Start_9_OG [0x09f]			
		AOCU_NM	5	02-76	00 12 4d 01 81
2016/07/23	16:08:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2016/07/23	16:08:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/07/23	16:08:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2016/07/23	16:08:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2016/07/23	16:08:26.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/07/23	16:10:56.0	XRT_QT_PROG_SET_428_OG [0x1ac]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2016/07/23	16:10:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/07/23	16:11:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/07/23	16:27:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/07/23	16:27:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/07/23	16:27:58.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/07/23	16:28:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]			
		AOCU_NM	5	02-76	00 09 68 01 81
2016/07/23	16:28:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2016/07/23	16:28:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/07/23	16:28:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2016/07/23	16:28:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2016/07/23	16:28:26.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/07/23	16:30:56.0	XRT_QT_PROG_SET_428_OG [0x1ac]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2016/07/23	16:30:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/07/23	16:31:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/07/23	16:59:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/07/23	16:59:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/07/23	16:59:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/07/23	16:59:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]			

2016/07/23	17:02:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/07/23	17:44:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/07/23	17:44:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/07/23	17:44:58.0	XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]	XRT_FOCUS_RECAL	2	07-F8	78	00
2016/07/23	17:45:00.0	AOCS_Ore-point_Start_11_OG [0x0a1]	AOCU_NM	5	02-76	00	01 68 01 81
2016/07/23	17:48:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2016/07/23	17:49:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2016/07/23	17:51:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2016/07/23	17:51:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/07/23	17:51:58.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d
2016/07/23	17:52:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/07/23	18:04:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/07/23	18:04:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/07/23	18:04:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2016/07/23	18:05:00.0	AOCS_Ore-point_Start_12_OG [0x0a2]	AOCU_NM	5	02-76	00	f8 8c 01 81
2016/07/23	18:05:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/07/23	18:05:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/07/23	18:05:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/07/23	18:05:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/07/23	18:05:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/07/23	18:07:56.0	XRT_QT_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11
2016/07/23	18:07:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2016/07/23	18:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/07/23	18:37:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/07/23	18:37:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/07/23	18:37:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/07/23	18:37:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/07/23	18:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/07/23	19:21:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/07/23	19:21:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/07/23	19:21:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2016/07/23	19:21:30.0	AOCS_Ore-point_Start_13_OG [0x0a3]	AOCU_NM	5	02-76	00	ef a6 01 81
2016/07/23	19:21:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/07/23	19:21:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/07/23	19:21:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/07/23	19:21:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/07/23	19:21:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/07/23	19:24:26.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02
2016/07/23	19:24:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2016/07/23	19:24:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/07/23	19:41:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/07/23	19:41:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/07/23	19:41:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2016/07/23	19:41:30.0	AOCS_Ore-point_Start_14_OG [0x0a4]	AOCU_NM	5	02-76	00	e6 c1 01 81
2016/07/23	19:41:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/07/23	19:41:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	

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2016/07/23	19:41:52.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/07/23	19:41:54.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/07/23	19:41:56.0	XRT_FLD_RESET_433_OG [0x1b1]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2016/07/23	19:44:26.0	XRT_QT_PROG_SET_428_OG [0x1ac]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	11	
2016/07/23	19:44:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	07	
2016/07/23	19:44:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/07/23	20:16:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/07/23	20:16:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/07/23	20:16:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2016/07/23	20:16:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/07/23	20:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/07/23	20:58:24.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/07/23	20:58:26.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/07/23	20:58:28.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2016/07/23	20:58:30.0	AOCS_ORe-point_Start_15_OG [0x0a5]					
		AOCU_NM	5	02-76	00	dd dc 01 81	
2016/07/23	20:58:48.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2016/07/23	20:58:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/07/23	20:58:52.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/07/23	20:58:54.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/07/23	20:58:56.0	XRT_FLD_RESET_433_OG [0x1b1]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2016/07/23	21:01:26.0	XRT_QT_PROG_SET_428_OG [0x1ac]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	11	
2016/07/23	21:01:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	07	
2016/07/23	21:01:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/07/23	21:18:24.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/07/23	21:18:26.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/07/23	21:18:28.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2016/07/23	21:18:30.0	AOCS_ORe-point_Start_16_OG [0x0a6]					
		AOCU_NM	5	02-76	00	d4 fe 01 81	
2016/07/23	21:18:48.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2016/07/23	21:18:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/07/23	21:18:52.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/07/23	21:18:54.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/07/23	21:18:56.0	XRT_FLD_RESET_433_OG [0x1b1]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2016/07/23	21:21:26.0	XRT_QT_PROG_SET_428_OG [0x1ac]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	11	
2016/07/23	21:21:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	07	
2016/07/23	21:21:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/07/23	21:54:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/07/23	21:54:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/07/23	21:54:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2016/07/23	21:54:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/07/23	21:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/07/23	22:34:24.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/07/23	22:34:26.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/07/23	22:34:28.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2016/07/23	22:34:30.0	AOCS_ORe-point_Start_17_OG [0x0a7]					
		AOCU_NM	5	02-76	00	cc 19 01 81	
2016/07/23	22:34:48.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2016/07/23	22:34:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/07/23	22:34:52.0	XRT_AEC_RESET_448_OG [0x1c0]					

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2016/07/23	22:34:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/07/23	22:34:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/07/23	22:37:26.0	XRT_QT_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11		
2016/07/23	22:37:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07		
2016/07/23	22:37:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/07/23	22:54:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/07/23	22:54:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/07/23	22:54:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2016/07/23	22:54:30.0	AOCs_OrE-point_Start_18_OG [0x0a8]	AOCU_NM	5	02-76	00	c3	34	01 81
2016/07/23	22:54:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2016/07/23	22:54:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2016/07/23	22:54:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2016/07/23	22:54:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/07/23	22:54:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/07/23	22:57:26.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02		
2016/07/23	22:57:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07		
2016/07/23	22:57:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/07/23	23:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/07/23	23:32:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/07/23	23:32:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/07/23	23:32:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/07/23	23:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/07/24	00:01:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/07/24	00:01:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/07/24	00:01:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2016/07/24	00:01:30.0	AOCs_OrE-point_Start_19_OG [0x0a9]	AOCU_NM	5	02-76	00	ba	4e	01 81
2016/07/24	00:01:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2016/07/24	00:01:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2016/07/24	00:01:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2016/07/24	00:01:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/07/24	00:01:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/07/24	00:04:26.0	XRT_QT_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11		
2016/07/24	00:04:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07		
2016/07/24	00:04:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/07/24	00:21:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/07/24	00:21:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/07/24	00:21:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2016/07/24	00:21:30.0	AOCs_OrE-point_Start_20_OG [0x0aa]	AOCU_NM	5	02-76	00	b1	69	01 81
2016/07/24	00:21:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2016/07/24	00:21:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2016/07/24	00:21:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2016/07/24	00:21:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/07/24	00:21:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/07/24	00:24:26.0	XRT_QT_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11		
2016/07/24	00:24:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07		
2016/07/24	00:24:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			



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2016/07/24	00:41:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/07/24	00:41:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/07/24	00:41:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2016/07/24	00:41:30.0	AOCS_Ore-point_Start_21_OG [0x0ab]	AOCU_NM	5	02-76	00 03 8d b0 19	
2016/07/24	00:41:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/07/24	00:41:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/07/24	00:41:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/07/24	00:41:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/07/24	00:41:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/07/24	00:44:26.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a	
2016/07/24	00:44:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2016/07/24	00:44:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/07/24	01:11:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/07/24	01:11:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/07/24	01:11:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/07/24	01:11:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/07/24	01:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/07/24	01:39:00.0	XRT_Custom_430_OG [0x1ae]					
2016/07/24	01:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/07/24	02:47:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/07/24	02:47:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/07/24	02:47:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/07/24	02:47:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/07/24	02:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/07/24	03:17:30.0	XRT_Custom_430_OG [0x1ae]					
2016/07/24	03:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/07/24	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/07/24	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/07/24	03:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2016/07/24	04:00:00.0	AOCS_Ore-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	00 00 00 00 00	
2016/07/24	04:00:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2016/07/24	04:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2016/07/24	04:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/07/24	04:02:58.0	XRT_QT_PROG_SET_418_OG [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2016/07/24	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/07/24	04:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/07/24	04:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/07/24	04:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2016/07/24	04:10:00.0	AOCS_Ore-point_Start_21_OG [0x0ab]	AOCU_NM	5	02-76	00 03 8d b0 19	
2016/07/24	04:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/07/24	04:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/07/24	04:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/07/24	04:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/07/24	04:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/07/24	04:12:56.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a	
2016/07/24	04:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2016/07/24	04:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/07/24	04:16:30.0	XRT_CTRL_MANU_400_OG [0x190]					

2016/07/24	04:16:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/07/24	04:16:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/07/24	04:16:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/07/24	04:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/07/24	04:55:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/07/24	04:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]							
2016/07/24	05:57:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]				1	07-F0	c0	
2016/07/24	05:57:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/07/24	05:57:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/07/24	05:57:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/07/24	06:00:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/07/24	06:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/07/24	06:00:30.0	XRT_TCIB_XRT_S_HTR_A_ENA_417_OG [0x1a1]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/07/24	23:00:00.0	AOCS_ORe-point_Start_23_OG [0x0ad]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/07/25	02:00:00.0	AOCS_ORe-point_Start_21_OG [0x0ab]	XRT_TCIB_XRT_S_HTR_A_ENA_417_OG [0x1a1]	0	04-BC					
2016/07/25	04:00:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]	TCIB_XRT_S_HTR_A_ENA	0	04-BC					
2016/07/25	04:10:00.0	AOCS_ORe-point_Start_24_OG [0x0ae]	AOCS_ORe-point_Start_23_OG [0x0ad]				5	02-76	00 1c 93 50 72	
2016/07/26	03:40:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	00 03 8d b0 19				
2016/07/26	03:50:00.0	AOCS_ORe-point_Start_24_OG [0x0ae]	AOCU_NM	5	02-76	00 00 00 00 00				
2016/07/26	11:03:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	01 00 00 00 00				
			AOCU_NM	5	02-76	00 00 00 00 00				
			AOCU_NM	5	02-76	00 00 00 00 00				