

XRT Timeline to be uploaded on 2016/08/09

Period: 2016/08/09 10:59:00 - 2016/08/18 09:46:00

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Normal mode

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XOB #1AEC: G-Band Alignment with North Pole Q90 2x2 (G-band and VLS=CLS) - 1msec (Al/poly) - 4096msec - 5min cadence - Partial Sun-wNGT

Term	Pointing (x, y)	Comment
08/09 11:24:00 - 08/09 13:08:54	Fixed (0.0, 930.0)	# OP start + 10min + Coalignment
PROG= 19 1-time(s)		
└─ Subr= 1 24-time(s) 300.0sec		
└─ Seqn= 98 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 1ms Obs 2x2 2048x1536 (1024, 768) Q=90 0 0 2.0sec
└─ Seqn= 63 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band close	Safe Norm 1ms Obs 2x2 2048x1536 (1024, 768) Q=90 0 0 2.0sec
└─ Seqn= 45 1-time(s) 2.0sec		
└─ Al-poly/Open	med-Be/Open close	Safe Norm 4.00s Obs 2x2 2048x1536 (1024, 768) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1AED: G-Band Alignment with East limb Q90 2x2 (G-band and VLS=CLS) - 1msec - (Al/poly) 1443msec - 8 min cadence-wNGT

Term	Pointing (x, y)	Comment
08/09 13:24:00 - 08/09 15:08:54	Fixed (-970.0, 0.0)	Coalignment for the east limb
PROG= 12 1-time(s)		
└─ Subr= 1 15-time(s) 480.0sec		
└─ Seqn= 19 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 1ms Obs 2x2 1536x2048 (1280, 1024) Q=90 0 0 2.0sec
└─ Seqn= 43 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band close	Safe Norm 1ms Obs 2x2 1536x2048 (1280, 1024) Q=90 0 0 2.0sec
└─ Seqn= 70 1-time(s) 2.0sec		
└─ Al-poly/Open	med-Be/Open close	Safe Norm 1.41s Obs 2x2 1536x2048 (1280, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1AFF: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
08/09 15:16:00 - 08/09 17:59:24	Track (-821.7, -1.3) @ 08/09 15:09:00	AR observations
08/09 18:12:30 - 08/10 00:29:54	Track (-807.8, -4.0) @ 08/09 18:09:30	AR observations
08/10 05:57:00 - 08/10 17:40:54	Track (-746.8, -14.2) @ 08/10 05:54:00	AR observations
08/10 18:34:00 - 08/11 00:24:30	Track (-674.7, -23.7) @ 08/10 17:51:00	AR observations
08/11 06:13:00 - 08/11 18:02:24	Track (-590.8, -32.5) @ 08/11 06:10:00	AR observations
08/11 18:15:30 - 08/12 06:19:54	Track (-500.6, -40.1) @ 08/11 18:12:30	AR observations
08/12 06:33:00 - 08/12 17:24:30	Track (-401.4, -46.6) @ 08/12 06:30:00	AR observations
08/12 18:19:00 - 08/13 05:52:24	Track (-300.9, -51.6) @ 08/12 18:16:00	AR observations
08/13 06:05:30 - 08/13 17:49:54	Track (-196.4, -55.3) @ 08/13 06:02:30	AR observations
08/13 18:42:00 - 08/14 05:59:54	Track (-87.6, -57.6) @ 08/13 18:00:00	AR observations
PROG= 11 Inf.-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 56 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
└─ Open/G-band	Open/G-band close	Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
└─ Subr= 2 5-time(s) 2.0sec		
└─ Seqn= 75 1-time(s) 2.0sec		
└─ Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec
└─ Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec
└─ thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ Seqn= 96 4-time(s) 60.0sec		
└─ Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec
└─ thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 15.0sec
└─ Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 2.0sec
└─ thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 15.0sec
└─ Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec
└─ thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1B43: Synoptic Q95 2x2 - Al/mesh(24/256/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(33/512/2048) + T

Term	Pointing (x, y)	Comment
08/09 18:02:30 - 08/09 18:09:24	Fixed (0.0, 0.0)	synoptic, shifted -0.5 min
08/10 05:47:00 - 08/10 05:53:54	Fixed (0.0, 0.0)	synoptic, shifted -16.0 min
08/10 17:44:00 - 08/10 17:50:54	Fixed (0.0, 0.0)	synoptic, shifted -19.0 min
08/11 06:03:00 - 08/11 06:09:54	Fixed (0.0, 0.0)	synoptic
08/11 18:05:30 - 08/11 18:12:24	Fixed (0.0, 0.0)	synoptic, shifted 2.5 min
08/12 06:23:00 - 08/12 06:29:54	Fixed (0.0, 0.0)	synoptic, shifted 20.0 min
08/12 18:09:00 - 08/12 18:15:54	Fixed (0.0, 0.0)	synoptic, shifted 6.0 min
08/13 05:55:30 - 08/13 06:02:24	Fixed (0.0, 0.0)	synoptic, shifted -7.5 min
08/13 17:53:00 - 08/13 17:59:54	Fixed (0.0, 0.0)	synoptic, shifted -10.0 min
PROG= 01 1-time(s)		

Subr= 1		1-time(s)		2.0sec											
Seqn= 5		1-time(s)		2.0sec											
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec		
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec		
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec		
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512	(1024, 1024)	DPCM	0	0	2.0sec		
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048	(1024, 1024)	DPCM	0	0	2.0sec		
Seqn= 1		1-time(s)		2.0sec											
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	24ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Seqn= 12		1-time(s)		2.0sec											
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	32ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Seqn= 85		1-time(s)		2.0sec											
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
thin-Be/Open	thin-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Seqn= 54		1-time(s)		2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec		
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval				

XOB #1B38: CME - HOP201 - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 512x512 at 1064 1048, thin-Be, and Al/poly context, with G-b

Term	Pointing (x, y)	Comment
08/10 00:33:00 - 08/10 05:43:54	Track (-489.7, 143.6) @ 08/10 00:30:00	HOP 322 filament obs.
08/11 00:48:30 - 08/11 05:59:54	Track (-298.7, 132.7) @ 08/11 00:30:00	HOP 322 filament obs.

PROG= 15 Inf.-time(s)

Subr= 1		1-time(s)		2.0sec											
Seqn= 69		1-time(s)		2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	512x512	(1064, 1048)	DPCM	0	0	2.0sec		
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	512x512	(1064, 1048)	DPCM	0	0	2.0sec		
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512	(1064, 1048)	Q=98	0	0	2.0sec		
Subr= 2		5-time(s)		2.0sec											
Seqn= 80		1-time(s)		2.0sec											
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	512x512	(1064, 1048)	Q=95	2	0	2.0sec		
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	512x512	(1064, 1048)	Q=95	3	0	2.0sec		
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512	(1064, 1048)	Q=95	2	0	2.0sec		
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512	(1064, 1048)	Q=95	3	0	2.0sec		
Seqn= 96		4-time(s)		60.0sec											
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384	(1064, 1048)	Q=95	1	0	2.0sec		
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	1	0	15.0sec		
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384	(1064, 1048)	Q=95	1	1	2.0sec		
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	1	1	15.0sec		
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384	(1064, 1048)	Q=95	1	2	2.0sec		
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	1	2	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval				

XOB #1B35: Synoptic 7 Filter w/ Al-mesh(24/256/2897), Al-poly(45/512/4096), Thin-Be(181/2048/11571) - Thick-Be(65536), Al-poly+Ti-poly(362/4096), Med-Al

Term	Pointing (x, y)	Comment
08/14 06:03:00 - 08/14 06:14:30	Fixed (0.0, 0.0)	synoptic

PROG= 04 1-time(s)

Subr= 1		1-time(s)		2.0sec											
Seqn= 5		1-time(s)		2.0sec											
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec		
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec		
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec		
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512	(1024, 1024)	DPCM	0	0	2.0sec		
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048	(1024, 1024)	DPCM	0	0	2.0sec		
Seqn= 1		1-time(s)		2.0sec											
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	24ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Seqn= 99		1-time(s)		2.0sec											
Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Seqn= 67		1-time(s)		2.0sec											
thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Seqn= 54		1-time(s)		4.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec		
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Subr= 2		1-time(s)		2.0sec											
Seqn= 46		2-time(s)		2.0sec											
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec		

Seqn= 94		2-time(s)		2.0sec											
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	354ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec		
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec		
Seqn= 68		2-time(s)		2.0sec											
med-Al/Open	med-Al/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec		
med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval			

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Flare mode

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XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
08/09 15:16:00 - 08/09 17:59:24	Track (-821.7, -1.3) @ 08/09 15:09:00	AR observations
08/09 18:12:30 - 08/10 00:29:54	Track (-807.8, -4.0) @ 08/09 18:09:30	AR observations
08/10 00:33:00 - 08/10 05:43:54	Track (-489.7, 143.6) @ 08/10 00:30:00	HOP 322 filament obs.
08/10 05:57:00 - 08/10 17:40:54	Track (-746.8, -14.2) @ 08/10 05:54:00	AR observations
08/10 18:34:00 - 08/11 00:24:30	Track (-674.7, -23.7) @ 08/10 17:51:00	AR observations
08/11 00:48:30 - 08/11 05:59:54	Track (-298.7, 132.7) @ 08/11 00:30:00	HOP 322 filament obs.
08/11 06:13:00 - 08/11 18:02:24	Track (-590.8, -32.5) @ 08/11 06:10:00	AR observations
08/11 18:15:30 - 08/12 06:19:54	Track (-500.6, -40.1) @ 08/11 18:12:30	AR observations
08/12 06:33:00 - 08/12 17:24:30	Track (-401.4, -46.6) @ 08/12 06:30:00	AR observations
08/12 18:19:00 - 08/13 05:52:24	Track (-300.9, -51.6) @ 08/12 18:16:00	AR observations
08/13 06:05:30 - 08/13 17:49:54	Track (-196.4, -55.3) @ 08/13 06:02:30	AR observations
08/13 18:42:00 - 08/14 05:59:54	Track (-87.6, -57.6) @ 08/13 18:00:00	AR observations

PROG= 07 30-time(s)

Subr= 1		20-time(s)		2.0sec											
Seqn= 11		1-time(s)		2.0sec											
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512	(1024, 1024)	Q=95	2	0	2.0sec		
Seqn=100		1-time(s)		10.0sec											
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384	(1024, 1024)	Q=95	2	0	2.0sec		
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec		
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec		
Subr= 2		1-time(s)		2.0sec											
Seqn= 10		1-time(s)		2.0sec											
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec		
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec		
Seqn= 11		1-time(s)		2.0sec											
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512	(1024, 1024)	Q=95	2	0	2.0sec		
Seqn= 84		1-time(s)		2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384	(1024, 1024)	Q=98	0	0	2.0sec		
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384	(1024, 1024)	Q=98	0	0	2.0sec		
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384	(1024, 1024)	Q=98	0	0	2.0sec		
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512	(1024, 1024)	Q=98	0	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval			

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Active Region Search

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NOT USED

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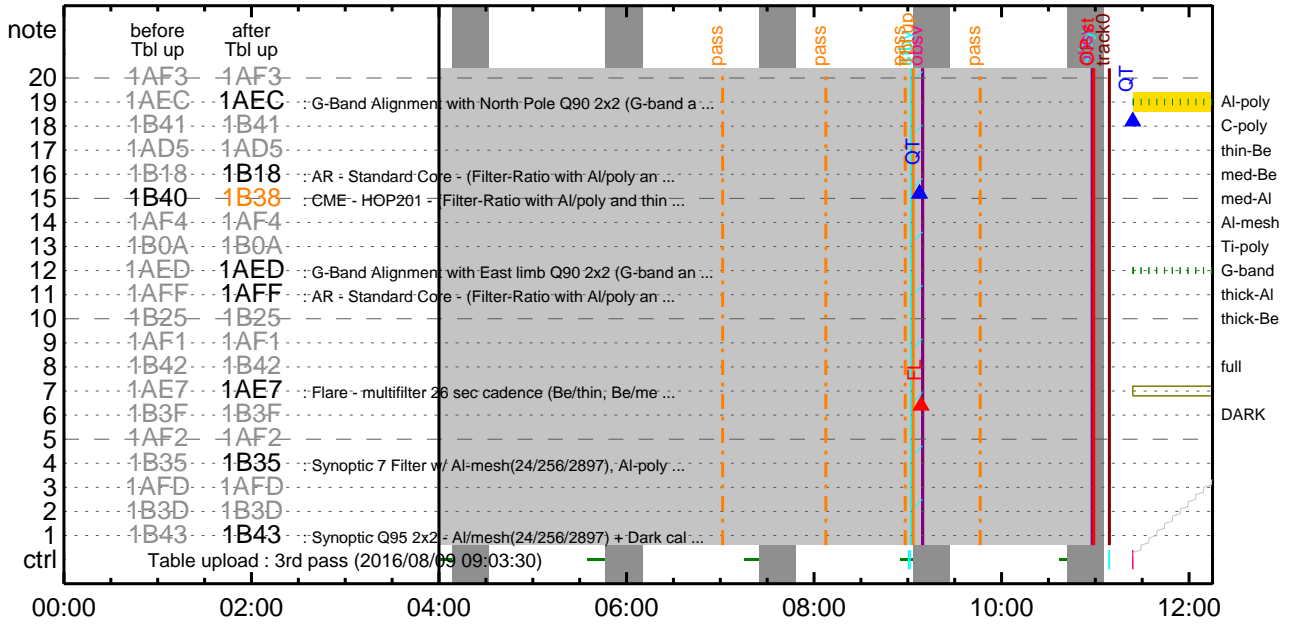
Flare Detection

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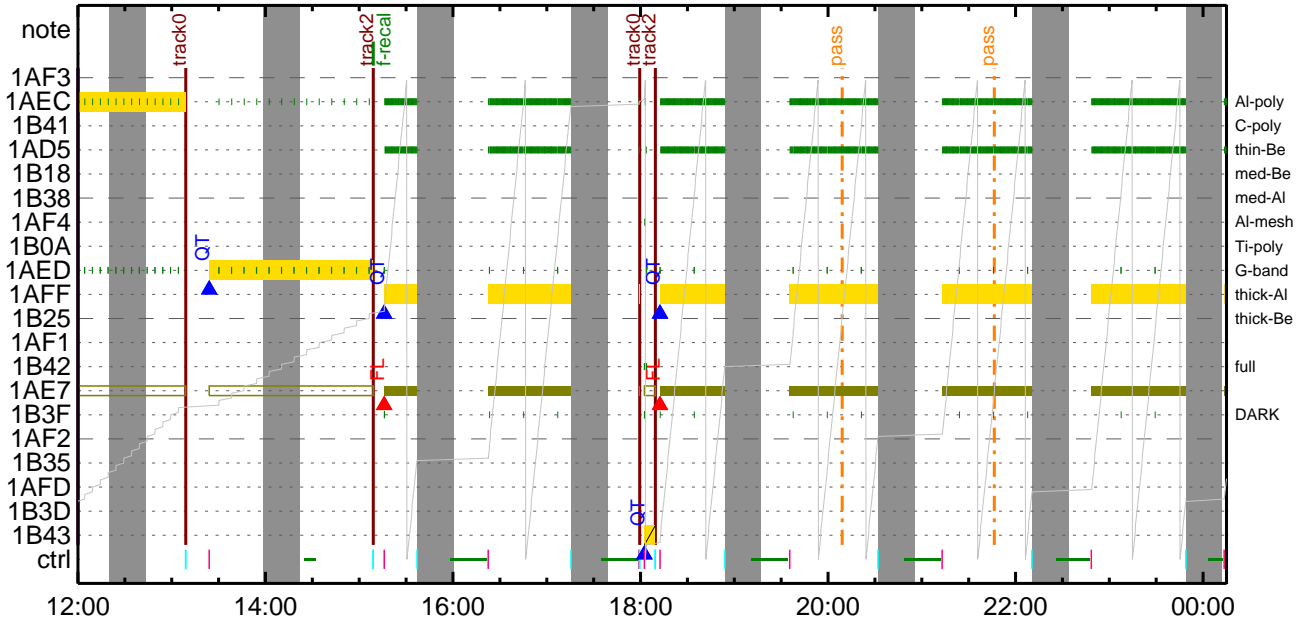
FLD Patrol

Term	Pointing (x, y)	Comment										
08/09 15:13:18 - 08/09 17:59:48	Track (-821.7, -1.3) @ 08/09 15:09:00	AR observations										
08/09 18:09:48 - 08/10 05:44:18	Track (-807.8, -4.0) @ 08/09 18:09:30	AR observations										
08/10 05:54:18 - 08/10 17:41:18	Track (-746.8, -14.2) @ 08/10 05:54:00	AR observations										
08/10 17:51:18 - 08/11 06:00:18	Track (-674.7, -23.7) @ 08/10 17:51:00	AR observations										
08/11 06:10:18 - 08/11 18:02:48	Track (-590.8, -32.5) @ 08/11 06:10:00	AR observations										
08/11 18:12:48 - 08/12 06:20:18	Track (-500.6, -40.1) @ 08/11 18:12:30	AR observations										
08/12 06:30:18 - 08/12 18:06:18	Track (-401.4, -46.6) @ 08/12 06:30:00	AR observations										
08/12 18:16:18 - 08/13 05:52:48	Track (-300.9, -51.6) @ 08/12 18:16:00	AR observations										
08/13 06:02:48 - 08/13 17:50:18	Track (-196.4, -55.3) @ 08/13 06:02:30	AR observations										
08/13 18:00:18 - 08/14 06:00:18	Track (-87.6, -57.6) @ 08/13 18:00:00	AR observations										
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8			Q=50	30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval

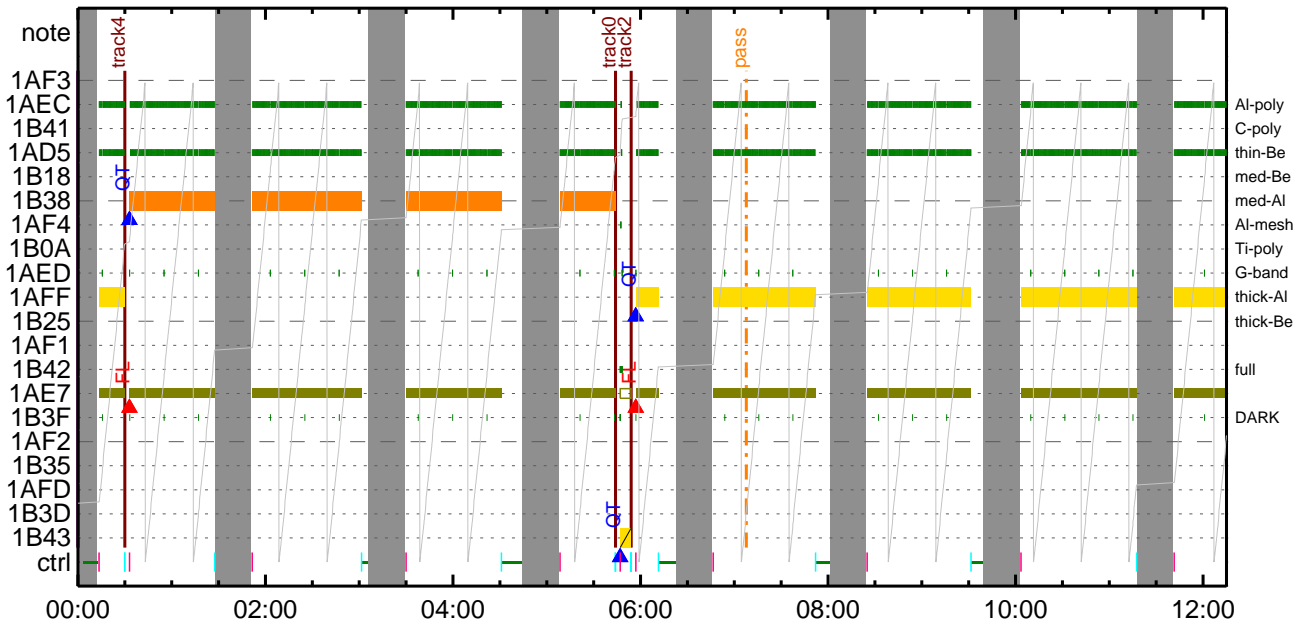
CMDI #0101 2016/08/09



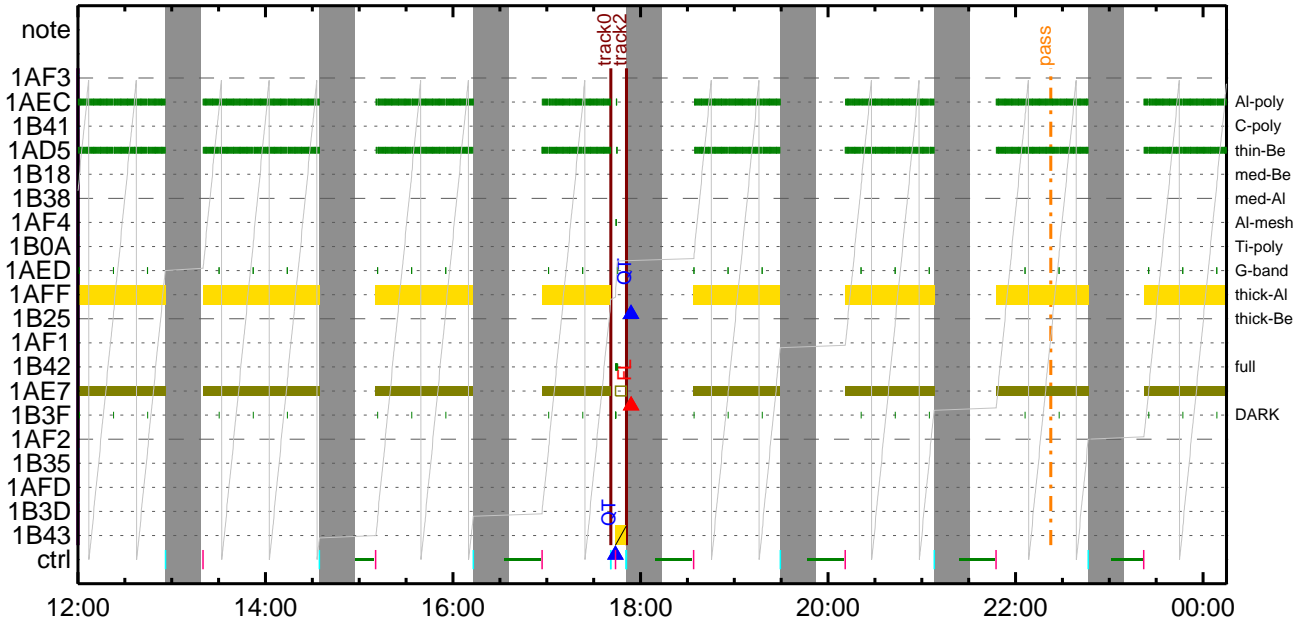
CMDI #0101 2016/08/09



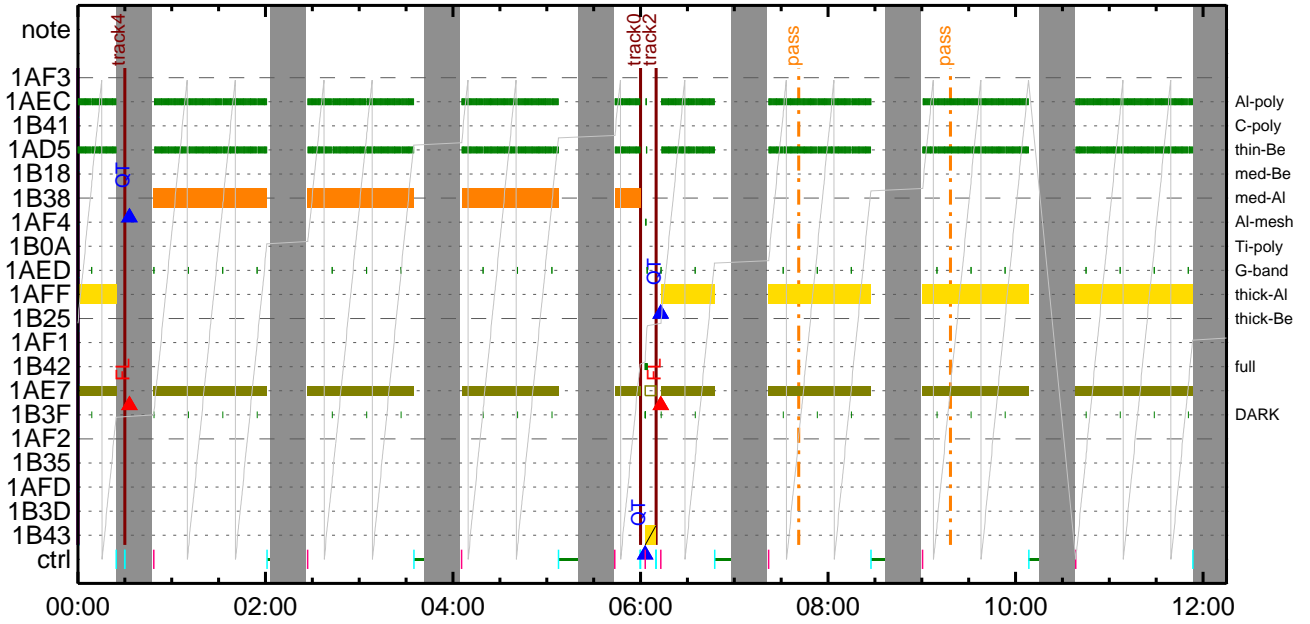
CMDI #0101 2016/08/10



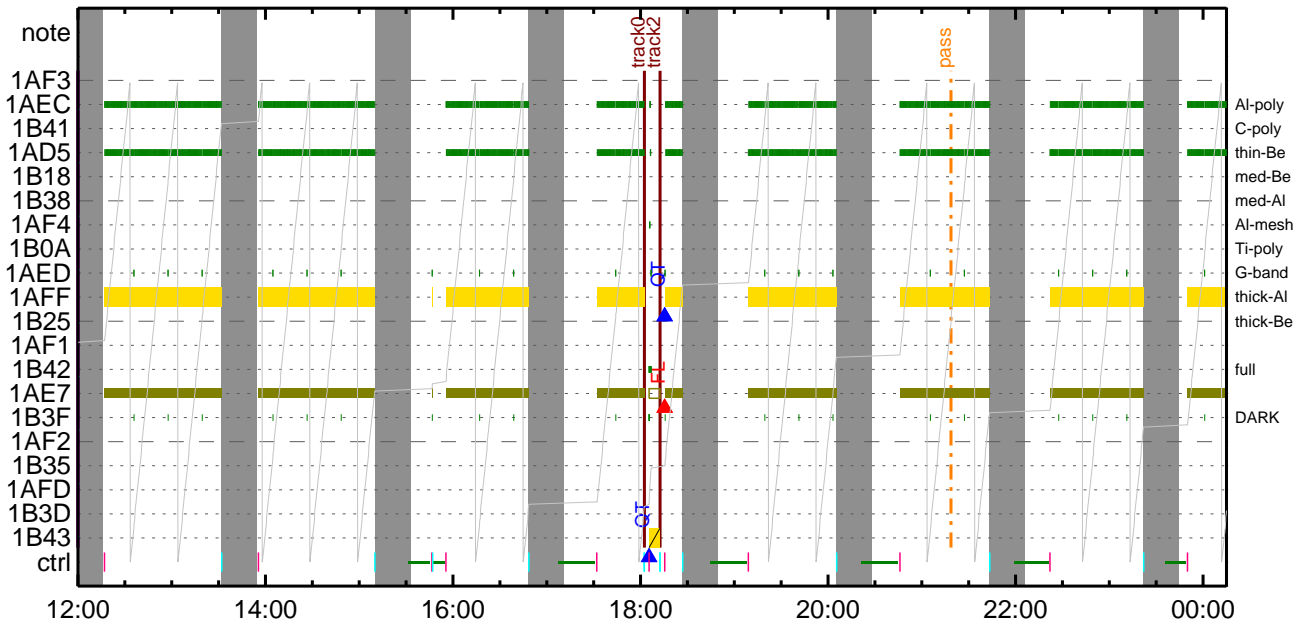
CMDI #0101 2016/08/10



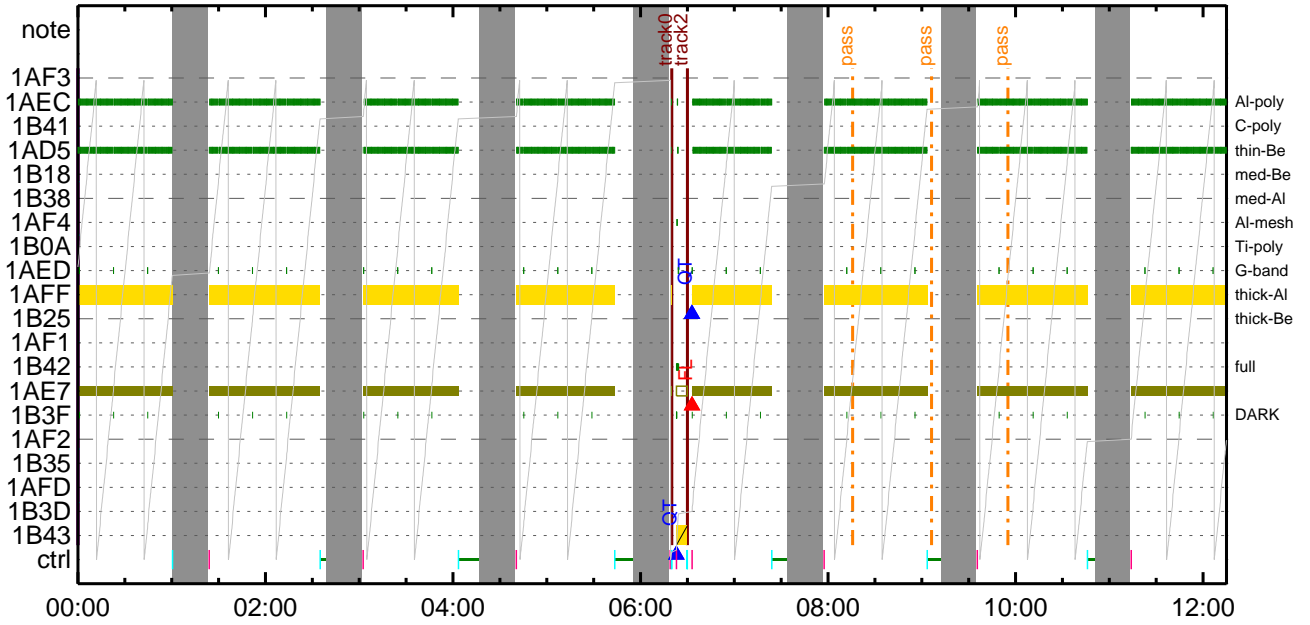
CMDI #0101 2016/08/11



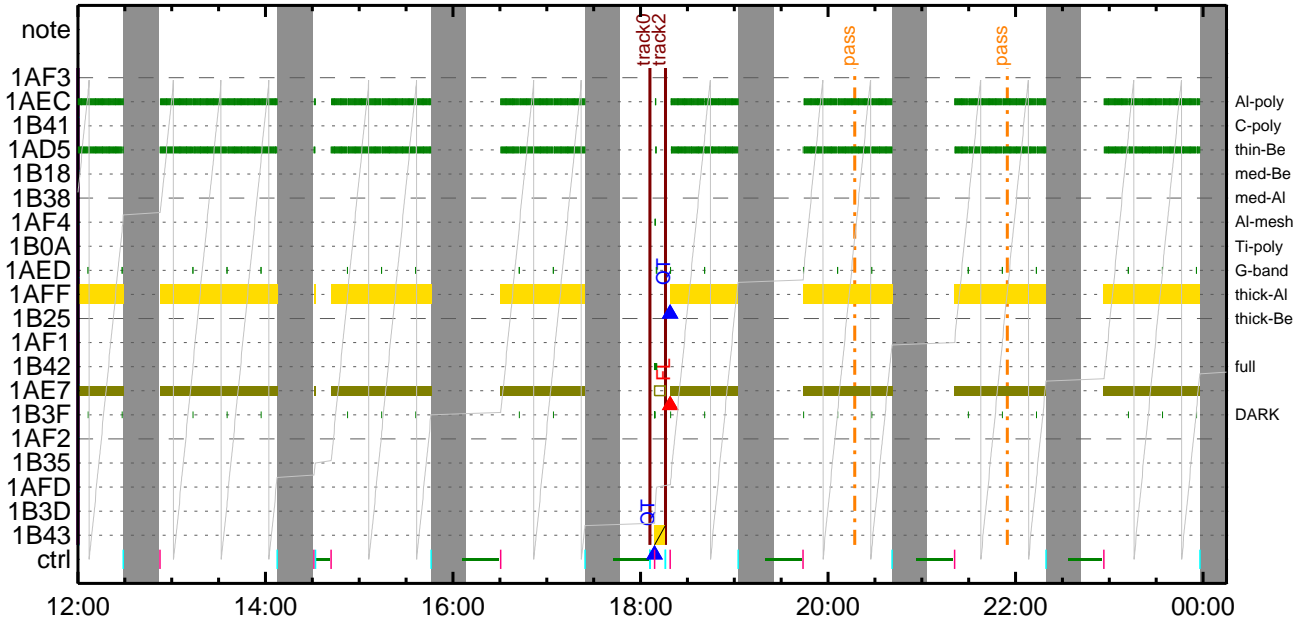
CMDI #0101 2016/08/11



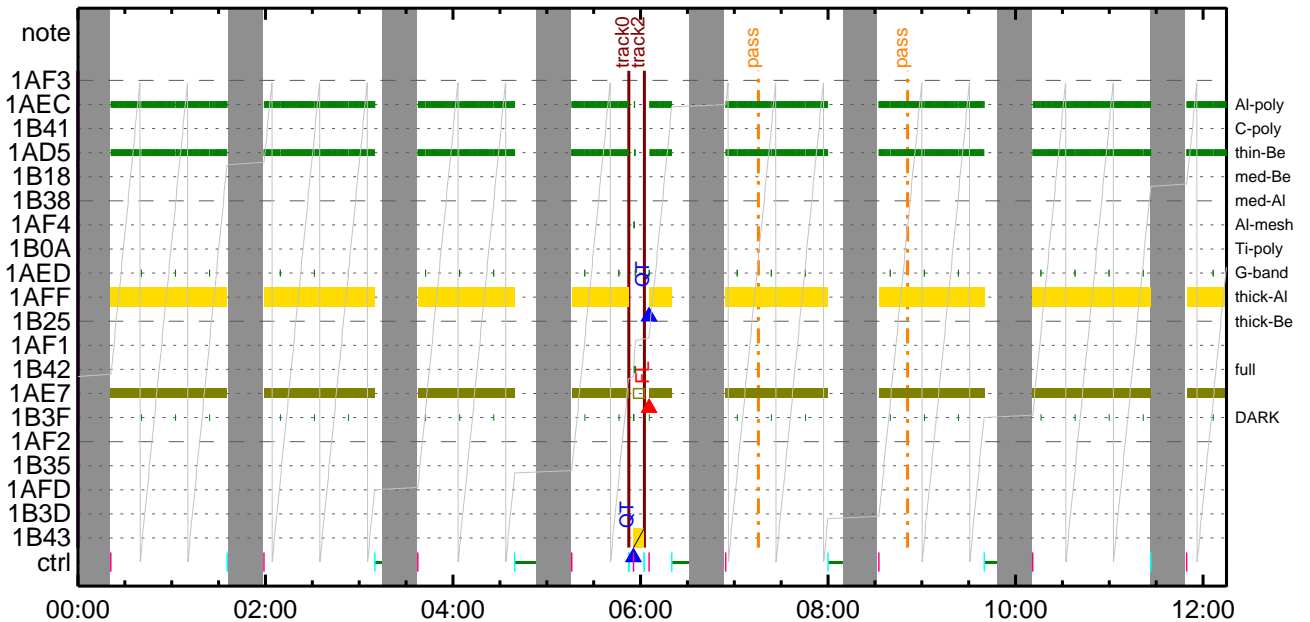
CMDI #0101 2016/08/12



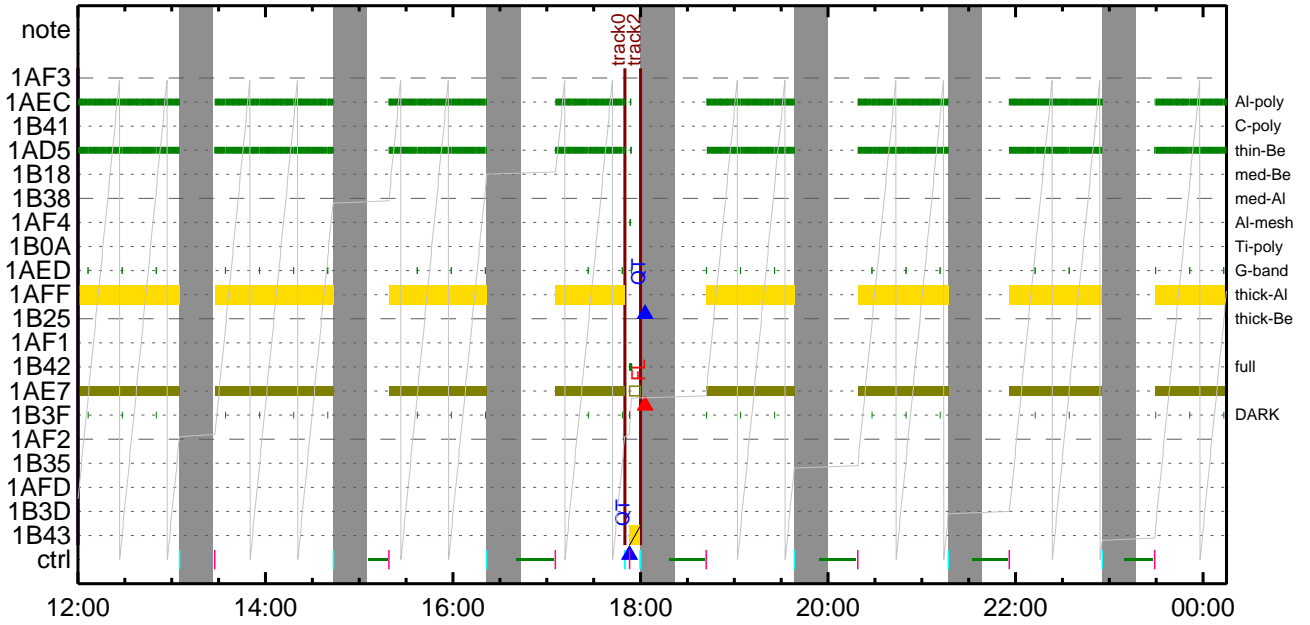
CMDI #0101 2016/08/12



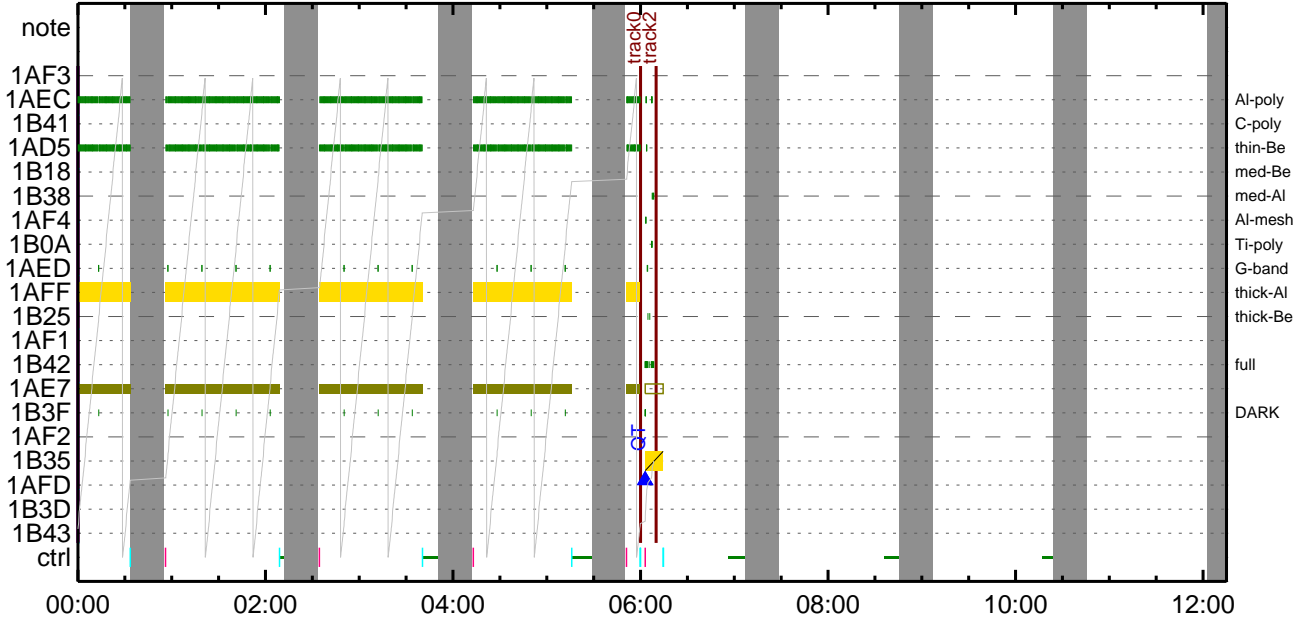
CMDI #0101 2016/08/13



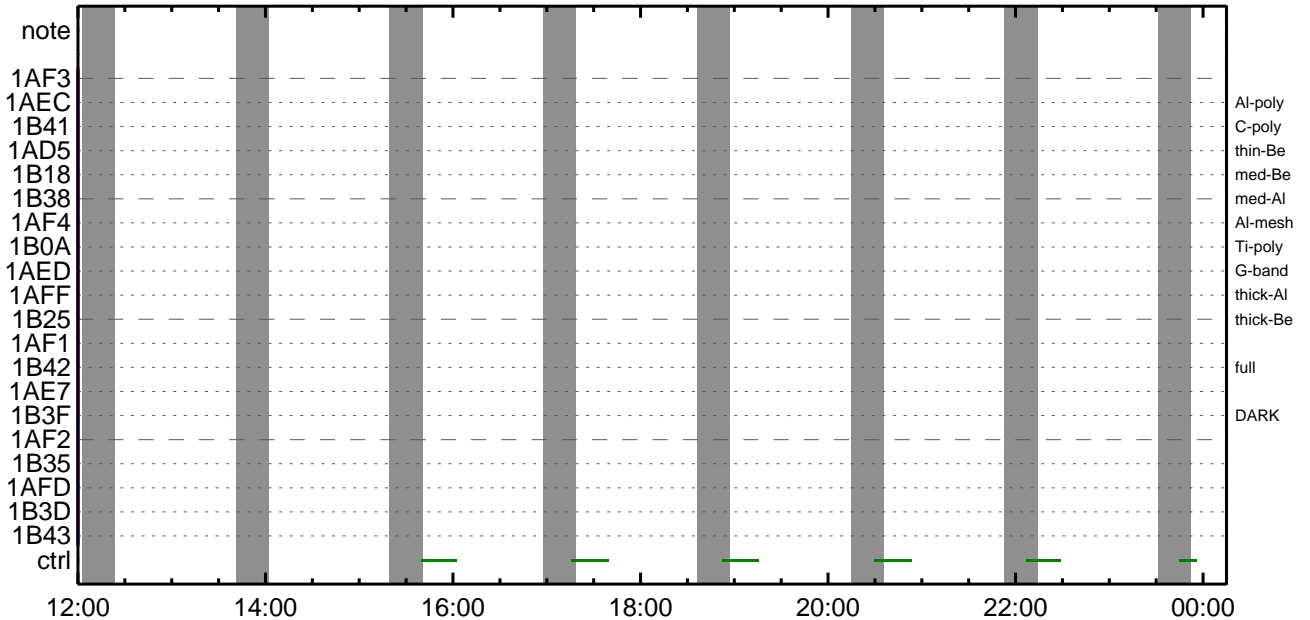
CMDI #0101 2016/08/13



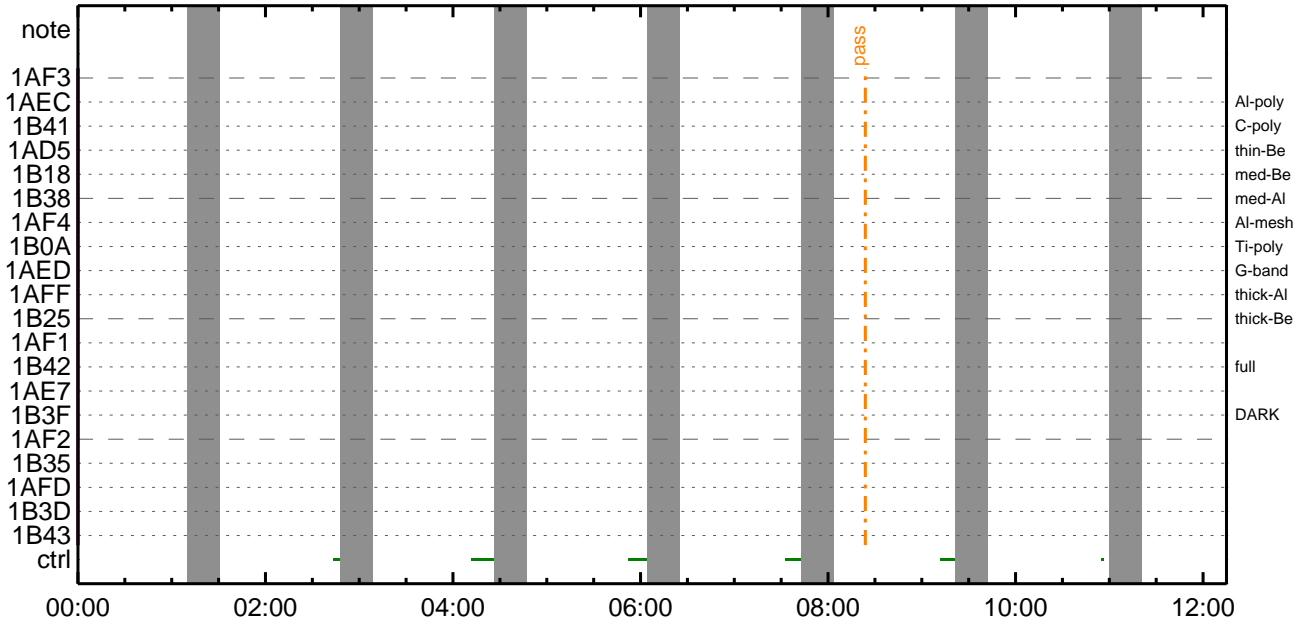
CMDI #0101 2016/08/14



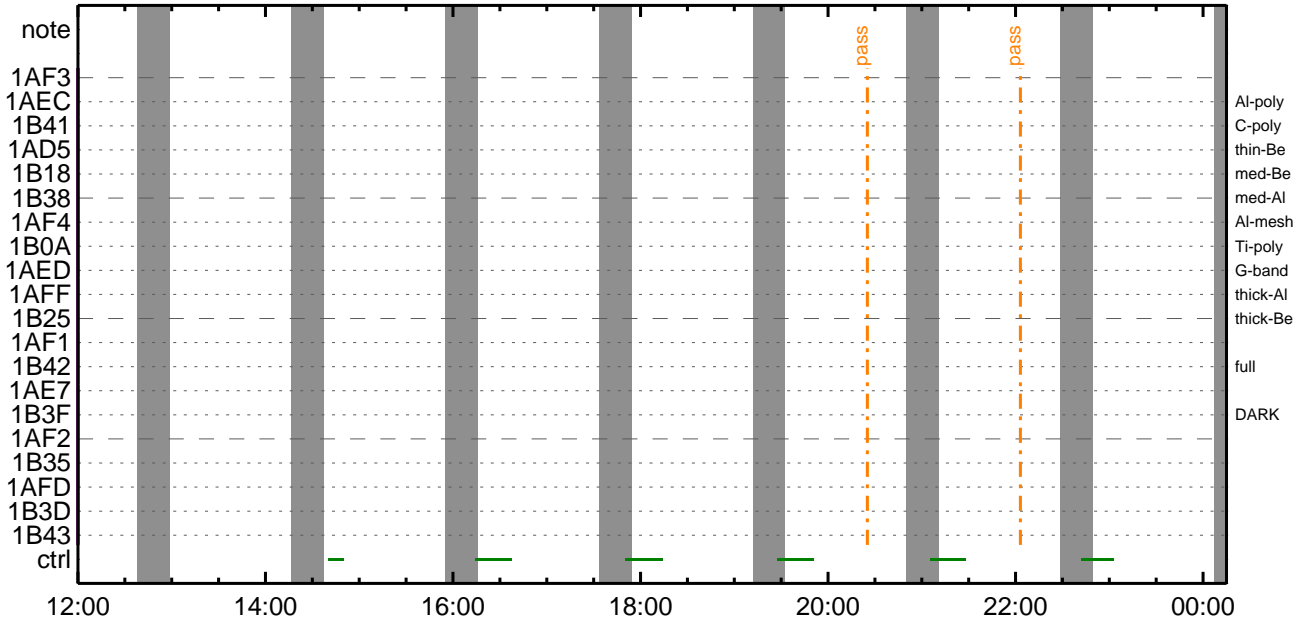
CMDI #0101 2016/08/14



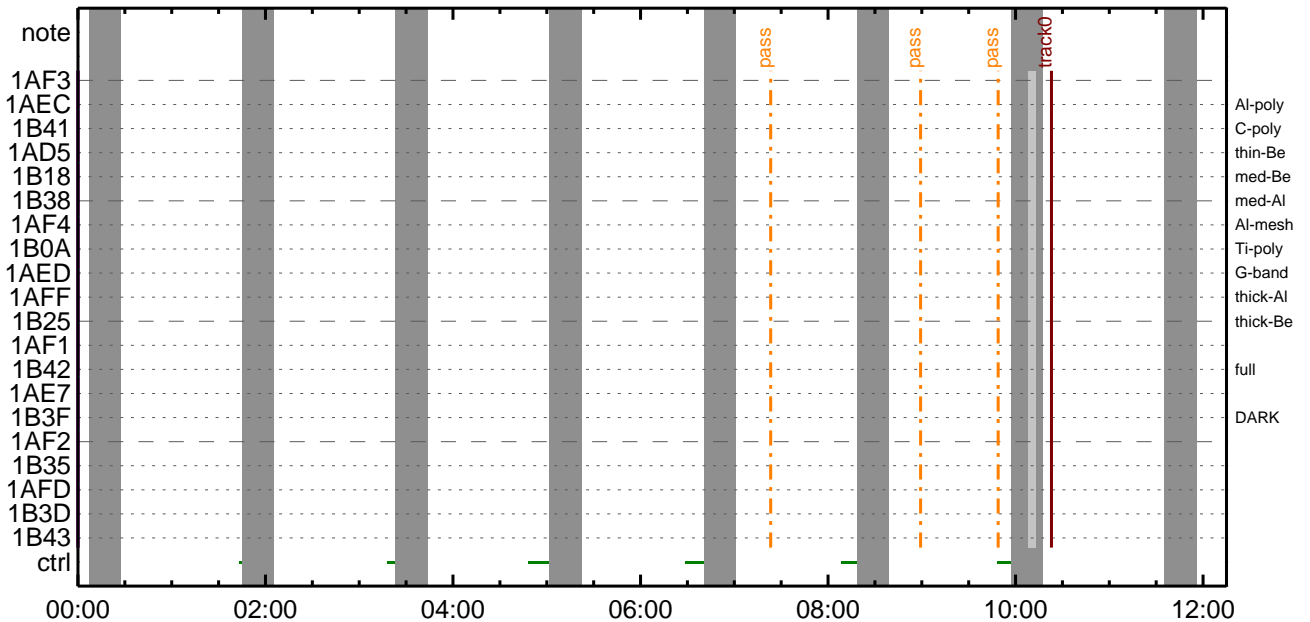
CMDI #0101 2016/08/15



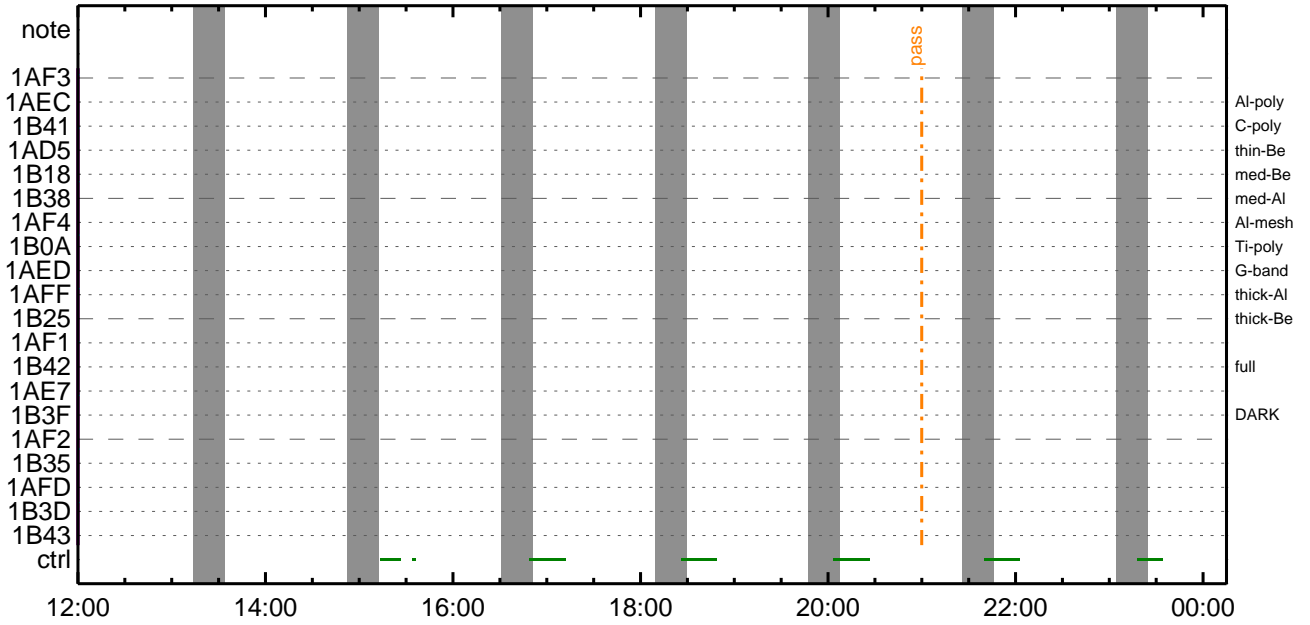
CMDI #0101 2016/08/15



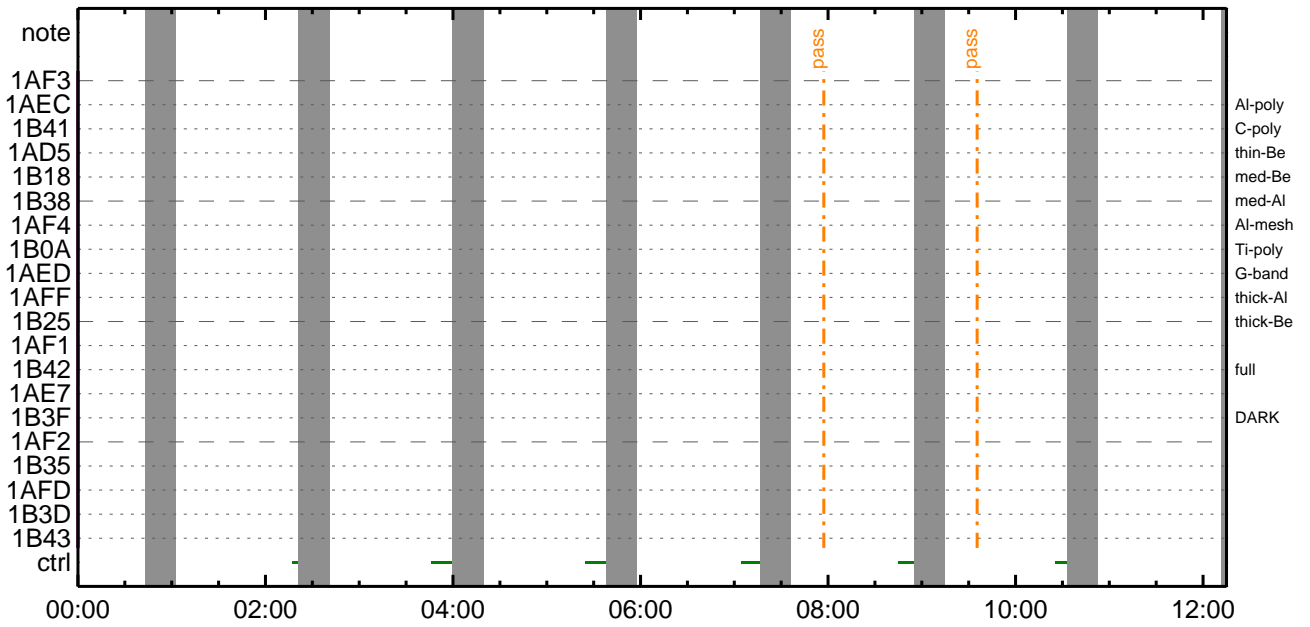
CMDI #0101 2016/08/16



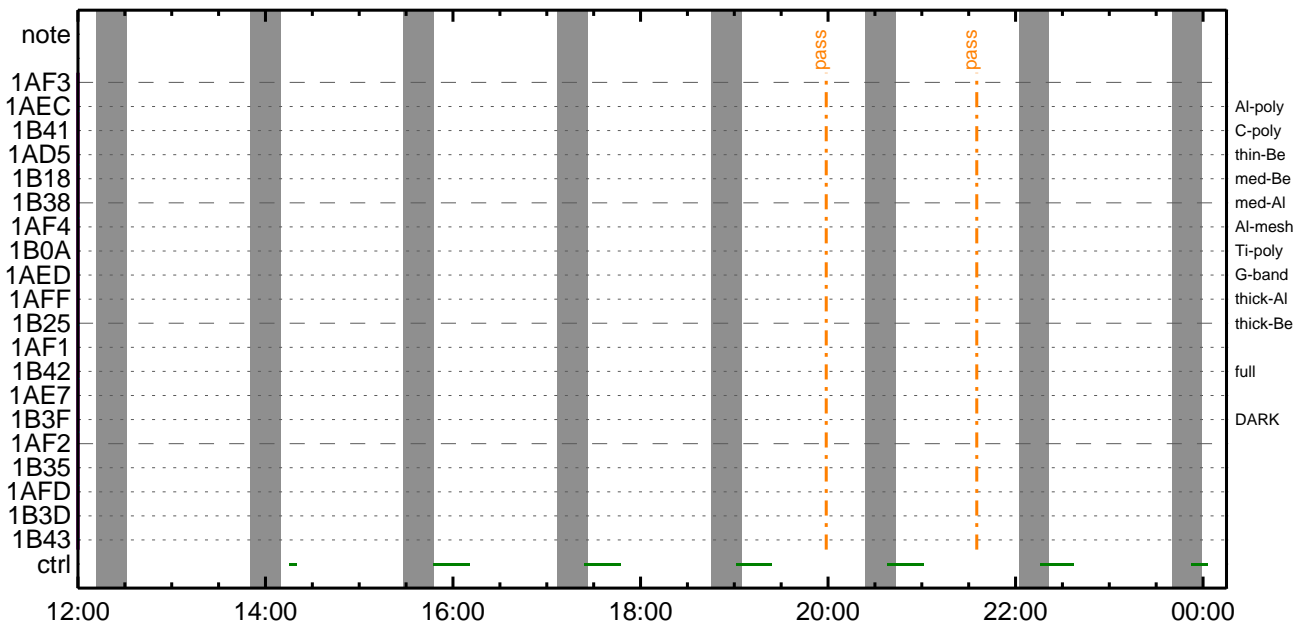
CMDI #0101 2016/08/16



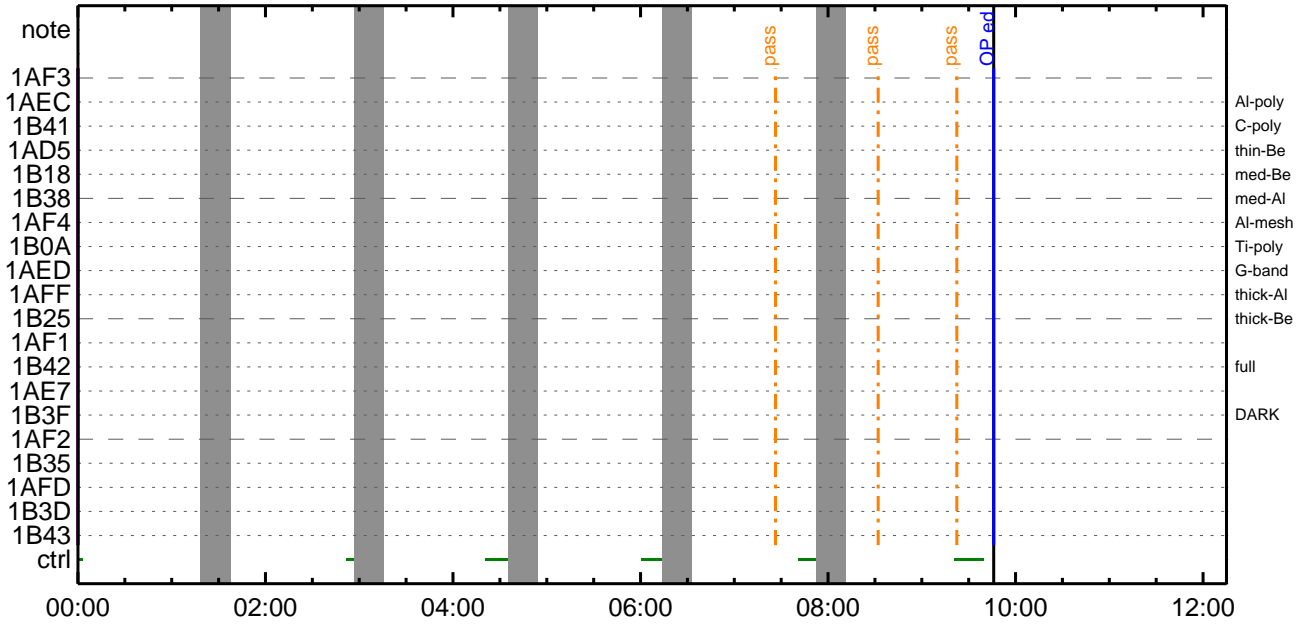
CMDI #0101 2016/08/17



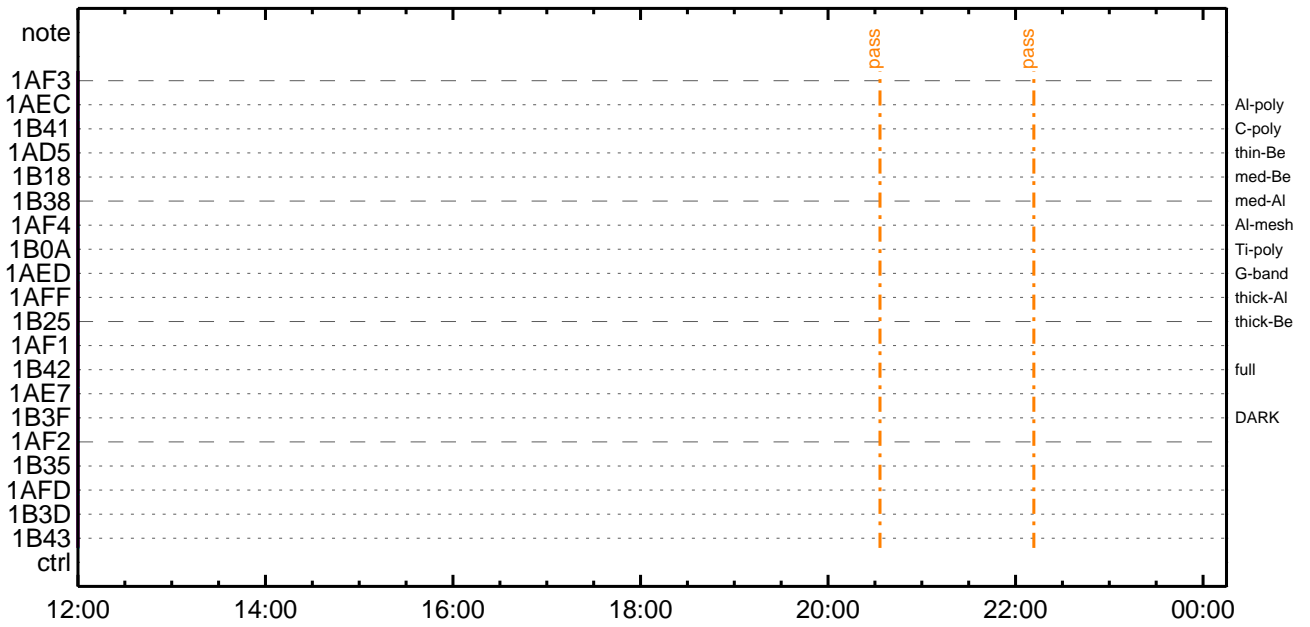
CMDI #0101 2016/08/17



CMDI #0101 2016/08/18



CMDI #0101 2016/08/18



(a) Spacecraft Operation Procedure (real-commands)

```
main-038 2016-08-09 12:46:30 219 33 SOLAR-B MAIN //
0001 . C.
0002 . C. ***** AOS *****
0003 . C.
0004 . C. ;ãAOSYÁY$YÁY`¼Á»Û;ä
0005 . C.
0006 . C. YÁYB;¼Y³YFÝÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 . C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 . C. Áí;È¿¿ðÁð•µ°Æ»Í×ÁÇ¿ÍYçYÁY×Yí;¼YÉ;ÈÈè¼µ•íÉ;Èè¼°ÇÖð•¼¿¼l¹ç¿í;çÁ®, ùñ¹ðèððçÁ+¿®ð•ðÈððð³ðÈ; Æ
0011 +. DC 02-8E AOCU_ORB_UPD
0012 . C.
0013 . C.
0014 . C. *****
0015 . C. OP/OGYí;¼YÉ; | YÁYóY×
0016 . C. *****
0017 . C.
0018 . C. ;ãOP/OGYí;¼YÉ;ä
0019 . S. OP op-038:OP
0020 . ()
0021 . S. OG og-038:OG
0022 . ()
0023 . C.
0024 . C. ;ãNMOG&OPÍ°èYÁYóY×;ä
0025 . C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 . C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 . C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 . C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 . C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 . C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 . C. çç[HK1_PKT_FORM_NO] EQ 7
0036 . C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 . C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 . C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 . C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. YÁYóY×¼¼ª Ì»ðð³ ÍÇ§
0041 . C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGðÍ¼È¹ç•è²ÍOKðð³ ÍÇ§
0043 . C.
0044 . C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 . C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 . C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 . C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 . C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 . C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 . C. çç[HK1_PKT_FORM_NO] EQ 7
0055 . C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 . C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 . C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 . C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. YÁYóY×¼¼ª Ì»ðð³ ÍÇ§
0060 . C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGðÍ¼È¹ç•è²ÍOKðð³ ÍÇ§
0062 . C.
0063 . C. NMOG(0x210000-0x210FFF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 . C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 . C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 . C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 . C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 . C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 . C. çç[HK1_PKT_FORM_NO] EQ 7
0074 . C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 . C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 . C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 . C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. YÁYóY×¼¼ª Ì»ðð³ ÍÇ§
0079 . C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG,RAM ID=OPðÍ¼È¹ç•è²ÍOKðð³ ÍÇ§
0081 . C.
0082 . C. ***** òÈ²¼òÍ¼Á´ ¶¼°òÈÈ-ò°Á+¿® (¼áµ-YÁYóY×¼¼È¼çððÁÖÁæç¼ª° ò¼¼ì¹çðçðá) *****
0083 . C. DHUYá;¼YÉ; È¼Y¼; Yí;¼YÉ; ÈòðÍá¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 . C. çç[HK1_PKT_FORM_NO] EQ 2
0087 . C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 . C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0089 . C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 . C.
0091 . C. *****
0092 . C. TI-CMD SET (OPOG STOP/COPY/START)
0093 . C. *****
0094 . C.
0095 . C. NOTICE ;§ OPOG UPLOADð-Á+¿®NGðÍ¼¹ç; ç°È²¼¼òÍTI-CMDÁ+¿®ðÍ¼Á¹Òð•ðÈððð³ðÈ; Æ
```

0096 C. ...
0097 C.
0098 C. TI ... (UT)
0099 +. TI 2016-08-09 10:54:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C. ... EQ 1COUNTUP
0102 C.
0103 +. TI 2016-08-09 10:54:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C. ... EQ 1COUNTUP
0106 C.
0107 +. TI 2016-08-09 10:54:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C. ... EQ 1COUNTUP
0110 C.
0111 +. TI 2016-08-09 10:58:59.5
0112 DC 01-B2 DHU_OP_START
0113 C. ... EQ 1COUNTUP
0114 C.
0115 C. ...
0116 C. ... EQ ENA
0117 C. ... EQ 4
0118 C. ... EQ DHU
0119 C. ... EQ 0xB3
0120 C.
0121 C. *****
0122 C. TI ...
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF; 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C. ... EQ 07
0129 C. ... EQ 2B
0130 C. ... EQ 3
0131 C. ... EQ 0
0132 C. ... EQ DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C. ... EQ 7
0136 C. ... EQ 0.25 s
0137 C. ... EQ 32k
0138 C. ... EQ 4M
0139 C. ... EQ EXEC
0140 C.
0141 C. ...
0142 C. ... EQ NON
0143 C.
0144 C. RAM ID=TI_TBL ...
0145 C.
0146 C. DHU ...
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C. ... EQ 2
0150 C. ... EQ 0.5S
0151 C. ... EQ 32K
0152 C. ... EQ 4M
0153 C.
0154 C.
0155 C. BAT ... HK1_BAT_CHG_CUR = [] A
0156 C. 1.5A ... HI->LO ...
0157 C. 1.5A ... HI->LO ...
0158 C.
0159 C. ***** BAT_CHG_RATE_HI->LO *****
0160 +. DC 04-F9 TCIB_BAT_CHG_RATE_LO
0161 C. [] HK1_BAT_CHG_RATE = LO ?
0162 C.
0163 C. ***** BAT_VT_#6->#4 ... *****
0164 C. [] HK1_V/T_B1_SET/RST = RESET ?
0165 C. [] HK1_V/T_B2_SET/RST = SET ?
0166 C. [] HK1_V/T_B3_SET/RST = RESET ?
0167 C. [] HK1_V/T_NUM = 06 ?
0168 +. DC 04-D5 TCIB_BAT_VT_B3_SET
0169 +. DC 04-D6 TCIB_BAT_VT_B12_RST
0170 C. [] HK1_V/T_B1_SET/RST = RESET ?
0171 C. [] HK1_V/T_B2_SET/RST = RESET ?
0172 C. [] HK1_V/T_B3_SET/RST = SET ?
0173 C. [] HK1_V/T_NUM = 04 ?
0174 C. ***** BAT_CHG_RATE_LO->HI *****
0175 C. ... HI->LO ...
0176 +. DC 04-D8 TCIB_BAT_CHG_RATE_HI
0177 C. [] HK1_BAT_CHG_RATE = HI ?
0178 C.
0179 C. Stop EIS observation and temporarily disable EIS mode changes
0180 C.
0181 C.
0182 C. ***** Start EIS operation (TI set) *****
0183 C. Execute, after the success of OP upload.
0184 C. Set EIS TI-commands
0185 +. TI 2016-08-09 10:58:30.0
0186 DC 07-FC EIS_MODE_MANU
0187 BC (21 02)
0188 +. TI 2016-08-09 10:58:40.0
0189 DC 07-FC EIS_MODE_CHG_DIS
0190 BC (22)
0191 C. [] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0192 C. ***** End EIS operation (TI set) *****
0193 C.

0194 C.
0195 C.
0196 C. ***** XRT START *****
0197 C. Execute, after the success of OP upload.
0198 +. TI 2016-08-09 10:58:00.0
0199 DC 07-F0 MDP_XRT_MODE_STBY
0200 BC (c3)
0201 . C. [] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0202 C.
0203 C. ***** XRT END *****
0204 C.
0205 . C. ***** MDP 'úÃîî»ô¼ÝðĔÂð¹ñēDCBC•x²è *****
0206 C. (%ã°îŸÓŸĀŸĔŸŒŸĖŸáŸçŸèñĔ¼ññ¼Ā»Ūñ¹ñé)
0207 . S. DC-BC dcbc-402:DCBC
0208 (MDP_known_event)
0209 C.
0210 C.
0211 . C. ***** ŸĐŸ¹•Ī Daily±¿ĪÑñĔ'Øñ¹ñēDCBC•x²è *****
0212 . S. DC-BC dcbc-153:DCBC
0213 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0214 C.
0215 C.
0216 . C. ;ãLOSŸĀŸŸŸĀŸ⁻¼Ā»Ū;ã
0217 C.
0218 . C. ***** LOS *****
0219 C.


```
0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCS DUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. ***** AOCS Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 + DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 977582.6 +- 1.0 (s) [ ]
0136 C.
0137 . C.
0138 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139 +. DC 07-FC EIS_MODE_CHG_ENA
0140 BC (20)
0141 . C. Verify EIS_MODE_CHG_FLG is ENA
0142 +. DC 07-FC EIS_MODE_MANU
0143 BC (21 02)
0144 . C. Verify EIS in MANUAL mode
0145 . C. Estimated OBSTBL upload time is 2m5s
0146 C. *****
0147 C. EIS START OBSTBL LOAD
0148 C. *****
0149 . S. RAM ram-820:EIS_OBSTBL
0150 ( )
0151 +. DC 07-FC EIS_DUMP_OBSTBL
0152 BC (07 07 07 00 00 70 00)
0153 C.
0154 C. Execute, after the success of OBSTBL upload.
0155 C. Set EIS TI-commands
0156 +. TI 2016-08-09 10:58:50.0
0157 DC 07-FC EIS_MODE_CHG_ENA
0158 BC (20)
0159 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0160 C. *****
0161 C. EIS END OBSTBL LOAD
0162 C. *****
0163 C.
0164 . C. ***** MDP 'úÃîî»ö¼ÝðËÄð¹ñèDCBC•x²è *****
0165 C. (¼á°îÝÓÝÄÝËÝÞÝËÝ¼ÝèñË¼ñ¼Ä»Ü¹ñè)
0166 . S. DC-BC dcbc-402:DCBC
0167 (MDP_known_event)
0168 C.
0169 C.
0170 . C. ***** ÝÐÝ¹•Ï Daily±¿îÑñË'Ø¹ñèDCBC•x²è *****
0171 . S. DC-BC dcbc-153:DCBC
0172 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0173 C.
0174 C.
0175 . C. ;ãLOSÝÄÝSÝÄÝ-¼Ä»Ü;ã
0176 C.
0177 . C. ***** LOS *****
0178 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-040 2016-08-09 12:46:30 123 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÁYB;¼Y³YF¥ÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿αΑβ•μ°Æ»Í×ÁÇαÍ¥çYÁY×¥í;¼¥É;ÈÈ%μ•íÉ;ÈÈ¼°ÇÔα•α¿¼í¹çαÍ;çÁ®, ùα¹αèαβαÇÁ+¿®α•αÈααα³αÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop SP table >
0018 +. DC 07-F0 MDP_SP_CTRL_MANU
0019 BC (61)
0020 C. -----
0021 C. MDP_SP_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload SP Observation Table>
0025 . S. RAM ram-289:MDP_OBS_S
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_S >
0029 +. DC 07-F0 MDP_DUMP_SPTBL
0030 BC (83 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_S verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C.
0036 C. ***** XRT START *****
0037 C.
0038 +. DC 07-F0 MDP_XRT_CTRL_MANU
0039 BC (c1)
0040 +. DC 07-F0 MDP_XRT_CTRL_MANU
0041 BC (c1)
0042 +. DC 07-F0 MDP_XRT_MODE_STBY
0043 BC (c3)
0044 . C. ----- Success Verify ? OK / NG_____
0045 C.
0046 C. XRT Obs. Table Upload
0047 . S. RAM ram-291:MDP_OBS_X
0048 ( )
0049 C.
0050 +. DC 07-F0 MDP_DUMP_XRTTBL
0051 BC (84 07 00 00 00 3a d4)
0052 . C. ----- Comparison Check ? OK / ERR _____
0053 C.
0054 C.
0055 +. DC 07-F0 MDP_XRT_ROI_SET
0056 BC (cd 01 b1 b1 04 04)
0057 +. DC 07-F0 MDP_XRT_ROI_SET
0058 BC (cd 02 b1 b1 08 08)
0059 +. DC 07-F0 MDP_XRT_ROI_SET
0060 BC (cd 03 b1 b1 08 08)
0061 +. DC 07-F0 MDP_XRT_ROI_SET
0062 BC (cd 04 b1 b1 06 06)
0063 +. DC 07-F0 MDP_XRT_ROI_SET
0064 BC (cd 06 85 83 06 06)
0065 +. DC 07-F0 MDP_XRT_ROI_SET
0066 BC (cd 07 80 60 20 18)
0067 +. DC 07-F0 MDP_XRT_ROI_SET
0068 BC (cd 08 a0 80 18 20)
0069 +. DC 07-F0 MDP_XRT_ROI_SET
0070 BC (cd 09 80 80 20 20)
0071 +. DC 07-F0 MDP_XRT_ROI_SET
0072 BC (cd 0a 80 80 20 08)
0073 +. DC 07-F0 MDP_XRT_ROI_SET
0074 BC (cd 0b 80 80 08 20)
0075 +. DC 07-F0 MDP_XRT_ROI_SET
0076 BC (cd 0c 85 83 08 08)
0077 +. DC 07-F0 MDP_XRT_ROI_SET
0078 BC (cd 0f 80 80 06 06)
0079 +. DC 07-F0 MDP_XRT_ROI_SET
0080 BC (cd 10 80 80 08 08)
0081 +. DC 07-F0 MDP_XRT_FLD_ENA
0082 BC (d8)
0083 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0084 BC (c8)
0085 +. DC 07-F0 MDP_XRT_ARS_DIS
0086 BC (d5)
0087 +. DC 07-F0 MDP_XRT_AEC_RESET
0088 BC (d0)
0089 +. DC 07-F0 MDP_XRT_FLD_RESET
0090 BC (da)
0091 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0092 BC (c4 10)
0093 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0094 BC (c5 07)
0095 . C. ----- Success Verify ? OK / NG _____
```



```
0096 C.  
0097 C.  
0098 . C. All OK? Yes--> Please Proceed. / No --> Stop here.  
0099 C.  
0100 +. DC 07-F0 MDP_XRT_MODE_OBSV  
0101 BC (c2)  
0102 +. TI 2016-08-09 10:58:02.0  
0103 DC 07-F0 MDP_XRT_MODE_OBSV  
0104 BC (c2)  
0105 . C. ----- Success Verify ? OK / NG ____  
0106 C.  
0107 C. ***** XRT END *****  
0108 C.  
0109 . C. ***** MDP `úÃîñî»ö¼ŷñÊÂðñ¹ñèDCBC•x²è *****  
0110 C. (¼ã°îŷÔŷÁŷÊŷÏŷËŷäŷåŷæŷçŷèŷéŷº¼ñ¼Á»Ûñ¹ñè)  
0111 . S. DC-BC dcbc-402:DCBC  
0112 (MDP_known_event)  
0113 C.  
0114 C.  
0115 . C. ***** ŷDŷ¹•Ī Daily±;îññè'øñ¹ñèDCBC•x²è *****  
0116 . S. DC-BC dcbc-153:DCBC  
0117 (SPECIAL-CMD_DAILY_OPERATIN_DCB)  
0118 C.  
0119 C.  
0120 . C. ;ãLOŞŷÁŷŞŷÃŷ~¼Á»Û;ã  
0121 C.  
0122 . C. ***** LOS *****  
0123 C.
```



```
0096 C.
0097 C.
0098 C.
0099 . C. ***** MDP 'úÃîñî»ò¼ŷñĒĀĐñ¹ñĕDCBC•x²ĕ *****
0100 C. (¼á°îŷÓŷĀŷĒŷŲŷĒŷáŷçŷĕñĒ¼ñ¼Ā»Űñ¹ñĕ)
0101 . S. DC-BC dcbc-402:DCBC
0102 (MDP_known_event)
0103 C.
0104 C.
0105 . C. ***** ŷĐŷ¹•İ Daily±;îññĒ'Øñ¹ñĕDCBC•x²ĕ *****
0106 . S. DC-BC dcbc-153:DCBC
0107 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0108 C.
0109 C.
0110 . C. ;ãLOSŷĀŷŲŷĀŷ-¼Ā»Ű;ã
0111 C.
0112 . C. ***** LOS *****
0113 C.
```

*** OP Sequence for XRT ***

```

2016/08/09 11:08:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/08/09 11:08:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/08/09 11:08:58.0 XRT_FOCUS_POSITION_438_OG [0x1b6]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2016/08/09 11:09:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 00 ad 59 00 00
2016/08/09 11:09:18.0 XRT_FLD_DIS_440_OG [0x1b8]
                        MDP_XRT_FLD_DIS 1 07-F0 d9
2016/08/09 11:23:54.0 XRT_FLRCTRL_DIS_441_OG [0x1b9]
                        MDP_XRT_FLRCTRL_DIS 1 07-F0 c9
2016/08/09 11:23:56.0 XRT_ARS_DIS_435_OG [0x1b3]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2016/08/09 11:23:58.0 XRT_QT_PROG_SET_444_OG [0x1bc]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 13
2016/08/09 11:24:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2016/08/09 13:08:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/08/09 13:08:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/08/09 13:08:58.0 XRT_FOCUS_POSITION_438_OG [0x1b6]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2016/08/09 13:09:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 00 00 00 56 35
2016/08/09 13:09:18.0 XRT_FLD_DIS_440_OG [0x1b8]
                        MDP_XRT_FLD_DIS 1 07-F0 d9
2016/08/09 13:23:54.0 XRT_FLRCTRL_DIS_441_OG [0x1b9]
                        MDP_XRT_FLRCTRL_DIS 1 07-F0 c9
2016/08/09 13:23:56.0 XRT_ARS_DIS_435_OG [0x1b3]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2016/08/09 13:23:58.0 XRT_QT_PROG_SET_429_OG [0x1ad]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0c
2016/08/09 13:24:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2016/08/09 15:08:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/08/09 15:08:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/08/09 15:08:58.0 XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]
                        XRT_FOCUS_RECAL 2 07-F8 78 00
2016/08/09 15:09:00.0 AOCs_OrE-point_Start_3_OG [0x099]
                        AOCU_NM 5 02-76 02 00 00 00 00
2016/08/09 15:12:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2016/08/09 15:13:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2016/08/09 15:13:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2016/08/09 15:13:22.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2016/08/09 15:13:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2016/08/09 15:13:26.0 XRT_FLD_RESET_433_OG [0x1b1]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2016/08/09 15:15:56.0 XRT_QT_PROG_SET_449_OG [0x1c1]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0b
2016/08/09 15:15:58.0 XRT_FL_PROG_SET_436_OG [0x1b4]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 07
2016/08/09 15:16:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2016/08/09 15:37:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/08/09 15:37:02.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2016/08/09 15:37:04.0 XRT_PREFLR_STRT_421_OG [0x1a5]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2016/08/09 15:40:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2016/08/09 16:21:30.0 XRT_Custom_430_OG [0x1ae]
2016/08/09 16:22:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2016/08/09 17:15:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/08/09 17:15:32.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2016/08/09 17:15:34.0 XRT_PREFLR_STRT_421_OG [0x1a5]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2016/08/09 17:18:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2016/08/09 17:58:00.5 XRT_Custom_430_OG [0x1ae]
2016/08/09 17:59:00.5 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2016/08/09 17:59:24.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/08/09 17:59:26.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/08/09 17:59:28.0 XRT_FOCUS_POSITION_403_OG [0x193]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2016/08/09 17:59:30.0 AOCs_OrE-point_Start_4_OG [0x09a]

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2016/08/09	17:59:48.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00	00	00	00	00
		MDP_XRT_FLD_DIS		1	07-F0	d9				
2016/08/09	18:02:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]		1	07-F0	c9				
		MDP_XRT_FLRCTRL_DIS		1	07-F0	c9				
2016/08/09	18:02:26.0	XRT_ARS_DIS_423_OG [0x1a7]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2016/08/09	18:02:28.0	XRT_QT_PROG_SET_425_OG [0x1a9]		2	07-F0	c4	01			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	01			
2016/08/09	18:02:30.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2016/08/09	18:09:24.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/08/09	18:09:26.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/08/09	18:09:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]		4	07-F8	22	fe	97	00	
		XRT_FOCUS_POSITION		4	07-F8	22	fe	97	00	
2016/08/09	18:09:30.0	AOCS_Ore-point_Start_3_OG [0x099]		5	02-76	02	00	00	00	00
		AOCU_NM		5	02-76	02	00	00	00	00
2016/08/09	18:09:48.0	XRT_FLD_ENA_411_OG [0x19b]		1	07-F0	d8				
		MDP_XRT_FLD_ENA		1	07-F0	d8				
2016/08/09	18:09:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		1	07-F0	c8				
		MDP_XRT_FLRCTRL_ENA		1	07-F0	c8				
2016/08/09	18:09:52.0	XRT_AEC_RESET_448_OG [0x1c0]		1	07-F0	d0				
		MDP_XRT_AEC_RESET		1	07-F0	d0				
2016/08/09	18:09:54.0	XRT_ARS_DIS_423_OG [0x1a7]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2016/08/09	18:09:56.0	XRT_FLD_RESET_433_OG [0x1b1]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2016/08/09	18:12:26.0	XRT_QT_PROG_SET_449_OG [0x1c1]		2	07-F0	c4	0b			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	0b			
2016/08/09	18:12:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]		2	07-F0	c5	07			
		MDP_XRT_FL_PROG_SET		2	07-F0	c5	07			
2016/08/09	18:12:30.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2016/08/09	18:54:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/08/09	18:54:02.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2016/08/09	18:54:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2016/08/09	18:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2016/08/09	19:34:30.0	XRT_Custom_430_OG [0x1ae]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2016/08/09	19:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2016/08/09	20:32:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/08/09	20:32:02.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2016/08/09	20:32:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2016/08/09	20:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2016/08/09	21:12:00.0	XRT_Custom_430_OG [0x1ae]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2016/08/09	21:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2016/08/09	22:10:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/08/09	22:10:32.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2016/08/09	22:10:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2016/08/09	22:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2016/08/09	22:47:30.0	XRT_Custom_430_OG [0x1ae]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2016/08/09	22:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2016/08/09	23:49:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/08/09	23:49:02.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2016/08/09	23:49:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2016/08/09	23:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2016/08/10	00:12:30.0	XRT_Custom_430_OG [0x1ae]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2016/08/10	00:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2016/08/10	00:29:54.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/08/10	00:29:56.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/08/10	00:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]		4	07-F8	22	fe	97	00	
		XRT_FOCUS_POSITION		4	07-F8	22	fe	97	00	
2016/08/10	00:30:00.0	AOCS_ORe-point_Start_5_OG [0x09b]		5	02-76	04	00	00	00	00
		AOCU_NM		5	02-76	04	00	00	00	00
2016/08/10	00:30:18.0	XRT_FLD_ENA_411_OG [0x19b]		1	07-F0	d8				
		MDP_XRT_FLD_ENA		1	07-F0	d8				
2016/08/10	00:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		1	07-F0	c8				
		MDP_XRT_FLRCTRL_ENA		1	07-F0	c8				
2016/08/10	00:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]		1	07-F0	d0				
		MDP_XRT_AEC_RESET		1	07-F0	d0				

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2016/08/10	00:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/08/10	00:30:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/10	00:32:56.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2016/08/10	00:32:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/08/10	00:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/10	01:27:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/10	01:27:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/10	01:27:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/10	01:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/10	01:50:30.0	XRT_Custom_430_OG [0x1ae]				
2016/08/10	01:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/10	03:01:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/10	03:01:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/10	03:01:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/10	03:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/10	03:29:00.5	XRT_Custom_430_OG [0x1ae]				
2016/08/10	03:30:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/10	04:31:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/10	04:31:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/10	04:31:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/10	04:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/10	05:07:30.0	XRT_Custom_430_OG [0x1ae]				
2016/08/10	05:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/10	05:43:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/10	05:43:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/10	05:43:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/08/10	05:44:00.0	AOCS_OrE-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 00 00 00 00
2016/08/10	05:44:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9
2016/08/10	05:46:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/08/10	05:46:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/08/10	05:46:58.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2016/08/10	05:47:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/10	05:53:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/10	05:53:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/10	05:53:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/08/10	05:54:00.0	AOCS_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	02 00 00 00 00
2016/08/10	05:54:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/08/10	05:54:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/08/10	05:54:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/08/10	05:54:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/08/10	05:54:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/10	05:56:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2016/08/10	05:56:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/08/10	05:57:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/10	06:11:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/10	06:11:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/10	06:11:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/10	06:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9

2016/08/10	06:45:30.0	XRT_Custom_430_OG [0x1ae]			
2016/08/10	06:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/10	07:52:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/10	07:52:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/10	07:52:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/10	07:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/10	08:24:00.0	XRT_Custom_430_OG [0x1ae]			
2016/08/10	08:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/10	09:31:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/10	09:31:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/10	09:31:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/10	09:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/10	10:02:30.0	XRT_Custom_430_OG [0x1ae]			
2016/08/10	10:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/10	11:17:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/10	11:17:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/10	11:17:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/10	11:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/10	11:40:30.0	XRT_Custom_430_OG [0x1ae]			
2016/08/10	11:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/10	12:56:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/10	12:56:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/10	12:56:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/10	12:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/10	13:19:00.0	XRT_Custom_430_OG [0x1ae]			
2016/08/10	13:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/10	14:34:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/10	14:34:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/10	14:34:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/10	14:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/10	15:09:30.0	XRT_Custom_430_OG [0x1ae]			
2016/08/10	15:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/10	16:13:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/10	16:13:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/10	16:13:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/10	16:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/10	16:56:00.0	XRT_Custom_430_OG [0x1ae]			
2016/08/10	16:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/10	17:40:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/10	17:40:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/10	17:40:58.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/08/10	17:41:00.0	AOCS_Ore-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	00 00 00 00 00
2016/08/10	17:41:18.0	XRT_FLD_DIS_406_OG [0x196]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2016/08/10	17:43:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/08/10	17:43:56.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2016/08/10	17:43:58.0	XRT_QT_PROG_SET_425_OG [0x1a9]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2016/08/10	17:44:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/10	17:50:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/10	17:50:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/10	17:50:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00

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2016/08/10	17:51:00.0	AOCS_OrE-point_Start_3_OG [0x099]								
		AOCU_NM	5	02-76	02	00	00	00	00	
2016/08/10	17:51:18.0	XRT_FLD_ENA_411_OG [0x19b]								
		MDP_XRT_FLD_ENA	1	07-F0	d8					
2016/08/10	17:51:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]								
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2016/08/10	17:51:22.0	XRT_AEC_RESET_448_OG [0x1c0]								
		MDP_XRT_AEC_RESET	1	07-F0	d0					
2016/08/10	17:51:24.0	XRT_ARS_DIS_423_OG [0x1a7]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2016/08/10	17:51:26.0	XRT_FLD_RESET_433_OG [0x1b1]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2016/08/10	17:53:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b				
2016/08/10	17:53:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]								
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	07				
2016/08/10	18:33:00.5	XRT_Custom_430_OG [0x1ae]								
2016/08/10	18:34:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2016/08/10	19:29:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2016/08/10	19:29:32.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2016/08/10	19:29:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2016/08/10	19:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2016/08/10	20:10:00.0	XRT_Custom_430_OG [0x1ae]								
2016/08/10	20:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2016/08/10	21:08:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2016/08/10	21:08:02.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2016/08/10	21:08:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2016/08/10	21:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2016/08/10	21:46:30.0	XRT_Custom_430_OG [0x1ae]								
2016/08/10	21:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2016/08/10	22:46:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2016/08/10	22:46:32.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2016/08/10	22:46:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2016/08/10	22:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2016/08/10	23:21:00.0	XRT_Custom_430_OG [0x1ae]								
2016/08/10	23:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2016/08/11	00:24:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2016/08/11	00:24:32.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2016/08/11	00:24:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2016/08/11	00:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2016/08/11	00:29:54.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2016/08/11	00:29:56.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2016/08/11	00:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]								
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00		
2016/08/11	00:30:00.0	AOCS_OrE-point_Start_5_OG [0x09b]								
		AOCU_NM	5	02-76	04	00	00	00	00	
2016/08/11	00:30:18.0	XRT_FLD_ENA_411_OG [0x19b]								
		MDP_XRT_FLD_ENA	1	07-F0	d8					
2016/08/11	00:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]								
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2016/08/11	00:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]								
		MDP_XRT_AEC_RESET	1	07-F0	d0					
2016/08/11	00:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2016/08/11	00:30:26.0	XRT_FLD_RESET_433_OG [0x1b1]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2016/08/11	00:32:56.0	XRT_QT_PROG_SET_407_OG [0x197]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f				
2016/08/11	00:32:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]								
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	07				
2016/08/11	00:47:30.0	XRT_Custom_430_OG [0x1ae]								
2016/08/11	00:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2016/08/11	02:01:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2016/08/11	02:01:02.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2016/08/11	02:01:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2016/08/11	02:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					

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2016/08/11	02:26:00.0	XRT_Custom_430_OG [0x1ae]			
2016/08/11	02:27:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/11	03:35:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/11	03:35:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/11	03:35:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/11	03:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/11	04:04:30.0	XRT_Custom_430_OG [0x1ae]			
2016/08/11	04:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/11	05:07:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/11	05:07:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/11	05:07:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/11	05:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/11	05:42:30.0	XRT_Custom_430_OG [0x1ae]			
2016/08/11	05:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/11	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/11	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/11	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/08/11	06:00:00.0	AOCS_Or-e-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	00 00 00 00 00
2016/08/11	06:00:18.0	XRT_FLD_DIS_406_OG [0x196]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2016/08/11	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/08/11	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2016/08/11	06:02:58.0	XRT_QT_PROG_SET_425_OG [0x1a9]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2016/08/11	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/11	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/11	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/11	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/08/11	06:10:00.0	AOCS_Or-e-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	02 00 00 00 00
2016/08/11	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2016/08/11	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/08/11	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2016/08/11	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2016/08/11	06:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/11	06:12:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2016/08/11	06:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/08/11	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/11	06:47:30.5	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/11	06:47:32.5	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/11	06:47:34.5	XRT_PREFLR_STRT_421_OG [0x1a5]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/11	06:50:44.5	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/11	07:21:00.0	XRT_Custom_430_OG [0x1ae]			
2016/08/11	07:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/11	08:27:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/11	08:27:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/11	08:27:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/11	08:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/11	08:59:30.0	XRT_Custom_430_OG [0x1ae]			
2016/08/11	09:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/11	10:08:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/11	10:08:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da

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2016/08/11	10:08:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/08/11	10:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/08/11	10:37:30.0	XRT_Custom_430_OG [0x1ae]							
2016/08/11	10:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/08/11	11:53:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/08/11	11:53:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/08/11	11:53:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/08/11	11:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/08/11	12:16:00.0	XRT_Custom_430_OG [0x1ae]							
2016/08/11	12:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/08/11	13:32:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/08/11	13:32:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/08/11	13:32:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/08/11	13:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/08/11	13:54:30.0	XRT_Custom_430_OG [0x1ae]							
2016/08/11	13:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/08/11	15:10:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/08/11	15:10:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/08/11	15:10:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/08/11	15:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/08/11	15:45:30.0	XRT_Custom_430_OG [0x1ae]							
2016/08/11	15:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/08/11	15:47:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/08/11	15:47:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/08/11	15:47:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/08/11	15:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/08/11	15:54:30.0	XRT_Custom_430_OG [0x1ae]							
2016/08/11	15:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/08/11	16:48:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/08/11	16:48:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/08/11	16:48:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/08/11	16:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/08/11	17:31:00.0	XRT_Custom_430_OG [0x1ae]							
2016/08/11	17:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/08/11	18:02:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/08/11	18:02:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/08/11	18:02:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/08/11	18:02:30.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2016/08/11	18:02:48.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/08/11	18:05:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/08/11	18:05:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/08/11	18:05:28.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2016/08/11	18:05:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/08/11	18:12:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/08/11	18:12:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/08/11	18:12:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/08/11	18:12:30.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2016/08/11	18:12:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/08/11	18:12:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/08/11	18:12:52.0	XRT_AEC_RESET_448_OG [0x1c0]							

2016/08/11	18:12:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/08/11	18:12:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/08/11	18:15:26.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/11	18:15:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2016/08/11	18:15:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/08/11	18:27:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/11	18:27:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/11	18:27:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/11	18:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/11	19:08:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/11	19:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/11	20:05:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/11	20:05:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/11	20:05:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/11	20:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/11	20:45:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/11	20:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/11	21:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/11	21:43:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/11	21:43:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/11	21:43:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/11	21:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/11	21:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/11	22:21:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/11	22:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/11	23:22:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/11	23:22:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/11	23:22:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/11	23:22:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/11	23:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/11	23:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/11	23:49:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/11	23:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/12	01:00:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/12	01:00:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/12	01:00:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/12	01:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/12	01:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/12	01:23:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/12	01:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/12	02:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/12	02:35:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/12	02:35:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/12	02:35:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/12	02:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/12	02:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/12	03:01:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/12	03:02:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/12	04:03:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/12	04:03:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/12	04:03:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/12	04:03:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/12	04:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/12	04:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/12	04:39:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/12	04:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/12	05:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/12	05:43:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/12	05:43:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/12	05:43:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da

2016/08/12	05:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e8	
2016/08/12	06:18:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/08/12	06:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/08/12	06:19:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/08/12	06:19:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/08/12	06:19:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2016/08/12	06:20:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 00 00 00 00	
2016/08/12	06:20:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2016/08/12	06:22:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2016/08/12	06:22:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/08/12	06:22:58.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01	
2016/08/12	06:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/08/12	06:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/08/12	06:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/08/12	06:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2016/08/12	06:30:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	02 00 00 00 00	
2016/08/12	06:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/08/12	06:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/08/12	06:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/08/12	06:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/08/12	06:30:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/08/12	06:32:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b	
2016/08/12	06:32:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2016/08/12	06:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/08/12	07:24:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/08/12	07:24:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/08/12	07:24:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/08/12	07:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/08/12	07:56:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/08/12	07:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/08/12	09:03:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/08/12	09:03:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/08/12	09:03:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/08/12	09:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/08/12	09:34:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/08/12	09:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/08/12	10:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/08/12	10:46:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/08/12	10:46:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/08/12	10:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/08/12	11:13:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/08/12	11:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/08/12	12:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/08/12	12:29:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/08/12	12:29:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/08/12	12:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/08/12	12:51:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/08/12	12:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/08/12	14:07:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	

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2016/08/12	14:07:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/12	14:07:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/12	14:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/12	14:30:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/12	14:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/12	14:32:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/12	14:32:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/12	14:32:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/12	14:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/12	14:41:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/12	14:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/12	15:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/12	15:46:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/12	15:46:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/12	15:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/12	16:29:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/12	16:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/12	17:24:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/12	17:24:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/12	17:24:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/12	17:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/12	18:05:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/12	18:05:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/12	18:05:58.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/12	18:05:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/12	18:06:00.0	XRT_FOCUS_POSITION	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/08/12	18:06:00.0	AOCs_OrE-point_Start_4_OG [0x09a]	AOCs_OrE-point_Start_4_OG [0x09a]	5	02-76	00 00 00 00 00
2016/08/12	18:06:18.0	XRT_FLD_DIS_406_OG [0x196]	AOCs_OrE-point_Start_4_OG [0x09a]	5	02-76	00 00 00 00 00
2016/08/12	18:08:54.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9
2016/08/12	18:08:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2016/08/12	18:08:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/08/12	18:08:58.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/08/12	18:08:58.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/08/12	18:09:00.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2016/08/12	18:09:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2016/08/12	18:15:54.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/12	18:15:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/12	18:15:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/12	18:15:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/12	18:15:58.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/12	18:15:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/12	18:16:00.0	XRT_FOCUS_POSITION	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/08/12	18:16:00.0	AOCs_OrE-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/08/12	18:16:00.0	AOCs_OrE-point_Start_3_OG [0x099]	AOCs_OrE-point_Start_3_OG [0x099]	5	02-76	02 00 00 00 00
2016/08/12	18:16:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCs_OrE-point_Start_3_OG [0x099]	5	02-76	02 00 00 00 00
2016/08/12	18:16:20.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/08/12	18:16:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/08/12	18:16:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/08/12	18:16:24.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/08/12	18:16:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/08/12	18:16:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/08/12	18:16:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/08/12	18:18:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/12	18:18:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/12	18:18:58.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2016/08/12	18:18:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2016/08/12	18:19:00.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/08/12	18:19:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/08/12	19:02:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/12	19:02:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/12	19:02:32.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/12	19:02:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/12	19:02:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/12	19:02:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/12	19:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/12	19:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/12	19:43:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/12	19:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/12	19:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0

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2016/08/12	20:41:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/08/12	20:41:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/08/12	20:41:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/08/12	20:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/08/12	21:20:00.0	XRT_Custom_430_OG [0x1ae]							
2016/08/12	21:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/08/12	22:19:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/08/12	22:19:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/08/12	22:19:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/08/12	22:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/08/12	22:55:30.0	XRT_Custom_430_OG [0x1ae]							
2016/08/12	22:56:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/08/12	23:58:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/08/12	23:58:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/08/12	23:58:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/08/13	00:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/08/13	00:20:00.0	XRT_Custom_430_OG [0x1ae]							
2016/08/13	00:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/08/13	01:35:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/08/13	01:35:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/08/13	01:35:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/08/13	01:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/08/13	01:58:00.0	XRT_Custom_430_OG [0x1ae]							
2016/08/13	01:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/08/13	03:10:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/08/13	03:10:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/08/13	03:10:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/08/13	03:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/08/13	03:36:30.0	XRT_Custom_430_OG [0x1ae]							
2016/08/13	03:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/08/13	04:39:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/08/13	04:39:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/08/13	04:39:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/08/13	04:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/08/13	05:15:00.0	XRT_Custom_430_OG [0x1ae]							
2016/08/13	05:16:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/08/13	05:52:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/08/13	05:52:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/08/13	05:52:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/08/13	05:52:30.0	AOCS_Orе-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2016/08/13	05:52:48.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/08/13	05:55:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/08/13	05:55:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/08/13	05:55:28.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2016/08/13	05:55:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/08/13	06:02:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/08/13	06:02:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/08/13	06:02:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/08/13	06:02:30.0	AOCS_Orе-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2016/08/13	06:02:48.0	XRT_FLD_ENA_411_OG [0x19b]							

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2016/08/13	06:02:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/08/13	06:02:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/08/13	06:02:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/08/13	06:02:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/13	06:05:26.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2016/08/13	06:05:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/08/13	06:05:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/13	06:20:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/13	06:20:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/13	06:20:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/13	06:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/13	06:53:30.0	XRT_Custom_430_OG [0x1ae]				
2016/08/13	06:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/13	08:00:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/13	08:00:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/13	08:00:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/13	08:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/13	08:31:30.0	XRT_Custom_430_OG [0x1ae]				
2016/08/13	08:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/13	09:40:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/13	09:40:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/13	09:40:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/13	09:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/13	10:10:00.0	XRT_Custom_430_OG [0x1ae]				
2016/08/13	10:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/13	11:26:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/13	11:26:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/13	11:26:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/13	11:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/13	11:48:30.0	XRT_Custom_430_OG [0x1ae]				
2016/08/13	11:49:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/13	13:05:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/13	13:05:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/13	13:05:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/13	13:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/13	13:26:30.0	XRT_Custom_430_OG [0x1ae]				
2016/08/13	13:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/13	14:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/13	14:43:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/13	14:43:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/13	14:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/13	15:18:00.0	XRT_Custom_430_OG [0x1ae]				
2016/08/13	15:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/13	16:21:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/13	16:21:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/13	16:21:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/13	16:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/13	17:04:30.0	XRT_Custom_430_OG [0x1ae]				
2016/08/13	17:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/13	17:49:54.0	XRT_CTRL_MANU_402_OG [0x192]				

2016/08/13	17:49:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/13	17:49:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/13	17:50:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/08/13	17:50:18.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00 00 00 00 00
2016/08/13	17:52:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2016/08/13	17:52:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/08/13	17:52:58.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/08/13	17:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2016/08/13	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/13	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/13	17:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/13	18:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/08/13	18:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02 00 00 00 00
2016/08/13	18:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/08/13	18:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/08/13	18:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/08/13	18:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/08/13	18:02:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/13	18:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2016/08/13	18:41:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/08/13	18:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2016/08/13	19:38:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c1
2016/08/13	19:38:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/13	19:38:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/13	19:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/13	20:18:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/13	20:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2016/08/13	21:17:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/13	21:17:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/13	21:17:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/13	21:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/13	21:55:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/13	21:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2016/08/13	22:55:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/13	22:55:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/13	22:55:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/13	22:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/13	23:28:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/13	23:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2016/08/14	00:33:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/14	00:33:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/14	00:33:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/14	00:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/08/14	00:55:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/08/14	00:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2016/08/14	02:09:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/08/14	02:09:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/08/14	02:09:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/08/14	02:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8

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2016/08/14	02:33:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/08/14	02:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
2016/08/14	03:40:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/08/14	03:40:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/08/14	03:40:34.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/08/14	03:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/08/14	04:12:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/08/14	04:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
2016/08/14	05:16:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/08/14	05:16:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/08/14	05:16:04.0	XRT_PREFLR_STRT_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/08/14	05:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/08/14	05:50:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/08/14	05:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
2016/08/14	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/08/14	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/08/14	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/08/14	06:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2016/08/14	06:00:18.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00 00 00 00 00			
2016/08/14	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2016/08/14	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2016/08/14	06:02:58.0	XRT_QT_PROG_SET_418_OG [0x1a2]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/08/14	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04			
2016/08/14	06:10:00.0	AOCS_ORe-point_Start_3_OG [0x099]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/08/14	06:14:30.0	XRT_CTRL_MANU_402_OG [0x192]	AOCU_NM	5	02-76	02 00 00 00 00			
2016/08/14	06:14:40.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/08/14	06:15:00.0	XRT_TCIB_XRT_S_HTR_A_ENA_417_OG [0x1a1]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/08/16	10:23:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	TCIB_XRT_S_HTR_A_ENA	0	04-BC				
			AOCU_NM	5	02-76	00 00 00 00 00			