XRT Timeline to be uploaded on 2016/08/16

Period: 2016/08/16 10:13:00 - 2016/08/20 10:56:00

* * * * * **Normal mode** * * * * *

XOB #1AF1: CCD Monitor During Bakeout - G-band 3ms - 1kx1k - Q90 - 1st Quadrant -Al/mesh(512ms), Al/Poly(1443ms) - w leak image-3ms

Term	Pointing (x, y)	Comment
08/17 12:38:00 - 08/17 12:44:54	Fixed (-528.4, -528.4)	# XRT pointing 1/4

PROG= 09 1-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 86 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 55 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 3 2-time(s) 2.0sec

Seqn= 54 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1AF2: CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh (512ms), Al/Poly (1443ms) - w leak image-3 ms

Term	Pointing (x, y)	Comment
08/17 12:48:00 - 08/17 12:54:54	Fixed (528.4, -528.4)	# XRT pointing 2/4

PROG= 05 1-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 15 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 55 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 3 2-time(s) 2.0sec

Seqn= 54 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1AF3: CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - Q90 - 3rd Quadrant - Al/mesh (512ms), Al/Poly (1443ms) - w leak image-3 ms

Term	Pointing (x, y)	Comment
08/17 12:58:00 - 08/17 13:04:54	Fixed (528.4, 528.4)	# XRT pointing 3/4

PROG= 20 1-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 35 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 55 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 3 2-time(s) 2.0sec

Seqn= 54 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1AF4: CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh (512ms), Al/Poly (1443ms) - w leak image-3 ms

Term	Pointing (x, y)	Comment
08/17 13:08:00 - 08/17 13:14:54	Fixed (-528.4, 528.4)	# XRT pointing 4/4

PROG= 14 1-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 3 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

- **Seqn= 55 2-time(s) 2.0sec**
  - Open/Al-mesh Open/Ti-poly close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Al-poly/Open med-Be/Open close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
- **Subr= 3 2-time(s) 2.0sec**
  - **Seqn= 54 1-time(s) 2.0sec**
    - Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
    - Open/G-band Open/G-band close Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #1AAC: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 60s cadence, G-band - 384x384 3ms

Term	Pointing (x, y)	Comment
08/17 13:18:00 - 08/17 18:29:54	Fixed (-26.0, 866.0)	# HOP81 (N-pole)

PROG= 06 Inf.-time(s)
- **Subr= 1 1-time(s) 2.0sec**
  - **Seqn= 9 2-time(s) 2.0sec**
    - Open/G-band Open/G-band close Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
- **Subr= 2 1-time(s) 2.0sec**
  - **Seqn= 7 1-time(s) 30.0sec**
    - Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec
- **Subr= 3 30-time(s) 2.0sec**
  - **Seqn= 57 1-time(s) 60.0sec**
    - Open/Al-mesh Open/Al-mesh close Safe Norm 4.00s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec
    - Al-poly/Open Al-poly/Open close Safe Norm 5.66s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #1B43: Synoptic Q95 2x2 - Al/mesh(24/256/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(33/512/2048) + Th

Term	Pointing (x, y)	Comment
08/17 18:37:00 - 08/17 18:39:24	Fixed (0.0, 0.0)	synoptic, shifted 30 min
08/18 04:03:00 - 08/18 04:09:54	Fixed (0.0, 0.0)	synoptic, shifted -2hour requested by HOP173

PROG= 01 1-time(s)
- **Subr= 1 1-time(s) 2.0sec**
  - **Seqn= 5 1-time(s) 2.0sec**
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
  - **Seqn= 1 1-time(s) 2.0sec**
    - Open/Al-mesh Open/Al-mesh close Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - **Seqn= 12 1-time(s) 2.0sec**
    - Al-poly/Open Al-poly/thick-Al close Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Al-poly/Open Al-poly/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Al-poly/Open Al-poly/thick-Al close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - **Seqn= 85 1-time(s) 2.0sec**
    - thin-Be/Open thin-Be/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - thin-Be/Open thin-Be/Open close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - thin-Be/Open thin-Be/Open close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - **Seqn= 54 1-time(s) 2.0sec**
    - Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
    - Open/G-band Open/G-band close Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #1AFF: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
08/17 19:25:01 - 08/17 20:23:30	Track (644.5, -58.9) @ 08/17 18:40:30	# IHOP317 (with BBSO)
08/18 08:12:00 - 08/18 09:15:00	Track (774.8, -7.3) @ 08/18 08:00:00	# AR track

PROG= 11 Inf.-time(s)
- **Subr= 1 1-time(s) 2.0sec**
  - **Seqn= 56 1-time(s) 2.0sec**
    - Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
    - Open/G-band Open/G-band close Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
- **Subr= 2 5-time(s) 2.0sec**
  - **Seqn= 75 1-time(s) 2.0sec**
    - Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec
    - Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
    - thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec
    - thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
  - **Seqn= 96 4-time(s) 60.0sec**
    - Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec
    - thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 15.0sec
    - Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 2.0sec
    - thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 15.0sec
    - Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec
    - thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #1B40: AR - Standard Core - (Three Filter with Al/poly, thin-Be and med-Be) with PFB, 384x384 at 1064 1048, with G-band (3ms/3ms VLS=CLS), 90 cad

Term	Pointing (x, y)	Comment
08/17 21:03:00 - 08/18 00:29:54	Fixed (880.0, 150.0)	# HOP 316

PROG= 10 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 56 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0 2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0 2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0 2.0sec

Subr= 2 5-time(s) 2.0sec

Seqn= 22 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0 2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0 2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0 2.0sec

Seqn= 27 4-time(s) 90.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0 2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0 25.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0 2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1 25.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2 2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #1B38: CME - HOP201 - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 512x512 at 1064 1048, thin-Be, and Al/poly context, with G-ba

Term	Pointing (x, y)	Comment
08/18 00:33:00 - 08/18 03:29:54	Track (704.1, -27.2) @ 08/18 00:30:00	# IHOP 322 (with Hida/FSO)

PROG= 15 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 69 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0 2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0 2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0 2.0sec

Subr= 2 5-time(s) 2.0sec

Seqn= 80 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0 2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0 2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0 2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0 2.0sec

Seqn= 96 4-time(s) 60.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0 2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0 15.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1 2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1 15.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2 2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #1B44: AR-(filter ratio Al/poly thin-Be), 512x512 at 1064 1048, with G-band 3ms, PFB, 60s cad

Term	Pointing (x, y)	Comment
08/18 03:33:00 - 08/18 03:59:54	Track (803.6, 43.2) @ 08/18 03:30:00	# HOP 173
08/18 04:13:00 - 08/18 05:59:54	Track (806.7, 43.8) @ 08/18 04:10:00	# HOP 173

PROG= 13 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 78 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0 2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0 2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0 2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 66 60-time(s) 60.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0 2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0 2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1 2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1 2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2 2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #1AEC: G-Band Alignment with North Pole Q90 2x2 (G-band and VLS=CLS) - 1msec (Al/poly) - 4096msec - 5min cadence - Partial Sun-wNGT

Term	Pointing (x, y)	Comment
08/18 06:15:05 - 08/18 07:59:54	Fixed (0.0, 930.0)	# Co-alignment (N-pole)

PROG= 19 1-time(s)

Subr= 1 24-time(s) 300.0sec

Seqn= 98 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	2x2	1024x1024 (1536, 1536)	Q=90	0	0 2.0sec

Seqn= 63 1-time(s) 2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Seqn= 45 1-time(s) 2.0sec												
Al-poly/Open	med-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	1024x1024 (1536, 1536)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer		Interval

Flare mode

XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
08/17 13:18:00 - 08/17 18:29:54	Fixed (-26.0, 866.0)	# HOP81 (N-pole)
08/17 19:25:01 - 08/17 20:23:30	Track (644.5, -58.9) @ 08/17 18:40:30	# IHOP317 (with BBSO)
08/17 21:03:00 - 08/18 00:29:54	Fixed (880.0, 150.0)	# HOP 316
08/18 00:33:00 - 08/18 03:29:54	Track (704.1, -27.2) @ 08/18 00:30:00	# IHOP 322 (with Hida/FSO)
08/18 03:33:00 - 08/18 03:59:54	Track (803.6, 43.2) @ 08/18 03:30:00	# HOP 173
08/18 04:13:00 - 08/18 05:59:54	Track (806.7, 43.8) @ 08/18 04:10:00	# HOP 173
08/18 08:12:00 - 08/18 09:15:00	Track (774.8, -7.3) @ 08/18 08:00:00	# AR track

PROG= 07 30-time(s)												
Subr= 1 20-time(s) 2.0sec												
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn=100 1-time(s) 10.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 10 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 84 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer		Interval

Active Region Search

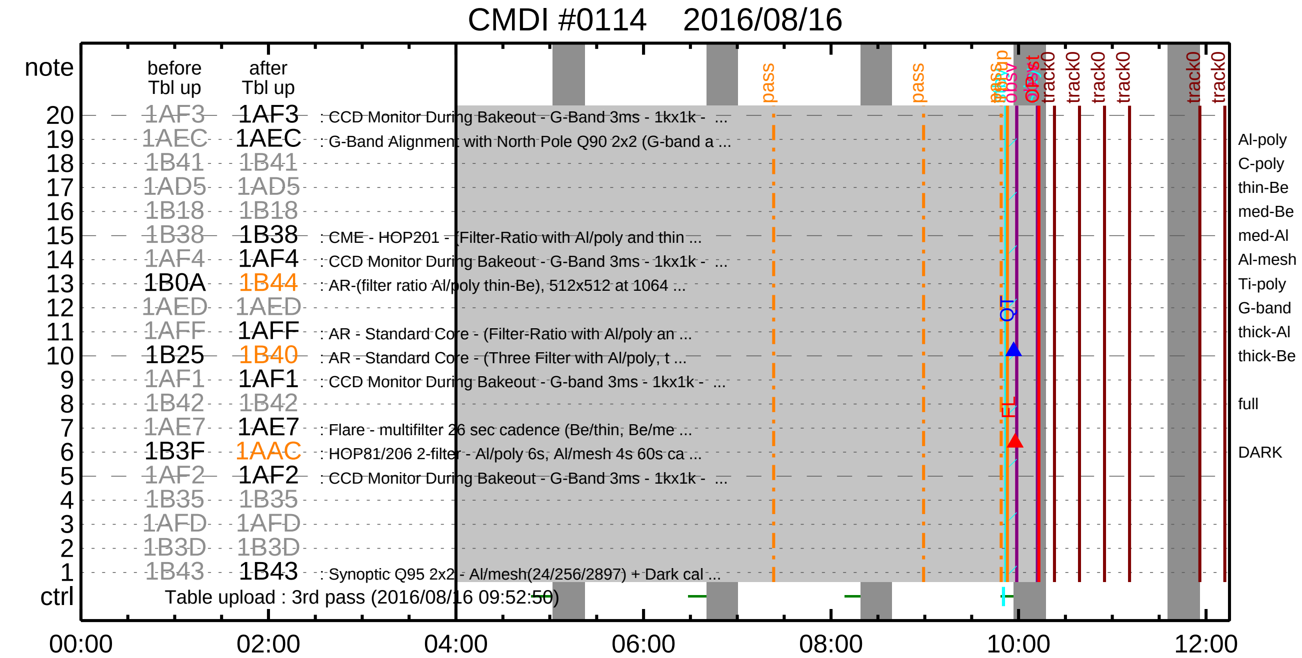
NOT USED

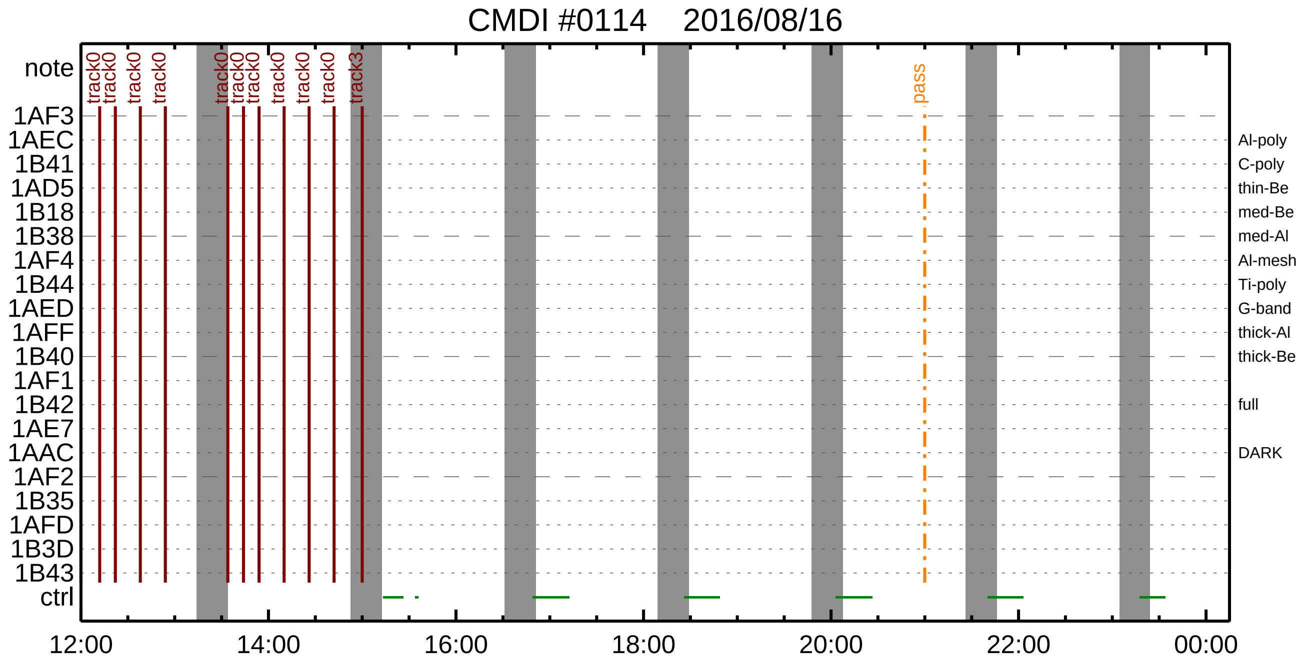
Flare Detection

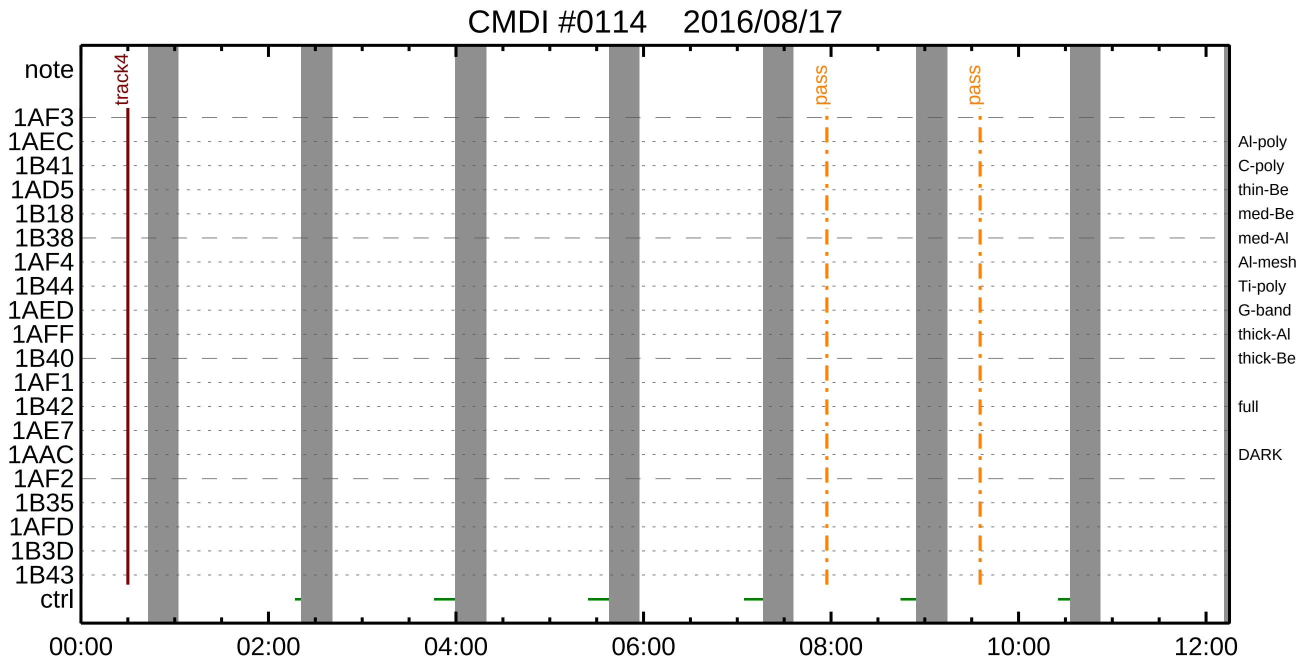
FLD Patrol

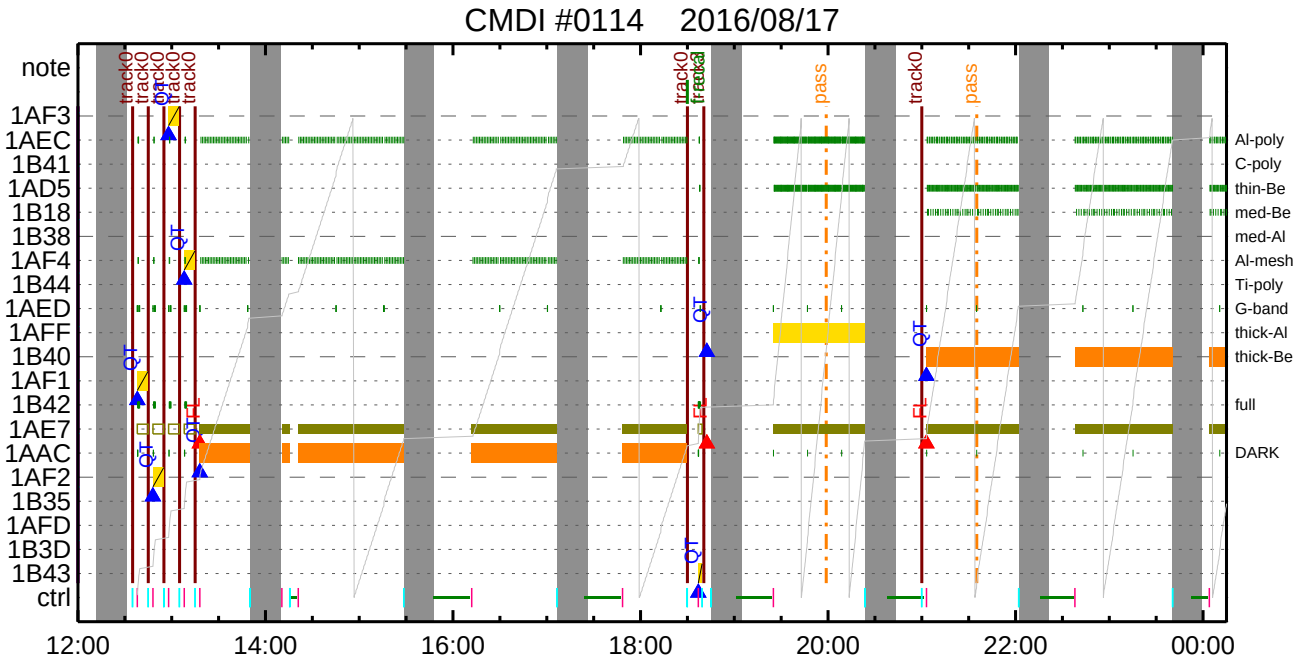
Term	Pointing (x, y)	Comment
08/17 13:15:18 - 08/17 18:34:18	Fixed (-26.0, 866.0)	# HOP81 (N-pole)
08/17 18:39:48 - 08/18 04:00:18	Fixed (0.0, 0.0)	synoptic, shifted 30 min
08/18 04:10:18 - 08/18 06:00:23	Track (806.7, 43.8) @ 08/18 04:10:00	# HOP 173
08/18 08:00:18 - 08/20 10:56:00	Track (774.8, -7.3) @ 08/18 08:00:00	# AR track

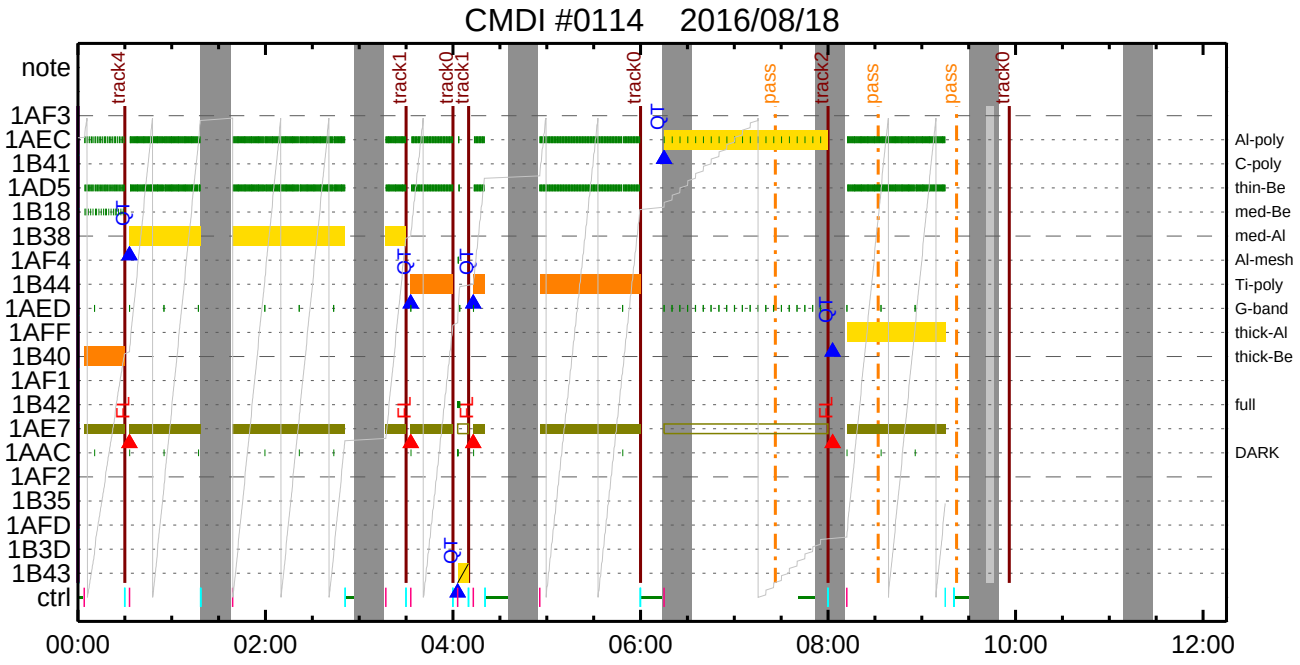
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer		Interval

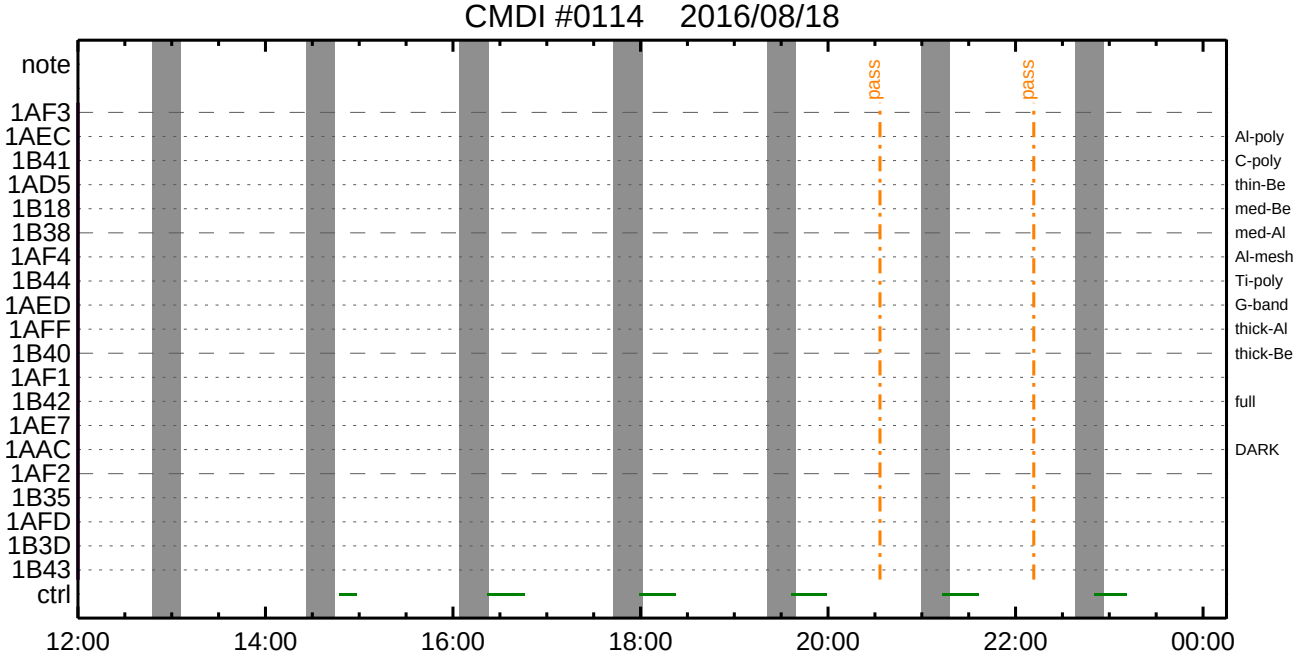

CMDI #0114 2016/08/16
note
before Tbl up
after Tbl up
: CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - ...
: G-Band Alignment with North Pole Q90 2x2 (G-band a ...
: CME - HOP201 - Filter-Ratio with Al/poly and thin ...
: CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - ...
: AR-(filter ratio Al/poly thin-Be), 512x512 at 1064 ...
: AR - Standard Core - (Filter-Ratio with Al/poly an ...
: AR - Standard Core - (Three Filter with Al/poly, t ...
: CCD Monitor During Bakeout - G-band 3ms - 1kx1k - ...
: Flare - multifilter 26 sec cadence (Be/thin, Be/me ...
: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 60s ca ...
: CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - ...
: Synoptic Q95 2x2 - Al/mesh(24/256/2897) + Dark cal ...
Table upload : 3rd pass (2016/08/16 09:52:50)
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
pass
track0
OT
FL
00:00 02:00 04:00 06:00 08:00 10:00 12:00

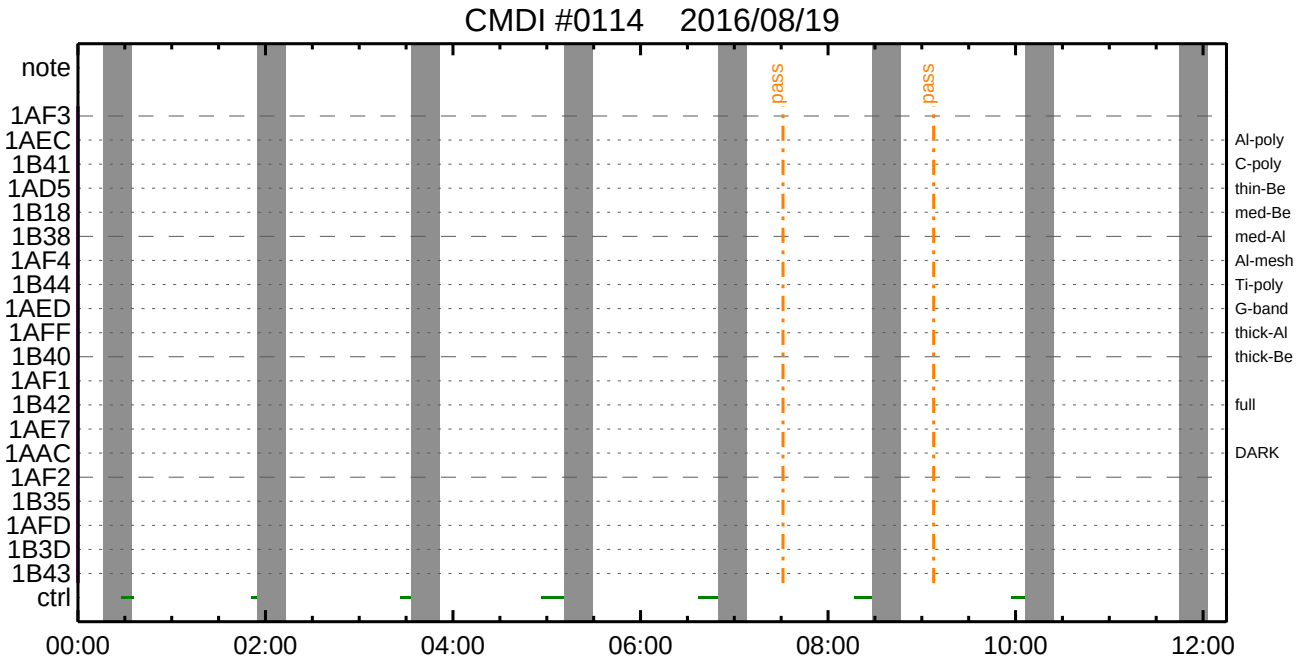

CMDI #0114 2016/08/16
note
1AF3
1AEC
1B41
1AD5
1B18
1B38
1AF4
1B44
1AED
1AFF
1B40
1AF1
1B42
1AE7
1AAC
1AF2
1B35
1AFD
1B3D
1B43
ctrl
track0
track3
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00 14:00 16:00 18:00 20:00 22:00 00:00

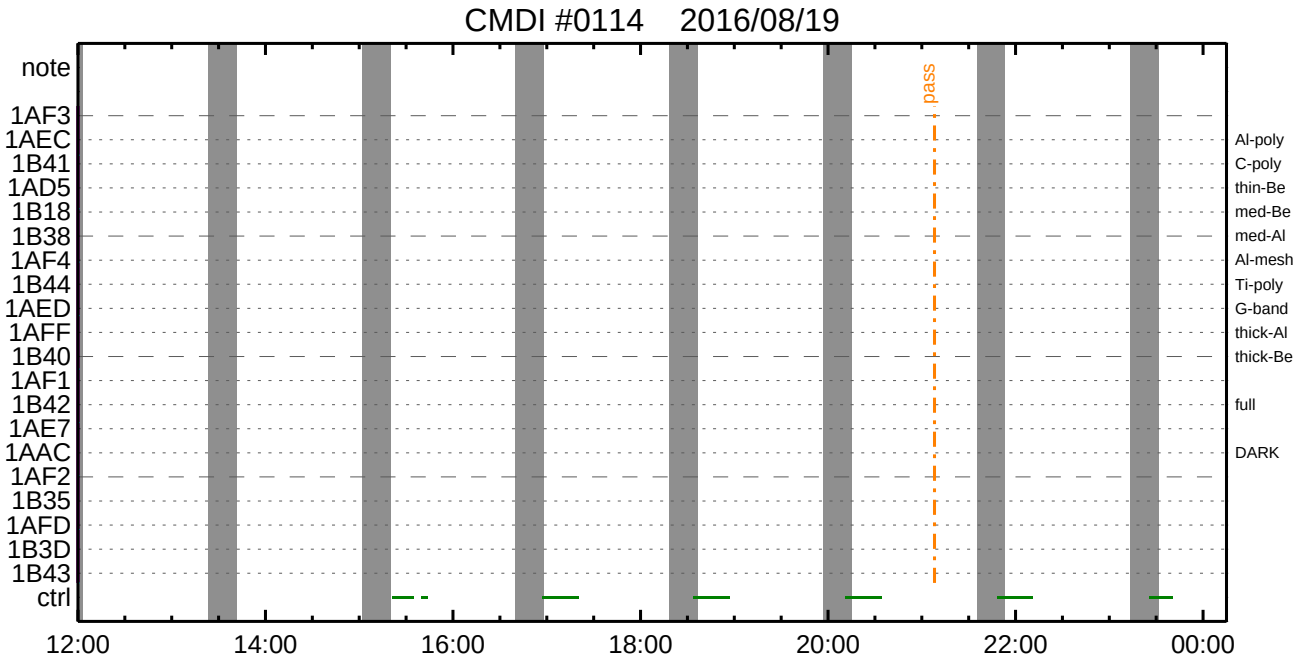

CMDI #0114 2016/08/17
note
1AF3
1AEC
1B41
1AD5
1B18
1B38
1AF4
1B44
1AED
1AFF
1B40
1AF1
1B42
1AE7
1AAC
1AF2
1B35
1AFD
1B3D
1B43
ctrl
track4
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00 02:00 04:00 06:00 08:00 10:00 12:00

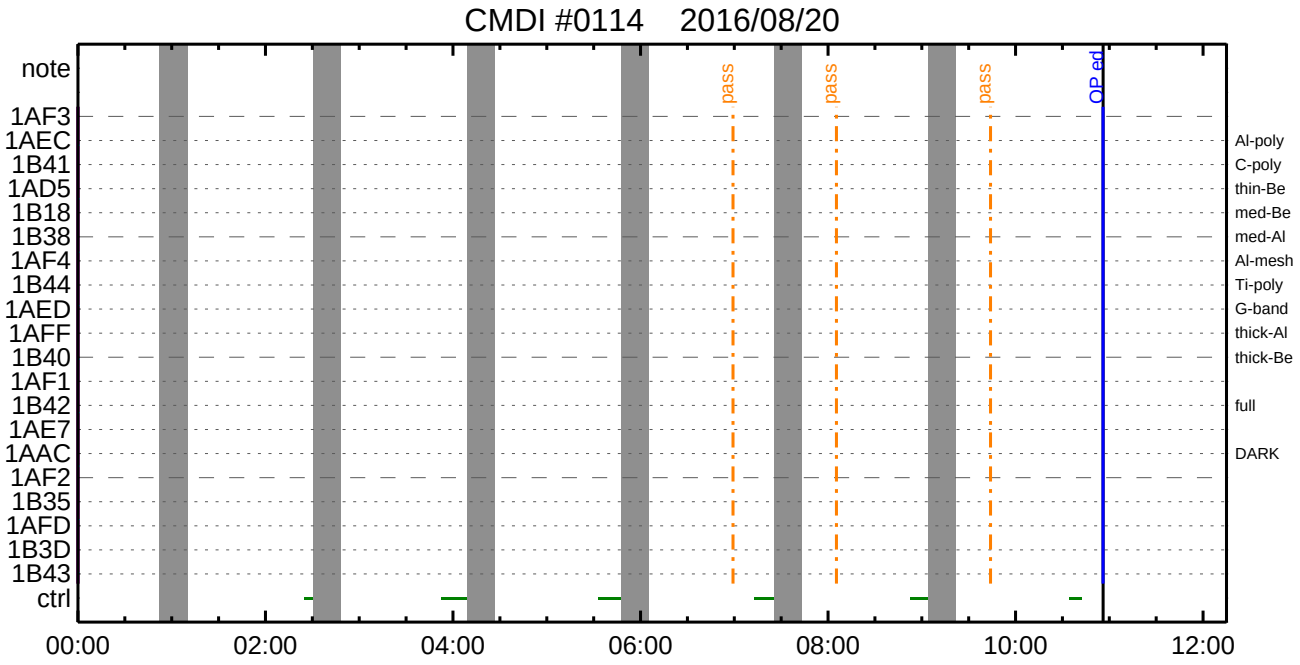

CMDI #0114 2016/08/17
note
1AF3
1AEC
1B41
1AD5
1B18
1B38
1AF4
1B44
1AED
1AFF
1B40
1AF1
1B42
1AE7
1AAC
1AF2
1B35
1AFD
1B3D
1B43
ctrl
track0
QT
pass
FL
12:00
14:00
16:00
18:00
20:00
22:00
00:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK

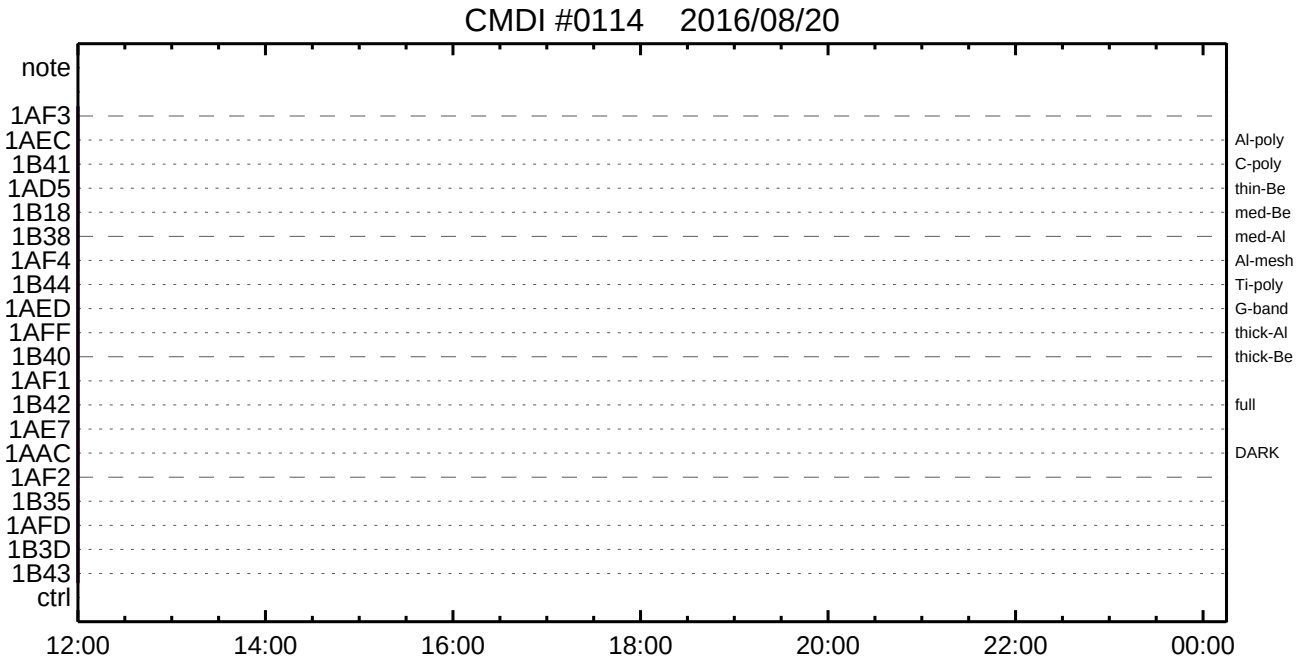

CMDI #0114 2016/08/18
note
1AF3
1AEC
1B41
1AD5
1B18
1B38
1AF4
1B44
1AED
1AFF
1B40
1AF1
1B42
1AE7
1AAC
1AF2
1B35
1AFD
1B3D
1B43
ctrl
track4
track1
track0
QT
pass
track2
FL
00:00
02:00
04:00
06:00
08:00
10:00
12:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK


CMDI #0114 2016/08/18
note
1AF3
1AEC
1B41
1AD5
1B18
1B38
1AF4
1B44
1AED
1AFF
1B40
1AF1
1B42
1AE7
1AAC
1AF2
1B35
1AFD
1B3D
1B43
ctrl
pass
12:00
14:00
16:00
18:00
20:00
22:00
00:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK


CMDI #0114 2016/08/19
note
1AF3
1AEC
1B41
1AD5
1B18
1B38
1AF4
1B44
1AED
1AFF
1B40
1AF1
1B42
1AE7
1AAC
1AF2
1B35
1AFD
1B3D
1B43
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0114 2016/08/19
note
1AF3
1AEC
1B41
1AD5
1B18
1B38
1AF4
1B44
1AED
1AFF
1B40
1AF1
1B42
1AE7
1AAC
1AF2
1B35
1AFD
1B3D
1B43
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0114 2016/08/20
note
1AF3
1AEC
1B41
1AD5
1B18
1B38
1AF4
1B44
1AED
1AFF
1B40
1AF1
1B42
1AE7
1AAC
1AF2
1B35
1AFD
1B3D
1B43
ctrl
pass
pass
pass
OP end
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0114 2016/08/20
note
1AF3
1AEC
1B41
1AD5
1B18
1B38
1AF4
1B44
1AED
1AFF
1B40
1AF1
1B42
1AE7
1AAC
1AF2
1B35
1AFD
1B3D
1B43
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

```
(a) Spacecraft Operation Procedure (real-commands)

main-051 2016-08-16 14:20:36 289 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇÎ»þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ****************************
0099     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0100     C. ****************************
0101     C.
0102   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0103   . S. OP    op-051:OP
0104     ()
0105   . S. OG    og-051:OG
0106     ()
0107     C.
0108   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0109     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0110   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111     BC       (20 00 7f 01 02)
0112     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0113     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0114     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0115     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0116     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0117   +. DC 01-22 DHU_MODE_CHNG
0118     BC       (07 0b f8)
0119     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0120     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0121     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0122     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0123     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0124   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0125     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0126   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0127     C.
0128     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0129   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130     BC       (20 80 7f 01 02)
0131     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0132     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0133     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0134     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0135     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0136   +. DC 01-22 DHU_MODE_CHNG
0137     BC       (07 0b f8)
0138     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0139     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0140     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0141     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0142     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0143   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0144     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0145   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0146     C.
0147     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149     BC       (21 00 41 01 02)
0150     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0151     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0152     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0153     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0154     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0155   +. DC 01-22 DHU_MODE_CHNG
0156     BC       (07 0b f8)
0157     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0158     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0159     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0160     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0161     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0162   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0163     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0164   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0165     C.
0166   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0167     C. DHU¥â¡¼¥É¡Ê¼ý¸¸¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0168   +. DC 01-22 DHU_MODE_CHNG
0169     BC       (02 0a f8)
0170     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     2
0171     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0172     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0173     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0174     C.
0175   . C. ********************************
0176     C. TI-CMD SET (OPOG STOP/COPY/START)
0177     C. ********************************
0178     C.
0179   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
0180     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¹¤Ç¹Ô¤¦¤³¤È¡£
0181     C.
0182   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
0183   +. TI 2016-08-16 10:08:00.0
0184     DC 01-B3 DHU_OP_STOP
0185     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0186     C.
0187   +. TI 2016-08-16 10:08:01.0
0188     DC 01-B4 DHU_OP_COPY
0189     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0190     C.
0191   +. TI 2016-08-16 10:08:01.0
0192     DC 01-B5 DHU_OPOG_COPY
0193     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
```

```
0194     C.
0195   +. TI 2016-08-16 10:12:59.5
0196     DC 01-B2 DHU_OP_START
0197     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0198     C.
0199     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0200     C.                      ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0201     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     4
0202     C.                      ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0203     C.                      ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0204     C.
0205    . C. ****************************
0206     C. TIÎÎ°è¥À¥ó¥×
0207     C. ****************************
0208     C.
0209     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0210   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211     BC       (03 ab 03 01 02)
0212     C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0213     C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0214     C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0215     C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0216     C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0217   +. DC 01-22 DHU_MODE_CHNG
0218     BC       (07 0b f8)
0219     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
0220     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0221     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0222     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0223     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0224     C.
0225    . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0226     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0227     C.
0228    . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0229     C.
0230    . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0231   +. DC 01-22 DHU_MODE_CHNG
0232     BC       (02 0a f8)
0233     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     2
0234     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0235     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0236     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0237     C.
0238     C. ********************************
0239     C.  SOT TI command set
0240     C. ********************************
0241     C. Execute, after the success of OP upload.
0242   +. TI 2016-08-16 10:12:16.0
0243     DC 07-F0 MDP_SOT_MODE_STBY
0244     BC       (41)
0245    . C. ------------------------------------
0246     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0247     C. ------------------------------------
0248     C. ************ SOT END ***********
0249    . C. Stop EIS observation and temporarily disable EIS mode changes
0250     C.
0251     C.
0252     C. ********** Start EIS operation (TI set) **********
0253     C. Execute, after the success of OP upload.
0254     C. Set EIS TI-commands
0255   +. TI 2016-08-16 10:12:30.0
0256     DC 07-FC EIS_MODE_MANU
0257     BC       (21 02)
0258   +. TI 2016-08-16 10:12:40.0
0259     DC 07-FC EIS_MODE_CHG_DIS
0260     BC       (22)
0261    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0262     C. ************ End EIS operation (TI set) ***********
0263     C.
0264     C.
0265     C.
0266     C. ************ XRT START ***********
0267     C. Execute, after the success of OP upload.
0268   +. TI 2016-08-16 10:12:00.0
0269     DC 07-F0 MDP_XRT_MODE_STBY
0270     BC       (c3)
0271    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0272     C.
0273     C. ************ XRT END ***********
0274     C.
0275    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0276     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0277    . S. DC-BC dcbc-402:DCBC
0278     (MDP_known_event)
0279     C.
0280     C.
0281    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0282    . S. DC-BC dcbc-153:DCBC
0283     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284     C.
0285     C.
0286    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0287     C.
0288    . C. ********** LOS **********
0289     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-052 2016-08-16 14:20:36 178 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0099     C. Upload the Orbit Element and the Target Attitude
0100     C. RAM-ID:TARGET_ATT
0101   . S. RAM   ram-150:TARGET_ATT
0102     ()
0103     C.
0104     C.
0105     C. Set the dump memory area of TARGET_ATT
0106   +. DC 02-48 AOCU_DUMP_SET
0107     BC       (07 00 00 00 18 00)
0108     C.
0109     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110     C.
0111     C.
0112     C. Change the TLMFormatNo for the AOCS Dump Format
0113   +. DC 01-22 DHU_MODE_CHNG
0114     BC       (04 0b f8)
0115     C.
0116     C. Wait for AOCSDUMP to end
0117     C.
0118   . C. Check the dump memory
0119     C.
0120     C.                                      Result = OK [ ]
0121     C.
0122   +. DC 01-22 DHU_MODE_CHNG
0123     BC       (02 0a f8)
0124     C.
0125     C.                         <A_***>[TLM STS] FMT = 2 [ ]
0126     C.
0127   +. DC 02-8E AOCU_ORB_UPD
0128    . C.
0129    . C. ***** AOCS Commands (Orbital Element Update) *****
0130     C. Update the orbital element
0131   +. DC 02-50 AOCU_ORB_PRPGT_START
0132     BC       (16)
0133   +  DC 02-8E AOCU_ORB_UPD
0134     C.
0135     C.       <A_ORB>[ORBIT] EPC = 1542786.6 +- 1.0 (s) [ ]
0136     C.
0137    . C.
0138    . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139   +. DC 07-FC EIS_MODE_CHG_ENA
0140     BC       (20)
0141    . C. Verify EIS_MODE_CHG_FLG is ENA
0142   +. DC 07-FC EIS_MODE_MANU
0143     BC       (21 02)
0144    . C. Verify EIS in MANUAL mode
0145    . C. Estimated OBSTBL upload time is 1m35s
0146     C. ********************************
0147     C. EIS START OBSTBL LOAD
0148     C. ********************************
0149    . S. RAM   ram-820:EIS_OBSTBL
0150     ()
0151   +. DC 07-FC EIS_DUMP_OBSTBL
0152     BC       (07 07 07 00 00 70 00)
0153     C.
0154     C. Execute, after the success of OBSTBL upload.
0155     C. Set EIS TI-commands
0156   +. TI 2016-08-16 10:12:50.0
0157     DC 07-FC EIS_MODE_CHG_ENA
0158     BC       (20)
0159    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0160     C. ********************************
0161     C. EIS END OBSTBL LOAD
0162     C. ********************************
0163     C.
0164    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0165     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0166    . S. DC-BC dcbc-402:DCBC
0167     (MDP_known_event)
0168     C.
0169     C.
0170    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0171    . S. DC-BC dcbc-153:DCBC
0172     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0173     C.
0174     C.
0175    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0176     C.
0177    . C. ********** LOS **********
0178     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-053 2016-08-16 14:20:36 140 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¼É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ********************************
0015     C.  SOT table upload
0016     C. ********************************
0017   . C. < Stop SP table >
0018   +. DC 07-F0 MDP_SP_CTRL_MANU
0019     BC       (61)
0020     C. ------------------------------------
0021     C.  MDP_SP_CTRL_MODE      = MANU    [ ]
0022     C. ------------------------------------
0023     C.
0024   . C. <Upload SP Observation Table>
0025   . S. RAM   ram-281:MDP_OBS_S
0026     ()
0027     C.
0028   . C. < Dump RAMID=MDP_OBS_S >
0029   +. DC 07-F0 MDP_DUMP_SPTBL
0030     BC       (83 07 00 00 00 38 b8)
0031     C. ------------------------------------
0032     C.  MDP_OBS_S verify = OK/NG        [ ]
0033     C. ------------------------------------
0034     C.
0035     C. ********************************
0036     C.  SOT TI command set
0037     C. ********************************
0038     C. Execute, after the success of TBL upload.
0039   +. TI 2016-08-16 10:12:18.0
0040     DC 07-F0 MDP_SOT_MODE_OBSV
0041     BC       (40)
0042   . C. ------------------------------------
0043     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0044     C. ------------------------------------
0045     C.
0046     C.
0047     C. ************ XRT START ************
0048     C.
0049   +. DC 07-F0 MDP_XRT_CTRL_MANU
0050     BC       (c1)
0051   +. DC 07-F0 MDP_XRT_CTRL_MANU
0052     BC       (c1)
0053   +  DC 07-F0 MDP_XRT_MODE_STBY
0054     BC       (c3)
0055   . C. ------------ Success Verify ?              OK / NG____
0056     C.
0057     C. XRT Obs. Table Upload
0058   . S. RAM   ram-291:MDP_OBS_X
0059     ()
0060     C.
0061   +. DC 07-F0 MDP_DUMP_XRTTBL
0062     BC       (84 07 00 00 00 3a d4)
0063   . C. ------------ Comparison Check ?                        OK / ERR ____
0064     C.
0065     C.
0066   +. DC 07-F0 MDP_XRT_ROI_SET
0067     BC       (cd 01 b1 b1 04 04)
0068   +  DC 07-F0 MDP_XRT_ROI_SET
0069     BC       (cd 02 b1 b1 08 08)
0070   +  DC 07-F0 MDP_XRT_ROI_SET
0071     BC       (cd 03 b1 b1 08 08)
0072   +  DC 07-F0 MDP_XRT_ROI_SET
0073     BC       (cd 04 b1 b1 06 06)
0074   +  DC 07-F0 MDP_XRT_ROI_SET
0075     BC       (cd 05 85 83 06 06)
0076   +  DC 07-F0 MDP_XRT_ROI_SET
0077     BC       (cd 06 85 83 06 06)
0078   +  DC 07-F0 MDP_XRT_ROI_SET
0079     BC       (cd 07 c0 c0 10 10)
0080   +  DC 07-F0 MDP_XRT_ROI_SET
0081     BC       (cd 08 80 80 20 20)
0082   +  DC 07-F0 MDP_XRT_ROI_SET
0083     BC       (cd 09 40 c0 10 10)
0084   +  DC 07-F0 MDP_XRT_ROI_SET
0085     BC       (cd 0a 40 40 10 10)
0086   +  DC 07-F0 MDP_XRT_ROI_SET
0087     BC       (cd 0b c0 40 10 10)
0088   +  DC 07-F0 MDP_XRT_ROI_SET
0089     BC       (cd 0c 80 80 20 08)
0090   +  DC 07-F0 MDP_XRT_ROI_SET
0091     BC       (cd 0d 80 80 08 20)
0092   +  DC 07-F0 MDP_XRT_ROI_SET
0093     BC       (cd 0e 85 83 08 08)
0094   +  DC 07-F0 MDP_XRT_ROI_SET
0095     BC       (cd 0f 80 80 06 06)
```

```
0096  +  DC 07-F0 MDP_XRT_ROI_SET
0097     BC       (cd 10 80 80 08 08)
0098  +  DC 07-F0 MDP_XRT_FLD_ENA
0099     BC       (d8)
0100  +  DC 07-F0 MDP_XRT_FLRCTRL_ENA
0101     BC       (c8)
0102  +  DC 07-F0 MDP_XRT_ARS_DIS
0103     BC       (d5)
0104  +. DC 07-F0 MDP_XRT_AEC_RESET
0105     BC       (d0)
0106  +. DC 07-F0 MDP_XRT_FLD_RESET
0107     BC       (da)
0108  +. DC 07-F0 MDP_XRT_QT_PROG_SET
0109     BC       (c4 0b)
0110  +. DC 07-F0 MDP_XRT_FL_PROG_SET
0111     BC       (c5 07)
0112   . C. ---------- Success Verify ?            OK / NG ____
0113     C.
0114     C.
0115   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0116     C.
0117  +. DC 07-F0 MDP_XRT_MODE_OBSV
0118     BC       (c2)
0119  +. TI 2016-08-16 10:12:02.0
0120     DC 07-F0 MDP_XRT_MODE_OBSV
0121     BC       (c2)
0122   . C. ---------- Success Verify ?            OK / NG ____
0123     C.
0124     C. ************ XRT END ***********
0125     C.
0126   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0127     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0128   . S. DC-BC dcbc-402:DCBC
0129     (MDP_known_event)
0130     C.
0131     C.
0132   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0133   . S. DC-BC dcbc-153:DCBC
0134     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0135     C.
0136     C.
0137   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0138     C.
0139   . C. *********** LOS ***********
0140     C.
```

```
*** OP Sequence for XRT ***

2016/08/16  10:23:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 00 00 00 ac cd
2016/08/16  10:39:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 d6 67
2016/08/16  10:55:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 00 00 00 00 00
2016/08/16  11:11:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 00 00 00 29 99
2016/08/16  11:56:00.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                AOCU_NM              5 02-76 00 00 00 53 33
2016/08/16  12:12:00.0  AOCS_ORe-point_Start_6_OG [0x09c]
                                AOCU_NM              5 02-76 00 d6 36 b7 8e
2016/08/16  12:22:00.0  AOCS_ORe-point_Start_7_OG [0x09d]
                                AOCU_NM              5 02-76 00 b4 b5 db 75
2016/08/16  12:38:00.0  AOCS_ORe-point_Start_8_OG [0x09e]
                                AOCU_NM              5 02-76 00 ac 5b 00 00
2016/08/16  12:54:00.0  AOCS_ORe-point_Start_9_OG [0x09f]
                                AOCU_NM              5 02-76 00 b4 b5 24 8b
2016/08/16  13:34:00.0  AOCS_ORe-point_Start_10_OG [0x0a0]
                                AOCU_NM              5 02-76 00 d6 36 48 72
2016/08/16  13:44:00.0  AOCS_ORe-point_Start_11_OG [0x0a1]
                                AOCU_NM              5 02-76 00 29 ca b7 8e
2016/08/16  13:54:00.0  AOCS_ORe-point_Start_12_OG [0x0a2]
                                AOCU_NM              5 02-76 00 4b 4b db 75
2016/08/16  14:10:00.0  AOCS_ORe-point_Start_13_OG [0x0a3]
                                AOCU_NM              5 02-76 00 53 a5 00 00
2016/08/16  14:26:00.0  AOCS_ORe-point_Start_14_OG [0x0a4]
                                AOCU_NM              5 02-76 00 4b 4b 24 8b
2016/08/16  14:42:00.0  AOCS_ORe-point_Start_15_OG [0x0a5]
                                AOCU_NM              5 02-76 00 29 db 48 72
2016/08/16  15:00:00.0  AOCS_ORe-point_Start_16_OG [0x0a6]
                                AOCU_NM              5 02-76 03 00 00 00 00
2016/08/17  00:30:00.0  AOCS_ORe-point_Start_17_OG [0x0a7]
                                AOCU_NM              5 02-76 04 00 00 00 00
2016/08/17  06:15:00.0  XRT_TCIB_XRT_S_HTR_A_DIS_422_OG [0x1a6]
                                TCIB_XRT_S_HTR_A_DIS 0 04-C0
2016/08/17  12:34:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/17  12:34:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/17  12:34:58.0  XRT_FOCUS_POSITION_426_OG [0x1aa]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2016/08/17  12:35:00.0  AOCS_ORe-point_Start_18_OG [0x0a8]
                                AOCU_NM              5 02-76 00 2e f9 2e f9
2016/08/17  12:35:18.0  XRT_FLD_DIS_437_OG [0x1b5]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2016/08/17  12:35:20.0  XRT_FLRCTRL_DIS_409_OG [0x199]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2016/08/17  12:37:56.0  XRT_ARS_DIS_435_OG [0x1b3]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2016/08/17  12:37:58.0  XRT_QT_PROG_SET_420_OG [0x1a4]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 09
2016/08/17  12:38:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/17  12:44:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/17  12:44:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/17  12:44:58.0  XRT_FOCUS_POSITION_426_OG [0x1aa]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2016/08/17  12:45:00.0  AOCS_ORe-point_Start_19_OG [0x0a9]
                                AOCU_NM              5 02-76 00 2e f9 d1 07
2016/08/17  12:45:18.0  XRT_FLD_DIS_437_OG [0x1b5]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2016/08/17  12:45:20.0  XRT_FLRCTRL_DIS_409_OG [0x199]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2016/08/17  12:47:56.0  XRT_ARS_DIS_435_OG [0x1b3]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2016/08/17  12:47:58.0  XRT_QT_PROG_SET_413_OG [0x19d]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 05
2016/08/17  12:48:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/17  12:54:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/17  12:54:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/17  12:54:58.0  XRT_FOCUS_POSITION_426_OG [0x1aa]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2016/08/17  12:55:00.0  AOCS_ORe-point_Start_20_OG [0x0aa]
                                AOCU_NM              5 02-76 00 d1 07 d1 07
2016/08/17  12:55:18.0  XRT_FLD_DIS_437_OG [0x1b5]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2016/08/17  12:55:20.0  XRT_FLRCTRL_DIS_409_OG [0x199]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2016/08/17  12:57:56.0  XRT_ARS_DIS_435_OG [0x1b3]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2016/08/17  12:57:58.0  XRT_QT_PROG_SET_446_OG [0x1be]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 14
2016/08/17  12:58:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/17  13:04:54.0  XRT_CTRL_MANU_402_OG [0x192]
```

```
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/17  13:04:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/17  13:04:58.0  XRT_FOCUS_POSITION_426_OG [0x1aa]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2016/08/17  13:05:00.0  AOCS_ORe-point_Start_21_OG [0x0ab]
                                    AOCU_NM              5 02-76 00 d1 07 2e f9
2016/08/17  13:05:18.0  XRT_FLD_DIS_437_OG [0x1b5]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2016/08/17  13:05:20.0  XRT_FLRCTRL_DIS_409_OG [0x199]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2016/08/17  13:07:56.0  XRT_ARS_DIS_435_OG [0x1b3]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2016/08/17  13:07:58.0  XRT_QT_PROG_SET_401_OG [0x191]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0e
2016/08/17  13:08:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/17  13:14:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/17  13:14:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/17  13:14:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2016/08/17  13:15:00.0  AOCS_ORe-point_Start_22_OG [0x0ac]
                                    AOCU_NM              5 02-76 00 b3 03 02 4d
2016/08/17  13:15:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                    MDP_XRT_FLD_ENA      1 07-F0 d8
2016/08/17  13:15:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                    MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2016/08/17  13:15:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                    MDP_XRT_AEC_RESET    1 07-F0 d0
2016/08/17  13:15:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2016/08/17  13:15:26.0  XRT_FLD_RESET_433_OG [0x1b1]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/17  13:17:56.0  XRT_QT_PROG_SET_432_OG [0x1b0]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 06
2016/08/17  13:17:58.0  XRT_FL_PROG_SET_436_OG [0x1b4]
                                    MDP_XRT_FL_PROG_SET  2 07-F0 c5 07
2016/08/17  13:18:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/17  13:50:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/17  13:50:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/17  13:50:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/17  13:50:06.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/17  13:53:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/17  14:09:30.0  XRT_Custom_430_OG [0x1ae]
2016/08/17  14:10:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/17  14:15:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/17  14:15:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/17  14:15:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/17  14:15:36.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/17  14:18:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/17  14:20:00.0  XRT_Custom_430_OG [0x1ae]
2016/08/17  14:21:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/17  15:28:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/17  15:28:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/17  15:28:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/17  15:28:36.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/17  15:31:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/17  16:11:00.0  XRT_Custom_430_OG [0x1ae]
2016/08/17  16:12:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/17  17:06:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/17  17:06:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/17  17:06:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/17  17:06:36.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/17  17:09:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/17  17:47:30.0  XRT_Custom_430_OG [0x1ae]
2016/08/17  17:48:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
```

```
2016/08/17  18:29:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/17  18:29:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/17  18:29:58.0  XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]
                                XRT_FOCUS_RECAL      2 07-F8 78 00
2016/08/17  18:30:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 00 00 00 00 00
2016/08/17  18:33:58.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2016/08/17  18:34:18.0  XRT_FLD_DIS_406_OG [0x196]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2016/08/17  18:36:54.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2016/08/17  18:36:56.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2016/08/17  18:36:58.0  XRT_QT_PROG_SET_425_OG [0x1a9]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 01
2016/08/17  18:37:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/17  18:39:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/17  18:39:26.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/17  18:39:28.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2016/08/17  18:39:48.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2016/08/17  18:39:50.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2016/08/17  18:39:52.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2016/08/17  18:39:54.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2016/08/17  18:39:56.0  XRT_FLD_RESET_433_OG [0x1b1]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/17  18:40:30.0  AOCS_ORe-point_Start_16_OG [0x0a6]
                                AOCU_NM              5 02-76 03 00 00 00 00
2016/08/17  18:42:26.0  XRT_QT_PROG_SET_449_OG [0x1c1]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2016/08/17  18:42:28.0  XRT_FL_PROG_SET_436_OG [0x1b4]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 07
2016/08/17  18:45:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/17  18:45:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/17  18:45:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/17  18:45:06.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/17  18:48:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/17  19:24:01.0  XRT_Custom_430_OG [0x1ae]
2016/08/17  19:25:01.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/17  20:23:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/17  20:23:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/17  20:23:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/17  20:23:36.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/17  20:26:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/17  20:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/17  20:59:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/17  20:59:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2016/08/17  21:00:00.0  AOCS_ORe-point_Start_23_OG [0x0ad]
                                AOCU_NM              5 02-76 00 f2 a8 b1 cb
2016/08/17  21:00:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2016/08/17  21:00:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2016/08/17  21:00:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2016/08/17  21:00:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2016/08/17  21:00:26.0  XRT_FLD_RESET_433_OG [0x1b1]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/17  21:02:56.0  XRT_QT_PROG_SET_416_OG [0x1a0]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0a
2016/08/17  21:02:58.0  XRT_FL_PROG_SET_436_OG [0x1b4]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 07
2016/08/17  21:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/17  22:02:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/17  22:02:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
```

```
2016/08/17  22:02:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/17  22:02:06.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/17  22:05:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/17  22:37:00.0  XRT_Custom_430_OG [0x1ae]
2016/08/17  22:38:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/17  23:40:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/17  23:40:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/17  23:40:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/17  23:40:36.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/17  23:43:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/18  00:03:00.0  XRT_Custom_430_OG [0x1ae]
2016/08/18  00:04:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/18  00:29:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  00:29:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  00:29:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2016/08/18  00:30:00.0  AOCS_ORe-point_Start_17_OG [0x0a7]
                                AOCU_NM              5 02-76 04 00 00 00 00
2016/08/18  00:30:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2016/08/18  00:30:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2016/08/18  00:30:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2016/08/18  00:30:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2016/08/18  00:30:26.0  XRT_FLD_RESET_433_OG [0x1b1]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/18  00:32:56.0  XRT_QT_PROG_SET_407_OG [0x197]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0f
2016/08/18  00:32:58.0  XRT_FL_PROG_SET_436_OG [0x1b4]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 07
2016/08/18  00:33:00.5  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/18  01:18:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  01:18:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  01:18:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/18  01:18:36.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/18  01:21:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/18  01:38:00.0  XRT_Custom_430_OG [0x1ae]
2016/08/18  01:39:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/18  02:51:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  02:51:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  02:51:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/18  02:51:06.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/18  02:54:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/18  03:16:00.0  XRT_Custom_430_OG [0x1ae]
2016/08/18  03:17:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/18  03:29:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  03:29:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  03:29:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2016/08/18  03:30:00.0  AOCS_ORe-point_Start_24_OG [0x0ae]
                                AOCU_NM              5 02-76 01 00 00 00 00
2016/08/18  03:30:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2016/08/18  03:30:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2016/08/18  03:30:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2016/08/18  03:30:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2016/08/18  03:30:26.0  XRT_FLD_RESET_433_OG [0x1b1]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/18  03:32:56.0  XRT_QT_PROG_SET_439_OG [0x1b7]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2016/08/18  03:32:58.0  XRT_FL_PROG_SET_436_OG [0x1b4]
```

```
                                    MDP_XRT_FL_PROG_SET  2 07-F0 c5 07
2016/08/18  03:33:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/18  03:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  03:59:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  03:59:58.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2016/08/18  04:00:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                    AOCU_NM              5 02-76 00 00 00 00 00
2016/08/18  04:00:18.0  XRT_FLD_DIS_406_OG [0x196]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2016/08/18  04:02:54.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2016/08/18  04:02:56.0  XRT_ARS_DIS_423_OG [0x1a7]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2016/08/18  04:02:58.0  XRT_QT_PROG_SET_425_OG [0x1a9]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 01
2016/08/18  04:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/18  04:09:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  04:09:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  04:09:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2016/08/18  04:10:00.0  AOCS_ORe-point_Start_24_OG [0x0ae]
                                    AOCU_NM              5 02-76 01 00 00 00 00
2016/08/18  04:10:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                    MDP_XRT_FLD_ENA      1 07-F0 d8
2016/08/18  04:10:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                    MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2016/08/18  04:10:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                    MDP_XRT_AEC_RESET    1 07-F0 d0
2016/08/18  04:10:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2016/08/18  04:10:26.0  XRT_FLD_RESET_433_OG [0x1b1]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/18  04:12:56.0  XRT_QT_PROG_SET_439_OG [0x1b7]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2016/08/18  04:12:58.0  XRT_FL_PROG_SET_436_OG [0x1b4]
                                    MDP_XRT_FL_PROG_SET  2 07-F0 c5 07
2016/08/18  04:13:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/18  04:20:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  04:20:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  04:20:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/18  04:20:36.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/18  04:23:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/18  04:54:30.0  XRT_Custom_430_OG [0x1ae]
2016/08/18  04:55:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/18  05:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  05:59:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  05:59:58.0  XRT_FOCUS_POSITION_438_OG [0x1b6]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2016/08/18  06:00:00.0  AOCS_ORe-point_Start_25_OG [0x0af]
                                    AOCU_NM              5 02-76 00 ad 59 00 00
2016/08/18  06:00:18.0  XRT_ROI_A_427_OG [0x1ab]
                                    MDP_XRT_ROI_SET      6 07-F0 cd 06 85 83 06 06
                                    MDP_XRT_ROI_SET      6 07-F0 cd 07 80 60 20 18
                                    MDP_XRT_ROI_SET      6 07-F0 cd 0f 80 80 06 06
                                    MDP_XRT_ROI_SET      6 07-F0 cd 10 80 80 08 08
2016/08/18  06:00:23.0  XRT_FLD_DIS_440_OG [0x1b8]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2016/08/18  06:14:59.0  XRT_FLRCTRL_DIS_441_OG [0x1b9]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2016/08/18  06:15:01.0  XRT_ARS_DIS_435_OG [0x1b3]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2016/08/18  06:15:03.0  XRT_QT_PROG_SET_444_OG [0x1bc]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 13
2016/08/18  06:15:05.0  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/18  07:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  07:59:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  07:59:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2016/08/18  08:00:00.0  AOCS_ORe-point_Start_26_OG [0x0b0]
                                    AOCU_NM              5 02-76 02 00 00 00 00
2016/08/18  08:00:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                    MDP_XRT_FLD_ENA      1 07-F0 d8
2016/08/18  08:00:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                    MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
```

```
2016/08/18  08:00:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2016/08/18  08:00:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2016/08/18  08:00:26.0  XRT_FLD_RESET_433_OG [0x1b1]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/18  08:02:56.0  XRT_QT_PROG_SET_449_OG [0x1c1]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2016/08/18  08:02:58.0  XRT_FL_PROG_SET_436_OG [0x1b4]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 07
2016/08/18  08:11:00.0  XRT_Custom_430_OG [0x1ae]
2016/08/18  08:12:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/18  09:15:00.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  09:20:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  09:20:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  09:20:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/18  09:20:36.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/18  09:23:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/18  09:56:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 00 00 00 00 00
```