XRT Timeline to be uploaded on 2016/08/18

Period: 2016/08/18 09:46:00 - 2016/08/23 10:32:00

* * * * * **Normal mode** * * * * *

XOB #1AAC: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 60s cadence, G-band - 384x384 3ms

Term	Pointing (x, y)	Comment
08/18 09:59:00 - 08/18 15:59:54	Fixed (-26.0, -954.0)	# OP start + 10min/HOP-81 S-pole

PROG= 06 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 9 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 7 1-time(s) 30.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec

Subr= 3 30-time(s) 2.0sec

Seqn= 57 1-time(s) 60.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec

XOB #1AED: G-Band Alignment with East limb Q90 2x2 (G-band and VLS=CLS) - 1msec - (Al/poly) 1443msec - 8 min cadence-wNGT

Term	Pointing (x, y)	Comment
08/18 16:15:00 - 08/18 18:22:24	Fixed (-970.0, 0.0)	Co-alignment at E-limb

PROG= 12 1-time(s)

Subr= 1 15-time(s) 480.0sec

Seqn= 19 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	2x2	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec

Seqn= 43 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec

Seqn= 70 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	1536x2048 (1280, 1024)	Q=95	0	0	2.0sec

XOB #1B35: Synoptic 7 Filter w/ Al-mesh(24/256/2897), Al-poly(45/512/4096), Thin-Be(181/2048/11571) - Thick-Be(65536), Al-poly+Ti-poly(362/4096), Med-Al

Term	Pointing (x, y)	Comment
08/18 18:25:30 - 08/18 18:42:24	Fixed (0.0, 0.0)	synoptic, shifted 22.5 min, extended 20 min

PROG= 04 1-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 5 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 1 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 99 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 67 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 54 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 46 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 94 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 68 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
med-Al/Open	med-Al/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

XOB #1AFF: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
08/18 18:45:30 - 08/18 20:59:30	Track (787.9, -39.4) @ 08/18 18:42:30	#HOP-317

PROG= 11 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 56 1-time(s) 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Subr= 2 5-time(s) 2.0sec

Seqn= 75 1-time(s) 2.0sec

Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Seqn= 96 4-time(s) 60.0sec

Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	15.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	15.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #1B40: AR - Standard Core - (Three Filter with Al/poly, thin-Be and med-Be) with PFB, 384x384 at 1064 1048, with G-band (3ms/3ms VLS=CLS), 90 cad

Term	Pointing (x, y)	Comment
08/18 21:37:30 - 08/18 23:59:54	Fixed (880.0, 150.0)	HOP-316 EIS off limb

PROG= 10 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 56 1-time(s) 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Subr= 2 5-time(s) 2.0sec

Seqn= 22 1-time(s) 2.0sec

Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Seqn= 27 4-time(s) 90.0sec

Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	25.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	25.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #1B38: CME - HOP201 - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 512x512 at 1064 1048, thin-Be, and Al/poly context, with G-ba

Term	Pointing (x, y)	Comment
08/19 00:03:00 - 08/19 01:51:00	Track (-401.3, 8.3) @ 08/19 00:00:00	HOP-322 AR obs.
08/19 23:42:00 - 08/20 02:25:00	Track (-201.1, -0.4) @ 08/19 23:10:00	#HOP-322 AR obs.

PROG= 15 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 69 1-time(s) 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec

Subr= 2 5-time(s) 2.0sec

Seqn= 80 1-time(s) 2.0sec

Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec

Seqn= 96 4-time(s) 60.0sec

Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	15.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	15.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #1B44: AR-(filter ratio Al/poly thin-Be), 512x512 at 1064 1048, with G-band 3ms, PFB, 60s cad

Term	Pointing (x, y)	Comment
08/19 02:33:00 - 08/19 03:26:30	Track (903.9, 115.7) @ 08/19 02:30:00	HOP-173 AR12574/12577
08/19 04:13:00 - 08/19 06:02:54	Track (738.2, -329.8) @ 08/19 04:10:00	HOP-173 AR12576
08/19 06:06:00 - 08/19 11:29:54	Track (891.1, 21.1) @ 08/19 06:03:00	#track AR12574/12577
08/20 02:49:00 - 08/20 03:52:30	Track (936.1, 141.1) @ 08/20 02:30:00	HOP-173 AR12574/12577
08/20 04:43:00 - 08/20 08:52:30	Track (841.6, -307.6) @ 08/20 04:40:00	#HOP-173 AR12576

PROG= 13 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 78 1-time(s) 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 66 60-time(s) 60.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #1B43: Synoptic Q95 2x2 - Al/mesh(24/256/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(33/512/2048) + Ti

Term	Pointing (x, y)	Comment
08/19 04:03:00 - 08/19 04:09:54	Fixed (0.0, 0.0)	synoptic, shifted manually for HOP-173
08/19 23:03:00 - 08/19 23:13:30	Fixed (0.0, 0.0)	synoptic shifted manually
08/20 04:33:00 - 08/20 04:39:54	Fixed (0.0, 0.0)	synoptic, shifted manually for HOP-173

PROG= 01 1-time(s)													
Subr= 1 1-time(s) 2.0sec													
Seqn= 5 1-time(s) 2.0sec													
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 1 1-time(s) 2.0sec													
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 12 1-time(s) 2.0sec													
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 85 1-time(s) 2.0sec													
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54 1-time(s) 2.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #1AD5: CME watch - 4x4 - AEC 2 - Be-thin - G-band (2x2,1ms) - Leak (2x2,1ms) - 60s cad

Term	Pointing (x, y)	Comment
08/19 11:33:00 - 08/19 11:45:00	Track (-103.9, 3.3) @ 08/19 11:30:00	SOT SP focus test

PROG= 17 Inf.-time(s)													
Subr= 1 60-time(s) 60.0sec													
Seqn= 29 1-time(s) 4.0sec													
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 26 1-time(s) 2.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #1AFD: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, Al/Poly context, with G-band (3ms/3ms

Term	Pointing (x, y)	Comment
08/19 12:04:00 - 08/19 22:59:54	Track (-299.6, 3.1) @ 08/19 12:00:00	AR12578 and EIS sensitivity monitoring(internal offset), HOP-320 from 16 UT

PROG= 03 Inf.-time(s)													
Subr= 1 1-time(s) 2.0sec													
Seqn= 56 1-time(s) 2.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 42 3-time(s) 2.0sec													
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 32 30-time(s) 120.0sec													
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	34.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	34.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

✲ ✲ ✲ ✲ ✲ Flare mode ✲ ✲ ✲ ✲ ✲

XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
08/18 09:59:00 - 08/18 15:59:54	Fixed (-26.0, -954.0)	# OP start + 10min/HOP-81 S-pole
08/18 18:45:30 - 08/18 20:59:30	Track (787.9, -39.4) @ 08/18 18:42:30	#HOP-317
08/18 21:37:30 - 08/18 23:59:54	Fixed (880.0, 150.0)	HOP-316 EIS off limb
08/19 00:03:00 - 08/19 01:51:00	Track (-401.3, 8.3) @ 08/19 00:00:00	HOP-322 AR obs.
08/19 02:33:00 - 08/19 03:26:30	Track (903.9, 115.7) @ 08/19 02:30:00	HOP-173 AR12574/12577
08/19 04:13:00 - 08/19 06:02:54	Track (738.2, -329.8) @ 08/19 04:10:00	HOP-173 AR12576
08/19 06:06:00 - 08/19 11:29:54	Track (891.1, 21.1) @ 08/19 06:03:00	#track AR12574/12577
08/19 11:33:00 - 08/19 11:45:00	Track (-103.9, 3.3) @ 08/19 11:30:00	SOT SP focus test
08/19 12:04:00 - 08/19 22:59:54	Track (-299.6, 3.1) @ 08/19 12:00:00	AR12578 and EIS sensitivity monitoring(internal offset), HOP-320 from 16 UT
08/19 23:42:00 - 08/20 02:25:00	Track (-201.1, -0.4) @ 08/19 23:10:00	#HOP-322 AR obs.
08/20 02:49:00 - 08/20 03:52:30	Track (936.1, 141.1) @ 08/20 02:30:00	HOP-173 AR12574/12577
08/20 04:43:00 - 08/20 08:52:30	Track (841.6, -307.6) @ 08/20 04:40:00	#HOP-173 AR12576

PROG= 07 30-time(s)

Subr= 1 20-time(s) 2.0sec

Seqn= 11 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec

Seqn=100 1-time(s) 10.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 10 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Seqn= 11 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec

Seqn= 84 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec

✲ ✲ ✲ ✲ ✲ Active Region Search ✲ ✲ ✲ ✲ ✲

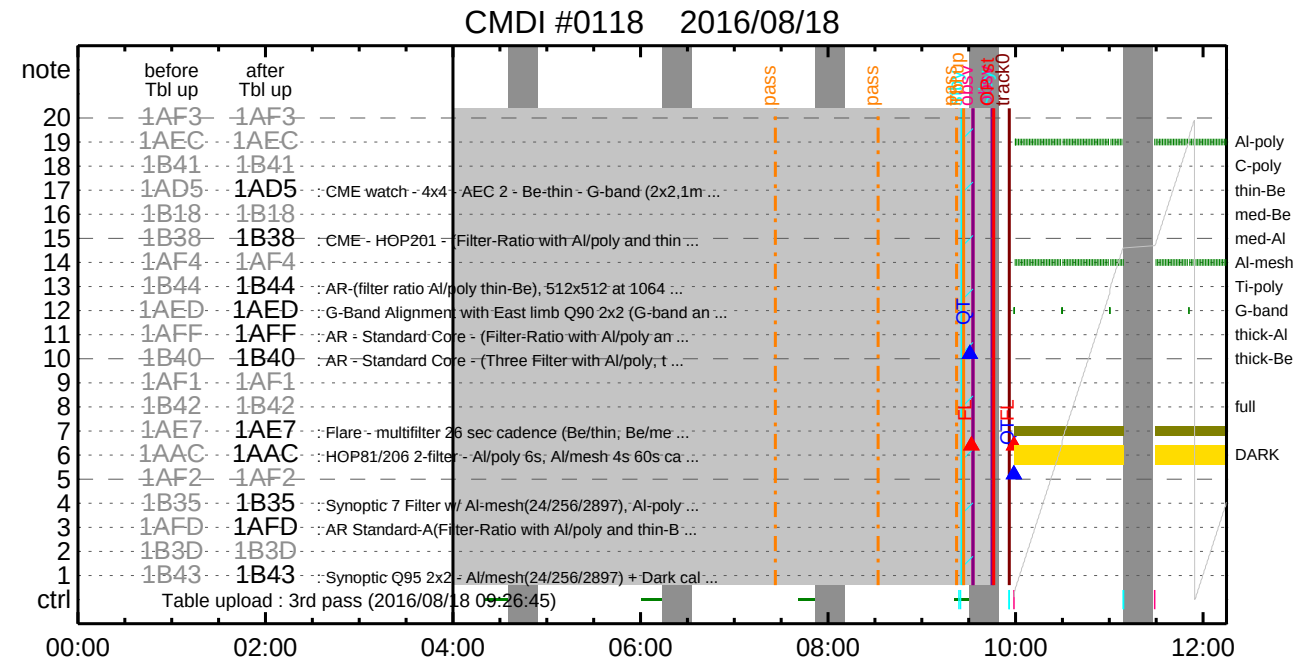
NOT USED

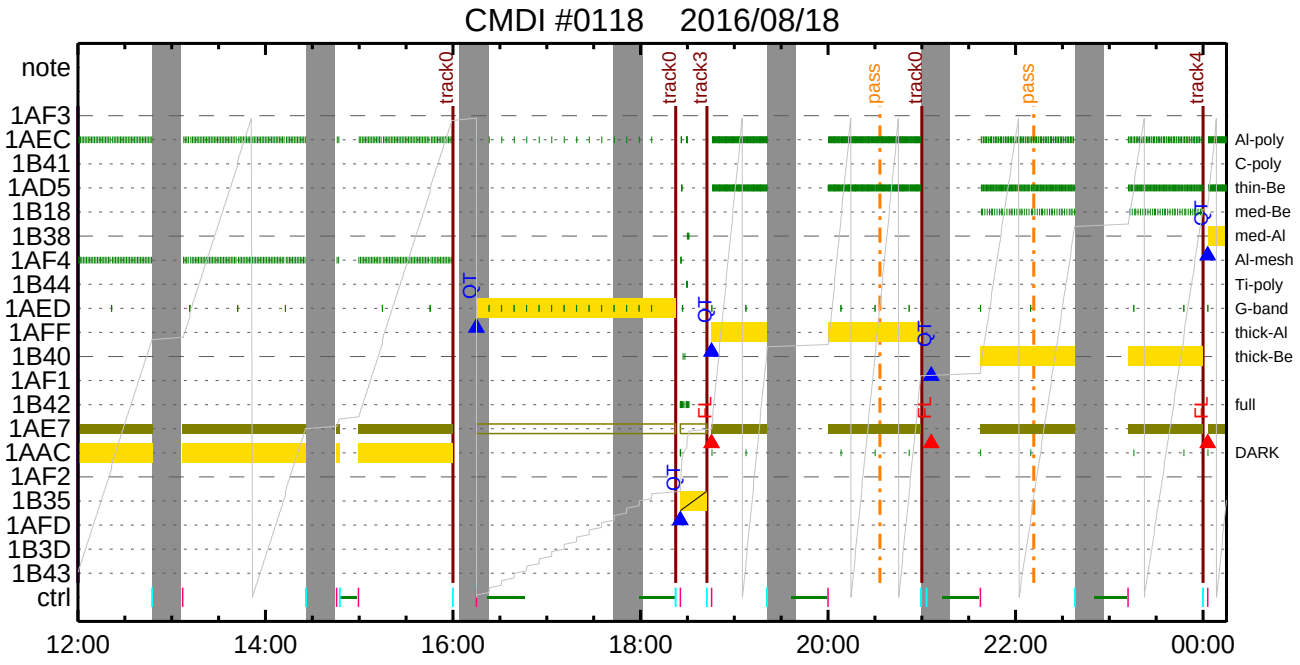
✲ ✲ ✲ ✲ ✲ Flare Detection ✲ ✲ ✲ ✲ ✲

FLD Patrol

Term	Pointing (x, y)	Comment
08/18 18:42:48 - 08/19 04:00:18	Track (787.9, -39.4) @ 08/18 18:42:30	#HOP-317
08/19 04:10:18 - 08/19 23:00:18	Track (738.2, -329.8) @ 08/19 04:10:00	HOP-173 AR12576
08/19 23:39:18 - 08/20 04:30:18	Track (-201.1, -0.4) @ 08/19 23:10:00	#HOP-322 AR obs.
08/20 04:40:18 - 08/23 10:32:00	Track (841.6, -307.6) @ 08/20 04:40:00	#HOP-173 AR12576

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec


CMDI #0118 2016/08/18
note
before Tbl up
after Tbl up
20 1AF3 1AF3
19 1AEC 1AEC
18 1B41 1B41
17 1AD5 1AD5 : CME watch - 4x4 - AEC 2 - Be-thin - G-band (2x2,1m ...
16 1B18 1B18
15 1B38 1B38 : CME - HOP201 - (Filter-Ratio with Al/poly and thin ...
14 1AF4 1AF4
13 1B44 1B44 : AR-(filter ratio Al/poly thin-Be), 512x512 at 1064 ...
12 1AED 1AED : G-Band Alignment with East limb Q90 2x2 (G-band an ...
11 1AFF 1AFF : AR - Standard Core - (Filter-Ratio with Al/poly an ...
10 1B40 1B40 : AR - Standard Core - (Three Filter with Al/poly, t ...
9 1AF1 1AF1
8 1B42 1B42
7 1AE7 1AE7 : Flare - multifilter 26 sec cadence (Be/thin, Be/me ...
6 1AAC 1AAC : HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 60s ca ...
5 1AF2 1AF2
4 1B35 1B35 : Synoptic 7 Filter w/ Al-mesh(24/256/2897), Al-poly ...
3 1AFD 1AFD : AR Standard-A(Filter-Ratio with Al/poly and thin-B ...
2 1B3D 1B3D
1 1B43 1B43 : Synoptic Q95 2x2 - Al/mesh(24/256/2897) + Dark cal ...
ctrl Table upload : 3rd pass (2016/08/18 09:26:45)
pass
pass
pass
track0
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0118 2016/08/18
note
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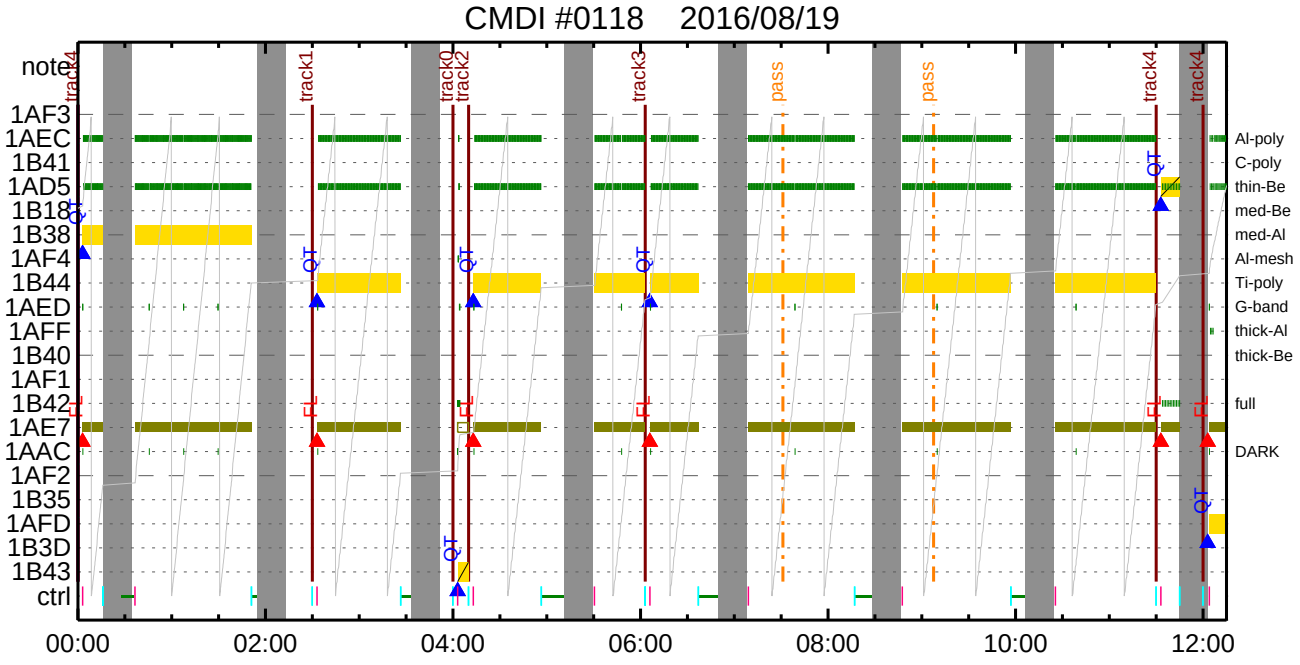
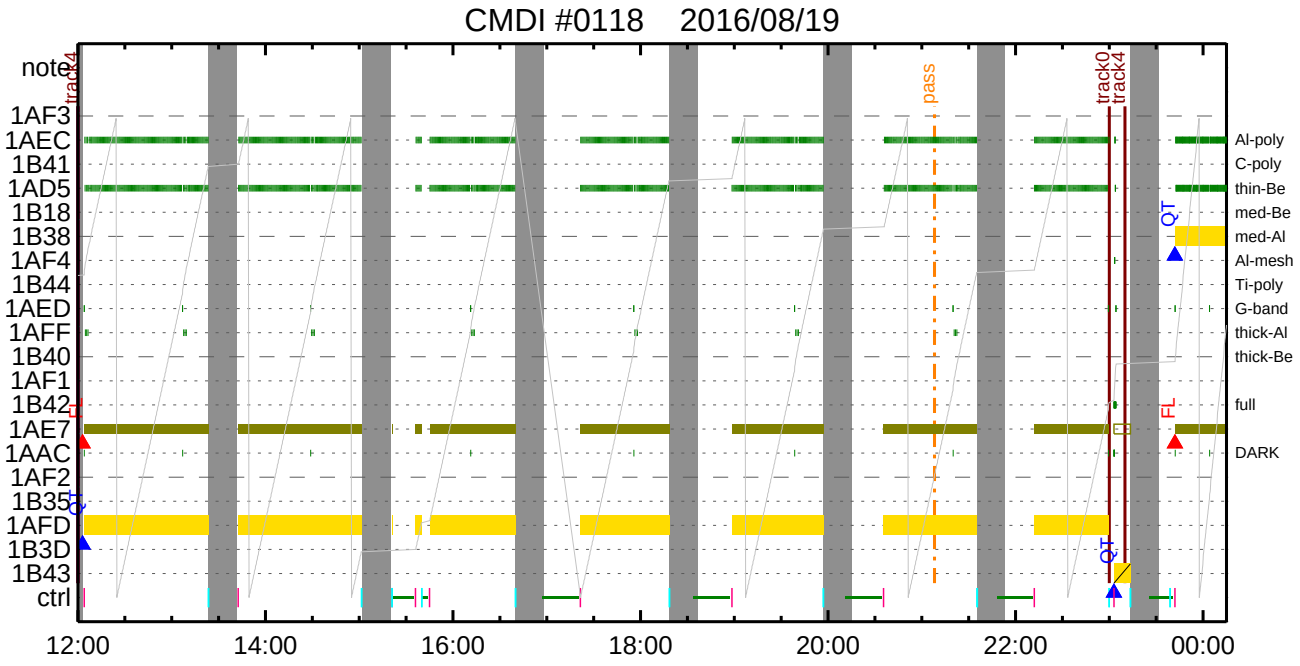
1B41
1AD5
1B18
1B38
1AF4
1B44
1AED
1AFF
1B40
1AF1
1B42
1AE7
1AAC
1AF2
1B35
1AFD
1B3D
1B43
ctrl
track0
track0
track3
pass
track0
pass
track4
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0118 2016/08/19
note
track4
track1
track0
track2
track3
pass
pass
track4
track4
1AF3
1AEC
1B41
1AD5
1B18
1B38
1AF4
1B44
1AED
1AFF
1B40
1AF1
1B42
1AE7
1AAC
1AF2
1B35
1AFD
1B3D
1B43
ctrl
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0118 2016/08/19
note
1AF3
1AEC
1B41
1AD5
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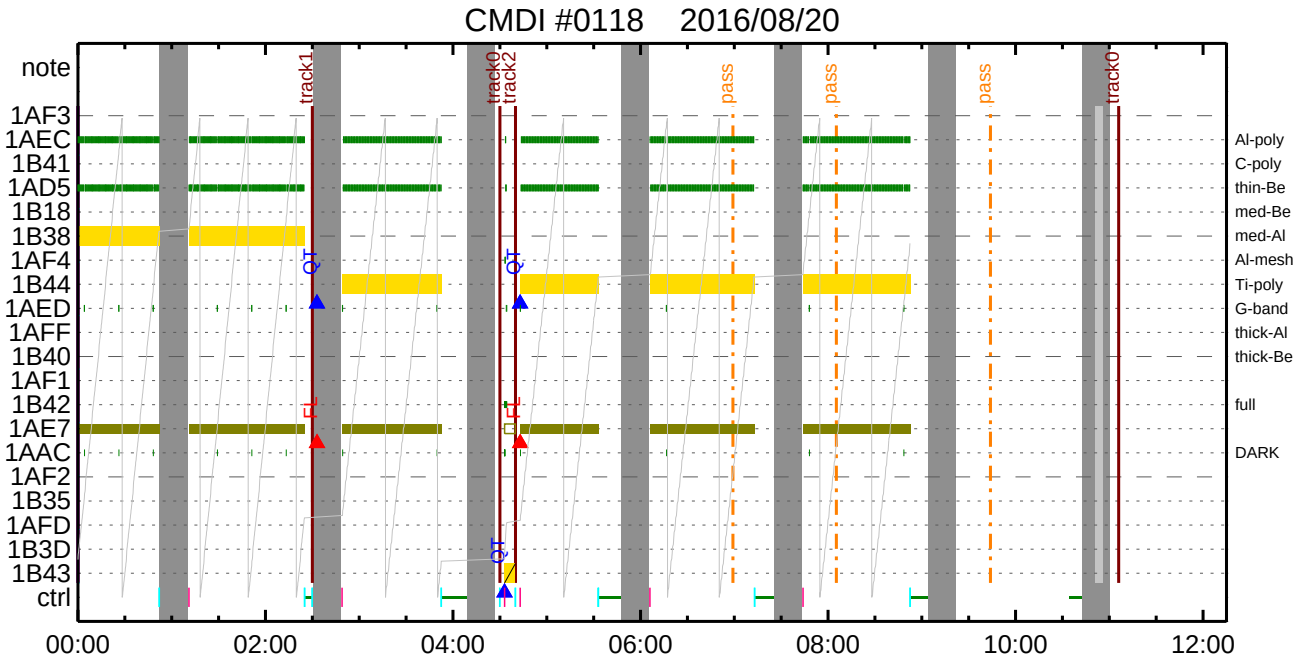
1AF4
1B44
1AED
1AFF
1B40
1AF1
1B42
1AE7
1AAC
1AF2
1B35
1AFD
1B3D
1B43
ctrl
track4
pass
track0
track4
QT
FL
FL
QT
QT
12:00
14:00
16:00
18:00
20:00
22:00
00:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK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note
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1B41
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1AF4
1B44
1AED
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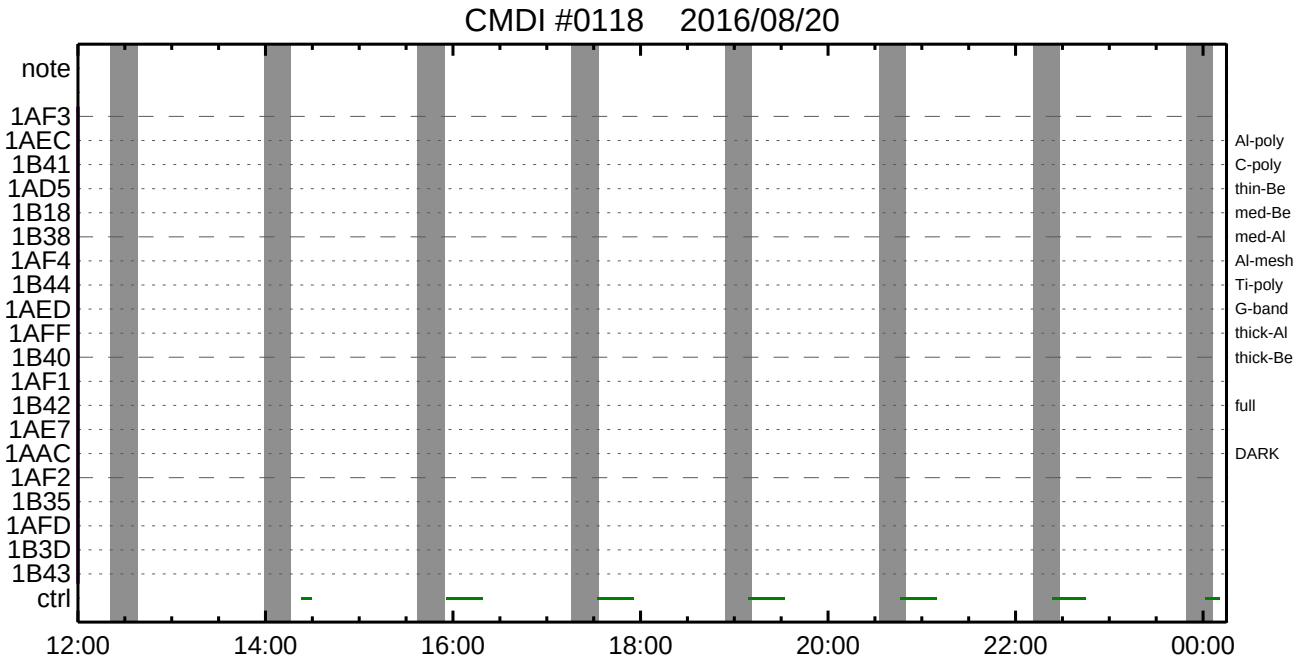
1AF1
1B42
1AE7
1AAC
1AF2
1B35
1AFD
1B3D
1B43
ctrl
track1
track0
track2
pass
pass
pass
track0
QT
QT
FL
FL
QT
00:00
02:00
04:00
06:00
08:00
10:00
12:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK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note
1AF3
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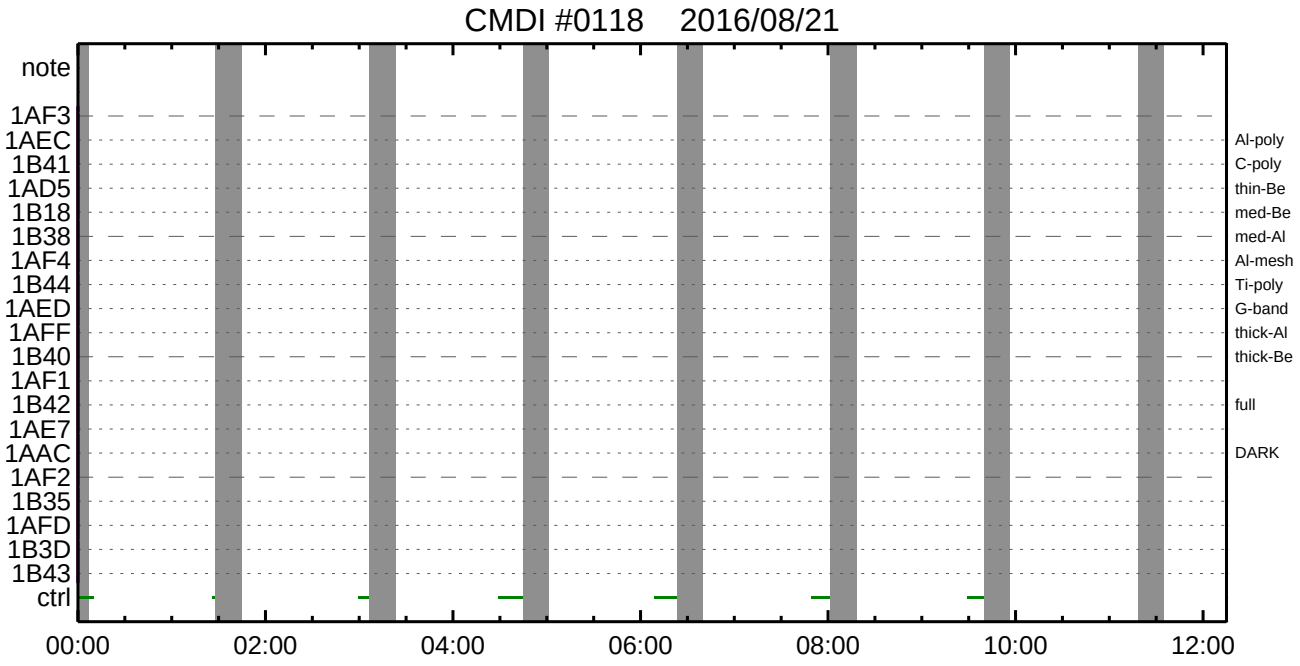
1B41
1AD5
1B18
1B38
1AF4
1B44
1AED
1AFF
1B40
1AF1
1B42
1AE7
1AAC
1AF2
1B35
1AFD
1B3D
1B43
ctrl
12:00
14:00
16:00
18:00
20:00
22:00
00:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK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note
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1AD5
1B18
1B38
1AF4
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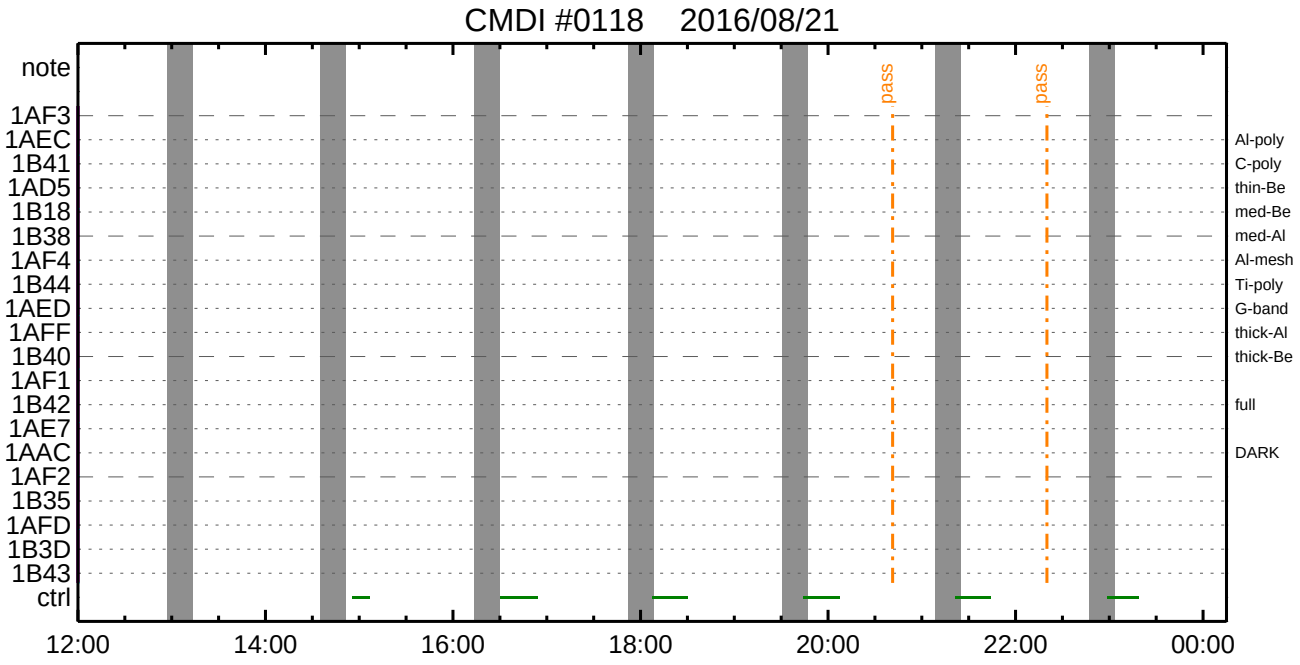
1AED
1AFF
1B40
1AF1
1B42
1AE7
1AAC
1AF2
1B35
1AFD
1B3D
1B43
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00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note
pass
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1B41
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1B38
1AF4
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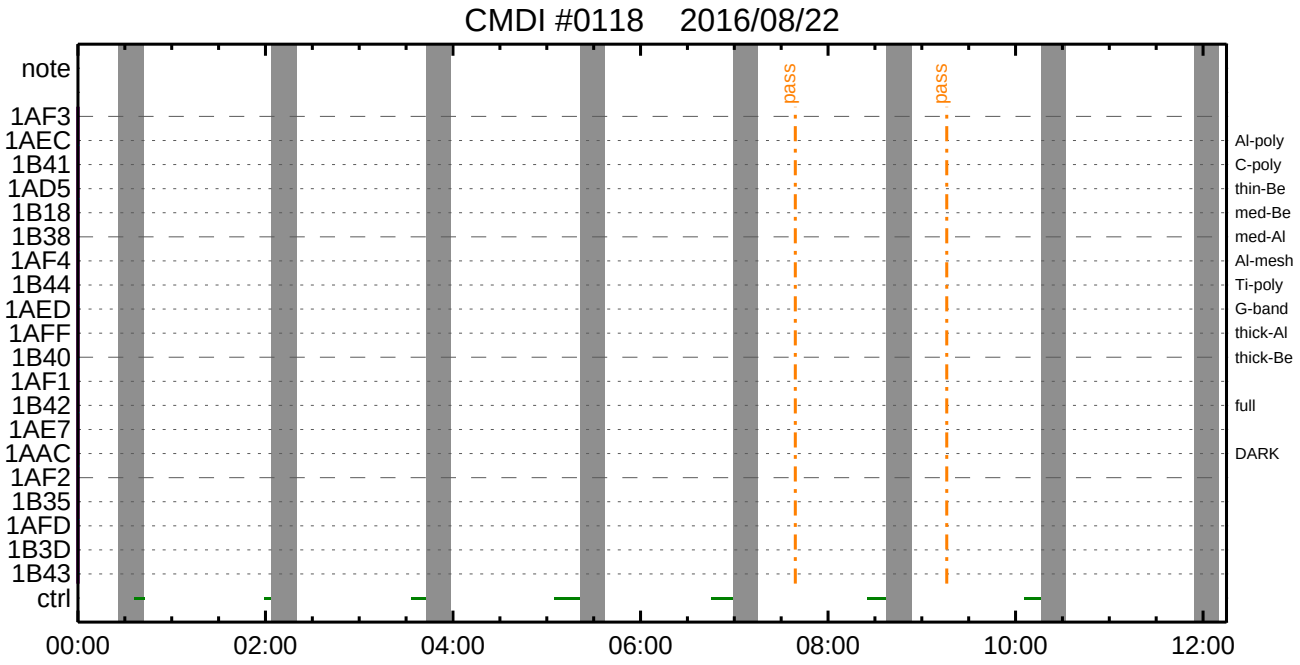
1AED
1AFF
1B40
1AF1
1B42
1AE7
1AAC
1AF2
1B35
1AFD
1B3D
1B43
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00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note
pass
pass
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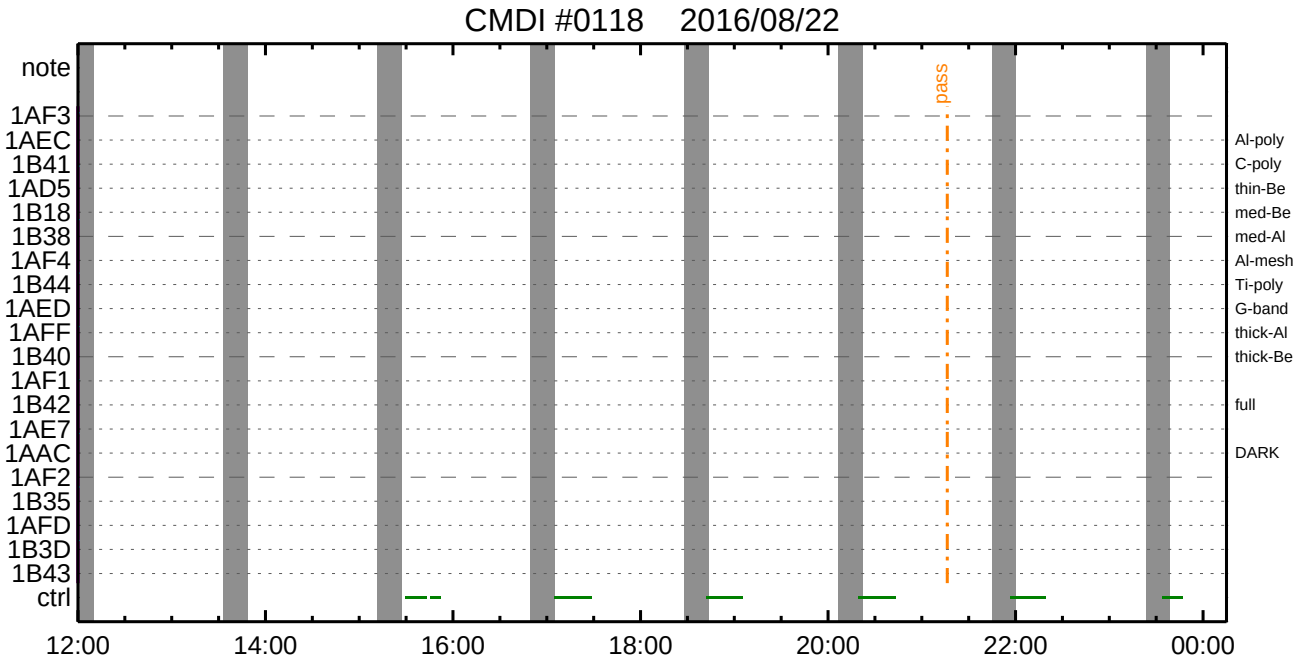
1B38
1AF4
1B44
1AED
1AFF
1B40
1AF1
1B42
1AE7
1AAC
1AF2
1B35
1AFD
1B3D
1B43
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00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note
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1AD5
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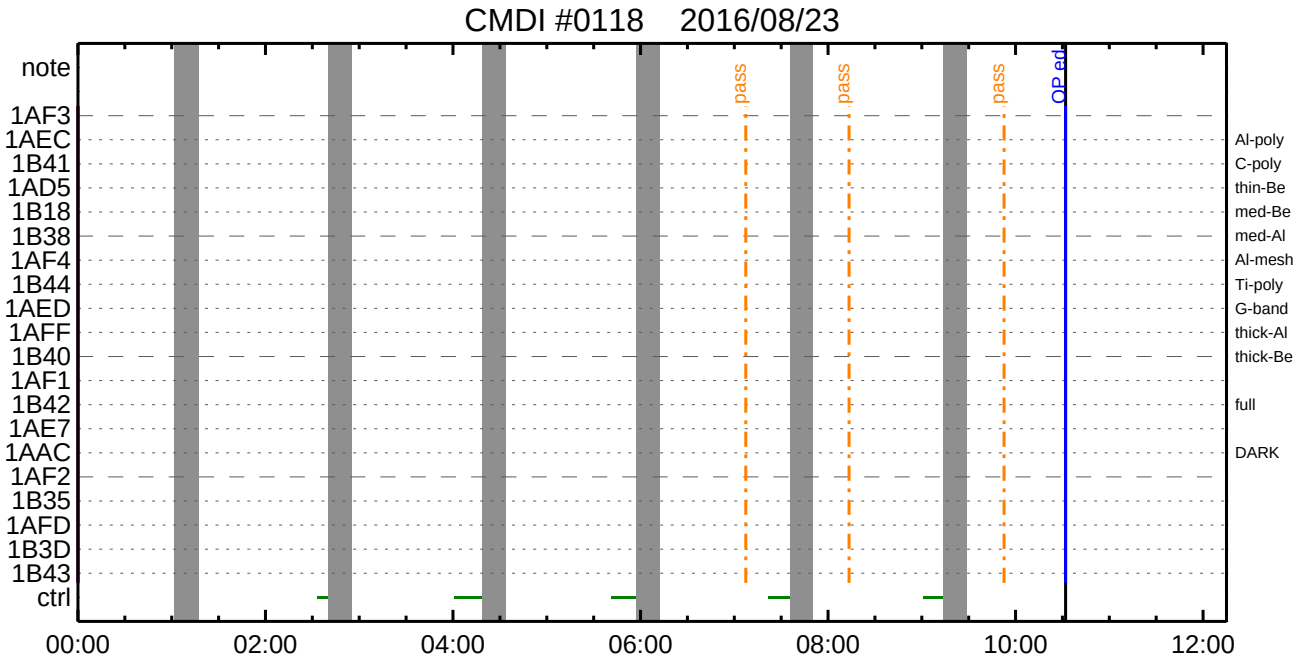
1AED
1AFF
1B40
1AF1
1B42
1AE7
1AAC
1AF2
1B35
1AFD
1B3D
1B43
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00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note
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1B41
1AD5
1B18
1B38
1AF4
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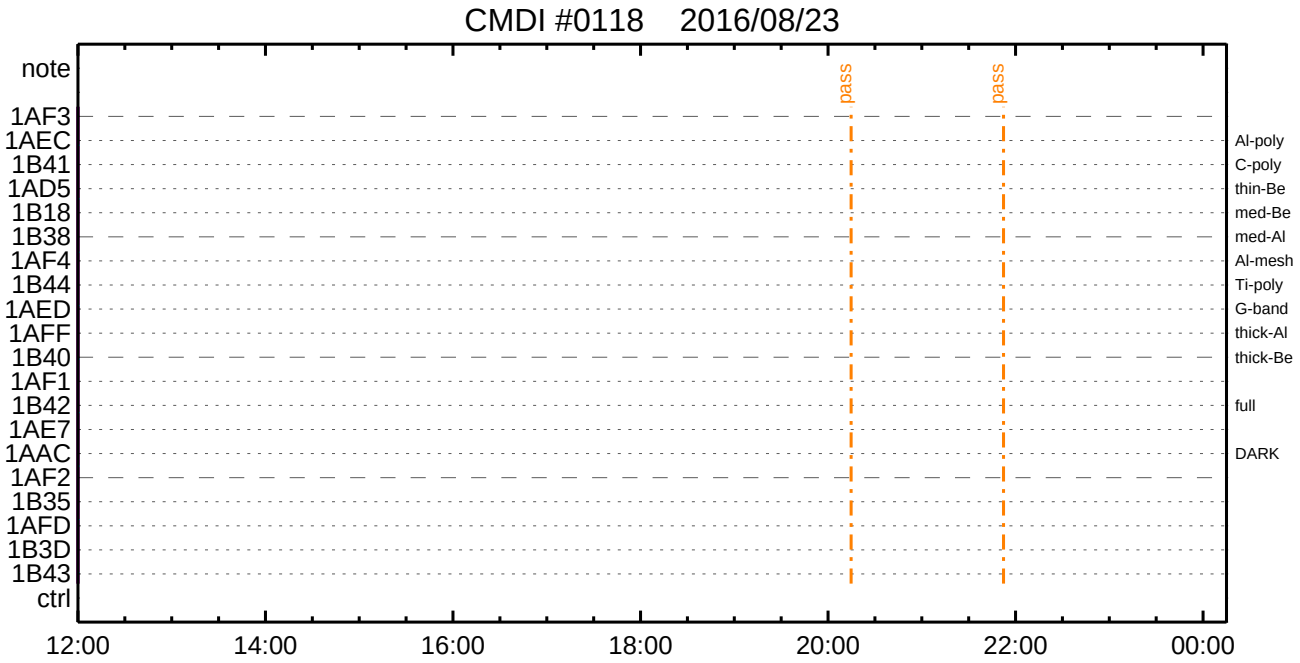
1AED
1AFF
1B40
1AF1
1B42
1AE7
1AAC
1AF2
1B35
1AFD
1B3D
1B43
ctrl
pass
pass
pass
OP ed
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00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note
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1AD5
1B18
1B38
1AF4
1B44
1AED
1AFF
1B40
1AF1
1B42
1AE7
1AAC
1AF2
1B35
1AFD
1B3D
1B43
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

```
(a) Spacecraft Operation Procedure (real-commands)

main-056 2016-08-18 13:37:34 205 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0016     C. ****************************
0017     C.
0018   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0019   . S. OP    op-056:OP
0020     ()
0021   . S. OG    og-056:OG
0022     ()
0023     C.
0024   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0025     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0026   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027     BC       (20 00 7f 01 02)
0028     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0029     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0030     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0031     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0032     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0033   +. DC 01-22 DHU_MODE_CHNG
0034     BC       (07 0b f8)
0035     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0036     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0037     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0038     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0039     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0040   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0041     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0042   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0043     C.
0044     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0045   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046     BC       (20 80 7f 01 02)
0047     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0048     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0049     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0050     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0051     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0052   +. DC 01-22 DHU_MODE_CHNG
0053     BC       (07 0b f8)
0054     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0055     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0056     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0057     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0058     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0059   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0060     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0061   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0062     C.
0063     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065     BC       (21 00 41 01 02)
0066     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0067     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0068     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0069     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0070     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0071   +. DC 01-22 DHU_MODE_CHNG
0072     BC       (07 0b f8)
0073     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0074     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0075     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0076     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0077     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0078   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0079     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0080   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0081     C.
0082   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0083     C. DHU¥â¡¼¥É¡Ê¼ý¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0084   +. DC 01-22 DHU_MODE_CHNG
0085     BC       (02 0a f8)
0086     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     2
0087     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0088     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0089     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0090     C.
0091   . C. ********************************
0092     C. TI-CMD SET (OPOG STOP/COPY/START)
0093     C. ********************************
0094     C.
0095   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
```

```
0096     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¹¤Ç¹Î¤¦¤³¤È¡£
0097     C.
0098   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
0099   +. TI 2016-08-18 09:41:00.0
0100     DC 01-B3 DHU_OP_STOP
0101     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0102     C.
0103   +. TI 2016-08-18 09:41:01.0
0104     DC 01-B4 DHU_OP_COPY
0105     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0106     C.
0107   +. TI 2016-08-18 09:41:01.0
0108     DC 01-B5 DHU_OPOG_COPY
0109     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0110     C.
0111   +. TI 2016-08-18 09:45:59.5
0112     DC 01-B2 DHU_OP_START
0113     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0114     C.
0115     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0116     C.                      ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0117     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     4
0118     C.                      ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0119     C.                      ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0120     C.
0121   . C. ****************************
0122     C. TIÎÎ°è¥À¥ó¥×
0123     C. ****************************
0124     C.
0125     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0126   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127     BC       (03 ab 03 01 02)
0128     C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0129     C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0130     C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0131     C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0132     C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0133   +. DC 01-22 DHU_MODE_CHNG
0134     BC       (07 0b f8)
0135     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
0136     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0137     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0138     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0139     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0140     C.
0141   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0142     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0143     C.
0144   . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0145     C.
0146   . C. DHU¥â¡¼¥É¡Ê¼ý¸¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0147   +. DC 01-22 DHU_MODE_CHNG
0148     BC       (02 0a f8)
0149     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     2
0150     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0151     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0152     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0153     C.
0154     C. ******************************
0155     C.  SOT TI command set
0156     C. ******************************
0157     C. Execute, after the success of OP upload.
0158   +. TI 2016-08-18 09:45:16.0
0159     DC 07-F0 MDP_SOT_MODE_STBY
0160     BC       (41)
0161   . C. ------------------------------------
0162     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0163     C. ------------------------------------
0164     C. ************ SOT END ************
0165   . C. Stop EIS observation and temporarily disable EIS mode changes
0166     C.
0167     C.
0168     C. ********** Start EIS operation (TI set) **********
0169     C. Execute, after the success of OP upload.
0170     C. Set EIS TI-commands
0171   +. TI 2016-08-18 09:45:30.0
0172     DC 07-FC EIS_MODE_MANU
0173     BC       (21 02)
0174   +. TI 2016-08-18 09:45:40.0
0175     DC 07-FC EIS_MODE_CHG_DIS
0176     BC       (22)
0177   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0178     C. ************ End EIS operation (TI set) ************
0179     C.
0180     C.
0181     C.
0182     C. ************ XRT START ************
0183     C. Execute, after the success of OP upload.
0184   +. TI 2016-08-18 09:45:00.0
0185     DC 07-F0 MDP_XRT_MODE_STBY
0186     BC       (c3)
0187   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0188     C.
0189     C. ************ XRT END ************
0190     C.
0191   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0192     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0193   . S. DC-BC dcbc-402:DCBC
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0194     (MDP_known_event)
0195     C.
0196     C.
0197   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0198   . S. DC-BC dcbc-153:DCBC
0199     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200     C.
0201     C.
0202   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0203     C.
0204   . C. ********** LOS **********
0205     C.
```

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(a) Spacecraft Operation Procedure (real-commands)

main-057 2016-08-18 13:37:34 169 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0099     C. Upload the Orbit Element and the Target Attitude
0100     C. RAM-ID:TARGET_ATT
0101   . S. RAM   ram-150:TARGET_ATT
0102     ()
0103     C.
0104     C.
0105     C. Set the dump memory area of TARGET_ATT
0106   +. DC 02-48 AOCU_DUMP_SET
0107     BC       (07 00 00 00 18 00)
0108     C.
0109     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110     C.
0111     C.
0112     C. Change the TLMFormatNo for the AOCS Dump Format
0113   +. DC 01-22 DHU_MODE_CHNG
0114     BC       (04 0b f8)
0115     C.
0116     C. Wait for AOCSDUMP to end
0117     C.
0118   . C. Check the dump memory
0119     C.
0120     C.                                      Result = OK [ ]
0121     C.
0122   +. DC 01-22 DHU_MODE_CHNG
0123     BC       (02 0a f8)
0124     C.
0125     C.                         <A_***>[TLM STS] FMT = 2 [ ]
0126     C.
0127   +. DC 02-8E AOCU_ORB_UPD
0128    . C.
0129    . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130   +. DC 07-FC EIS_MODE_CHG_ENA
0131     BC       (20)
0132    . C. Verify EIS_MODE_CHG_FLG is ENA
0133   +. DC 07-FC EIS_MODE_MANU
0134     BC       (21 02)
0135    . C. Verify EIS in MANUAL mode
0136    . C. Estimated OBSTBL upload time is 49s
0137     C. ********************************
0138     C. EIS START OBSTBL LOAD
0139     C. ********************************
0140   . S. RAM   ram-820:EIS_OBSTBL
0141     ()
0142   +. DC 07-FC EIS_DUMP_OBSTBL
0143     BC       (07 07 07 00 00 70 00)
0144     C.
0145     C. Execute, after the success of OBSTBL upload.
0146     C. Set EIS TI-commands
0147   +. TI 2016-08-18 09:45:50.0
0148     DC 07-FC EIS_MODE_CHG_ENA
0149     BC       (20)
0150   . C.                        [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0151     C. ********************************
0152     C. EIS END OBSTBL LOAD
0153     C. ********************************
0154     C.
0155   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0156     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0157   . S. DC-BC dcbc-402:DCBC
0158     (MDP_known_event)
0159     C.
0160     C.
0161   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0162   . S. DC-BC dcbc-153:DCBC
0163     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164     C.
0165     C.
0166   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0167     C.
0168   . C. ********** LOS **********
0169     C.
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(a) Spacecraft Operation Procedure (real-commands)

main-058 2016-08-18 13:37:34 134 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ********************************
0015     C.  SOT table upload
0016     C. ********************************
0017   . C. < Stop SP table >
0018   +. DC 07-F0 MDP_SP_CTRL_MANU
0019     BC       (61)
0020     C. ------------------------------------
0021     C.  MDP_SP_CTRL_MODE      = MANU    [ ]
0022     C. ------------------------------------
0023     C.
0024   . C. <Upload SP Observation Table>
0025   . S. RAM   ram-282:MDP_OBS_S
0026     ()
0027     C.
0028   . C. < Dump RAMID=MDP_OBS_S >
0029   +. DC 07-F0 MDP_DUMP_SPTBL
0030     BC       (83 07 00 00 00 38 b8)
0031     C. ------------------------------------
0032     C.  MDP_OBS_S verify = OK/NG        [ ]
0033     C. ------------------------------------
0034     C.
0035     C. ********************************
0036     C.  SOT TI command set
0037     C. ********************************
0038     C. Execute, after the success of TBL upload.
0039   +. TI 2016-08-18 09:45:18.0
0040     DC 07-F0 MDP_SOT_MODE_OBSV
0041     BC       (40)
0042   . C. ------------------------------------
0043     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0044     C. ------------------------------------
0045     C.
0046     C.
0047     C. ************ XRT START ************
0048     C.
0049   +. DC 07-F0 MDP_XRT_CTRL_MANU
0050     BC       (c1)
0051   +. DC 07-F0 MDP_XRT_CTRL_MANU
0052     BC       (c1)
0053   +  DC 07-F0 MDP_XRT_MODE_STBY
0054     BC       (c3)
0055   . C. ------------ Success Verify ?              OK / NG____
0056     C.
0057     C. XRT Obs. Table Upload
0058   . S. RAM   ram-291:MDP_OBS_X
0059     ()
0060     C.
0061   +. DC 07-F0 MDP_DUMP_XRTTBL
0062     BC       (84 07 00 00 00 3a d4)
0063   . C. ------------ Comparison Check ?                        OK / ERR ____
0064     C.
0065     C.
0066   +. DC 07-F0 MDP_XRT_ROI_SET
0067     BC       (cd 01 b1 b1 04 04)
0068   +  DC 07-F0 MDP_XRT_ROI_SET
0069     BC       (cd 02 b1 b1 08 08)
0070   +  DC 07-F0 MDP_XRT_ROI_SET
0071     BC       (cd 03 b1 b1 08 08)
0072   +  DC 07-F0 MDP_XRT_ROI_SET
0073     BC       (cd 04 b1 b1 06 06)
0074   +  DC 07-F0 MDP_XRT_ROI_SET
0075     BC       (cd 05 85 83 06 06)
0076   +  DC 07-F0 MDP_XRT_ROI_SET
0077     BC       (cd 06 85 83 06 06)
0078   +  DC 07-F0 MDP_XRT_ROI_SET
0079     BC       (cd 07 a0 80 18 20)
0080   +  DC 07-F0 MDP_XRT_ROI_SET
0081     BC       (cd 08 80 80 20 20)
0082   +  DC 07-F0 MDP_XRT_ROI_SET
0083     BC       (cd 09 80 80 20 08)
0084   +  DC 07-F0 MDP_XRT_ROI_SET
0085     BC       (cd 0a 80 80 08 20)
0086   +  DC 07-F0 MDP_XRT_ROI_SET
0087     BC       (cd 0b 85 83 08 08)
0088   +  DC 07-F0 MDP_XRT_ROI_SET
0089     BC       (cd 0f 80 80 06 06)
0090   +  DC 07-F0 MDP_XRT_ROI_SET
0091     BC       (cd 10 80 80 08 08)
0092   +  DC 07-F0 MDP_XRT_FLD_ENA
0093     BC       (d8)
0094   +  DC 07-F0 MDP_XRT_FLRCTRL_ENA
0095     BC       (c8)
```

```
0096  +  DC 07-F0 MDP_XRT_ARS_DIS
0097     BC       (d5)
0098  +. DC 07-F0 MDP_XRT_AEC_RESET
0099     BC       (d0)
0100  +. DC 07-F0 MDP_XRT_FLD_RESET
0101     BC       (da)
0102  +. DC 07-F0 MDP_XRT_QT_PROG_SET
0103     BC       (c4 0b)
0104  +. DC 07-F0 MDP_XRT_FL_PROG_SET
0105     BC       (c5 07)
0106   . C. ---------- Success Verify ?            OK / NG ____
0107     C.
0108     C.
0109   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0110     C.
0111  +. DC 07-F0 MDP_XRT_MODE_OBSV
0112     BC       (c2)
0113  +. TI 2016-08-18 09:45:02.0
0114     DC 07-F0 MDP_XRT_MODE_OBSV
0115     BC       (c2)
0116   . C. ---------- Success Verify ?            OK / NG ____
0117     C.
0118     C. ************ XRT END ***********
0119     C.
0120   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0121     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0122   . S. DC-BC dcbc-402:DCBC
0123     (MDP_known_event)
0124     C.
0125     C.
0126   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0127   . S. DC-BC dcbc-153:DCBC
0128     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0129     C.
0130     C.
0131   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0132     C.
0133   . C. *********** LOS ***********
0134     C.
```

```
*** OP Sequence for XRT ***

2016/08/18  09:55:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  09:55:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  09:55:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2016/08/18  09:56:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 00 54 cc 02 4d
2016/08/18  09:56:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2016/08/18  09:56:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2016/08/18  09:56:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2016/08/18  09:56:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2016/08/18  09:56:26.0  XRT_FLD_RESET_433_OG [0x1b1]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/18  09:58:56.0  XRT_QT_PROG_SET_432_OG [0x1b0]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 06
2016/08/18  09:58:58.0  XRT_FL_PROG_SET_436_OG [0x1b4]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 07
2016/08/18  09:59:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/18  11:09:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  11:09:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  11:09:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/18  11:09:06.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/18  11:12:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/18  11:28:00.0  XRT_Custom_430_OG [0x1ae]
2016/08/18  11:29:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/18  12:47:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  12:47:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  12:47:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/18  12:47:36.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/18  12:50:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/18  13:06:00.0  XRT_Custom_430_OG [0x1ae]
2016/08/18  13:07:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/18  14:26:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  14:26:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  14:26:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/18  14:26:06.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/18  14:29:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/18  14:44:30.0  XRT_Custom_430_OG [0x1ae]
2016/08/18  14:45:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/18  14:47:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  14:47:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  14:47:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/18  14:47:36.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/18  14:50:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/18  14:58:30.0  XRT_Custom_430_OG [0x1ae]
2016/08/18  14:59:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/18  15:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  15:59:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  15:59:58.0  XRT_FOCUS_POSITION_438_OG [0x1b6]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2016/08/18  16:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 56 35
2016/08/18  16:00:18.0  XRT_FLD_DIS_440_OG [0x1b8]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2016/08/18  16:14:54.0  XRT_FLRCTRL_DIS_441_OG [0x1b9]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2016/08/18  16:14:56.0  XRT_ARS_DIS_435_OG [0x1b3]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2016/08/18  16:14:58.0  XRT_QT_PROG_SET_429_OG [0x1ad]
```

```
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0c
2016/08/18  16:15:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/18  18:22:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  18:22:26.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  18:22:28.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2016/08/18  18:22:30.5  AOCS_ORe-point_Start_3_OG [0x099]
                                    AOCU_NM              5 02-76 00 00 00 00 00
2016/08/18  18:22:48.0  XRT_FLD_DIS_406_OG [0x196]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2016/08/18  18:25:24.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2016/08/18  18:25:26.0  XRT_ARS_DIS_423_OG [0x1a7]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2016/08/18  18:25:28.0  XRT_QT_PROG_SET_418_OG [0x1a2]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 04
2016/08/18  18:25:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/18  18:42:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  18:42:26.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  18:42:28.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2016/08/18  18:42:30.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                    AOCU_NM              5 02-76 03 00 00 00 00
2016/08/18  18:42:48.0  XRT_FLD_ENA_411_OG [0x19b]
                                    MDP_XRT_FLD_ENA      1 07-F0 d8
2016/08/18  18:42:50.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                    MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2016/08/18  18:42:52.0  XRT_AEC_RESET_448_OG [0x1c0]
                                    MDP_XRT_AEC_RESET    1 07-F0 d0
2016/08/18  18:42:54.0  XRT_ARS_DIS_423_OG [0x1a7]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2016/08/18  18:42:56.0  XRT_FLD_RESET_433_OG [0x1b1]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/18  18:45:26.0  XRT_QT_PROG_SET_449_OG [0x1c1]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2016/08/18  18:45:28.0  XRT_FL_PROG_SET_436_OG [0x1b4]
                                    MDP_XRT_FL_PROG_SET  2 07-F0 c5 07
2016/08/18  18:45:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/18  19:21:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  19:21:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  19:21:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/18  19:21:06.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/18  19:24:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/18  19:59:01.0  XRT_Custom_430_OG [0x1ae]
2016/08/18  20:00:01.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/18  20:59:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  20:59:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  20:59:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/18  20:59:36.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/18  21:00:00.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                    AOCU_NM              5 02-76 00 f2 a8 b1 cb
2016/08/18  21:02:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/18  21:03:00.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  21:03:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  21:03:04.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2016/08/18  21:03:24.0  XRT_FLD_ENA_411_OG [0x19b]
                                    MDP_XRT_FLD_ENA      1 07-F0 d8
2016/08/18  21:03:26.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                    MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2016/08/18  21:03:28.0  XRT_AEC_RESET_448_OG [0x1c0]
                                    MDP_XRT_AEC_RESET    1 07-F0 d0
2016/08/18  21:03:30.0  XRT_ARS_DIS_423_OG [0x1a7]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2016/08/18  21:03:32.0  XRT_FLD_RESET_433_OG [0x1b1]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/18  21:06:02.0  XRT_QT_PROG_SET_416_OG [0x1a0]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0a
2016/08/18  21:06:04.0  XRT_FL_PROG_SET_436_OG [0x1b4]
                                    MDP_XRT_FL_PROG_SET  2 07-F0 c5 07
2016/08/18  21:36:30.0  XRT_Custom_430_OG [0x1ae]
2016/08/18  21:37:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
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2016/08/18  22:38:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  22:38:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  22:38:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/18  22:38:06.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/18  22:41:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/18  23:11:00.0  XRT_Custom_430_OG [0x1ae]
2016/08/18  23:12:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/18  23:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  23:59:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/18  23:59:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2016/08/19  00:00:00.0  AOCS_ORe-point_Start_6_OG [0x09c]
                                AOCU_NM              5 02-76 04 00 00 00 00
2016/08/19  00:00:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2016/08/19  00:00:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2016/08/19  00:00:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2016/08/19  00:00:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2016/08/19  00:00:26.0  XRT_FLD_RESET_433_OG [0x1b1]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/19  00:02:56.0  XRT_QT_PROG_SET_407_OG [0x197]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0f
2016/08/19  00:02:58.0  XRT_FL_PROG_SET_436_OG [0x1b4]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 07
2016/08/19  00:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/19  00:16:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  00:16:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  00:16:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/19  00:16:06.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/19  00:19:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/19  00:35:30.0  XRT_Custom_430_OG [0x1ae]
2016/08/19  00:36:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/19  01:51:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  01:51:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  01:51:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/19  01:51:06.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/19  01:54:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/19  02:29:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  02:29:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  02:29:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2016/08/19  02:30:00.0  AOCS_ORe-point_Start_7_OG [0x09d]
                                AOCU_NM              5 02-76 01 00 00 00 00
2016/08/19  02:30:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2016/08/19  02:30:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2016/08/19  02:30:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2016/08/19  02:30:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2016/08/19  02:30:26.0  XRT_FLD_RESET_433_OG [0x1b1]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/19  02:32:56.0  XRT_QT_PROG_SET_439_OG [0x1b7]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2016/08/19  02:32:58.0  XRT_FL_PROG_SET_436_OG [0x1b4]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 07
2016/08/19  02:33:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/19  03:26:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  03:26:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  03:26:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/19  03:26:36.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/19  03:29:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
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                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/19  03:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  03:59:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  03:59:58.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2016/08/19  04:00:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                    AOCU_NM              5 02-76 00 00 00 00 00
2016/08/19  04:00:18.0  XRT_FLD_DIS_406_OG [0x196]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2016/08/19  04:02:54.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2016/08/19  04:02:56.0  XRT_ARS_DIS_423_OG [0x1a7]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2016/08/19  04:02:58.0  XRT_QT_PROG_SET_425_OG [0x1a9]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 01
2016/08/19  04:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/19  04:09:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  04:09:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  04:09:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2016/08/19  04:10:00.0  AOCS_ORe-point_Start_8_OG [0x09e]
                                    AOCU_NM              5 02-76 02 00 00 00 00
2016/08/19  04:10:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                    MDP_XRT_FLD_ENA      1 07-F0 d8
2016/08/19  04:10:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                    MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2016/08/19  04:10:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                    MDP_XRT_AEC_RESET    1 07-F0 d0
2016/08/19  04:10:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2016/08/19  04:10:26.0  XRT_FLD_RESET_433_OG [0x1b1]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/19  04:12:56.0  XRT_QT_PROG_SET_439_OG [0x1b7]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2016/08/19  04:12:58.0  XRT_FL_PROG_SET_436_OG [0x1b4]
                                    MDP_XRT_FL_PROG_SET  2 07-F0 c5 07
2016/08/19  04:13:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/19  04:56:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  04:56:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  04:56:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/19  04:56:36.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/19  04:59:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/19  05:29:30.0  XRT_Custom_430_OG [0x1ae]
2016/08/19  05:30:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/19  06:02:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  06:02:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  06:02:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2016/08/19  06:03:00.0  AOCS_ORe-point_Start_9_OG [0x09f]
                                    AOCU_NM              5 02-76 03 fb 8e fb 8e
2016/08/19  06:03:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                    MDP_XRT_FLD_ENA      1 07-F0 d8
2016/08/19  06:03:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                    MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2016/08/19  06:03:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                    MDP_XRT_AEC_RESET    1 07-F0 d0
2016/08/19  06:03:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2016/08/19  06:03:26.0  XRT_FLD_RESET_433_OG [0x1b1]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/19  06:05:56.0  XRT_QT_PROG_SET_439_OG [0x1b7]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2016/08/19  06:05:58.0  XRT_FL_PROG_SET_436_OG [0x1b4]
                                    MDP_XRT_FL_PROG_SET  2 07-F0 c5 07
2016/08/19  06:06:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/19  06:37:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  06:37:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  06:37:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/19  06:37:06.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/19  06:40:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/19  07:08:00.0  XRT_Custom_430_OG [0x1ae]
2016/08/19  07:09:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
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2016/08/19  08:17:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  08:17:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  08:17:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/19  08:17:06.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/19  08:20:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/19  08:46:30.0  XRT_Custom_430_OG [0x1ae]
2016/08/19  08:47:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/19  09:57:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  09:57:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  09:57:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/19  09:57:06.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/19  10:00:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/19  10:24:30.0  XRT_Custom_430_OG [0x1ae]
2016/08/19  10:25:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/19  11:29:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  11:29:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  11:29:58.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2016/08/19  11:30:00.0  AOCS_ORe-point_Start_10_OG [0x0a0]
                                AOCU_NM              5 02-76 04 00 00 ee 36
2016/08/19  11:30:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2016/08/19  11:30:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2016/08/19  11:30:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2016/08/19  11:30:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2016/08/19  11:30:26.0  XRT_FLD_RESET_433_OG [0x1b1]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/19  11:32:56.0  XRT_QT_PROG_SET_428_OG [0x1ac]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2016/08/19  11:32:58.0  XRT_FL_PROG_SET_436_OG [0x1b4]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 07
2016/08/19  11:33:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/19  11:45:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  11:45:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  11:45:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/19  11:45:06.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/19  11:48:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/19  11:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  11:59:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  11:59:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2016/08/19  12:00:00.0  AOCS_ORe-point_Start_6_OG [0x09c]
                                AOCU_NM              5 02-76 04 00 00 00 00
2016/08/19  12:00:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2016/08/19  12:00:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2016/08/19  12:00:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2016/08/19  12:00:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2016/08/19  12:00:26.0  XRT_FLD_RESET_433_OG [0x1b1]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/19  12:02:56.0  XRT_QT_PROG_SET_431_OG [0x1af]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 03
2016/08/19  12:02:58.0  XRT_FL_PROG_SET_436_OG [0x1b4]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 07
2016/08/19  12:03:00.0  XRT_Custom_430_OG [0x1ae]
2016/08/19  12:04:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/19  13:23:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  13:23:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  13:23:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/19  13:23:36.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
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2016/08/19  13:26:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/19  13:41:30.0  XRT_Custom_430_OG [0x1ae]
2016/08/19  13:42:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/19  15:01:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  15:01:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  15:01:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/19  15:01:36.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/19  15:04:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/19  15:20:00.0  XRT_Custom_430_OG [0x1ae]
2016/08/19  15:21:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/19  15:21:00.5  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  15:21:02.5  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  15:21:04.5  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/19  15:21:06.5  XRT_PREFLR_STRT_414_OG [0x19e]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/19  15:24:14.5  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/19  15:35:00.0  XRT_Custom_430_OG [0x1ae]
2016/08/19  15:36:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/19  15:40:01.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  15:40:03.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  15:40:05.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/19  15:40:07.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/19  15:43:15.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/19  15:44:00.0  XRT_Custom_430_OG [0x1ae]
2016/08/19  15:45:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/19  16:40:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  16:40:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  16:40:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/19  16:40:06.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/19  16:43:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/19  17:20:31.0  XRT_Custom_430_OG [0x1ae]
2016/08/19  17:21:31.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/19  18:18:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  18:18:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  18:18:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/19  18:18:36.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/19  18:21:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/19  18:57:30.0  XRT_Custom_430_OG [0x1ae]
2016/08/19  18:58:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/19  19:57:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  19:57:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  19:57:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/19  19:57:06.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/19  20:00:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/19  20:34:31.0  XRT_Custom_430_OG [0x1ae]
2016/08/19  20:35:31.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/19  21:35:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  21:35:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  21:35:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/19  21:35:36.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/19  21:38:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
```

```
2016/08/19  22:11:00.0  XRT_Custom_430_OG [0x1ae]
2016/08/19  22:12:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/19  22:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  22:59:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  22:59:58.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2016/08/19  23:00:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 00 00 00 00 00
2016/08/19  23:00:18.0  XRT_FLD_DIS_406_OG [0x196]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2016/08/19  23:02:54.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2016/08/19  23:02:56.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2016/08/19  23:02:58.0  XRT_QT_PROG_SET_425_OG [0x1a9]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 01
2016/08/19  23:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/19  23:10:00.0  AOCS_ORe-point_Start_6_OG [0x09c]
                                AOCU_NM              5 02-76 04 00 00 00 00
2016/08/19  23:13:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  23:13:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  23:13:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/19  23:13:36.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/19  23:16:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/19  23:38:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  23:38:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/19  23:38:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2016/08/19  23:39:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2016/08/19  23:39:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2016/08/19  23:39:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2016/08/19  23:39:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2016/08/19  23:39:26.0  XRT_FLD_RESET_433_OG [0x1b1]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/19  23:41:56.0  XRT_QT_PROG_SET_407_OG [0x197]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0f
2016/08/19  23:41:58.0  XRT_FL_PROG_SET_436_OG [0x1b4]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 07
2016/08/19  23:42:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/20  00:52:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/20  00:52:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/20  00:52:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/20  00:52:06.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/20  00:55:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/20  01:10:00.0  XRT_Custom_430_OG [0x1ae]
2016/08/20  01:11:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/20  02:25:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/20  02:25:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/20  02:25:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/20  02:25:06.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/20  02:28:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/20  02:29:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/20  02:29:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/20  02:29:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2016/08/20  02:30:00.0  AOCS_ORe-point_Start_7_OG [0x09d]
                                AOCU_NM              5 02-76 01 00 00 00 00
2016/08/20  02:30:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2016/08/20  02:30:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2016/08/20  02:30:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2016/08/20  02:30:24.0  XRT_ARS_DIS_423_OG [0x1a7]
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                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2016/08/20  02:30:26.0  XRT_FLD_RESET_433_OG [0x1b1]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/20  02:32:56.0  XRT_QT_PROG_SET_439_OG [0x1b7]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2016/08/20  02:32:58.0  XRT_FL_PROG_SET_436_OG [0x1b4]
                                    MDP_XRT_FL_PROG_SET  2 07-F0 c5 07
2016/08/20  02:48:00.0  XRT_Custom_430_OG [0x1ae]
2016/08/20  02:49:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/20  03:52:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/20  03:52:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/20  03:52:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/20  03:52:36.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/20  03:55:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/20  04:29:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/20  04:29:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/20  04:29:58.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2016/08/20  04:30:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                    AOCU_NM              5 02-76 00 00 00 00 00
2016/08/20  04:30:18.0  XRT_FLD_DIS_406_OG [0x196]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2016/08/20  04:32:54.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2016/08/20  04:32:56.0  XRT_ARS_DIS_423_OG [0x1a7]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2016/08/20  04:32:58.0  XRT_QT_PROG_SET_425_OG [0x1a9]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 01
2016/08/20  04:33:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/20  04:39:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/20  04:39:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/20  04:39:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2016/08/20  04:40:00.0  AOCS_ORe-point_Start_8_OG [0x09e]
                                    AOCU_NM              5 02-76 02 00 00 00 00
2016/08/20  04:40:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                    MDP_XRT_FLD_ENA      1 07-F0 d8
2016/08/20  04:40:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                    MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2016/08/20  04:40:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                    MDP_XRT_AEC_RESET    1 07-F0 d0
2016/08/20  04:40:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2016/08/20  04:40:26.0  XRT_FLD_RESET_433_OG [0x1b1]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/20  04:42:56.0  XRT_QT_PROG_SET_439_OG [0x1b7]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2016/08/20  04:42:58.0  XRT_FL_PROG_SET_436_OG [0x1b4]
                                    MDP_XRT_FL_PROG_SET  2 07-F0 c5 07
2016/08/20  04:43:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/20  05:33:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/20  05:33:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/20  05:33:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/20  05:33:06.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/20  05:36:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/20  06:05:00.0  XRT_Custom_430_OG [0x1ae]
2016/08/20  06:06:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/20  07:13:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/20  07:13:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/20  07:13:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/20  07:13:06.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/20  07:16:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/20  07:43:00.0  XRT_Custom_430_OG [0x1ae]
2016/08/20  07:44:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/08/20  08:52:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/20  08:52:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/08/20  08:52:34.0  XRT_FLD_RESET_415_OG [0x19f]
```

```
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2016/08/20  08:52:36.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/08/20  08:55:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/08/20  11:06:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                        AOCU_NM              5 02-76 00 00 00 00 00
```