

XRT Timeline to be uploaded on 2016/08/23

Period: 2016/08/23 10:32:00 - 2016/08/27 10:11:00

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Normal mode

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| XOB #1AC2: CME watch - 4x4 - AEC 2 - Be-thin - G-band (2x2,1ms) - Leak (2x2,1ms) - 120s cad | | | | | | | | | | | | |
|---|--|--------------------------------|-------|------|------|-------|-----|---------|------------------------|------|---|----------|
| Term | Pointing (x, y) | | | | | | | Comment | | | | |
| 08/23 11:08:00 - 08/23 14:59:24 | Track (-3.5, 765.8) [Ⓜ] 08/23 10:42:00 | # OP start + 10min QS at polar | | | | | | | | | | |
| PROG= 18 Inf.-time(s) | | | | | | | | | | | | |
| └─ Subr= 1 45-time(s) 120.0sec | | | | | | | | | | | | |
| └─ Seqn= 29 1-time(s) 4.0sec | | | | | | | | | | | | |
| | thin-Be/Open | med-Be/Open | close | Safe | Norm | 1.41s | Obs | 4x4 | 2048x2048 (1024, 1024) | Q=98 | 2 | 0 2.0sec |
| └─ Subr= 2 1-time(s) 2.0sec | | | | | | | | | | | | |
| └─ Seqn= 26 1-time(s) 2.0sec | | | | | | | | | | | | |
| | Open/G-band | Open/G-band | open | Safe | Norm | 1ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=90 | 0 | 0 2.0sec |
| | Open/G-band | Open/G-band | close | Safe | Norm | 1ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 2.0sec |
| Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval | | | | | | | | | | | | |

| XOB #1B46: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, Al/Poly context, with G-band (3ms/3ms) | | | | | | | | | | | | |
|---|---|--|-------|------|------|-------|-----|---------|----------------------|------|---|-----------|
| Term | Pointing (x, y) | | | | | | | Comment | | | | |
| 08/23 15:02:30 - 08/23 17:25:30 | Track (-169.1, 62.2) [Ⓜ] 08/23 15:00:00 | growing sunspot | | | | | | | | | | |
| 08/24 00:19:00 - 08/24 05:49:54 | Track (-88.6, 60.7) [Ⓜ] 08/24 00:00:00 | growing sunspot | | | | | | | | | | |
| 08/24 06:02:30 - 08/24 17:51:54 | Track (-34.5, 60.1) [Ⓜ] 08/24 06:00:00 | growing sunspot (HOP321 08UT-10UT with GREGOR) | | | | | | | | | | |
| 08/25 00:02:30 - 08/25 05:59:54 | Track (127.8, 60.7) [Ⓜ] 08/25 00:00:00 | growing sunspot | | | | | | | | | | |
| 08/25 06:12:30 - 08/25 10:14:30 | Track (182.7, 61.8) [Ⓜ] 08/25 06:10:00 | growing sunspot (HOP321 08UT-10UT with GREGOR) | | | | | | | | | | |
| PROG= 08 Inf.-time(s) | | | | | | | | | | | | |
| └─ Subr= 1 1-time(s) 2.0sec | | | | | | | | | | | | |
| └─ Seqn= 56 1-time(s) 2.0sec | | | | | | | | | | | | |
| | Open/G-band | Open/G-band | open | Safe | Norm | 3ms | Obs | 1x1 | 384x384 (1064, 1048) | DPCM | 0 | 0 2.0sec |
| | Open/G-band | Open/G-band | close | Safe | Norm | 3ms | Obs | 1x1 | 384x384 (1064, 1048) | DPCM | 0 | 0 2.0sec |
| | Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 16.0s | Obs | 1x1 | 384x384 (1064, 1048) | Q=98 | 0 | 0 2.0sec |
| └─ Seqn= 42 3-time(s) 2.0sec | | | | | | | | | | | | |
| | Al-poly/Open | thin-Be/Open | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 0 2.0sec |
| | thin-Be/Open | med-Be/Open | close | Safe | Norm | 5.66s | Obs | 1x1 | 512x512 (1064, 1048) | Q=95 | 3 | 0 2.0sec |
| | Open/thick-Al | Open/thick-Al | close | Safe | Norm | 16.0s | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 0 2.0sec |
| └─ Seqn= 32 40-time(s) 90.0sec | | | | | | | | | | | | |
| | thin-Be/Open | med-Be/Open | close | Safe | Norm | 1.00s | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 0 2.0sec |
| | Al-poly/Open | thin-Be/Open | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 0 34.0sec |
| | thin-Be/Open | med-Be/Open | close | Safe | Norm | 1.00s | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 1 2.0sec |
| | Al-poly/Open | thin-Be/Open | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 1 34.0sec |
| | thin-Be/Open | med-Be/Open | close | Safe | Norm | 1.00s | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 2 2.0sec |
| | Al-poly/Open | thin-Be/Open | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 2 2.0sec |
| Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval | | | | | | | | | | | | |

| XOB #1B43: Synoptic Q95 2x2 - Al/mesh(24/256/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(33/512/2048) + T | | | | | | | | | | | | |
|---|-------------------|-----------------------------|-------|------|------|-------|-----|---------|------------------------|------|---|----------|
| Term | Pointing (x, y) | | | | | | | Comment | | | | |
| 08/23 18:07:00 - 08/23 18:13:54 | Fixed (0.0, 0.0) | synoptic, shifted 4.0 min | | | | | | | | | | |
| 08/24 05:53:00 - 08/24 05:59:24 | Fixed (0.0, 0.0) | synoptic, shifted -10.0 min | | | | | | | | | | |
| 08/24 17:55:00 - 08/24 18:01:54 | Fixed (0.0, 0.0) | synoptic, shifted -8.0 min | | | | | | | | | | |
| 08/25 06:03:00 - 08/25 06:09:24 | Fixed (0.0, 0.0) | synoptic | | | | | | | | | | |
| PROG= 01 1-time(s) | | | | | | | | | | | | |
| └─ Subr= 1 1-time(s) 2.0sec | | | | | | | | | | | | |
| └─ Seqn= 5 1-time(s) 2.0sec | | | | | | | | | | | | |
| | Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 500ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=98 | 0 | 0 2.0sec |
| | Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 500ms | Obs | 4x4 | 2048x2048 (1024, 1024) | Q=98 | 0 | 0 2.0sec |
| | Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 500ms | Obs | 8x8 | 2048x2048 (1024, 1024) | Q=98 | 0 | 0 2.0sec |
| | Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 500ms | Obs | 1x1 | 2048x512 (1024, 1024) | DPCM | 0 | 0 2.0sec |
| | Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 500ms | Obs | 1x1 | 512x2048 (1024, 1024) | DPCM | 0 | 0 2.0sec |
| └─ Seqn= 1 1-time(s) 2.0sec | | | | | | | | | | | | |
| | Open/Al-mesh | Open/Al-mesh | close | Safe | Norm | 24ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 2.0sec |
| | Open/Al-mesh | Open/Al-mesh | close | Safe | Norm | 250ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 2.0sec |
| | Open/Al-mesh | Open/Al-mesh | close | Safe | Norm | 2.83s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 2.0sec |
| └─ Seqn= 12 1-time(s) 2.0sec | | | | | | | | | | | | |
| | Al-poly/Open | Al-poly/thick-Al | close | Safe | Norm | 32ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 2.0sec |
| | Al-poly/Open | Al-poly/Open | close | Safe | Norm | 500ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 2.0sec |
| | Al-poly/Open | Al-poly/thick-Al | close | Safe | Norm | 2.00s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 2.0sec |
| └─ Seqn= 85 1-time(s) 2.0sec | | | | | | | | | | | | |
| | thin-Be/Open | thin-Be/Open | close | Safe | Norm | 500ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 2.0sec |
| | thin-Be/Open | thin-Be/Open | close | Safe | Norm | 2.00s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 2.0sec |
| | thin-Be/Open | thin-Be/Open | close | Safe | Norm | 4.00s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 2.0sec |
| └─ Seqn= 54 1-time(s) 2.0sec | | | | | | | | | | | | |
| | Open/G-band | Open/G-band | open | Safe | Norm | 3ms | Obs | 1x1 | 2048x2048 (1024, 1024) | Q=90 | 0 | 0 2.0sec |
| | Open/G-band | Open/G-band | close | Safe | Norm | 3ms | Obs | 1x1 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 2.0sec |
| Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval | | | | | | | | | | | | |

XOB #1B48: HOP316 (Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, and Med-Be context, with G-band (3ms/3ms VLS=CLS

| Term | Pointing (x, y) | Comment |
|---------------------------------|--|---------------------------|
| 08/23 18:17:00 - 08/23 23:59:24 | Fixed (880.0, -250.0) | HOP316 Coronal loops |
| 08/24 18:40:00 - 08/24 23:59:24 | Fixed (880.0, -250.0) | HOP316 Coronal loops |
| PROG= 02 Inf.-time(s) | | |
| Subr= 1 1-time(s) 2.0sec | | |
| Seqn= 56 1-time(s) 2.0sec | | |
| Open/G-band | Open/G-band open Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) | DPCM 0 0 2.0sec |
| Open/G-band | Open/G-band close Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) | DPCM 0 0 2.0sec |
| Open/Ti-poly | Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) | Q=98 0 0 2.0sec |
| Subr= 2 5-time(s) 2.0sec | | |
| Seqn= 48 1-time(s) 2.0sec | | |
| thin-Be/Open | med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) | Q=95 2 0 2.0sec |
| thin-Be/Open | med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) | Q=95 3 0 2.0sec |
| med-Be/Open | Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) | Q=95 2 0 2.0sec |
| med-Be/Open | Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) | Q=95 3 0 2.0sec |
| Seqn= 96 5-time(s) 60.0sec | | |
| Al-poly/Open | thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) | Q=95 1 0 2.0sec |
| thin-Be/Open | med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) | Q=95 1 0 15.0sec |
| Al-poly/Open | thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) | Q=95 1 1 2.0sec |
| thin-Be/Open | med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) | Q=95 1 1 15.0sec |
| Al-poly/Open | thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) | Q=95 1 2 2.0sec |
| thin-Be/Open | med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) | Q=95 1 2 2.0sec |
| Default Filter | Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) | Comp. AEC Buffer Interval |

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Flare mode

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XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

| Term | Pointing (x, y) | Comment |
|---------------------------------|--|--|
| 08/23 11:08:00 - 08/23 14:59:24 | Track (-3.5, 765.8) @ 08/23 10:42:00 | # OP start + 10min QS at polar |
| 08/23 15:02:30 - 08/23 17:25:30 | Track (-169.1, 62.2) @ 08/23 15:00:00 | growing sunspot |
| 08/23 18:17:00 - 08/23 23:59:24 | Fixed (880.0, -250.0) | HOP316 Coronal loops |
| 08/24 00:19:00 - 08/24 05:49:54 | Track (-88.6, 60.7) @ 08/24 00:00:00 | growing sunspot |
| 08/24 06:02:30 - 08/24 17:51:54 | Track (-34.5, 60.1) @ 08/24 06:00:00 | growing sunspot (HOP321 08UT-10UT with GREGOR) |
| 08/24 18:40:00 - 08/24 23:59:24 | Fixed (880.0, -250.0) | HOP316 Coronal loops |
| 08/25 00:02:30 - 08/25 05:59:54 | Track (127.8, 60.7) @ 08/25 00:00:00 | growing sunspot |
| 08/25 06:12:30 - 08/25 10:14:30 | Track (182.7, 61.8) @ 08/25 06:10:00 | growing sunspot (HOP321 08UT-10UT with GREGOR) |

PROG= 07 30-time(s)

| | | |
|----------------------------|---|---------------------------|
| Subr= 1 20-time(s) 2.0sec | | |
| Seqn= 11 1-time(s) 2.0sec | | |
| Al-poly/Open | Al-poly/thick-Al close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) | Q=95 2 0 2.0sec |
| Seqn=100 1-time(s) 10.0sec | | |
| thin-Be/Open | med-Be/Open close Safe Norm 125ms Obs 1x1 384x384 (1024, 1024) | Q=95 2 0 2.0sec |
| med-Be/Open | Open/thick-Al close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) | Q=95 3 0 2.0sec |
| Open/thick-Al | Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) | Q=95 3 0 2.0sec |
| Subr= 2 1-time(s) 2.0sec | | |
| Seqn= 10 1-time(s) 2.0sec | | |
| med-Al/Open | med-Al/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) | Q=95 3 0 2.0sec |
| Open/thick-Be | Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) | Q=95 3 0 2.0sec |
| Seqn= 11 1-time(s) 2.0sec | | |
| Al-poly/Open | Al-poly/thick-Al close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) | Q=95 2 0 2.0sec |
| Seqn= 84 1-time(s) 2.0sec | | |
| Open/G-band | Open/G-band open Safe Norm 3ms Obs 1x1 384x384 (1024, 1024) | Q=98 0 0 2.0sec |
| Open/G-band | Open/G-band close Safe Norm 3ms Obs 1x1 384x384 (1024, 1024) | Q=98 0 0 2.0sec |
| Open/thick-Al | Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) | Q=98 0 0 2.0sec |
| Open/thick-Al | Open/thick-Al close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) | Q=98 0 0 2.0sec |
| Default Filter | Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) | Comp. AEC Buffer Interval |

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Active Region Search

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NOT USED

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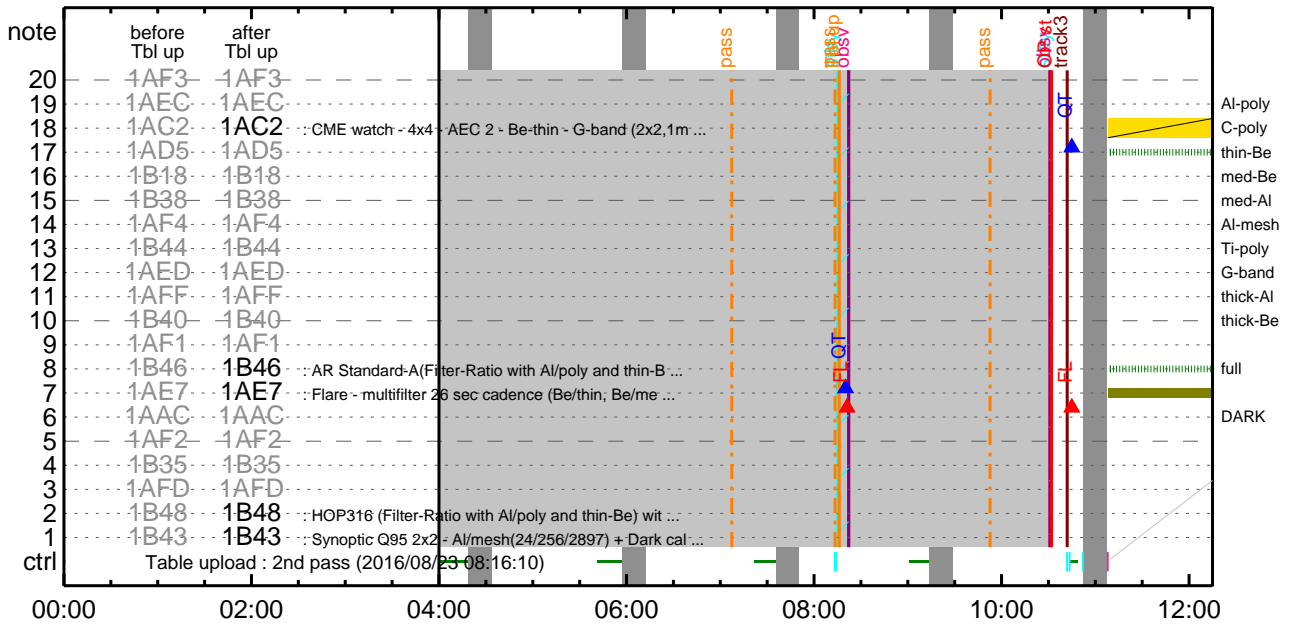
Flare Detection

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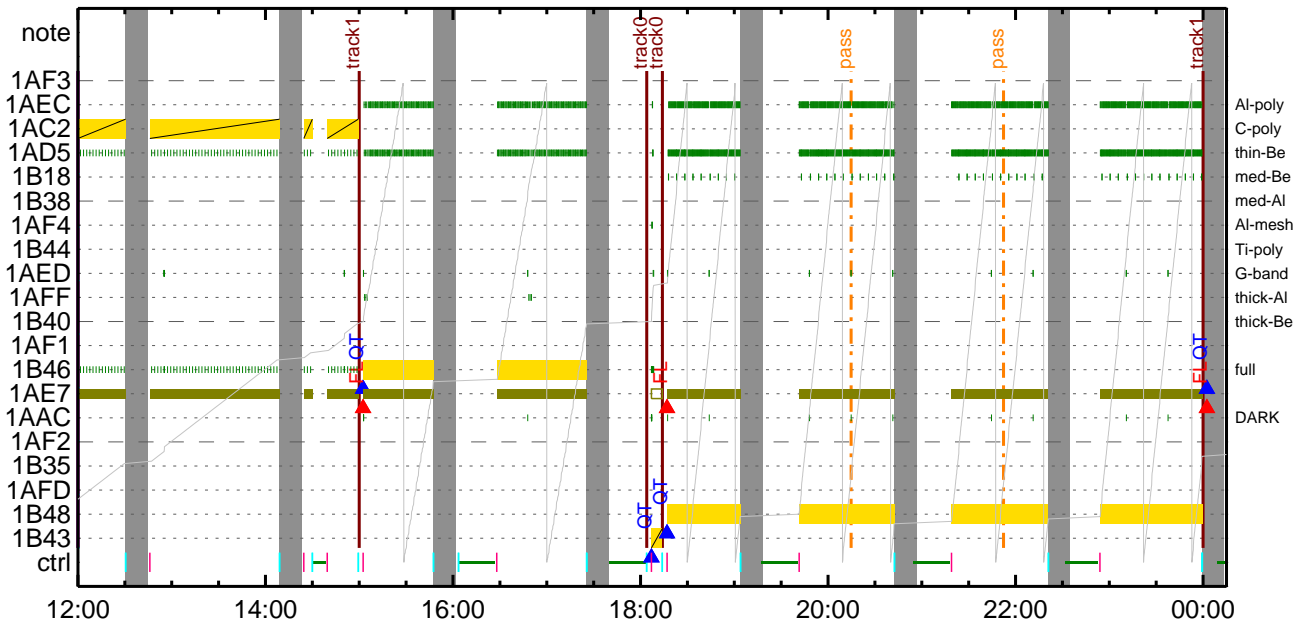
FLD Patrol

| Term | Pointing (x, y) | Comment |
|---------------------------------|---|-----------------------------|
| 08/23 18:14:18 - 08/24 05:50:18 | Fixed (880.0, -250.0) | HOP316 Coronal loops |
| 08/24 05:59:48 - 08/24 17:52:18 | Fixed (0.0, 0.0) | synoptic, shifted -10.0 min |
| 08/24 18:02:18 - 08/25 06:00:18 | Fixed (880.0, -250.0) | HOP316 Coronal loops |
| 08/25 06:09:48 - 08/27 10:11:00 | Fixed (0.0, 0.0) | synoptic |
| Open/Ti-poly | Open/thick-Al close Safe Norm 8ms Obs 8x8 | Q=50 30sec |
| Default Filter | Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) | Comp. AEC Buffer Interval |

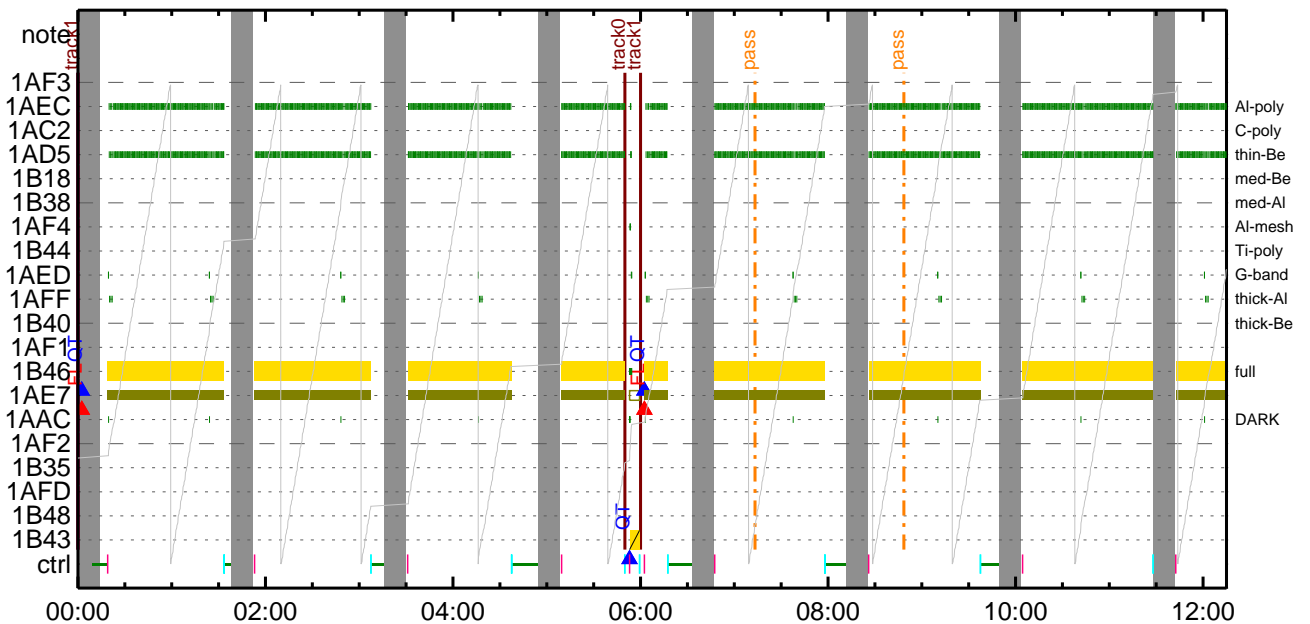
CMDI #0126 2016/08/23



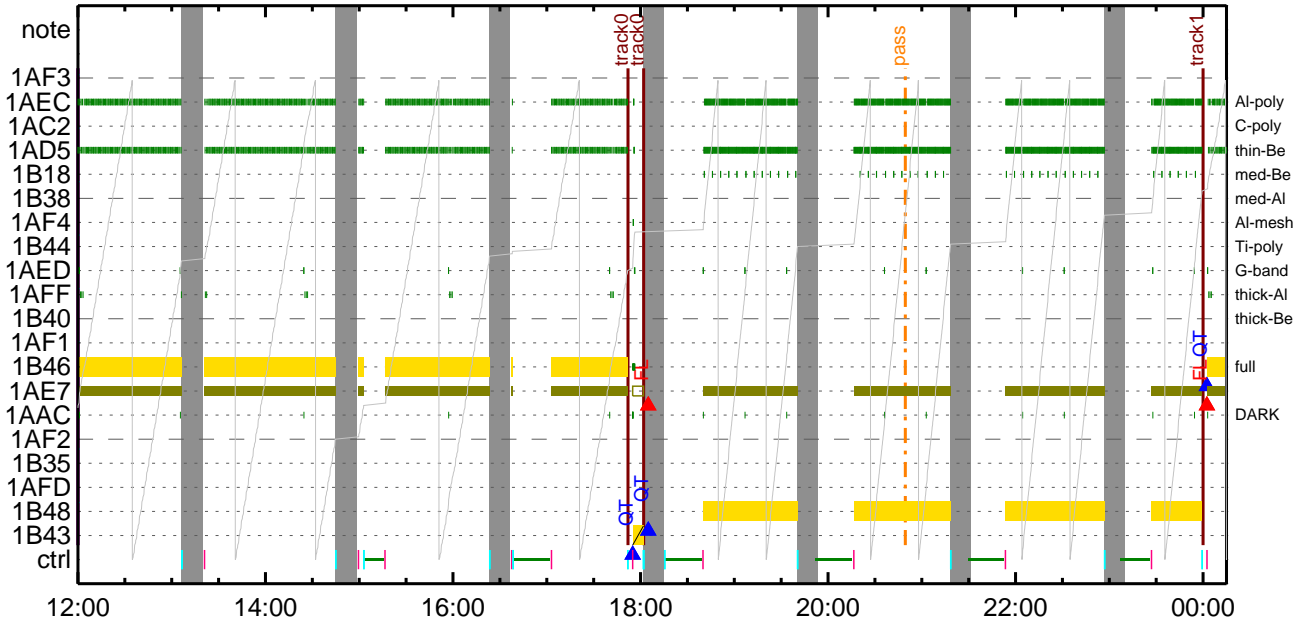
CMDI #0126 2016/08/23



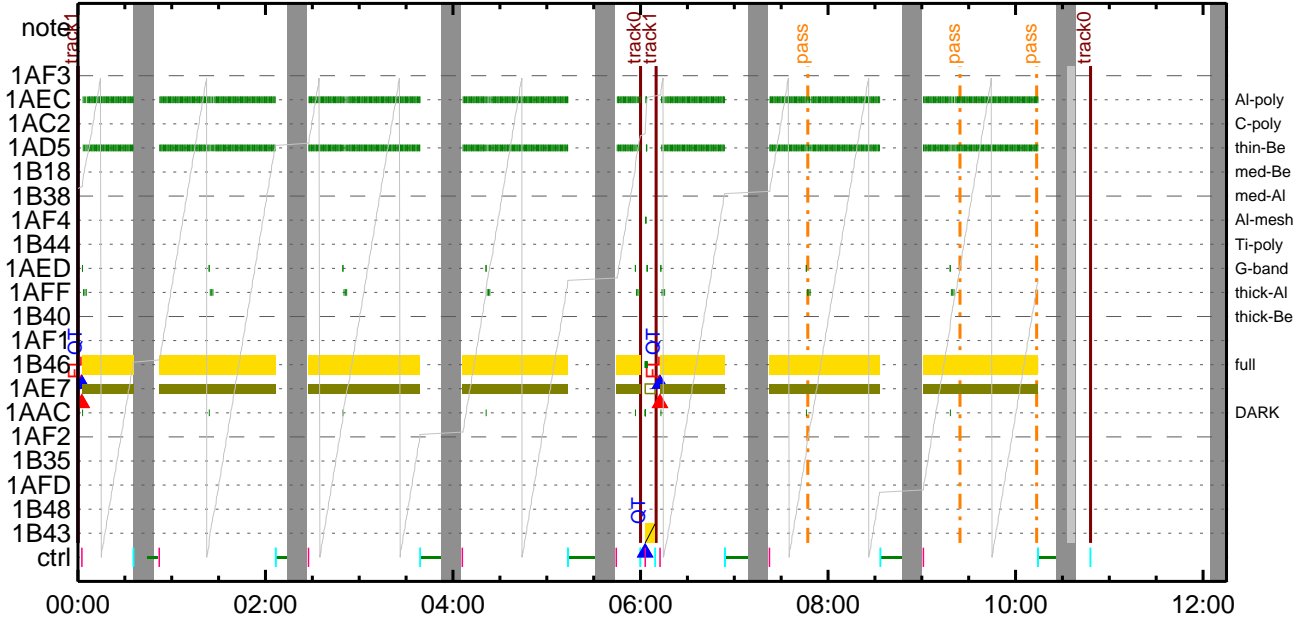
CMDI #0126 2016/08/24



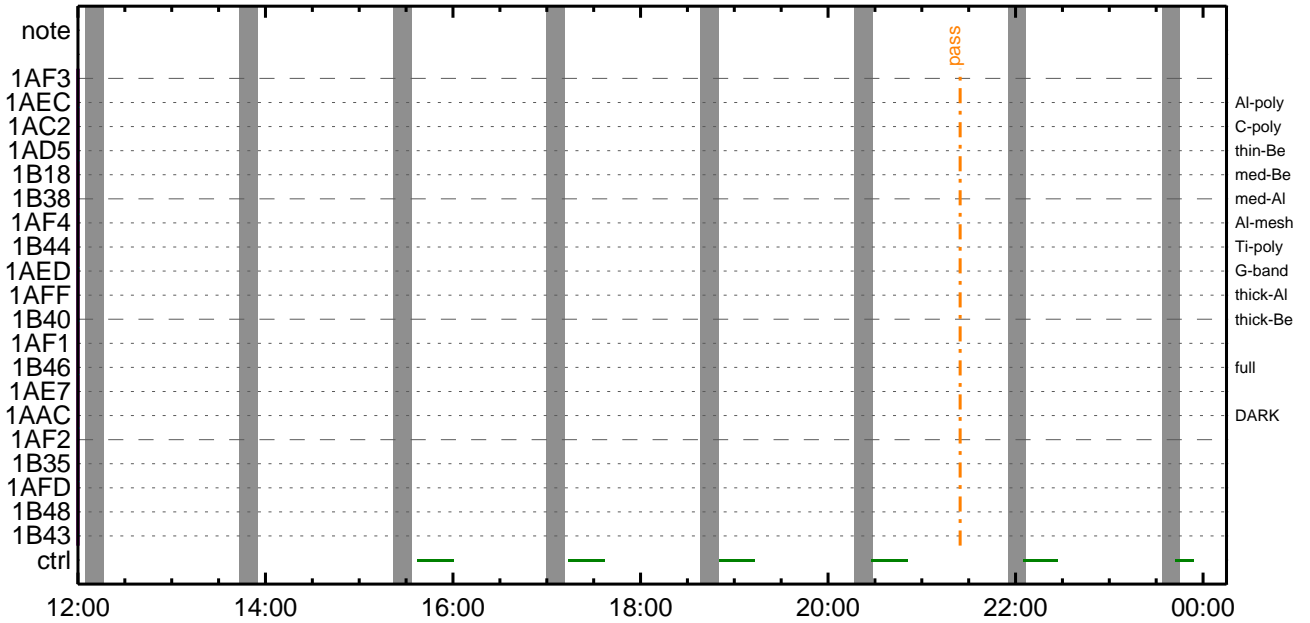
CMDI #0126 2016/08/24



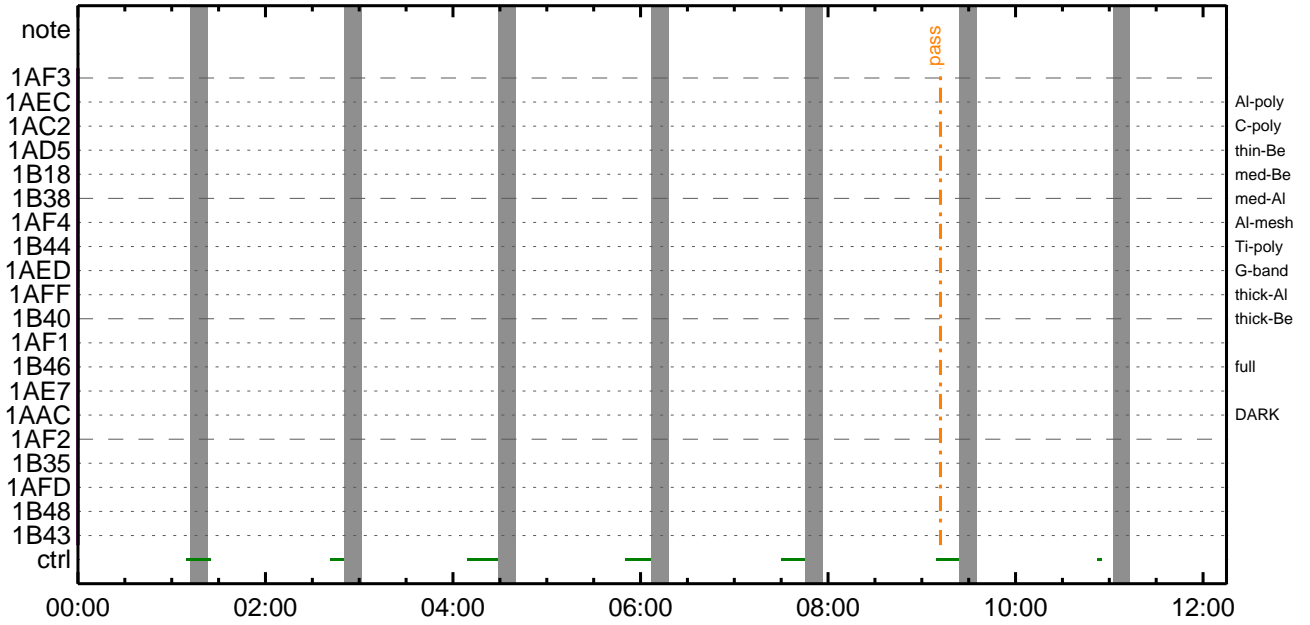
CMDI #0126 2016/08/25



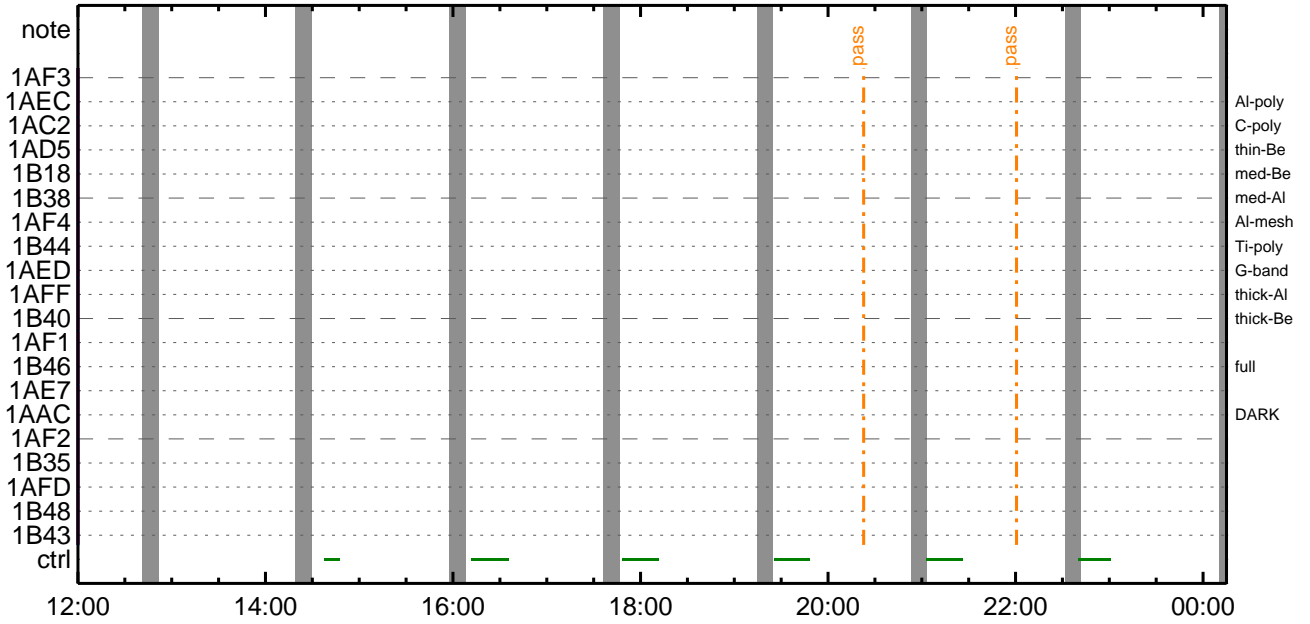
CMDI #0126 2016/08/25



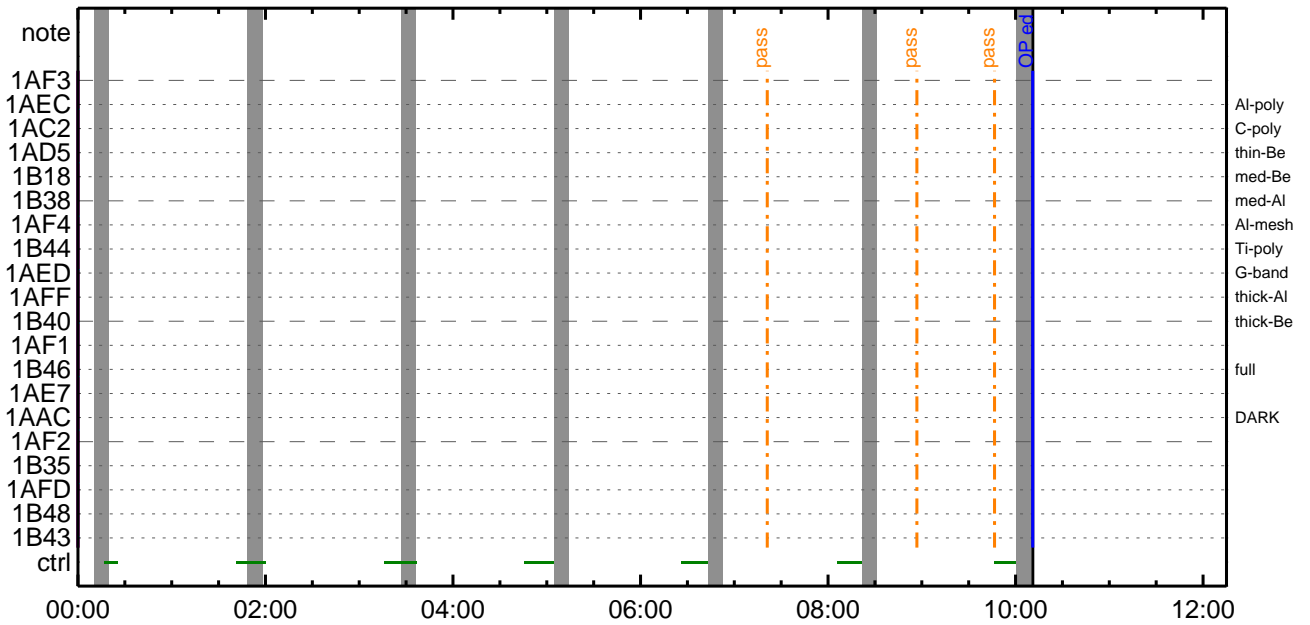
CMDI #0126 2016/08/26



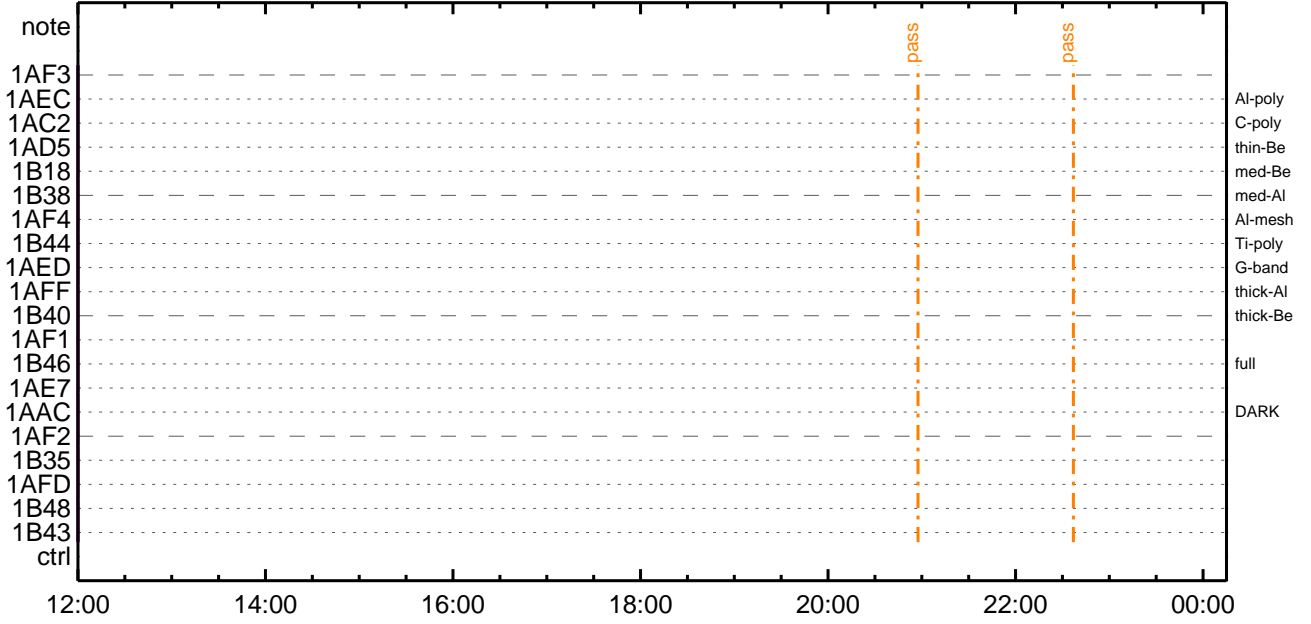
CMDI #0126 2016/08/26



CMDI #0126 2016/08/27



CMDI #0126 2016/08/27



(a) Spacecraft Operation Procedure (real-commands)

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main-066 2016-08-23 12:49:34 205 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;äAOSYÄY$YÄY-¼Ä»Û;ä
0005 C.
0006 C. YÄYB;¼Y³YFÝÖYÉÄ÷¿@
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Äí;Èð¿ðÄð•μ°£»Í×ÄÇðÍYÇYÄY×YÍ;¼YÉ;ÈÈè½μ•ííÈ;ÈðÈ¼°ÇÖð•α¿¼ì¹ÇðÍ;ÇÄ@,ùα¹αèððÇÄ÷¿@ð•ðÈððð³ðÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGYÍ;¼YÉ;|YÄYÖY×
0016 C. *****
0017 C.
0018 . C. ;äOP/OGYÍ;¼YÉ;ä
0019 . S. OP op-066:OP
0020 ()
0021 . S. OG og-066:OG
0022 ()
0023 C.
0024 . C. ;änMOG&OPí°èYÄYÖY×;ä
0025 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. ÇÇ[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. ÇÇ[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. ÇÇ[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. ÇÇ[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. ÇÇ[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. ÇÇ[HK1_PKT_FORM_NO] EQ 7
0036 C. ÇÇ[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. ÇÇ[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. ÇÇ[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. ÇÇ[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. YÄYÖY×½ªí»ðð³íÇ§
0041 C. ÇÇ[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGðí½È¹Ç•è²íOKðð³íÇ§
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. ÇÇ[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. ÇÇ[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. ÇÇ[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. ÇÇ[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. ÇÇ[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. ÇÇ[HK1_PKT_FORM_NO] EQ 7
0055 C. ÇÇ[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. ÇÇ[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. ÇÇ[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. ÇÇ[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. YÄYÖY×½ªí»ðð³íÇ§
0060 C. ÇÇ[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGðí½È¹Ç•è²íOKðð³íÇ§
0062 C.
0063 C. NMOG(0x210000-0x210FFF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. ÇÇ[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. ÇÇ[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. ÇÇ[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. ÇÇ[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. ÇÇ[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. ÇÇ[HK1_PKT_FORM_NO] EQ 7
0074 C. ÇÇ[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. ÇÇ[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. ÇÇ[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. ÇÇ[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. YÄYÖY×½ªí»ðð³íÇ§
0079 C. ÇÇ[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG,RAM ID=OPðí½È¹Ç•è²íOKðð³íÇ§
0081 C.
0082 . C. ***** öÈ²¼ðí½Ä´¶í°ðÈÈ-ðÄ÷¿@ (¼áμ-YÄYÖY×¼è½ÇððÄÖÄæÇ¼ªα°¼ð¿¼ì¹ÇðÇðä) *****
0083 C. DHUÝä;¼YÉ;È¼Y¼;YÍ;¼YÉ;Èððíäð¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. ÇÇ[HK1_PKT_FORM_NO] EQ 2
0087 C. ÇÇ[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. ÇÇ[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. ÇÇ[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;§ OPOG UPLOADð-Ä÷¿@NGðí½ì¹Ç;Ç°È²¼ðíTI-CMDÄ÷¿@ðí½Ä¹Öð•ðÈððð³ðÈ;£
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0096 C.                0300; SET0EDUMP0I00iYNY10C100|030E; E
0097 C.
0098 . C. TIY3YBY0YEB00ADi (UT)
0099 +. TI 2016-08-23 10:27:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                00[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0102 C.
0103 +. TI 2016-08-23 10:27:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                00[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0106 C.
0107 +. TI 2016-08-23 10:27:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                00[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0110 C.
0111 +. TI 2016-08-23 10:31:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                00[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0114 C.
0115 C.                0E2%0IAE%iIN0IYAY$YAY-1aIU
0116 C.                00[HK1_TI_CMD_ENA/DIS]            EQ        ENA
0117 C.                00[HK1_TI_CMD_NUM]              EQ        4
0118 C.                00[HK1_NEXT_EXEC_PIM]            EQ        DHU
0119 C.                00[HK1_NEXT_EXEC_DC]             EQ        0xB3
0120 C.
0121 . C. *****
0122 C. TI0I0EYAY0Yx
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF; $ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.                00[HK1_DMP_TOP_ADRS_1]            EQ        07
0129 C.                00[HK1_DMP_TOP_ADRS_0]            EQ        2B
0130 C.                00[HK1_DMP_BLOCK_NUM]            EQ        3
0131 C.                00[HK1_DMP_REPEAT_NUM]           EQ        0
0132 C.                00[HK1_DMA_DMP_PIM]             EQ        DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.                00[HK1_PKT_FORM_NO]              EQ        7
0136 C.                00[HK1_PKT_GEN_TIME]             EQ        0.25 s
0137 C.                00[HK1_S_TLM_BIT_RATE]           EQ        32k
0138 C.                00[HK1_X_TLM_BIT_RATE]          EQ        4M
0139 C.                00[HK1_DMP_CHK_FLG]             EQ        EXEC
0140 C.
0141 . C. YAY0Yx%aI»003IC$
0142 C.                00[HK1_DMP_CHK_FLG]             EQ        NON
0143 C.
0144 . C. RAM ID=TI_TBL0I%E1C.e2IOK003IC$
0145 C.
0146 . C. DHUYa;Y%E;E%Y%,Yi;Y%E;E00IA01
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.                00[HK1_PKT_FORM_NO]              EQ        2
0150 C.                00[HK1_PKT_GEN_TIME]             EQ        0.5S
0151 C.                00[HK1_S_TLM_BIT_RATE]           EQ        32K
0152 C.                00[HK1_X_TLM_BIT_RATE]          EQ        4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2016-08-23 10:31:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC (41)
0161 . C. -----
0162 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 . C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2016-08-23 10:31:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC (21 02)
0174 +. TI 2016-08-23 10:31:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC (22)
0177 . C. [ ] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2016-08-23 10:31:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC (c3)
0187 . C. [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 . C. ***** MDP 'uAI0I»0Y0EAD010EDCBC.x2e *****
0192 C. (%a0IY0YAYEYBYEYAYC%Y0E%0004A»U010E)
0193 . S. DC-BC dcbc-402:DCBC

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0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. ***** AOCs Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 + DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 2233991.5 +- 1.0 (s) [ ]
0136 C.
0137 . C.
0138 C.
0139 C. ***** XRT START *****
0140 C.
0141 +. DC 07-F0 MDP_XRT_CTRL_MANU
0142 BC (c1)
0143 +. DC 07-F0 MDP_XRT_CTRL_MANU
0144 BC (c1)
0145 + DC 07-F0 MDP_XRT_MODE_STBY
0146 BC (c3)
0147 . C. ----- Success Verify ? OK / NG_____
0148 C.
0149 C. XRT Obs. Table Upload
0150 . S. RAM ram-291:MDP_OBS_X
0151 ( )
0152 C.
0153 +. DC 07-F0 MDP_DUMP_XRTTBL
0154 BC (84 07 00 00 00 3a d4)
0155 . C. ----- Comparison Check ? OK / ERR _____
0156 C.
0157 C.
0158 +. DC 07-F0 MDP_XRT_ROI_SET
0159 BC (cd 01 b1 b1 04 04)
0160 + DC 07-F0 MDP_XRT_ROI_SET
0161 BC (cd 02 b1 b1 08 08)
0162 + DC 07-F0 MDP_XRT_ROI_SET
0163 BC (cd 03 b1 b1 08 08)
0164 + DC 07-F0 MDP_XRT_ROI_SET
0165 BC (cd 04 b1 b1 06 06)
0166 + DC 07-F0 MDP_XRT_ROI_SET
0167 BC (cd 05 85 83 06 06)
0168 + DC 07-F0 MDP_XRT_ROI_SET
0169 BC (cd 06 85 83 06 06)
0170 + DC 07-F0 MDP_XRT_ROI_SET
0171 BC (cd 07 85 83 08 08)
0172 + DC 07-F0 MDP_XRT_ROI_SET
0173 BC (cd 08 80 80 20 20)
0174 + DC 07-F0 MDP_XRT_ROI_SET
0175 BC (cd 09 80 80 20 08)
0176 + DC 07-F0 MDP_XRT_ROI_SET
0177 BC (cd 0a 80 80 08 20)
0178 + DC 07-F0 MDP_XRT_ROI_SET
0179 BC (cd 0f 80 80 06 06)
0180 + DC 07-F0 MDP_XRT_ROI_SET
0181 BC (cd 10 80 80 08 08)
0182 + DC 07-F0 MDP_XRT_FLD_ENA
0183 BC (d8)
0184 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0185 BC (c8)
0186 + DC 07-F0 MDP_XRT_ARS_DIS
0187 BC (d5)
0188 +. DC 07-F0 MDP_XRT_AEC_RESET
0189 BC (d0)
0190 +. DC 07-F0 MDP_XRT_FLD_RESET
0191 BC (da)
0192 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0193 BC (c4 08)

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0194 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0195 BC (c5 07)
0196 . C. ----- Success Verify ? OK / NG ____
0197 C.
0198 C.
0199 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0200 C.
0201 +. DC 07-F0 MDP_XRT_MODE_OBSV
0202 BC (c2)
0203 +. TI 2016-08-23 10:31:02.0
0204 DC 07-F0 MDP_XRT_MODE_OBSV
0205 BC (c2)
0206 . C. ----- Success Verify ? OK / NG ____
0207 C.
0208 C. ***** XRT END *****
0209 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0210 +. DC 07-FC EIS_MODE_CHG_ENA
0211 BC (20)
0212 . C. Verify EIS_MODE_CHG_FLG is ENA
0213 +. DC 07-FC EIS_MODE_MANU
0214 BC (21 02)
0215 . C. Verify EIS in MANUAL mode
0216 . C. Estimated OBSTBL upload time is 0ms
0217 C. *****
0218 C. EIS START OBSTBL LOAD
0219 C. *****
0220 . S. RAM ram-821:EIS_OBSTBL
0221 ( )
0222 +. DC 07-FC EIS_DUMP_OBSTBL
0223 BC (07 07 07 00 00 70 00)
0224 C.
0225 C. Execute, after the success of OBSTBL upload.
0226 C. Set EIS TI-commands
0227 +. TI 2016-08-23 10:31:50.0
0228 DC 07-FC EIS_MODE_CHG_ENA
0229 BC (20)
0230 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0231 C. *****
0232 C. EIS END OBSTBL LOAD
0233 C. *****
0234 C.
0235 . C. ***** MDP 'úÃîâî»ö¼ÝñÊÄðñ¹ñèDCBC•x²è *****
0236 C. (%á°îÝÖÝÄÝËÝÞÝËÝáÝçÝèñ¼¼ñ¼Ä»Ûñ¹ñè)
0237 . S. DC-BC dcbc-402:DCBC
0238 (MDP_known_event)
0239 C.
0240 C.
0241 . C. ***** ÝÐÝ¹•î Daily±¿îÑñÊ´Øñ¹ñèDCBC•x²è *****
0242 . S. DC-BC dcbc-153:DCBC
0243 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0244 C.
0245 C.
0246 . C. ;ãLOSÝÄÝ$ÝÄÝ¹¼Ä»Û;ä
0247 C.
0248 . C. ***** LOS *****
0249 C.

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0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop SP table >
0102 +. DC 07-F0 MDP_SP_CTRL_MANU
0103 BC (61)
0104 C. -----
0105 C. MDP_SP_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload SP Observation Table>
0109 . S. RAM ram-284:MDP_OBS_S
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_S >
0113 +. DC 07-F0 MDP_DUMP_SPTBL
0114 BC (83 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_S verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 C. *****
0120 C. SOT TI command set
0121 C. *****
0122 C. Execute, after the success of TBL upload.
0123 +. TI 2016-08-23 10:31:18.0
0124 DC 07-F0 MDP_SOT_MODE_OBSV
0125 BC (40)
0126 . C. -----
0127 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0128 C. -----
0129 C.
0130 C.
0131 . C. ***** MDP 'úÃîâî»ö¼ÝªÄâÐª¹ªèDCBC•x²è *****
0132 C. (%ã°ïÿÔŸÄŸÊŸŒŸËŸáŸ¢Ÿ£ªË¼ª¼Á»Ûª¹ªè)
0133 . S. DC-BC dcbc-402:DCBC
0134 (MDP_known_event)
0135 C.
0136 C.
0137 . C. ***** ŸÐŸ¹•ï Daily±¿îÑªÉ´Øª¹ªèDCBC•x²è *****
0138 . S. DC-BC dcbc-153:DCBC
0139 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0140 C.
0141 C.
0142 . C. ;ãLOSŸÁŸŸŸÄŸ⁻¼Á»Ûª¹ª
0143 C.
0144 . C. ***** LOS *****
0145 C.
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*** OP Sequence for XRT ***

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2016/08/23 10:41:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/08/23 10:41:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/08/23 10:41:58.0 XRT_FOCUS_POSITION_403_OG [0x193]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2016/08/23 10:42:00.5 AOCs_Ore-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 03 00 00 00 00
2016/08/23 10:42:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2016/08/23 10:42:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2016/08/23 10:42:22.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2016/08/23 10:42:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2016/08/23 10:42:26.0 XRT_FLD_RESET_433_OG [0x1b1]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2016/08/23 10:43:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/08/23 10:43:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/08/23 10:43:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2016/08/23 10:43:36.0 XRT_PREFLR_STRT_414_OG [0x19e]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2016/08/23 10:44:56.0 XRT_QT_PROG_SET_442_OG [0x1ba]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 12
2016/08/23 10:44:58.0 XRT_FL_PROG_SET_436_OG [0x1b4]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 07
2016/08/23 10:46:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2016/08/23 10:52:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/08/23 10:52:02.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/08/23 10:52:04.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2016/08/23 10:52:06.0 XRT_PREFLR_STRT_414_OG [0x19e]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2016/08/23 10:55:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2016/08/23 11:07:00.0 XRT_Custom_430_OG [0x1ae]
2016/08/23 11:08:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2016/08/23 12:30:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/08/23 12:30:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/08/23 12:30:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2016/08/23 12:30:36.0 XRT_PREFLR_STRT_414_OG [0x19e]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2016/08/23 12:33:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2016/08/23 12:45:00.0 XRT_Custom_430_OG [0x1ae]
2016/08/23 12:46:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2016/08/23 14:09:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/08/23 14:09:02.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/08/23 14:09:04.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2016/08/23 14:09:06.0 XRT_PREFLR_STRT_414_OG [0x19e]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2016/08/23 14:12:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2016/08/23 14:23:30.0 XRT_Custom_430_OG [0x1ae]
2016/08/23 14:24:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2016/08/23 14:30:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/08/23 14:30:02.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/08/23 14:30:04.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2016/08/23 14:30:06.0 XRT_PREFLR_STRT_414_OG [0x19e]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2016/08/23 14:33:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2016/08/23 14:38:30.0 XRT_Custom_430_OG [0x1ae]
2016/08/23 14:39:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2016/08/23 14:59:24.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/08/23 14:59:26.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/08/23 14:59:28.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2016/08/23 14:59:48.0 XRT_FLD_ENA_411_OG [0x19b]

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| | | | | | | | | | | |
|------------|------------|-----------------------------------|---------------------|---|-------|----------------|--|--|--|--|
| 2016/08/23 | 14:59:50.0 | XRT_FLRCTRL_ENA_412_OG [0x19c] | MDP_XRT_FLD_ENA | 1 | 07-F0 | d8 | | | | |
| | | | MDP_XRT_FLRCTRL_ENA | 1 | 07-F0 | c8 | | | | |
| 2016/08/23 | 14:59:52.0 | XRT_AEC_RESET_448_OG [0x1c0] | MDP_XRT_AEC_RESET | 1 | 07-F0 | d0 | | | | |
| 2016/08/23 | 14:59:54.0 | XRT_ARS_DIS_423_OG [0x1a7] | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 | | | | |
| 2016/08/23 | 14:59:56.0 | XRT_FLD_RESET_433_OG [0x1b1] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2016/08/23 | 15:00:00.0 | AOCS_Ore-point_Start_2_OG [0x098] | AOCU_NM | 5 | 02-76 | 01 00 00 00 00 | | | | |
| 2016/08/23 | 15:02:26.0 | XRT_QT_PROG_SET_434_OG [0x1b2] | MDP_XRT_QT_PROG_SET | 2 | 07-F0 | c4 08 | | | | |
| 2016/08/23 | 15:02:28.0 | XRT_FL_PROG_SET_436_OG [0x1b4] | MDP_XRT_FL_PROG_SET | 2 | 07-F0 | c5 07 | | | | |
| 2016/08/23 | 15:02:30.0 | XRT_CTRL_AUTO_408_OG [0x198] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2016/08/23 | 15:47:30.0 | XRT_CTRL_MANU_400_OG [0x190] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2016/08/23 | 15:47:32.0 | XRT_CTRL_MANU_402_OG [0x192] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2016/08/23 | 15:47:34.0 | XRT_FLD_RESET_415_OG [0x19f] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2016/08/23 | 15:47:36.0 | XRT_PREFLR_STRT_414_OG [0x19e] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | | |
| 2016/08/23 | 15:50:44.0 | XRT_PREFLR_STOP_419_OG [0x1a3] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | | |
| 2016/08/23 | 16:03:30.0 | XRT_CTRL_MANU_400_OG [0x190] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2016/08/23 | 16:03:32.0 | XRT_CTRL_MANU_402_OG [0x192] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2016/08/23 | 16:03:34.0 | XRT_FLD_RESET_415_OG [0x19f] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2016/08/23 | 16:03:36.0 | XRT_PREFLR_STRT_414_OG [0x19e] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | | |
| 2016/08/23 | 16:06:44.0 | XRT_PREFLR_STOP_419_OG [0x1a3] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | | |
| 2016/08/23 | 16:27:00.0 | XRT_Custom_430_OG [0x1ae] | | | | | | | | |
| 2016/08/23 | 16:28:00.0 | XRT_CTRL_AUTO_424_OG [0x1a8] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2016/08/23 | 17:25:30.0 | XRT_CTRL_MANU_400_OG [0x190] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2016/08/23 | 17:25:32.0 | XRT_CTRL_MANU_402_OG [0x192] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2016/08/23 | 17:25:34.0 | XRT_FLD_RESET_415_OG [0x19f] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2016/08/23 | 17:25:36.0 | XRT_PREFLR_STRT_414_OG [0x19e] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | | |
| 2016/08/23 | 17:28:44.0 | XRT_PREFLR_STOP_419_OG [0x1a3] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | | |
| 2016/08/23 | 18:03:54.0 | XRT_CTRL_MANU_402_OG [0x192] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2016/08/23 | 18:03:56.0 | XRT_CTRL_MANU_402_OG [0x192] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2016/08/23 | 18:03:58.0 | XRT_FOCUS_POSITION_403_OG [0x193] | XRT_FOCUS_POSITION | 4 | 07-F8 | 22 ff aa 00 | | | | |
| 2016/08/23 | 18:04:00.0 | AOCS_Ore-point_Start_3_OG [0x099] | AOCU_NM | 5 | 02-76 | 00 00 00 00 00 | | | | |
| 2016/08/23 | 18:04:18.0 | XRT_FLD_DIS_406_OG [0x196] | MDP_XRT_FLD_DIS | 1 | 07-F0 | d9 | | | | |
| 2016/08/23 | 18:06:54.0 | XRT_FLRCTRL_DIS_405_OG [0x195] | MDP_XRT_FLRCTRL_DIS | 1 | 07-F0 | c9 | | | | |
| 2016/08/23 | 18:06:56.0 | XRT_ARS_DIS_423_OG [0x1a7] | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 | | | | |
| 2016/08/23 | 18:06:58.0 | XRT_QT_PROG_SET_425_OG [0x1a9] | MDP_XRT_QT_PROG_SET | 2 | 07-F0 | c4 01 | | | | |
| 2016/08/23 | 18:07:00.0 | XRT_CTRL_AUTO_408_OG [0x198] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2016/08/23 | 18:13:54.0 | XRT_CTRL_MANU_402_OG [0x192] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2016/08/23 | 18:13:56.0 | XRT_CTRL_MANU_402_OG [0x192] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2016/08/23 | 18:13:58.0 | XRT_FOCUS_POSITION_410_OG [0x19a] | XRT_FOCUS_POSITION | 4 | 07-F8 | 22 fe 97 00 | | | | |
| 2016/08/23 | 18:14:00.0 | AOCS_Ore-point_Start_4_OG [0x09a] | AOCU_NM | 5 | 02-76 | 00 16 35 b1 cb | | | | |
| 2016/08/23 | 18:14:18.0 | XRT_FLD_ENA_411_OG [0x19b] | MDP_XRT_FLD_ENA | 1 | 07-F0 | d8 | | | | |
| 2016/08/23 | 18:14:20.0 | XRT_FLRCTRL_ENA_412_OG [0x19c] | MDP_XRT_FLRCTRL_ENA | 1 | 07-F0 | c8 | | | | |
| 2016/08/23 | 18:14:22.0 | XRT_AEC_RESET_448_OG [0x1c0] | MDP_XRT_AEC_RESET | 1 | 07-F0 | d0 | | | | |
| 2016/08/23 | 18:14:24.0 | XRT_ARS_DIS_423_OG [0x1a7] | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 | | | | |
| 2016/08/23 | 18:14:26.0 | XRT_FLD_RESET_433_OG [0x1b1] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2016/08/23 | 18:16:56.0 | XRT_QT_PROG_SET_431_OG [0x1af] | MDP_XRT_QT_PROG_SET | 2 | 07-F0 | c4 02 | | | | |
| 2016/08/23 | 18:16:58.0 | XRT_FL_PROG_SET_436_OG [0x1b4] | MDP_XRT_FL_PROG_SET | 2 | 07-F0 | c5 07 | | | | |
| 2016/08/23 | 18:17:00.0 | XRT_CTRL_AUTO_408_OG [0x198] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2016/08/23 | 19:04:00.0 | XRT_CTRL_MANU_400_OG [0x190] | | | | | | | | |

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|------------|------------|-----------------------------------|------------------------------|---|-------|-------------|--|--|
| 2016/08/23 | 19:04:02.0 | XRT_CTRL_MANU_402_OG [0x192] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | |
| 2016/08/23 | 19:04:04.0 | XRT_FLD_RESET_415_OG [0x19f] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | |
| 2016/08/23 | 19:04:06.0 | XRT_PREFLR_STRT_414_OG [0x19e] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | |
| 2016/08/23 | 19:07:14.0 | XRT_PREFLR_STOP_419_OG [0x1a3] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | |
| 2016/08/23 | 19:40:30.0 | XRT_Custom_430_OG [0x1ae] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | |
| 2016/08/23 | 19:41:30.0 | XRT_CTRL_AUTO_424_OG [0x1a8] | XRT_Custom_430_OG [0x1ae] | | | | | |
| 2016/08/23 | 20:42:30.0 | XRT_CTRL_MANU_400_OG [0x190] | XRT_CTRL_AUTO_424_OG [0x1a8] | | | | | |
| 2016/08/23 | 20:42:32.0 | XRT_CTRL_MANU_402_OG [0x192] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | |
| 2016/08/23 | 20:42:34.0 | XRT_FLD_RESET_415_OG [0x19f] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | |
| 2016/08/23 | 20:42:36.0 | XRT_PREFLR_STRT_414_OG [0x19e] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | |
| 2016/08/23 | 20:45:44.0 | XRT_PREFLR_STOP_419_OG [0x1a3] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | |
| 2016/08/23 | 21:18:00.0 | XRT_Custom_430_OG [0x1ae] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | |
| 2016/08/23 | 21:19:00.0 | XRT_CTRL_AUTO_424_OG [0x1a8] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | |
| 2016/08/23 | 22:21:00.0 | XRT_CTRL_MANU_400_OG [0x190] | XRT_CTRL_AUTO_424_OG [0x1a8] | | | | | |
| 2016/08/23 | 22:21:02.0 | XRT_CTRL_MANU_402_OG [0x192] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | |
| 2016/08/23 | 22:21:04.0 | XRT_FLD_RESET_415_OG [0x19f] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | |
| 2016/08/23 | 22:21:06.0 | XRT_PREFLR_STRT_414_OG [0x19e] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | |
| 2016/08/23 | 22:24:14.0 | XRT_PREFLR_STOP_419_OG [0x1a3] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | |
| 2016/08/23 | 22:53:00.0 | XRT_Custom_430_OG [0x1ae] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | |
| 2016/08/23 | 22:54:00.0 | XRT_CTRL_AUTO_424_OG [0x1a8] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | |
| 2016/08/23 | 23:59:24.0 | XRT_CTRL_MANU_402_OG [0x192] | XRT_Custom_430_OG [0x1ae] | | | | | |
| 2016/08/23 | 23:59:26.0 | XRT_CTRL_MANU_402_OG [0x192] | XRT_CTRL_AUTO_424_OG [0x1a8] | | | | | |
| 2016/08/23 | 23:59:28.0 | XRT_FOCUS_POSITION_410_OG [0x19a] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | |
| 2016/08/23 | 23:59:30.0 | XRT_CTRL_MANU_400_OG [0x190] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | |
| 2016/08/23 | 23:59:32.0 | XRT_CTRL_MANU_402_OG [0x192] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | |
| 2016/08/23 | 23:59:34.0 | XRT_FLD_RESET_415_OG [0x19f] | XRT_FOCUS_POSITION | 4 | 07-F8 | 22 fe 97 00 | | |
| 2016/08/23 | 23:59:36.0 | XRT_PREFLR_STRT_414_OG [0x19e] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | |
| 2016/08/23 | 23:59:48.0 | XRT_FLD_ENA_411_OG [0x19b] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | |
| 2016/08/23 | 23:59:50.0 | XRT_FLRCTRL_ENA_412_OG [0x19c] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | |
| 2016/08/23 | 23:59:52.0 | XRT_AEC_RESET_448_OG [0x1c0] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | |
| 2016/08/23 | 23:59:54.0 | XRT_ARS_DIS_423_OG [0x1a7] | MDP_XRT_FLD_ENA | 1 | 07-F0 | d8 | | |
| 2016/08/23 | 23:59:56.0 | XRT_FLD_RESET_433_OG [0x1b1] | MDP_XRT_FLRCTRL_ENA | 1 | 07-F0 | c8 | | |
| 2016/08/24 | 00:00:00.0 | AOCS_Orе-point_Start_2_OG [0x098] | MDP_XRT_AEC_RESET | 1 | 07-F0 | d0 | | |
| 2016/08/24 | 00:02:26.0 | XRT_QT_PROG_SET_434_OG [0x1b2] | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 | | |
| 2016/08/24 | 00:02:28.0 | XRT_FL_PROG_SET_436_OG [0x1b4] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | |
| 2016/08/24 | 00:02:44.0 | XRT_PREFLR_STOP_419_OG [0x1a3] | MDP_XRT_FLD_ENA | 1 | 07-F0 | d8 | | |
| 2016/08/24 | 00:18:00.0 | XRT_Custom_430_OG [0x1ae] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | |
| 2016/08/24 | 00:19:00.0 | XRT_CTRL_AUTO_424_OG [0x1a8] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | |
| 2016/08/24 | 01:33:30.0 | XRT_CTRL_MANU_400_OG [0x190] | XRT_Custom_430_OG [0x1ae] | | | | | |
| 2016/08/24 | 01:33:32.0 | XRT_CTRL_MANU_402_OG [0x192] | XRT_CTRL_AUTO_424_OG [0x1a8] | | | | | |
| 2016/08/24 | 01:33:34.0 | XRT_FLD_RESET_415_OG [0x19f] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | |
| 2016/08/24 | 01:33:36.0 | XRT_PREFLR_STRT_414_OG [0x19e] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | |
| 2016/08/24 | 01:36:44.0 | XRT_PREFLR_STOP_419_OG [0x1a3] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | |
| 2016/08/24 | 01:52:00.0 | XRT_Custom_430_OG [0x1ae] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | |
| 2016/08/24 | 01:53:00.0 | XRT_CTRL_AUTO_424_OG [0x1a8] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | |
| 2016/08/24 | 03:07:30.0 | XRT_CTRL_MANU_400_OG [0x190] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | |
| 2016/08/24 | 03:07:32.0 | XRT_CTRL_MANU_402_OG [0x192] | XRT_Custom_430_OG [0x1ae] | | | | | |
| 2016/08/24 | 03:07:34.0 | XRT_FLD_RESET_415_OG [0x19f] | XRT_CTRL_AUTO_424_OG [0x1a8] | | | | | |
| 2016/08/24 | 03:07:36.0 | XRT_PREFLR_STRT_414_OG [0x19e] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | |
| | | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | |
| | | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | |
| | | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | |
| | | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | |

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|------------|------------|-----------------------------------|---------------------|---|-------|----------------|--|
| 2016/08/24 | 03:10:44.0 | XRT_PREFLR_STOP_419_OG [0x1a3] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | |
| | | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | |
| 2016/08/24 | 03:30:00.0 | XRT_Custom_430_OG [0x1ae] | | | | | |
| 2016/08/24 | 03:31:00.0 | XRT_CTRL_AUTO_424_OG [0x1a8] | | | | | |
| | | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | |
| 2016/08/24 | 04:37:30.0 | XRT_CTRL_MANU_400_OG [0x190] | | | | | |
| | | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | |
| 2016/08/24 | 04:37:32.0 | XRT_CTRL_MANU_402_OG [0x192] | | | | | |
| | | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | |
| 2016/08/24 | 04:37:34.0 | XRT_FLD_RESET_415_OG [0x19f] | | | | | |
| | | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | |
| 2016/08/24 | 04:37:36.0 | XRT_PREFLR_STRT_414_OG [0x19e] | | | | | |
| | | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | |
| 2016/08/24 | 04:40:44.0 | XRT_PREFLR_STOP_419_OG [0x1a3] | | | | | |
| | | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | |
| 2016/08/24 | 05:08:30.0 | XRT_Custom_430_OG [0x1ae] | | | | | |
| 2016/08/24 | 05:09:30.0 | XRT_CTRL_AUTO_424_OG [0x1a8] | | | | | |
| | | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | |
| 2016/08/24 | 05:49:54.0 | XRT_CTRL_MANU_402_OG [0x192] | | | | | |
| | | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | |
| 2016/08/24 | 05:49:56.0 | XRT_CTRL_MANU_402_OG [0x192] | | | | | |
| | | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | |
| 2016/08/24 | 05:49:58.0 | XRT_FOCUS_POSITION_403_OG [0x193] | | | | | |
| | | | XRT_FOCUS_POSITION | 4 | 07-F8 | 22 ff aa 00 | |
| 2016/08/24 | 05:50:00.0 | AOCS_Ore-point_Start_3_OG [0x099] | | | | | |
| | | | AOCU_NM | 5 | 02-76 | 00 00 00 00 00 | |
| 2016/08/24 | 05:50:18.0 | XRT_FLD_DIS_406_OG [0x196] | | | | | |
| | | | MDP_XRT_FLD_DIS | 1 | 07-F0 | d9 | |
| 2016/08/24 | 05:52:54.0 | XRT_FLRCTRL_DIS_405_OG [0x195] | | | | | |
| | | | MDP_XRT_FLRCTRL_DIS | 1 | 07-F0 | c9 | |
| 2016/08/24 | 05:52:56.0 | XRT_ARS_DIS_423_OG [0x1a7] | | | | | |
| | | | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 | |
| 2016/08/24 | 05:52:58.0 | XRT_QT_PROG_SET_425_OG [0x1a9] | | | | | |
| | | | MDP_XRT_QT_PROG_SET | 2 | 07-F0 | c4 01 | |
| 2016/08/24 | 05:53:00.0 | XRT_CTRL_AUTO_408_OG [0x198] | | | | | |
| | | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | |
| 2016/08/24 | 05:59:24.0 | XRT_CTRL_MANU_402_OG [0x192] | | | | | |
| | | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | |
| 2016/08/24 | 05:59:26.0 | XRT_CTRL_MANU_402_OG [0x192] | | | | | |
| | | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | |
| 2016/08/24 | 05:59:28.0 | XRT_FOCUS_POSITION_410_OG [0x19a] | | | | | |
| | | | XRT_FOCUS_POSITION | 4 | 07-F8 | 22 fe 97 00 | |
| 2016/08/24 | 05:59:48.0 | XRT_FLD_ENA_411_OG [0x19b] | | | | | |
| | | | MDP_XRT_FLD_ENA | 1 | 07-F0 | d8 | |
| 2016/08/24 | 05:59:50.0 | XRT_FLRCTRL_ENA_412_OG [0x19c] | | | | | |
| | | | MDP_XRT_FLRCTRL_ENA | 1 | 07-F0 | c8 | |
| 2016/08/24 | 05:59:52.0 | XRT_AEC_RESET_448_OG [0x1c0] | | | | | |
| | | | MDP_XRT_AEC_RESET | 1 | 07-F0 | d0 | |
| 2016/08/24 | 05:59:54.0 | XRT_ARS_DIS_423_OG [0x1a7] | | | | | |
| | | | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 | |
| 2016/08/24 | 05:59:56.0 | XRT_FLD_RESET_433_OG [0x1b1] | | | | | |
| | | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | |
| 2016/08/24 | 06:00:00.0 | AOCS_Ore-point_Start_2_OG [0x098] | | | | | |
| | | | AOCU_NM | 5 | 02-76 | 01 00 00 00 00 | |
| 2016/08/24 | 06:02:26.0 | XRT_QT_PROG_SET_434_OG [0x1b2] | | | | | |
| | | | MDP_XRT_QT_PROG_SET | 2 | 07-F0 | c4 08 | |
| 2016/08/24 | 06:02:28.0 | XRT_FL_PROG_SET_436_OG [0x1b4] | | | | | |
| | | | MDP_XRT_FL_PROG_SET | 2 | 07-F0 | c5 07 | |
| 2016/08/24 | 06:02:30.0 | XRT_CTRL_AUTO_408_OG [0x198] | | | | | |
| | | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | |
| 2016/08/24 | 06:17:30.0 | XRT_CTRL_MANU_400_OG [0x190] | | | | | |
| | | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | |
| 2016/08/24 | 06:17:32.0 | XRT_CTRL_MANU_402_OG [0x192] | | | | | |
| | | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | |
| 2016/08/24 | 06:17:34.0 | XRT_FLD_RESET_415_OG [0x19f] | | | | | |
| | | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | |
| 2016/08/24 | 06:17:36.0 | XRT_PREFLR_STRT_414_OG [0x19e] | | | | | |
| | | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | |
| 2016/08/24 | 06:20:44.0 | XRT_PREFLR_STOP_419_OG [0x1a3] | | | | | |
| | | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | |
| 2016/08/24 | 06:46:30.0 | XRT_Custom_430_OG [0x1ae] | | | | | |
| 2016/08/24 | 06:47:30.0 | XRT_CTRL_AUTO_424_OG [0x1a8] | | | | | |
| | | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | |
| 2016/08/24 | 07:58:00.0 | XRT_CTRL_MANU_400_OG [0x190] | | | | | |
| | | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | |
| 2016/08/24 | 07:58:02.0 | XRT_CTRL_MANU_402_OG [0x192] | | | | | |
| | | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | |
| 2016/08/24 | 07:58:04.0 | XRT_FLD_RESET_415_OG [0x19f] | | | | | |
| | | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | |
| 2016/08/24 | 07:58:06.0 | XRT_PREFLR_STRT_414_OG [0x19e] | | | | | |
| | | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | |
| 2016/08/24 | 08:01:14.0 | XRT_PREFLR_STOP_419_OG [0x1a3] | | | | | |
| | | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | |
| 2016/08/24 | 08:25:00.0 | XRT_Custom_430_OG [0x1ae] | | | | | |
| 2016/08/24 | 08:26:00.0 | XRT_CTRL_AUTO_424_OG [0x1a8] | | | | | |
| | | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | |
| 2016/08/24 | 09:37:30.0 | XRT_CTRL_MANU_400_OG [0x190] | | | | | |
| | | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | |
| 2016/08/24 | 09:37:32.0 | XRT_CTRL_MANU_402_OG [0x192] | | | | | |
| | | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | |
| 2016/08/24 | 09:37:34.0 | XRT_FLD_RESET_415_OG [0x19f] | | | | | |
| | | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | |

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|------------|------------|-----------------------------------|---|-------|----------------|
| 2016/08/24 | 09:37:36.0 | XRT_PREFLR_STRT_414_OG [0x19e] | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 |
| 2016/08/24 | 09:40:44.0 | XRT_PREFLR_STOP_419_OG [0x1a3] | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 |
| 2016/08/24 | 10:03:30.0 | XRT_Custom_430_OG [0x1ae] | | | |
| 2016/08/24 | 10:04:30.0 | XRT_CTRL_AUTO_424_OG [0x1a8] | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |
| 2016/08/24 | 11:28:00.0 | XRT_CTRL_MANU_400_OG [0x190] | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2016/08/24 | 11:28:02.0 | XRT_CTRL_MANU_402_OG [0x192] | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2016/08/24 | 11:28:04.0 | XRT_FLD_RESET_415_OG [0x19f] | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da |
| 2016/08/24 | 11:28:06.0 | XRT_PREFLR_STRT_414_OG [0x19e] | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 |
| 2016/08/24 | 11:31:14.0 | XRT_PREFLR_STOP_419_OG [0x1a3] | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 |
| 2016/08/24 | 11:41:30.0 | XRT_Custom_430_OG [0x1ae] | | | |
| 2016/08/24 | 11:42:30.0 | XRT_CTRL_AUTO_424_OG [0x1a8] | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |
| 2016/08/24 | 13:06:30.0 | XRT_CTRL_MANU_400_OG [0x190] | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2016/08/24 | 13:06:32.0 | XRT_CTRL_MANU_402_OG [0x192] | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2016/08/24 | 13:06:34.0 | XRT_FLD_RESET_415_OG [0x19f] | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da |
| 2016/08/24 | 13:06:36.0 | XRT_PREFLR_STRT_414_OG [0x19e] | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 |
| 2016/08/24 | 13:09:44.0 | XRT_PREFLR_STOP_419_OG [0x1a3] | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 |
| 2016/08/24 | 13:20:00.0 | XRT_Custom_430_OG [0x1ae] | | | |
| 2016/08/24 | 13:21:00.0 | XRT_CTRL_AUTO_424_OG [0x1a8] | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |
| 2016/08/24 | 14:45:00.0 | XRT_CTRL_MANU_400_OG [0x190] | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2016/08/24 | 14:45:02.0 | XRT_CTRL_MANU_402_OG [0x192] | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2016/08/24 | 14:45:04.0 | XRT_FLD_RESET_415_OG [0x19f] | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da |
| 2016/08/24 | 14:45:06.0 | XRT_PREFLR_STRT_414_OG [0x19e] | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 |
| 2016/08/24 | 14:48:14.0 | XRT_PREFLR_STOP_419_OG [0x1a3] | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 |
| 2016/08/24 | 14:58:30.0 | XRT_Custom_430_OG [0x1ae] | | | |
| 2016/08/24 | 14:59:30.0 | XRT_CTRL_AUTO_424_OG [0x1a8] | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |
| 2016/08/24 | 15:03:00.0 | XRT_CTRL_MANU_400_OG [0x190] | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2016/08/24 | 15:03:02.0 | XRT_CTRL_MANU_402_OG [0x192] | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2016/08/24 | 15:03:04.0 | XRT_FLD_RESET_415_OG [0x19f] | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da |
| 2016/08/24 | 15:03:06.0 | XRT_PREFLR_STRT_414_OG [0x19e] | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 |
| 2016/08/24 | 15:06:14.0 | XRT_PREFLR_STOP_419_OG [0x1a3] | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 |
| 2016/08/24 | 15:15:30.0 | XRT_Custom_430_OG [0x1ae] | | | |
| 2016/08/24 | 15:16:30.0 | XRT_CTRL_AUTO_424_OG [0x1a8] | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |
| 2016/08/24 | 16:23:30.0 | XRT_CTRL_MANU_400_OG [0x190] | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2016/08/24 | 16:23:32.0 | XRT_CTRL_MANU_402_OG [0x192] | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2016/08/24 | 16:23:34.0 | XRT_FLD_RESET_415_OG [0x19f] | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da |
| 2016/08/24 | 16:23:36.0 | XRT_PREFLR_STRT_414_OG [0x19e] | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 |
| 2016/08/24 | 16:26:44.0 | XRT_PREFLR_STOP_419_OG [0x1a3] | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 |
| 2016/08/24 | 16:36:30.0 | XRT_Custom_430_OG [0x1ae] | | | |
| 2016/08/24 | 16:37:30.0 | XRT_CTRL_AUTO_424_OG [0x1a8] | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |
| 2016/08/24 | 16:38:30.0 | XRT_CTRL_MANU_400_OG [0x190] | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2016/08/24 | 16:38:32.0 | XRT_CTRL_MANU_402_OG [0x192] | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2016/08/24 | 16:38:34.0 | XRT_FLD_RESET_415_OG [0x19f] | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da |
| 2016/08/24 | 16:38:36.0 | XRT_PREFLR_STRT_414_OG [0x19e] | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 |
| 2016/08/24 | 16:41:44.0 | XRT_PREFLR_STOP_419_OG [0x1a3] | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 |
| 2016/08/24 | 17:02:00.0 | XRT_Custom_430_OG [0x1ae] | | | |
| 2016/08/24 | 17:03:00.0 | XRT_CTRL_AUTO_424_OG [0x1a8] | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |
| 2016/08/24 | 17:51:54.0 | XRT_CTRL_MANU_402_OG [0x192] | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2016/08/24 | 17:51:56.0 | XRT_CTRL_MANU_402_OG [0x192] | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2016/08/24 | 17:51:58.0 | XRT_FOCUS_POSITION_403_OG [0x193] | | | |
| | | XRT_FOCUS_POSITION | 4 | 07-F8 | 22 ff aa 00 |
| 2016/08/24 | 17:52:00.0 | AOCS_ORe-point_Start_3_OG [0x099] | | | |
| | | AOCU_NM | 5 | 02-76 | 00 00 00 00 00 |

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|------------|------------|-----------------------------------|---|-------|----------------|
| 2016/08/24 | 17:52:18.0 | XRT_FLD_DIS_406_OG [0x196] | | | |
| | | MDP_XRT_FLD_DIS | 1 | 07-F0 | d9 |
| 2016/08/24 | 17:54:54.0 | XRT_FLRCTRL_DIS_405_OG [0x195] | | | |
| | | MDP_XRT_FLRCTRL_DIS | 1 | 07-F0 | c9 |
| 2016/08/24 | 17:54:56.0 | XRT_ARS_DIS_423_OG [0x1a7] | | | |
| | | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 |
| 2016/08/24 | 17:54:58.0 | XRT_QT_PROG_SET_425_OG [0x1a9] | | | |
| | | MDP_XRT_QT_PROG_SET | 2 | 07-F0 | c4 01 |
| 2016/08/24 | 17:55:00.0 | XRT_CTRL_AUTO_408_OG [0x198] | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |
| 2016/08/24 | 18:01:54.0 | XRT_CTRL_MANU_402_OG [0x192] | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2016/08/24 | 18:01:56.0 | XRT_CTRL_MANU_402_OG [0x192] | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2016/08/24 | 18:01:58.0 | XRT_FOCUS_POSITION_410_OG [0x19a] | | | |
| | | XRT_FOCUS_POSITION | 4 | 07-F8 | 22 fe 97 00 |
| 2016/08/24 | 18:02:00.0 | AOCS_OrE-point_Start_4_OG [0x09a] | | | |
| | | AOCU_NM | 5 | 02-76 | 00 16 35 b1 cb |
| 2016/08/24 | 18:02:00.5 | XRT_CTRL_MANU_400_OG [0x190] | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2016/08/24 | 18:02:02.5 | XRT_CTRL_MANU_402_OG [0x192] | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2016/08/24 | 18:02:04.5 | XRT_FLD_RESET_415_OG [0x19f] | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da |
| 2016/08/24 | 18:02:06.5 | XRT_PREFLR_STRT_414_OG [0x19e] | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 |
| 2016/08/24 | 18:02:18.0 | XRT_FLD_ENA_411_OG [0x19b] | | | |
| | | MDP_XRT_FLD_ENA | 1 | 07-F0 | d8 |
| 2016/08/24 | 18:02:20.0 | XRT_FLRCTRL_ENA_412_OG [0x19c] | | | |
| | | MDP_XRT_FLRCTRL_ENA | 1 | 07-F0 | c8 |
| 2016/08/24 | 18:02:22.0 | XRT_AEC_RESET_448_OG [0x1c0] | | | |
| | | MDP_XRT_AEC_RESET | 1 | 07-F0 | d0 |
| 2016/08/24 | 18:02:24.0 | XRT_ARS_DIS_423_OG [0x1a7] | | | |
| | | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 |
| 2016/08/24 | 18:02:26.0 | XRT_FLD_RESET_433_OG [0x1b1] | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da |
| 2016/08/24 | 18:04:56.0 | XRT_QT_PROG_SET_431_OG [0x1af] | | | |
| | | MDP_XRT_QT_PROG_SET | 2 | 07-F0 | c4 02 |
| 2016/08/24 | 18:04:58.0 | XRT_FL_PROG_SET_436_OG [0x1b4] | | | |
| | | MDP_XRT_FL_PROG_SET | 2 | 07-F0 | c5 07 |
| 2016/08/24 | 18:05:14.5 | XRT_PREFLR_STOP_419_OG [0x1a3] | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 |
| 2016/08/24 | 18:15:30.0 | XRT_CTRL_MANU_400_OG [0x190] | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2016/08/24 | 18:15:32.0 | XRT_CTRL_MANU_402_OG [0x192] | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2016/08/24 | 18:15:34.0 | XRT_FLD_RESET_415_OG [0x19f] | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da |
| 2016/08/24 | 18:15:36.0 | XRT_PREFLR_STRT_414_OG [0x19e] | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 |
| 2016/08/24 | 18:18:44.0 | XRT_PREFLR_STOP_419_OG [0x1a3] | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 |
| 2016/08/24 | 18:39:00.0 | XRT_Custom_430_OG [0x1ae] | | | |
| 2016/08/24 | 18:40:00.0 | XRT_CTRL_AUTO_424_OG [0x1a8] | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |
| 2016/08/24 | 19:40:30.0 | XRT_CTRL_MANU_400_OG [0x190] | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2016/08/24 | 19:40:32.0 | XRT_CTRL_MANU_402_OG [0x192] | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2016/08/24 | 19:40:34.0 | XRT_FLD_RESET_415_OG [0x19f] | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da |
| 2016/08/24 | 19:40:36.0 | XRT_PREFLR_STRT_414_OG [0x19e] | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 |
| 2016/08/24 | 19:43:44.0 | XRT_PREFLR_STOP_419_OG [0x1a3] | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 |
| 2016/08/24 | 20:15:30.0 | XRT_Custom_430_OG [0x1ae] | | | |
| 2016/08/24 | 20:16:30.0 | XRT_CTRL_AUTO_424_OG [0x1a8] | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |
| 2016/08/24 | 21:18:30.5 | XRT_CTRL_MANU_400_OG [0x190] | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2016/08/24 | 21:18:32.5 | XRT_CTRL_MANU_402_OG [0x192] | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2016/08/24 | 21:18:34.5 | XRT_FLD_RESET_415_OG [0x19f] | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da |
| 2016/08/24 | 21:18:36.5 | XRT_PREFLR_STRT_414_OG [0x19e] | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 |
| 2016/08/24 | 21:21:44.5 | XRT_PREFLR_STOP_419_OG [0x1a3] | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 |
| 2016/08/24 | 21:52:30.0 | XRT_Custom_430_OG [0x1ae] | | | |
| 2016/08/24 | 21:53:30.0 | XRT_CTRL_AUTO_424_OG [0x1a8] | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 |
| 2016/08/24 | 22:57:00.0 | XRT_CTRL_MANU_400_OG [0x190] | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2016/08/24 | 22:57:02.0 | XRT_CTRL_MANU_402_OG [0x192] | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 |
| 2016/08/24 | 22:57:04.0 | XRT_FLD_RESET_415_OG [0x19f] | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da |
| 2016/08/24 | 22:57:06.0 | XRT_PREFLR_STRT_414_OG [0x19e] | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 |
| 2016/08/24 | 23:00:14.0 | XRT_PREFLR_STOP_419_OG [0x1a3] | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 |
| 2016/08/24 | 23:26:00.0 | XRT_Custom_430_OG [0x1ae] | | | |
| 2016/08/24 | 23:27:00.0 | XRT_CTRL_AUTO_424_OG [0x1a8] | | | |

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|------------|------------|-----------------------------------|---------------------|---|-------|----|----|----|----|
| 2016/08/24 | 23:59:24.0 | XRT_CTRL_MANU_402_OG [0x192] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | |
| | | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | |
| 2016/08/24 | 23:59:26.0 | XRT_CTRL_MANU_402_OG [0x192] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | |
| 2016/08/24 | 23:59:28.0 | XRT_FOCUS_POSITION_410_OG [0x19a] | XRT_FOCUS_POSITION | 4 | 07-F8 | 22 | fe | 97 | 00 |
| 2016/08/24 | 23:59:48.0 | XRT_FLD_ENA_411_OG [0x19b] | MDP_XRT_FLD_ENA | 1 | 07-F0 | d8 | | | |
| 2016/08/24 | 23:59:50.0 | XRT_FLRCTRL_ENA_412_OG [0x19c] | MDP_XRT_FLRCTRL_ENA | 1 | 07-F0 | c8 | | | |
| 2016/08/24 | 23:59:52.0 | XRT_AEC_RESET_448_OG [0x1c0] | MDP_XRT_AEC_RESET | 1 | 07-F0 | d0 | | | |
| 2016/08/24 | 23:59:54.0 | XRT_ARS_DIS_423_OG [0x1a7] | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 | | | |
| 2016/08/24 | 23:59:56.0 | XRT_FLD_RESET_433_OG [0x1b1] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | |
| 2016/08/25 | 00:00:00.0 | AOCs_OrE-point_Start_2_OG [0x098] | AOCU_NM | 5 | 02-76 | 01 | 00 | 00 | 00 |
| 2016/08/25 | 00:02:26.0 | XRT_QT_PROG_SET_434_OG [0x1b2] | MDP_XRT_QT_PROG_SET | 2 | 07-F0 | c4 | 08 | | |
| 2016/08/25 | 00:02:28.0 | XRT_FL_PROG_SET_436_OG [0x1b4] | MDP_XRT_FL_PROG_SET | 2 | 07-F0 | c5 | 07 | | |
| 2016/08/25 | 00:02:30.0 | XRT_CTRL_AUTO_408_OG [0x198] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | |
| 2016/08/25 | 00:35:30.0 | XRT_CTRL_MANU_400_OG [0x190] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | |
| 2016/08/25 | 00:35:32.0 | XRT_CTRL_MANU_402_OG [0x192] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | |
| 2016/08/25 | 00:35:34.0 | XRT_FLD_RESET_415_OG [0x19f] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | |
| 2016/08/25 | 00:35:36.0 | XRT_PREFLR_STRT_414_OG [0x19e] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | |
| 2016/08/25 | 00:38:44.0 | XRT_PREFLR_STOP_419_OG [0x1a3] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | |
| 2016/08/25 | 00:51:00.0 | XRT_Custom_430_OG [0x1ae] | | | | | | | |
| 2016/08/25 | 00:52:00.0 | XRT_CTRL_AUTO_424_OG [0x1a8] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | |
| 2016/08/25 | 02:06:30.0 | XRT_CTRL_MANU_400_OG [0x190] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | |
| 2016/08/25 | 02:06:32.0 | XRT_CTRL_MANU_402_OG [0x192] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | |
| 2016/08/25 | 02:06:34.0 | XRT_FLD_RESET_415_OG [0x19f] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | |
| 2016/08/25 | 02:06:36.0 | XRT_PREFLR_STRT_414_OG [0x19e] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | |
| 2016/08/25 | 02:09:44.0 | XRT_PREFLR_STOP_419_OG [0x1a3] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | |
| 2016/08/25 | 02:26:30.0 | XRT_Custom_430_OG [0x1ae] | | | | | | | |
| 2016/08/25 | 02:27:30.0 | XRT_CTRL_AUTO_424_OG [0x1a8] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | |
| 2016/08/25 | 03:39:00.0 | XRT_CTRL_MANU_400_OG [0x190] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | |
| 2016/08/25 | 03:39:02.0 | XRT_CTRL_MANU_402_OG [0x192] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | |
| 2016/08/25 | 03:39:04.0 | XRT_FLD_RESET_415_OG [0x19f] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | |
| 2016/08/25 | 03:39:06.0 | XRT_PREFLR_STRT_414_OG [0x19e] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | |
| 2016/08/25 | 03:42:14.0 | XRT_PREFLR_STOP_419_OG [0x1a3] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | |
| 2016/08/25 | 04:05:00.0 | XRT_Custom_430_OG [0x1ae] | | | | | | | |
| 2016/08/25 | 04:06:00.0 | XRT_CTRL_AUTO_424_OG [0x1a8] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | |
| 2016/08/25 | 05:13:30.0 | XRT_CTRL_MANU_400_OG [0x190] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | |
| 2016/08/25 | 05:13:32.0 | XRT_CTRL_MANU_402_OG [0x192] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | |
| 2016/08/25 | 05:13:34.0 | XRT_FLD_RESET_415_OG [0x19f] | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | |
| 2016/08/25 | 05:13:36.0 | XRT_PREFLR_STRT_414_OG [0x19e] | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | |
| 2016/08/25 | 05:16:44.0 | XRT_PREFLR_STOP_419_OG [0x1a3] | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | |
| 2016/08/25 | 05:43:30.0 | XRT_Custom_430_OG [0x1ae] | | | | | | | |
| 2016/08/25 | 05:44:30.0 | XRT_CTRL_AUTO_424_OG [0x1a8] | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | |
| 2016/08/25 | 05:59:54.0 | XRT_CTRL_MANU_402_OG [0x192] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | |
| 2016/08/25 | 05:59:56.0 | XRT_CTRL_MANU_402_OG [0x192] | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | |
| 2016/08/25 | 05:59:58.0 | XRT_FOCUS_POSITION_403_OG [0x193] | XRT_FOCUS_POSITION | 4 | 07-F8 | 22 | ff | aa | 00 |
| 2016/08/25 | 06:00:00.0 | AOCs_OrE-point_Start_3_OG [0x099] | AOCU_NM | 5 | 02-76 | 00 | 00 | 00 | 00 |
| 2016/08/25 | 06:00:18.0 | XRT_FLD_DIS_406_OG [0x196] | MDP_XRT_FLD_DIS | 1 | 07-F0 | d9 | | | |
| 2016/08/25 | 06:02:54.0 | XRT_FLRCTRL_DIS_405_OG [0x195] | MDP_XRT_FLRCTRL_DIS | 1 | 07-F0 | c9 | | | |
| 2016/08/25 | 06:02:56.0 | XRT_ARS_DIS_423_OG [0x1a7] | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 | | | |
| 2016/08/25 | 06:02:58.0 | XRT_QT_PROG_SET_425_OG [0x1a9] | MDP_XRT_QT_PROG_SET | 2 | 07-F0 | c4 | 01 | | |

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|------------|------------|-----------------------------------|---|-------|----------------|--|--|--|--|
| 2016/08/25 | 06:03:00.0 | XRT_CTRL_AUTO_408_OG [0x198] | | | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2016/08/25 | 06:09:24.0 | XRT_CTRL_MANU_402_OG [0x192] | | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2016/08/25 | 06:09:26.0 | XRT_CTRL_MANU_402_OG [0x192] | | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2016/08/25 | 06:09:28.0 | XRT_FOCUS_POSITION_410_OG [0x19a] | | | | | | | |
| | | XRT_FOCUS_POSITION | 4 | 07-F8 | 22 fe 97 00 | | | | |
| 2016/08/25 | 06:09:48.0 | XRT_FLD_ENA_411_OG [0x19b] | | | | | | | |
| | | MDP_XRT_FLD_ENA | 1 | 07-F0 | d8 | | | | |
| 2016/08/25 | 06:09:50.0 | XRT_FLRCTRL_ENA_412_OG [0x19c] | | | | | | | |
| | | MDP_XRT_FLRCTRL_ENA | 1 | 07-F0 | c8 | | | | |
| 2016/08/25 | 06:09:52.0 | XRT_AEC_RESET_448_OG [0x1c0] | | | | | | | |
| | | MDP_XRT_AEC_RESET | 1 | 07-F0 | d0 | | | | |
| 2016/08/25 | 06:09:54.0 | XRT_ARS_DIS_423_OG [0x1a7] | | | | | | | |
| | | MDP_XRT_ARS_DIS | 1 | 07-F0 | d5 | | | | |
| 2016/08/25 | 06:09:56.0 | XRT_FLD_RESET_433_OG [0x1b1] | | | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2016/08/25 | 06:10:00.0 | AOCS_ORe-point_Start_2_OG [0x098] | | | | | | | |
| | | AOCU_NM | 5 | 02-76 | 01 00 00 00 00 | | | | |
| 2016/08/25 | 06:12:26.0 | XRT_QT_PROG_SET_434_OG [0x1b2] | | | | | | | |
| | | MDP_XRT_QT_PROG_SET | 2 | 07-F0 | c4 08 | | | | |
| 2016/08/25 | 06:12:28.0 | XRT_FL_PROG_SET_436_OG [0x1b4] | | | | | | | |
| | | MDP_XRT_FL_PROG_SET | 2 | 07-F0 | c5 07 | | | | |
| 2016/08/25 | 06:12:30.0 | XRT_CTRL_AUTO_408_OG [0x198] | | | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2016/08/25 | 06:54:00.0 | XRT_CTRL_MANU_400_OG [0x190] | | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2016/08/25 | 06:54:02.0 | XRT_CTRL_MANU_402_OG [0x192] | | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2016/08/25 | 06:54:04.0 | XRT_FLD_RESET_415_OG [0x19f] | | | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2016/08/25 | 06:54:06.0 | XRT_PREFLR_STRT_414_OG [0x19e] | | | | | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | | |
| 2016/08/25 | 06:57:14.0 | XRT_PREFLR_STOP_419_OG [0x1a3] | | | | | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | | |
| 2016/08/25 | 07:21:30.0 | XRT_Custom_430_OG [0x1ae] | | | | | | | |
| 2016/08/25 | 07:22:30.0 | XRT_CTRL_AUTO_424_OG [0x1a8] | | | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2016/08/25 | 08:33:30.0 | XRT_CTRL_MANU_400_OG [0x190] | | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2016/08/25 | 08:33:32.0 | XRT_CTRL_MANU_402_OG [0x192] | | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2016/08/25 | 08:33:34.0 | XRT_FLD_RESET_415_OG [0x19f] | | | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2016/08/25 | 08:33:36.0 | XRT_PREFLR_STRT_414_OG [0x19e] | | | | | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | | |
| 2016/08/25 | 08:36:44.0 | XRT_PREFLR_STOP_419_OG [0x1a3] | | | | | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | | |
| 2016/08/25 | 09:00:00.0 | XRT_Custom_430_OG [0x1ae] | | | | | | | |
| 2016/08/25 | 09:01:00.0 | XRT_CTRL_AUTO_424_OG [0x1a8] | | | | | | | |
| | | MDP_XRT_CTRL_AUTO | 1 | 07-F0 | c0 | | | | |
| 2016/08/25 | 10:14:30.0 | XRT_CTRL_MANU_400_OG [0x190] | | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2016/08/25 | 10:14:32.0 | XRT_CTRL_MANU_402_OG [0x192] | | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2016/08/25 | 10:14:34.0 | XRT_FLD_RESET_415_OG [0x19f] | | | | | | | |
| | | MDP_XRT_FLD_RESET | 1 | 07-F0 | da | | | | |
| 2016/08/25 | 10:14:36.0 | XRT_PREFLR_STRT_414_OG [0x19e] | | | | | | | |
| | | MDP_XRT_PREFLR_STRT | 1 | 07-F0 | e8 | | | | |
| 2016/08/25 | 10:17:44.0 | XRT_PREFLR_STOP_419_OG [0x1a3] | | | | | | | |
| | | MDP_XRT_PREFLR_STOP | 1 | 07-F0 | e9 | | | | |
| 2016/08/25 | 10:47:54.0 | XRT_CTRL_MANU_402_OG [0x192] | | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2016/08/25 | 10:47:56.0 | XRT_CTRL_MANU_402_OG [0x192] | | | | | | | |
| | | MDP_XRT_CTRL_MANU | 1 | 07-F0 | c1 | | | | |
| 2016/08/25 | 10:48:00.0 | AOCS_ORe-point_Start_3_OG [0x099] | | | | | | | |
| | | AOCU_NM | 5 | 02-76 | 00 00 00 00 00 | | | | |