

# XRT Timeline to be uploaded on 2016/09/01

Period: 2016/09/01 12:31:00 - 2016/09/06 11:10:00

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Normal mode

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XOB #1B4D: HOP81/206 2-filter - Al/poly 8s, Al/mesh 6s 60s cadence, G-band - 384x384 3ms													
Term	Pointing (x, y)							Comment					
09/01 12:44:00 - 09/01 19:04:54	Fixed ( -26.0, 866.0)							# OP start + 10min - HOP206					
<b>PROG= 15 Inf.-time(s)</b>													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 9 2-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs 1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec	
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 7 1-time(s) 30.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs 1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec	
└─ Subr= 3 30-time(s) 2.0sec													
└─ Seqn= 24 1-time(s) 60.0sec													
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	5.66s	Obs 1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec	
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	8.00s	Obs 1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1B0A: Synoptic Q95 2x2 - Al/mesh(12/181/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(24/362/1443) + T													
Term	Pointing (x, y)							Comment					
09/01 19:08:00 - 09/01 19:14:54	Fixed ( 0.0, 0.0)							synoptic, shifted 60min					
09/02 06:02:00 - 09/02 06:08:54	Fixed ( 0.0, 0.0)							synoptic, shifted -1.0 min					
09/02 16:33:00 - 09/02 16:39:54	Fixed ( 0.0, 0.0)							synoptic shifted -90 min					
09/03 06:18:30 - 09/03 06:25:24	Fixed ( 0.0, 0.0)							synoptic, shifted 15.5 min					
<b>PROG= 10 1-time(s)</b>													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 5 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec	
└─ Seqn= 91 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	177ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└─ Seqn= 93 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	24ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	354ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└─ Seqn= 77 1-time(s) 2.0sec													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└─ Seqn= 54 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs 1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec	
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs 1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1B4F: HOP260 - modestly high cadence (20s thin-Be only) 384x384 at 1064 1048													
Term	Pointing (x, y)							Comment					
09/01 19:18:00 - 09/01 22:29:54	Track ( -589.3, 173.9) @ 09/01 19:15:00							HOP324					
<b>PROG= 17 Inf.-time(s)</b>													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 88 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs 1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs 1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
└─ Seqn= 47 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs 1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec	
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 13 50-time(s) 20.0sec													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1B48: HOP316 (Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, and Med-Be context, with G-band (3ms/3ms VLS=CLS													
Term	Pointing (x, y)							Comment					
09/01 22:33:00 - 09/02 00:33:30	Fixed ( 890.0, 140.0)							HOP316					
<b>PROG= 02 Inf.-time(s)</b>													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 56 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs 1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec	
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs 1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs 1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	

<b>Subr= 2</b>		<b>5-time(s)</b>		<b>2.0sec</b>											
<b>Seqn= 48</b>		<b>1-time(s)</b>		<b>2.0sec</b>											
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec			
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec			
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec			
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec			
<b>Seqn= 96</b>		<b>5-time(s)</b>		<b>60.0sec</b>											
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec			
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	15.0sec			
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec			
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	15.0sec			
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec			
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval				

<b>XOB #1B0E: CME watch - 4x4 - AEC 2 - Be-thin - G-band (2x2,1ms) - Leak (2x2,1ms) - 360s cad(G-band/Leak first)</b>														
Term		Pointing (x, y)						Comment						
09/02 00:48:00 - 09/02 01:56:30		Track ( 288.5, 471.6) @ 09/02 00:45:00						Coronal Hole Study						
<b>PROG= 12 Inf.-time(s)</b>														
<b>Subr= 1</b>		<b>1-time(s)</b>		<b>2.0sec</b>										
<b>Seqn= 26</b>		<b>1-time(s)</b>		<b>4.0sec</b>										
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec		
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
<b>Subr= 2</b>		<b>10-time(s)</b>		<b>360.0sec</b>										
<b>Seqn= 29</b>		<b>1-time(s)</b>		<b>2.0sec</b>										
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval			

<b>XOB #1B07: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with</b>														
Term		Pointing (x, y)						Comment						
09/02 06:12:00 - 09/02 15:27:00		Track ( -778.8, 33.3) @ 09/02 06:09:00						AR12585						
09/02 21:03:00 - 09/03 06:15:24		Track ( -693.8, 20.0) @ 09/02 21:00:00						AR12585						
09/03 06:28:30 - 09/03 08:59:00		Track ( -632.3, 12.3) @ 09/03 06:25:30						AR12585						
<b>PROG= 20 Inf.-time(s)</b>														
<b>Subr= 1</b>		<b>1-time(s)</b>		<b>2.0sec</b>										
<b>Seqn= 56</b>		<b>1-time(s)</b>		<b>2.0sec</b>										
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec		
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec		
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec		
<b>Subr= 2</b>		<b>1-time(s)</b>		<b>2.0sec</b>										
<b>Seqn= 75</b>		<b>5-time(s)</b>		<b>2.0sec</b>										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec		
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec		
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec		
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec		
<b>Seqn= 96</b>		<b>8-time(s)</b>		<b>360.0sec</b>										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec		
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	15.0sec		
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec		
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	15.0sec		
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec		
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval			

<b>XOB #1A32: HOP260 - High cadence (10s thin-Be only) 384x384 at 1064 1048</b>														
Term		Pointing (x, y)						Comment						
09/02 16:43:00 - 09/02 20:59:54		Track ( -434.3, 160.6) @ 09/02 16:40:00						HOP324						
<b>PROG= 11 Inf.-time(s)</b>														
<b>Subr= 1</b>		<b>1-time(s)</b>		<b>2.0sec</b>										
<b>Seqn= 88</b>		<b>1-time(s)</b>		<b>2.0sec</b>										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec		
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec		
<b>Seqn= 47</b>		<b>1-time(s)</b>		<b>2.0sec</b>										
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec		
<b>Subr= 2</b>		<b>1-time(s)</b>		<b>2.0sec</b>										
<b>Seqn= 13</b>		<b>50-time(s)</b>		<b>10.0sec</b>										
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval			

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**Flare mode**

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<b>XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512</b>												
Term		Pointing (x, y)						Comment				
09/01 12:44:00 - 09/01 19:04:54		Fixed ( -26.0, 866.0)						# OP start + 10min - HOP206				
09/01 19:18:00 - 09/01 22:29:54		Track ( -589.3, 173.9) @ 09/01 19:15:00						HOP324				
09/01 22:33:00 - 09/02 00:33:30		Fixed ( 890.0, 140.0)						HOP316				

09/02 00:48:00 - 09/02 01:56:30 Track ( 288.5, 471.6) @ 09/02 00:45:00 Coronal Hole Study  
 09/02 06:12:00 - 09/02 15:27:00 Track ( -778.8, 33.3) @ 09/02 06:09:00 AR12585  
 09/02 16:43:00 - 09/02 20:59:54 Track ( -434.3, 160.6) @ 09/02 16:40:00 HOP324  
 09/02 21:03:00 - 09/03 06:15:24 Track ( -693.8, 20.0) @ 09/02 21:00:00 AR12585  
 09/03 06:28:30 - 09/03 08:59:00 Track ( -632.3, 12.3) @ 09/03 06:25:30 AR12585

**PROG= 07 30-time(s)**

Subr= 1		20-time(s)	2.0sec											
Seqn= 11		1-time(s)	2.0sec											
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec		
Seqn=100		1-time(s)	10.0sec											
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec		
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
Subr= 2		1-time(s)	2.0sec											
Seqn= 10		1-time(s)	2.0sec											
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
Seqn= 11		1-time(s)	2.0sec											
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec		
Seqn= 84		1-time(s)	2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval			

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**Active Region Search**

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NOT USED

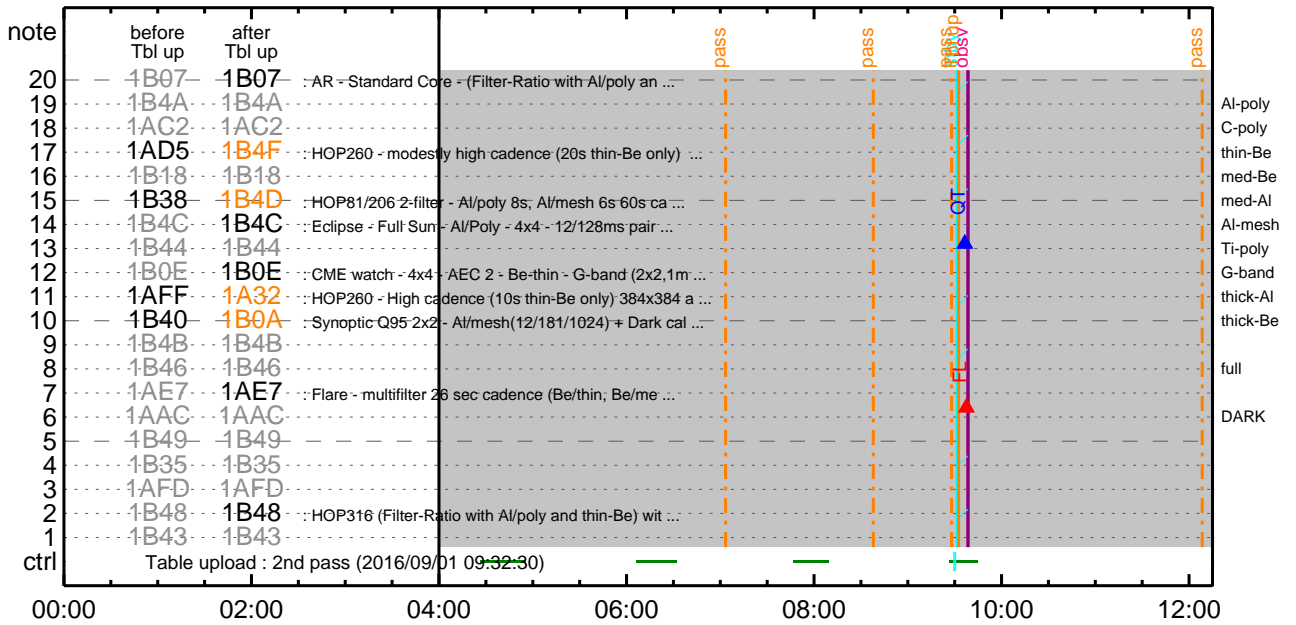
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**Flare Detection**

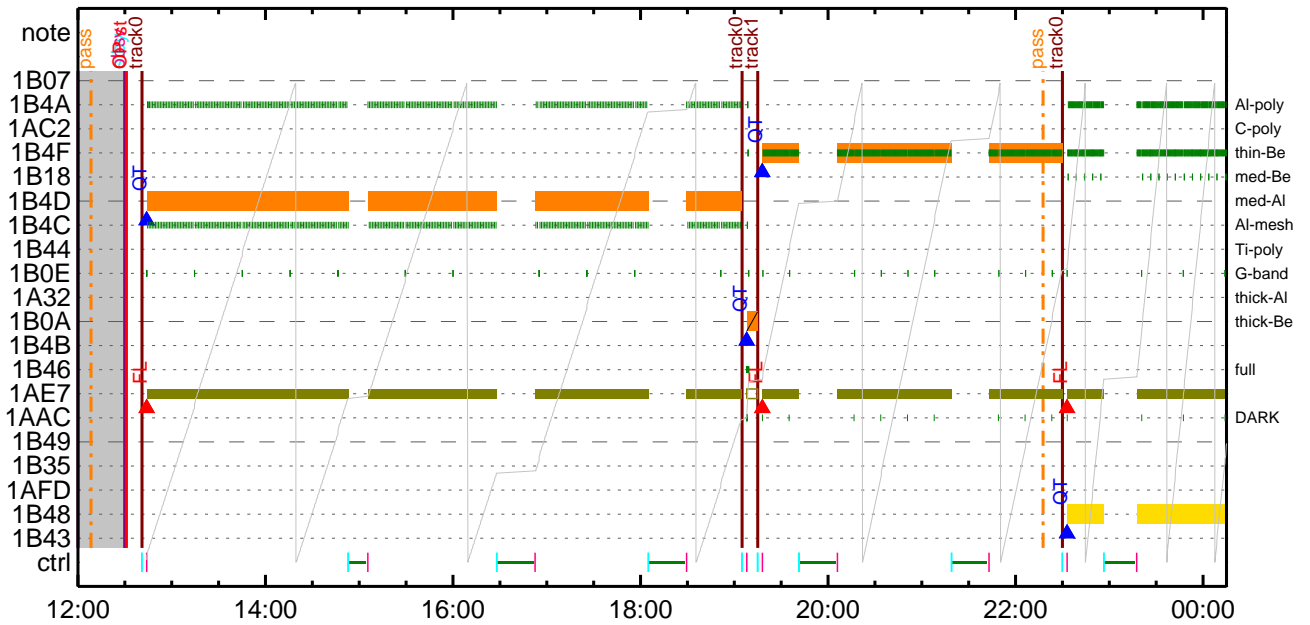
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<b>FLD Patrol</b>													
Term	Pointing (x, y)		Comment										
09/01 12:41:18 - 09/01 19:05:18	Fixed ( -26.0, 866.0)		# OP start + 10min - HOP206										
09/01 19:15:18 - 09/02 05:59:18	Track ( -589.3, 173.9) @ 09/01 19:15:00		HOP324										
09/02 06:09:18 - 09/02 16:30:18	Track ( -778.8, 33.3) @ 09/02 06:09:00		AR12585										
09/02 16:40:18 - 09/03 06:15:48	Track ( -434.3, 160.6) @ 09/02 16:40:00		HOP324										
09/03 06:25:48 - 09/06 11:10:00	Track ( -632.3, 12.3) @ 09/03 06:25:30		AR12585										
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50	30sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

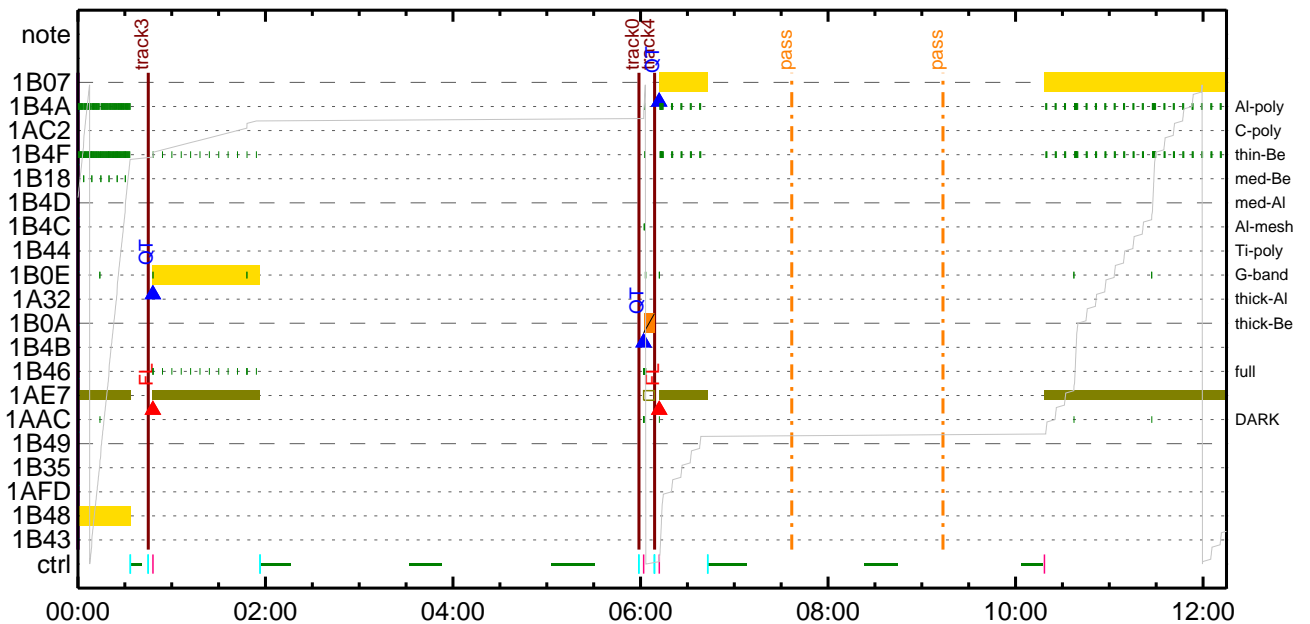
### CMDI #0147 2016/09/01



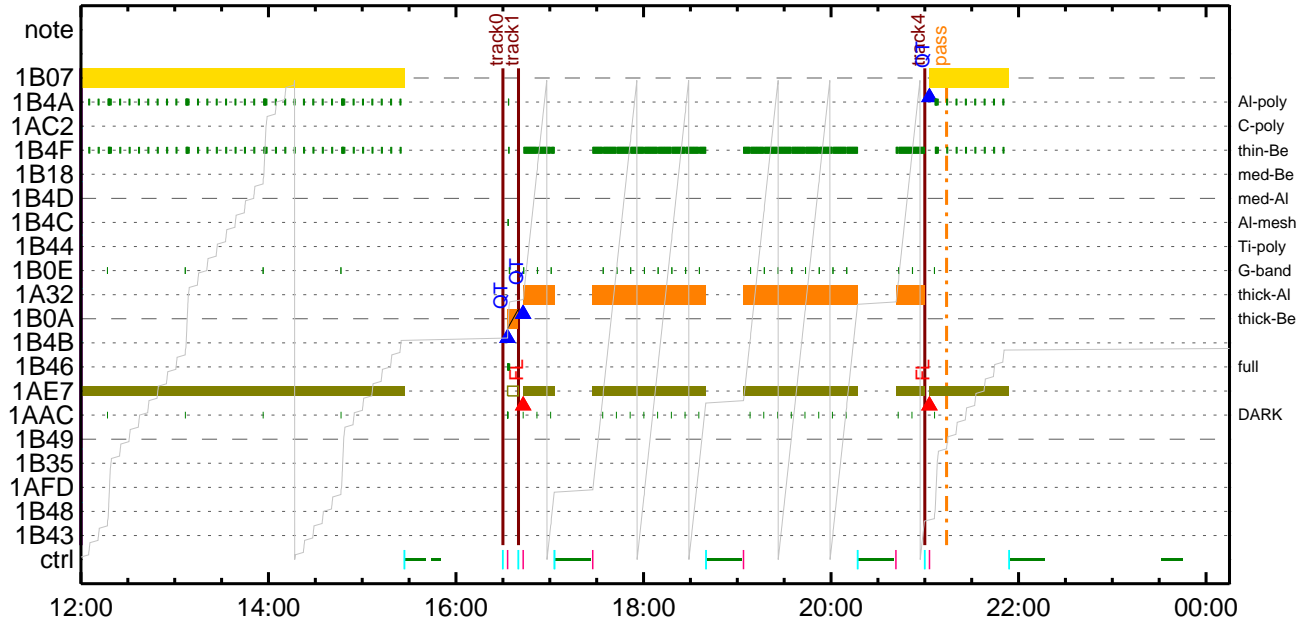
### CMDI #0147 2016/09/01



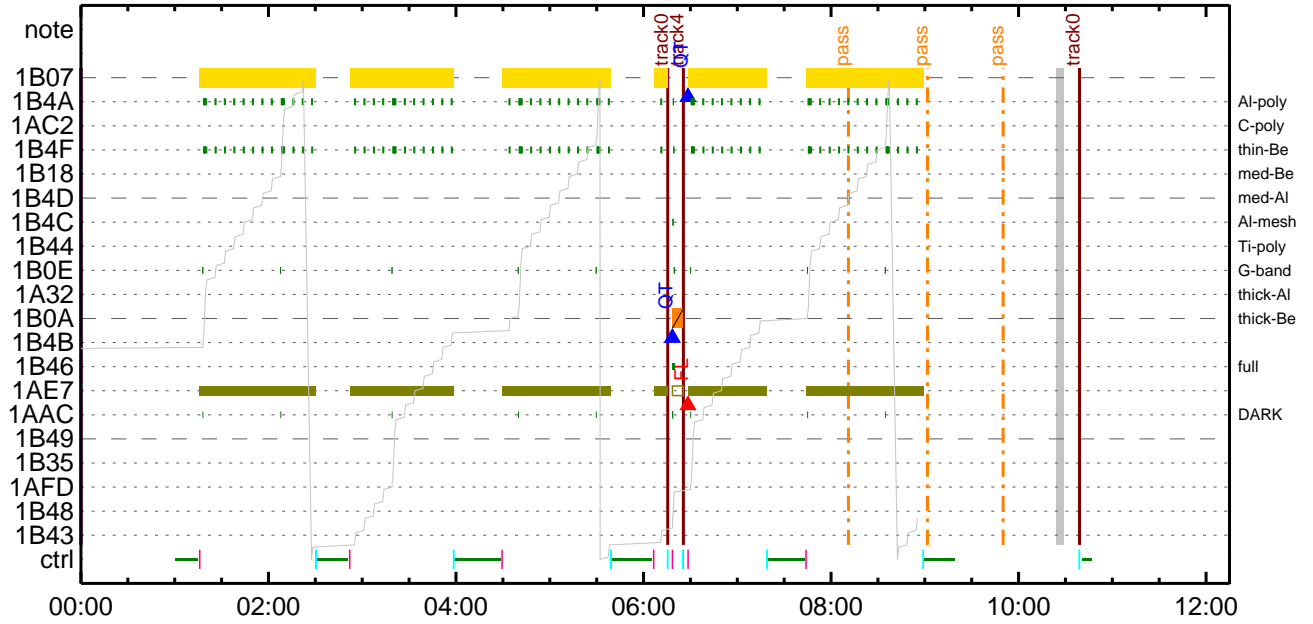
### CMDI #0147 2016/09/02



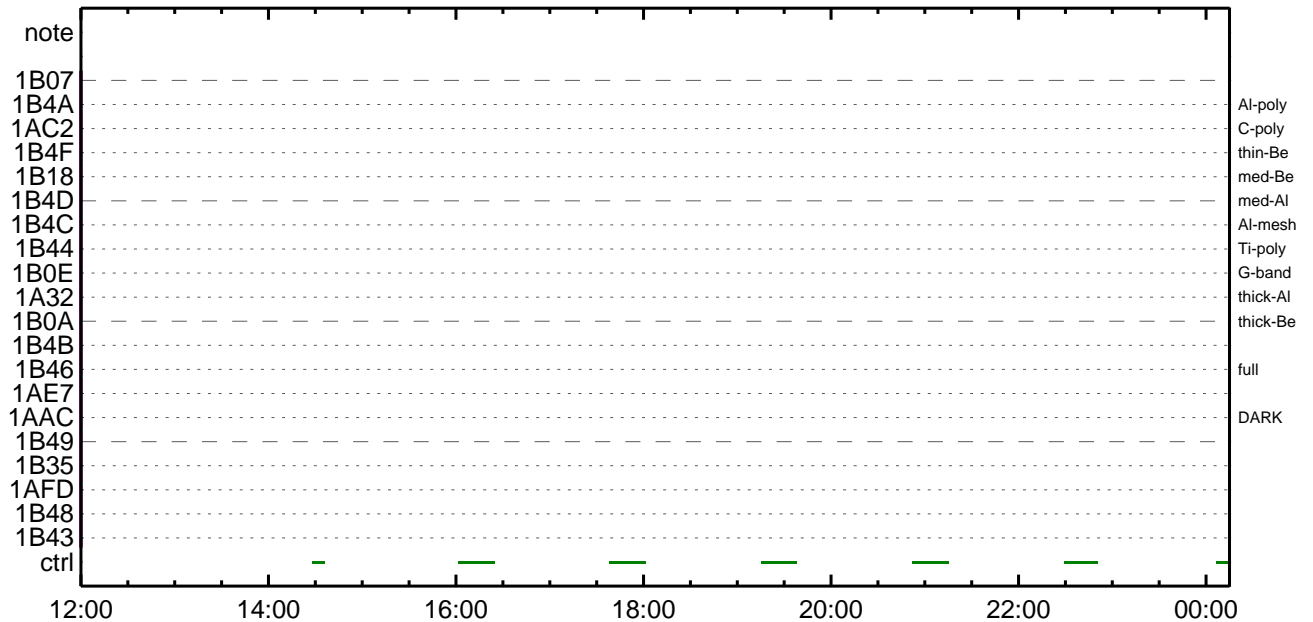
### CMDI #0147 2016/09/02



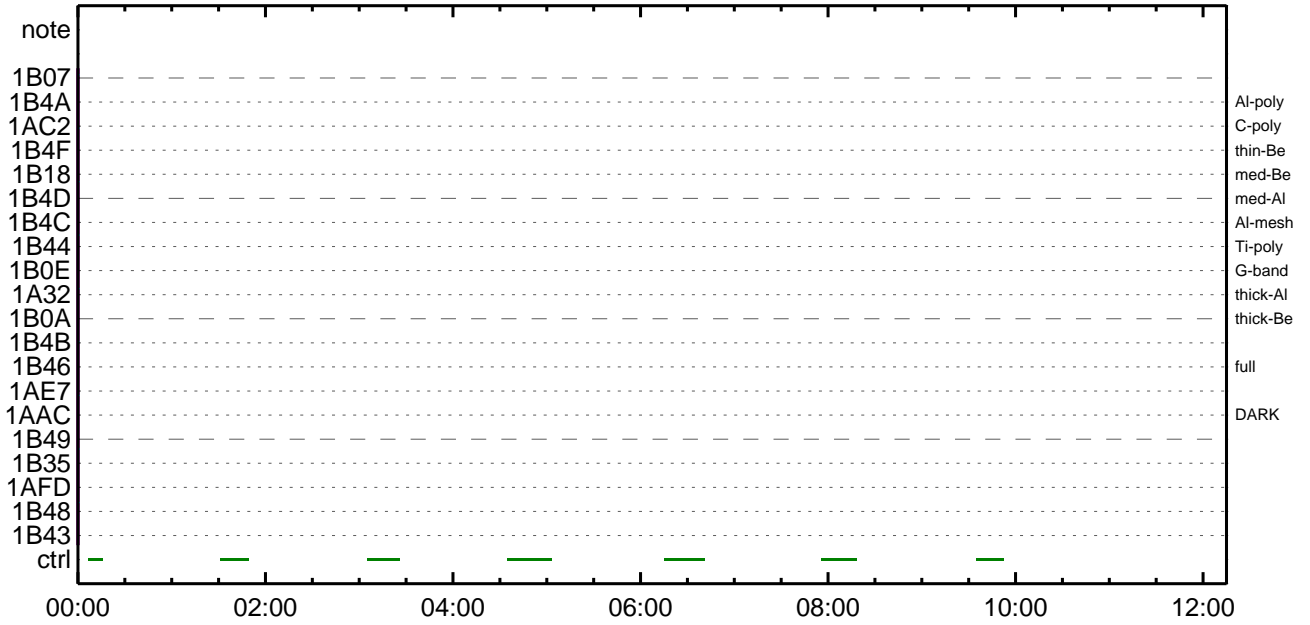
### CMDI #0147 2016/09/03



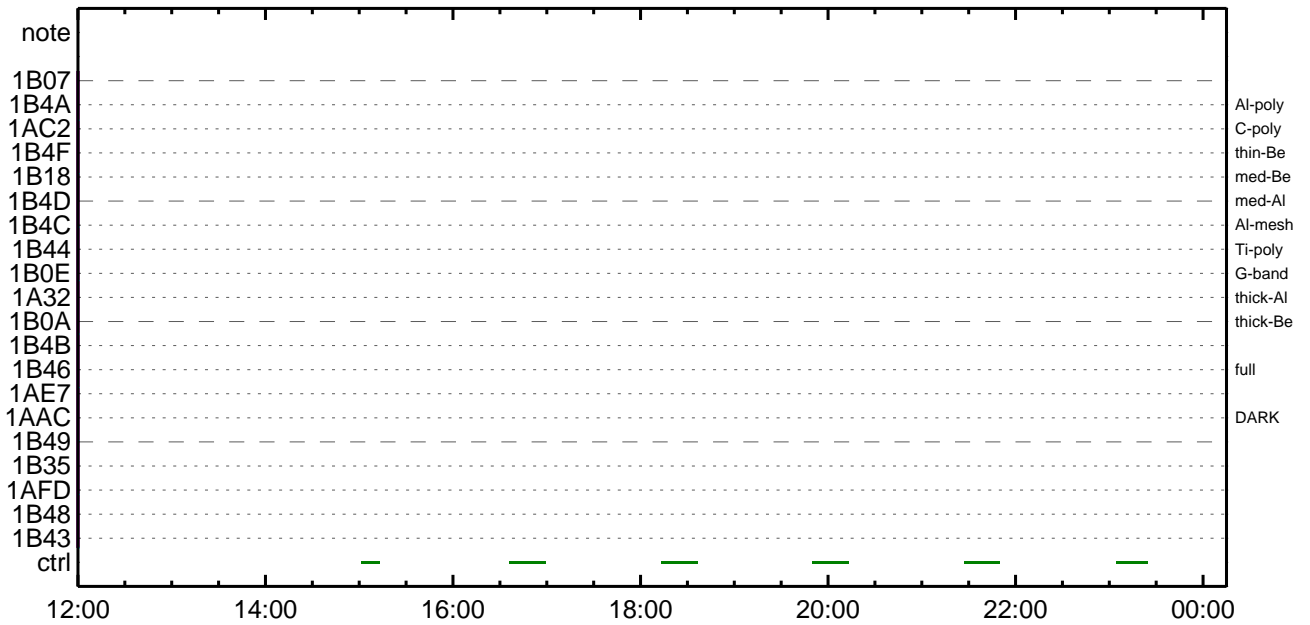
### CMDI #0147 2016/09/03



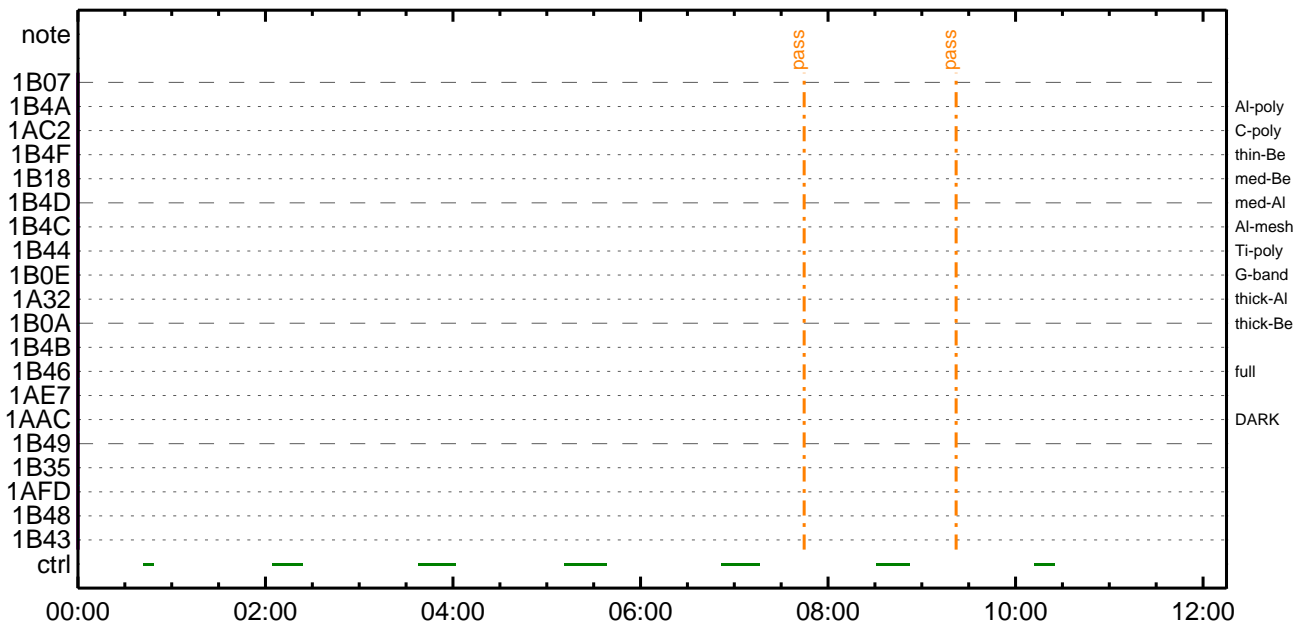
CMDI #0147 2016/09/04



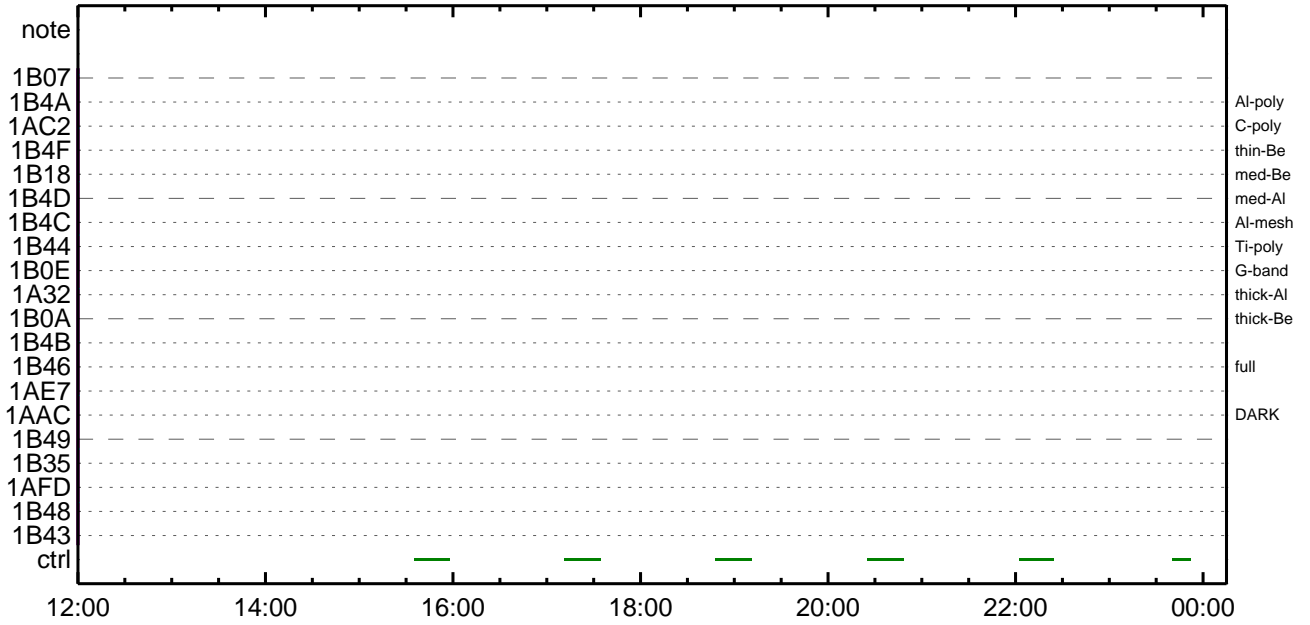
CMDI #0147 2016/09/04



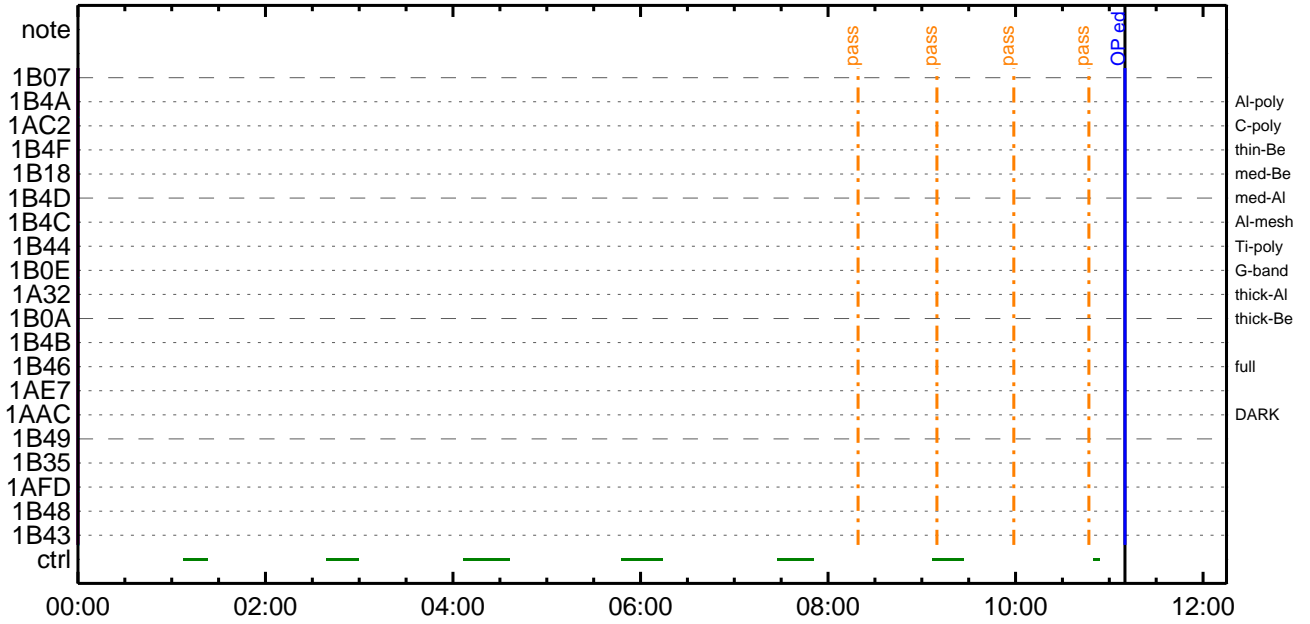
CMDI #0147 2016/09/05



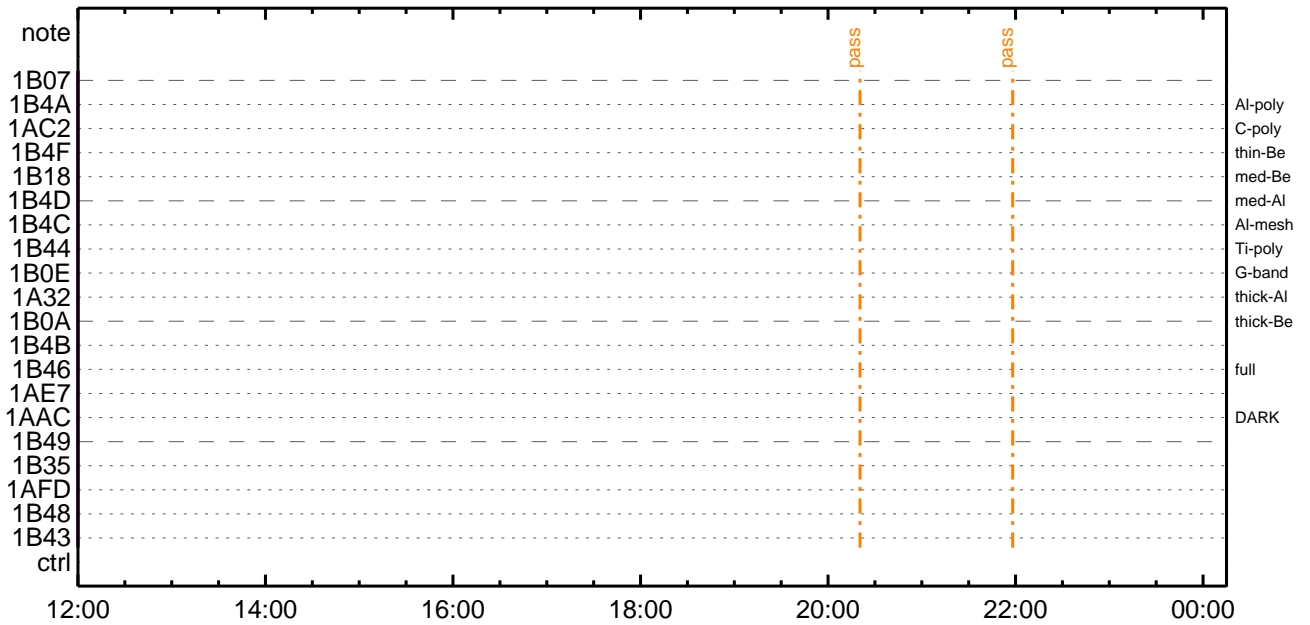
CMDI #0147 2016/09/05



CMDI #0147 2016/09/06



CMDI #0147 2016/09/06







```

0096 C.
0097 C.
0098 C. *****
0099 C. * AOCSEÜ¿ª½ªî»»p³îÇS±¿îÑ *
0100 C. *****
0101 C.
0102 C. ***** 1. Eü¿ª½ªî»»p³îÇS *****
0103 C. [ ] <ORB>[H/W TIMER] ECLIPS (HK2_SLR_ECLIPS_ST) EQ ELSE
0104 C. [ ] <--->[SW T STS] ECL (HK2_ECLIPS_STS_S/W) EQ OUT
0105 C.
0106 C.
0107 C. ***** 2. %âµ-³îÇS±¿îÑ <NGªî½ªî¹¿>¿¿²½µ-CMDªõÁ¿¿ª¹ªë *****
0108 C.
0109 +. DC 02-D3 AOCU_SLR_ECLPS_END
0110 C.
0111 C. [ ] <ORB>[H/W TIMER] ECLIPS (HK2_SLR_ECLIPS_ST) EQ ELSE
0112 C. [ ] <--->[SW T STS] ECL (HK2_ECLIPS_STS_S/W) EQ OUT
0113 C.
0114 C.
0115 C. ***** End of AOC S SPECIAL CMD *****
0116 C.
0117 C. *****
0118 C. OP/OG½ªî»»p³îÇS
0119 C. *****
0120 C.
0121 C. ;ãOP/OG½ªî»»p³îÇS
0122 S. OP op-084:OP
0123 ( )
0124 S. OG og-084:OG
0125 ( )
0126 C.
0127 C. ;ãNMOG&OPî½ªî»»p³îÇS;ã
0128 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 00 7f 01 02)
0131 C. ¿¿[HK1_DMP_TOP_ADRS_1] EQ 40
0132 C. ¿¿[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. ¿¿[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. ¿¿[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. ¿¿[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. ¿¿[HK1_PKT_FORM_NO] EQ 7
0139 C. ¿¿[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. ¿¿[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. ¿¿[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. ¿¿[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. %À½ªî»»p³îÇS
0144 C. ¿¿[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGªî½ªî¹¿.ë²îOKªõ³îÇS
0146 C.
0147 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (20 80 7f 01 02)
0150 C. ¿¿[HK1_DMP_TOP_ADRS_1] EQ 41
0151 C. ¿¿[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. ¿¿[HK1_DMP_BLOCK_NUM] EQ 127
0153 C. ¿¿[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. ¿¿[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. ¿¿[HK1_PKT_FORM_NO] EQ 7
0158 C. ¿¿[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. ¿¿[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. ¿¿[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. ¿¿[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. %À½ªî»»p³îÇS
0163 C. ¿¿[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOGªî½ªî¹¿.ë²îOKªõ³îÇS
0165 C.
0166 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0167 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0168 BC (21 00 41 01 02)
0169 C. ¿¿[HK1_DMP_TOP_ADRS_1] EQ 42
0170 C. ¿¿[HK1_DMP_TOP_ADRS_0] EQ 0
0171 C. ¿¿[HK1_DMP_BLOCK_NUM] EQ 65
0172 C. ¿¿[HK1_DMP_REPEAT_NUM] EQ 0
0173 C. ¿¿[HK1_DMA_DMP_PIM] EQ DHU
0174 +. DC 01-22 DHU_MODE_CHNG
0175 BC (07 0b f8)
0176 C. ¿¿[HK1_PKT_FORM_NO] EQ 7
0177 C. ¿¿[HK1_PKT_GEN_TIME] EQ 0.25 s
0178 C. ¿¿[HK1_S_TLM_BIT_RATE] EQ 32k
0179 C. ¿¿[HK1_X_TLM_BIT_RATE] EQ 4M
0180 C. ¿¿[HK1_DMP_CHK_FLG] EQ EXEC
0181 C. %À½ªî»»p³îÇS
0182 C. ¿¿[HK1_DMP_CHK_FLG] EQ NON
0183 C. RAM ID=NMOG, RAM ID=OPªî½ªî¹¿.ë²îOKªõ³îÇS
0184 C.
0185 C. ***** °ê²½µ-³îÇS±¿îÑ <NGªî½ªî¹¿>¿¿²½µ-CMDªõÁ¿¿ª¹ªëª¹ªë½ªî»»p³îÇS *****
0186 C. DHU½ªî»»p³îÇS±¿îÑ <NGªî½ªî¹¿>¿¿²½µ-CMDªõÁ¿¿ª¹ªëª¹ªë½ªî»»p³îÇS *****
0187 +. DC 01-22 DHU_MODE_CHNG
0188 BC (02 0a f8)
0189 C. ¿¿[HK1_PKT_FORM_NO] EQ 2
0190 C. ¿¿[HK1_PKT_GEN_TIME] EQ 0.5S
0191 C. ¿¿[HK1_S_TLM_BIT_RATE] EQ 32K
0192 C. ¿¿[HK1_X_TLM_BIT_RATE] EQ 4M
0193 C.

```



0292 C. \*\*\*\*\* XRT END \*\*\*\*\*  
0293 C.  
0294 . C. \*\*\*\*\* MDP 'úÃîî»ò¼ýð¹æDCBC•x²è \*\*\*\*\*  
0295 C. (¼ã°îÿÓÿÃÿËÿËÿáÿçÿèæ¼¼¼¼»Û¹æè)  
0296 . S. DC-BC dcbc-402:DCBC  
0297 (MDP\_known\_event)  
0298 C.  
0299 C.  
0300 . C. \*\*\*\*\* ÿÐÿ¹•ï Daily±¿îñæ´Ø¹æDCBC•x²è \*\*\*\*\*  
0301 . S. DC-BC dcbc-153:DCBC  
0302 (SPECIAL-CMD\_DAILY\_OPERATIN\_DCB)  
0303 C.  
0304 C.  
0305 . C. ;ãLOSÿÁÿ§ÿÃÿ¼¼»Û;ã  
0306 C.  
0307 . C. \*\*\*\*\* LOS \*\*\*\*\*  
0308 C.

(a) Spacecraft Operation Procedure (real-commands)

```
main-085 2016-09-01 14:25:54 148 33 SOLAR-B MAIN //
0001 . C.
0002 . C. ***** AOS *****
0003 . C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 . C.
0006 . C. YÀYB;¼Y³YF¥ÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 . C.
0009 . C. ***** APCS : Reload orbital element (send every contact) *****
0010 . C. Áí;Ë±¿±Á±•µ°È»Í×ÁÇ±Í¥ÇYÁY×¥í;¼YÉ;ËÈ%µ•íÉ;È±È¼°ÇÒ±•±¿¼ì¹Ç±Í;çÁ®; ù±¹±±±±±±ÇÁ+¿®±•±É±±±±±±±È;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 . C.
0013 . C.
0014 . C. ***** APCS Commands (Tracking Curve Upload) *****
0015 . C. Upload the Orbit Element and the Target Attitude
0016 . C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 . C. ( )
0019 . C.
0020 . C.
0021 . C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 . BC (07 00 00 00 18 00)
0024 . C.
0025 . C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 . C.
0027 . C.
0028 . C. Change the TLMFormatNo for the APCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 . BC (04 0b f8)
0031 . C.
0032 . C. Wait for AOCSDUMP to end
0033 . C.
0034 . C. Check the dump memory
0035 . C.
0036 . C. Result = OK [ ]
0037 . C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 . BC (02 0a f8)
0040 . C.
0041 . C. <A_***>[TLM STS] FMT = 2 [ ]
0042 . C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C.
0046 . C. ***** XRT START *****
0047 . C.
0048 +. DC 07-F0 MDP_XRT_CTRL_MANU
0049 . BC (c1)
0050 +. DC 07-F0 MDP_XRT_CTRL_MANU
0051 . BC (c1)
0052 +. DC 07-F0 MDP_XRT_MODE_STBY
0053 . BC (c3)
0054 . C. ----- Success Verify ? OK / NG____
0055 . C.
0056 . C. XRT Obs. Table Upload
0057 . S. RAM ram-291:MDP_OBS_X
0058 . C. ( )
0059 . C.
0060 +. DC 07-F0 MDP_DUMP_XRTTBL
0061 . BC (84 07 00 00 00 3a d4)
0062 . C. ----- Comparison Check ? OK / ERR _____
0063 . C.
0064 . C.
0065 +. DC 07-F0 MDP_XRT_ROI_SET
0066 . BC (cd 01 b1 b1 04 04)
0067 +. DC 07-F0 MDP_XRT_ROI_SET
0068 . BC (cd 02 b1 b1 08 08)
0069 +. DC 07-F0 MDP_XRT_ROI_SET
0070 . BC (cd 03 b1 b1 08 08)
0071 +. DC 07-F0 MDP_XRT_ROI_SET
0072 . BC (cd 04 b1 b1 06 06)
0073 +. DC 07-F0 MDP_XRT_ROI_SET
0074 . BC (cd 06 80 80 20 20)
0075 +. DC 07-F0 MDP_XRT_ROI_SET
0076 . BC (cd 07 85 83 06 06)
0077 +. DC 07-F0 MDP_XRT_ROI_SET
0078 . BC (cd 08 80 80 20 08)
0079 +. DC 07-F0 MDP_XRT_ROI_SET
0080 . BC (cd 09 80 80 08 20)
0081 +. DC 07-F0 MDP_XRT_ROI_SET
0082 . BC (cd 0f 80 80 06 06)
0083 +. DC 07-F0 MDP_XRT_ROI_SET
0084 . BC (cd 10 80 80 08 08)
0085 +. DC 07-F0 MDP_XRT_FLD_DIS
0086 . BC (d9)
0087 +. DC 07-F0 MDP_XRT_FLRCTRL_DIS
0088 . BC (c9)
0089 +. DC 07-F0 MDP_XRT_ARS_DIS
0090 . BC (d5)
0091 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0092 . BC (c4 0e)
0093 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0094 . BC (c5 07)
0095 . C. ----- Success Verify ? OK / NG _____
```

```

0096 C.
0097 C.
0098 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0099 C.
0100 +. DC 07-F0 MDP_XRT_MODE_OBSV
0101 BC (c2)
0102 +. TI 2016-09-01 12:30:02.0
0103 DC 07-F0 MDP_XRT_MODE_OBSV
0104 BC (c2)
0105 . C. ----- Success Verify ? OK / NG ____
0106 C.
0107 C. ***** XRT END *****
0108 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0109 +. DC 07-FC EIS_MODE_CHG_ENA
0110 BC (20)
0111 . C. Verify EIS_MODE_CHG_FLG is ENA
0112 +. DC 07-FC EIS_MODE_MANU
0113 BC (21 02)
0114 . C. Verify EIS in MANUAL mode
0115 . C. Estimated OBSTBL upload time is 1m15s
0116 C. *****
0117 C. EIS START OBSTBL LOAD
0118 C. *****
0119 . S. RAM ram-820:EIS_OBSTBL
0120 ( )
0121 +. DC 07-FC EIS_DUMP_OBSTBL
0122 BC (07 07 07 00 00 70 00)
0123 C.
0124 C. Execute, after the success of OBSTBL upload.
0125 C. Set EIS TI-commands
0126 +. TI 2016-09-01 12:30:50.0
0127 DC 07-FC EIS_MODE_CHG_ENA
0128 BC (20)
0129 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0130 C. *****
0131 C. EIS END OBSTBL LOAD
0132 C. *****
0133 C.
0134 . C. ***** MDP 'úÃîâî»ö¼ÝðËÂð¹ñèDCBC•x²è *****
0135 C. (¼á°îÿÓÿÄÿËÿPÿËÿáÿçÿÈñË¼ñ¼Ä»Ûñ¹ñè)
0136 . S. DC-BC dcbc-402:DCBC
0137 (MDP_known_event)
0138 C.
0139 C.
0140 . C. ***** ¥D¥¹•İ Daily±¿İÑñË'Øñ¹ñèDCBC•x²è *****
0141 . S. DC-BC dcbc-153:DCBC
0142 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0143 C.
0144 C.
0145 . C. ;ãLOS¥Á¥$¥Ä¥¹¼Ä»Û;ä
0146 C.
0147 . C. ***** LOS *****
0148 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-086 2016-09-01 14:25:54 80 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁY$;¼Y³YF¥ÓYÉÁ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;ÈçðÄñ•µ°È»Í×ÁÇçÁÍYçYÁY×Yí;¼YÉ;ÈÈè¼µ•ííÉ;ÈèÈ¼°ÇÖñ•ñç¼í¹çñí;çÁ®, ùñ¹ñèñðñçÁ÷ç®ñ•ñÈñññ³ñÈ;ñ
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C. *****
0015 C. * AOCSEüç²¼áí»»þ³íÇ§±çíÑ *
0016 C. *****
0017 C.
0018 . C. ***** 1. Èüç²¼Y¹YÉ;¼Yç¼Y¹ñí³íÇ§ *****
0019 C. [ ] <ORB>[H/W TIMER] ECLIPS (HK2_SLR_ECLIPS_ST) EQ ELSE
0020 C. [ ] <--->[SW T STS] ECL (HK2_ECLIPS_STS_S/W) EQ OUT
0021 C.
0022 C.
0023 . C. ***** 2. ¾áµ-³íÇ§ñ- <NGñí¼í¹ç>;ç²¼µ-CMDñðÁ÷ç®ñ¹ñè *****
0024 C.
0025 +. DC 02-D3 AOCU_SLR_ECLPS_END
0026 C.
0027 C. [ ] <ORB>[H/W TIMER] ECLIPS (HK2_SLR_ECLIPS_ST) EQ ELSE
0028 C. [ ] <--->[SW T STS] ECL (HK2_ECLIPS_STS_S/W) EQ OUT
0029 C.
0030 C.
0031 C. ***** End of AOCs SPECIAL CMD *****
0032 . C.
0033 . C. *****
0034 C. SOT table upload
0035 C. *****
0036 . C. < Stop SP table >
0037 +. DC 07-F0 MDP_SP_CTRL_MANU
0038 BC (61)
0039 C. -----
0040 C. MDP_SP_CTRL_MODE = MANU [ ]
0041 C. -----
0042 C.
0043 . C. <Upload SP Observation Table>
0044 . S. RAM ram-288:MDP_OBS_S
0045 ( )
0046 C.
0047 . C. < Dump RAMID=MDP_OBS_S >
0048 +. DC 07-F0 MDP_DUMP_SPTBL
0049 BC (83 07 00 00 00 38 b8)
0050 C. -----
0051 C. MDP_OBS_S verify = OK/NG [ ]
0052 C. -----
0053 C.
0054 C. *****
0055 C. SOT TI command set
0056 C. *****
0057 C. Execute, after the success of TBL upload.
0058 +. TI 2016-09-01 12:30:18.0
0059 DC 07-F0 MDP_SOT_MODE_OBSV
0060 BC (40)
0061 . C. -----
0062 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0063 C. -----
0064 C.
0065 C.
0066 . C. ***** MDP `úÁíñí»ð¼YñÈÁðñ¹ñèDCBC•x²è *****
0067 C. (¾á°íYÓYÁYÉY¥YÉYÁYçYèñÈ¾¼ññ¼Á»Ûñ¹ñè)
0068 . S. DC-BC dcbc-402:DCBC
0069 (MDP_known_event)
0070 C.
0071 C.
0072 . C. ***** YD¥¹•í Daily±çíÑñÈ´Øñ¹ñèDCBC•x²è *****
0073 . S. DC-BC dcbc-153:DCBC
0074 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0075 C.
0076 C.
0077 . C. ;ãLOSÁY$YÁY-¼Á»Û;ã
0078 C.
0079 . C. ***** LOS *****
0080 C.
```

\*\*\* OP Sequence for XRT \*\*\*

```

2016/09/01 12:40:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/09/01 12:40:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/09/01 12:40:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2016/09/01 12:41:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 00 b3 03 02 4d
2016/09/01 12:41:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2016/09/01 12:41:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2016/09/01 12:41:22.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2016/09/01 12:41:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2016/09/01 12:41:26.0 XRT_FLD_RESET_433_OG [0x1b1]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2016/09/01 12:43:56.0 XRT_QT_PROG_SET_426_OG [0x1aa]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0f
2016/09/01 12:43:58.0 XRT_FL_PROG_SET_436_OG [0x1b4]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 07
2016/09/01 12:44:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2016/09/01 14:53:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/09/01 14:53:02.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/09/01 14:53:04.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2016/09/01 14:53:06.0 XRT_PREFLR_STRT_414_OG [0x19e]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2016/09/01 14:56:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2016/09/01 15:04:30.0 XRT_Custom_430_OG [0x1ae]
2016/09/01 15:05:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2016/09/01 16:28:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/09/01 16:28:02.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/09/01 16:28:04.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2016/09/01 16:28:06.0 XRT_PREFLR_STRT_414_OG [0x19e]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2016/09/01 16:31:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2016/09/01 16:51:30.0 XRT_Custom_430_OG [0x1ae]
2016/09/01 16:52:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2016/09/01 18:05:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/09/01 18:05:02.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/09/01 18:05:04.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2016/09/01 18:05:06.0 XRT_PREFLR_STRT_414_OG [0x19e]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2016/09/01 18:08:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2016/09/01 18:28:30.0 XRT_Custom_430_OG [0x1ae]
2016/09/01 18:29:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2016/09/01 19:04:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/09/01 19:04:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/09/01 19:04:58.0 XRT_FOCUS_POSITION_403_OG [0x193]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2016/09/01 19:05:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 00 00 00 00 00
2016/09/01 19:05:18.0 XRT_FLD_DIS_406_OG [0x196]
                        MDP_XRT_FLD_DIS 1 07-F0 d9
2016/09/01 19:07:54.0 XRT_FLRCTRL_DIS_405_OG [0x195]
                        MDP_XRT_FLRCTRL_DIS 1 07-F0 c9
2016/09/01 19:07:56.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2016/09/01 19:07:58.0 XRT_QT_PROG_SET_427_OG [0x1ab]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0a
2016/09/01 19:08:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2016/09/01 19:14:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/09/01 19:14:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/09/01 19:14:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2016/09/01 19:15:00.0 AOCs_OrE-point_Start_3_OG [0x099]
                        AOCU_NM 5 02-76 01 00 00 00 00
2016/09/01 19:15:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8

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2016/09/01	19:15:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/09/01	19:15:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/09/01	19:15:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/09/01	19:15:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/09/01	19:17:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11	
2016/09/01	19:17:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07	
2016/09/01	19:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/09/01	19:41:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/09/01	19:41:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/09/01	19:41:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/09/01	19:41:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/09/01	19:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/09/01	20:05:00.0	XRT_Custom_430_OG [0x1ae]						
2016/09/01	20:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/09/01	21:19:01.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/09/01	21:19:03.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/09/01	21:19:05.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/09/01	21:19:07.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/09/01	21:22:15.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/09/01	21:42:00.0	XRT_Custom_430_OG [0x1ae]						
2016/09/01	21:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/09/01	22:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/09/01	22:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/09/01	22:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2016/09/01	22:30:00.0	AOCs_OrE-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00	f3 8e b0 e6	
2016/09/01	22:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2016/09/01	22:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/09/01	22:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/09/01	22:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/09/01	22:30:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/09/01	22:32:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02	
2016/09/01	22:32:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07	
2016/09/01	22:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/09/01	22:56:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/09/01	22:56:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/09/01	22:56:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/09/01	22:56:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/09/01	22:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/09/01	23:16:30.0	XRT_Custom_430_OG [0x1ae]						
2016/09/01	23:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/09/02	00:33:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/09/02	00:33:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/09/02	00:33:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/09/02	00:33:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/09/02	00:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/09/02	00:44:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/09/02	00:44:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/09/02	00:44:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	



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2016/09/02	00:45:00.0	AOCS_OrE-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	03	00	00	00	00
2016/09/02	00:45:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/09/02	00:45:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/09/02	00:45:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/09/02	00:45:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/09/02	00:45:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/09/02	00:47:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2016/09/02	00:47:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2016/09/02	00:48:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/09/02	01:56:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/02	01:56:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/02	01:56:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/09/02	01:56:36.0	XRT_PREFLR_STRT_414_OG [0x19e]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/09/02	01:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/09/02	05:58:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/02	05:58:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/02	05:58:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2016/09/02	05:59:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2016/09/02	05:59:18.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/09/02	06:01:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/09/02	06:01:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/09/02	06:01:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2016/09/02	06:02:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/09/02	06:08:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/02	06:08:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/02	06:08:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2016/09/02	06:09:00.0	AOCS_OrE-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	04	00	00	00	00
2016/09/02	06:09:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/09/02	06:09:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/09/02	06:09:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/09/02	06:09:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/09/02	06:09:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/09/02	06:11:56.0	XRT_QT_PROG_SET_446_OG [0x1be]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	14			
2016/09/02	06:11:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2016/09/02	06:12:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/09/02	06:43:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/02	06:43:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/02	06:43:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/09/02	06:43:06.0	XRT_PREFLR_STRT_414_OG [0x19e]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/09/02	06:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/09/02	10:17:30.0	XRT_Custom_430_OG [0x1ae]							
2016/09/02	10:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/09/02	15:27:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/02	15:27:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/02	15:27:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/09/02	15:27:06.0	XRT_PREFLR_STRT_414_OG [0x19e]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/09/02	15:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				

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2016/09/02	16:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/02	16:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/02	16:29:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/09/02	16:30:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2016/09/02	16:30:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9
2016/09/02	16:32:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/09/02	16:32:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/09/02	16:32:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2016/09/02	16:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/09/02	16:39:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/02	16:39:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/02	16:39:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/09/02	16:40:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01 00 00 00 00
2016/09/02	16:40:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/09/02	16:40:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/09/02	16:40:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/09/02	16:40:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/09/02	16:40:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/09/02	16:42:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2016/09/02	16:42:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/09/02	16:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/09/02	17:03:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/02	17:03:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/02	17:03:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/09/02	17:03:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/09/02	17:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/09/02	17:26:30.0	XRT_Custom_430_OG [0x1ae]				
2016/09/02	17:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/09/02	18:40:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/02	18:40:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/02	18:40:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/09/02	18:40:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/09/02	18:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/09/02	19:03:00.5	XRT_Custom_430_OG [0x1ae]				
2016/09/02	19:04:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/09/02	20:17:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/02	20:17:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/02	20:17:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/09/02	20:17:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/09/02	20:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/09/02	20:40:30.0	XRT_Custom_430_OG [0x1ae]				
2016/09/02	20:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/09/02	20:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/02	20:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/02	20:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/09/02	21:00:00.0	AOCS_Ore-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	04 00 00 00 00
2016/09/02	21:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/09/02	21:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8

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2016/09/02	21:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2016/09/02	21:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/09/02	21:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/09/02	21:02:56.0	XRT_QT_PROG_SET_446_OG [0x1be]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	14		
2016/09/02	21:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	07		
2016/09/02	21:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/09/02	21:54:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/09/02	21:54:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/09/02	21:54:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/09/02	21:54:06.0	XRT_PREFLR_STRT_414_OG [0x19e]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/09/02	21:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/09/03	01:15:00.0	XRT_Custom_430_OG [0x1ae]						
2016/09/03	01:16:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/09/03	02:30:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/09/03	02:30:32.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/09/03	02:30:34.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/09/03	02:30:36.0	XRT_PREFLR_STRT_414_OG [0x19e]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/09/03	02:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/09/03	02:51:00.0	XRT_Custom_430_OG [0x1ae]						
2016/09/03	02:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/09/03	03:58:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/09/03	03:58:32.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/09/03	03:58:34.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/09/03	03:58:36.0	XRT_PREFLR_STRT_414_OG [0x19e]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/09/03	04:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/09/03	04:28:30.0	XRT_Custom_430_OG [0x1ae]						
2016/09/03	04:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/09/03	05:39:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/09/03	05:39:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/09/03	05:39:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/09/03	05:39:06.0	XRT_PREFLR_STRT_414_OG [0x19e]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/09/03	05:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/09/03	06:05:30.0	XRT_Custom_430_OG [0x1ae]						
2016/09/03	06:06:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/09/03	06:15:24.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/09/03	06:15:26.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/09/03	06:15:28.0	XRT_FOCUS_POSITION_403_OG [0x193]						
		XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00		
2016/09/03	06:15:30.0	AOCS_OrE-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2016/09/03	06:15:48.0	XRT_FLD_DIS_406_OG [0x196]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2016/09/03	06:18:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2016/09/03	06:18:26.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/09/03	06:18:28.0	XRT_QT_PROG_SET_427_OG [0x1ab]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a		
2016/09/03	06:18:30.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/09/03	06:25:24.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/09/03	06:25:26.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/09/03	06:25:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
		XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00		
2016/09/03	06:25:30.0	AOCS_OrE-point_Start_6_OG [0x09c]						
		AOCU_NM	5	02-76	04 00 00 00 00			
2016/09/03	06:25:48.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2016/09/03	06:25:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						

2016/09/03	06:25:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
			MDP_XRT_AEC_RESET	1	07-F0	d0
2016/09/03	06:25:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/09/03	06:25:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/09/03	06:28:26.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2016/09/03	06:28:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/09/03	06:28:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/09/03	07:19:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/03	07:19:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/03	07:19:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/09/03	07:19:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/09/03	07:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/09/03	07:43:00.0	XRT_Custom_430_OG [0x1ae]				
2016/09/03	07:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/09/03	08:59:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/03	08:59:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/03	08:59:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/09/03	08:59:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/09/03	09:02:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/09/03	10:38:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/03	10:38:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/03	10:39:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00