

XRT Timeline to be uploaded on 2016/09/06

Period: 2016/09/06 11:10:00 - 2016/09/10 10:17:00

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Normal mode

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XOB #1B50: HOP325 MinXSS X-cal - multi-filters 4x4 - short exp												
Term	Pointing (x, y)						Comment					
09/06 11:23:00 - 09/06 12:01:54	Fixed (0.0, 0.0)						# OP start + 10min, HOP325					
PROG= 06 2-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 60 1-time(s) 2.0sec												
├─ Al-poly/Open Al-poly/Open close Safe Norm 4ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
├─ Al-poly/Open Al-poly/Open close Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
├─ Al-poly/Open Al-poly/Open close Safe Norm 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
Seqn= 62 1-time(s) 2.0sec												
├─ thin-Be/Open med-Be/Open close Safe Norm 16ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
├─ thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
├─ thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
Seqn= 4 1-time(s) 2.0sec												
├─ med-Be/Open med-Be/Open close Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
├─ med-Be/Open med-Be/Open close Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
├─ med-Be/Open med-Be/Open close Safe Norm 4.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
Seqn= 95 1-time(s) 2.0sec												
├─ med-Al/Open med-Al/Open close Safe Norm 354ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
├─ med-Al/Open med-Al/Open close Safe Norm 4.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
├─ med-Al/Open med-Al/Open close Safe Norm 16.0s Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
Subr= 2 1-time(s) 2.0sec												
Seqn= 2 1-time(s) 2.0sec												
├─ med-Al/Open med-Al/Open close Safe Dark 12ms Obs 4x4 2048x2048 (1024, 1024) DPCM 0 0 2.0sec												
├─ med-Al/Open med-Al/Open close Safe Dark 1.00s Obs 4x4 2048x2048 (1024, 1024) DPCM 0 0 2.0sec												
├─ med-Al/Open med-Al/Open close Safe Dark 32.0s Obs 4x4 2048x2048 (1024, 1024) DPCM 0 0 2.0sec												
Subr= 3 1-time(s) 2.0sec												
Seqn= 82 1-time(s) 2.0sec												
├─ Open/Al-mesh Open/Al-mesh close Safe Norm 4ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
├─ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
├─ Open/Al-mesh Open/Al-mesh close Safe Norm 707ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
Seqn= 31 1-time(s) 2.0sec												
├─ Open/thick-Al Open/thick-Be close Safe Norm 16.0s Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
├─ Open/thick-Al Open/thick-Be close Safe Norm 32.0s Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
Seqn= 64 1-time(s) 2.0sec												
├─ Open/thick-Be Open/thick-Be close Safe Norm 32.0s Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
<div style="display: flex; justify-content: space-between; font-size: small;"> Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval </div>												

XOB #1ADF: Synoptic 7 Filter w/ Al-mesh(8/128/1024), Al-poly(16/362/1443), Thin-Be(88/1024/5795) - Thick-Be(65536), Al-poly+Ti-poly(256/2048), Med-Al(40												
Term	Pointing (x, y)						Comment					
09/06 12:05:00 - 09/06 15:31:54	Fixed (0.0, 0.0)						(3/15)					
PROG= 08 1-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 5 1-time(s) 2.0sec												
├─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
├─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
├─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
├─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec												
├─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec												
Seqn= 33 1-time(s) 2.0sec												
├─ Open/Al-mesh Open/Al-mesh close Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
├─ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
├─ Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Seqn= 49 1-time(s) 2.0sec												
├─ Al-poly/Open Al-poly/Open close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
├─ Al-poly/Open Al-poly/Open close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
├─ Al-poly/Open Al-poly/thick-Al close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Seqn= 77 1-time(s) 2.0sec												
├─ thin-Be/Open thin-Be/Open close Safe Norm 86ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
├─ thin-Be/Open thin-Be/Open close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
├─ thin-Be/Open thin-Be/Open close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Seqn= 54 1-time(s) 4.0sec												
├─ Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
├─ Open/G-band Open/G-band close Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Subr= 2 1-time(s) 2.0sec												
Seqn= 46 2-time(s) 2.0sec												
├─ Open/thick-Be Open/thick-Be close Safe Norm 64.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
Seqn= 72 2-time(s) 2.0sec												
├─ Al-poly/Ti-poly Al-poly/thick-Al close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
├─ Al-poly/Ti-poly Al-poly/thick-Al close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
Seqn= 59 2-time(s) 2.0sec												
├─ med-Al/Open med-Al/thick-Al close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
├─ med-Al/Open med-Al/Open close Safe Norm 22.6s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #1B0A: Synoptic Q95 2x2 - Al/mesh(12/181/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(24/362/1443) + T

Term	Pointing (x, y)	Comment
09/06 15:35:00 - 09/06 15:59:54	Fixed (0.0, 0.0)	synoptic, shifted manually
09/07 05:59:00 - 09/07 06:06:02	Fixed (0.0, 0.0)	synoptic, shifted -4.0 min

PROG= 10 1-time(s)

└ **Subr= 1 1-time(s) 2.0sec**

└ **Seqn= 5 1-time(s) 2.0sec**

Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

└ **Seqn= 91 1-time(s) 2.0sec**

Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

└ **Seqn= 93 1-time(s) 2.0sec**

Al-poly/Open	Al-poly/Open	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

└ **Seqn= 77 1-time(s) 2.0sec**

thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

└ **Seqn= 54 1-time(s) 2.0sec**

Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1AFF: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
09/06 16:34:00 - 09/06 22:37:30	Track (102.1, -26.0) @ 09/06 16:00:00	HOP320
09/06 23:03:00 - 09/07 05:55:54	Track (165.9, -24.8) @ 09/06 23:00:00	AR12585 obs

PROG= 01 Inf.-time(s)

└ **Subr= 1 1-time(s) 2.0sec**

└ **Seqn= 56 1-time(s) 2.0sec**

Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

└ **Subr= 2 5-time(s) 2.0sec**

└ **Seqn= 75 1-time(s) 2.0sec**

Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

└ **Seqn= 96 4-time(s) 60.0sec**

Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	15.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	15.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Flare mode

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XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
09/06 16:34:00 - 09/06 22:37:30	Track (102.1, -26.0) @ 09/06 16:00:00	HOP320
09/06 23:03:00 - 09/07 05:55:54	Track (165.9, -24.8) @ 09/06 23:00:00	AR12585 obs

PROG= 07 30-time(s)

└ **Subr= 1 20-time(s) 2.0sec**

└ **Seqn= 11 1-time(s) 2.0sec**

Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
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└ **Seqn=100 1-time(s) 10.0sec**

thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

└ **Subr= 2 1-time(s) 2.0sec**

└ **Seqn= 10 1-time(s) 2.0sec**

med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

└ **Seqn= 11 1-time(s) 2.0sec**

Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
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└ **Seqn= 84 1-time(s) 2.0sec**

Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

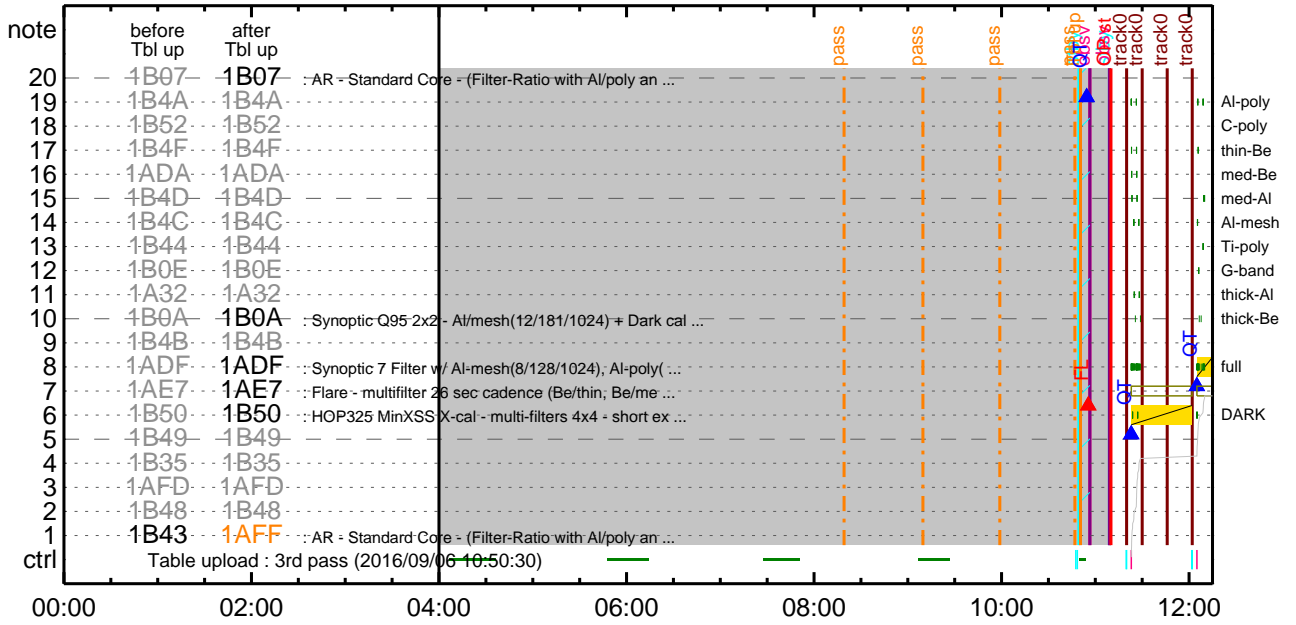
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Flare Detection

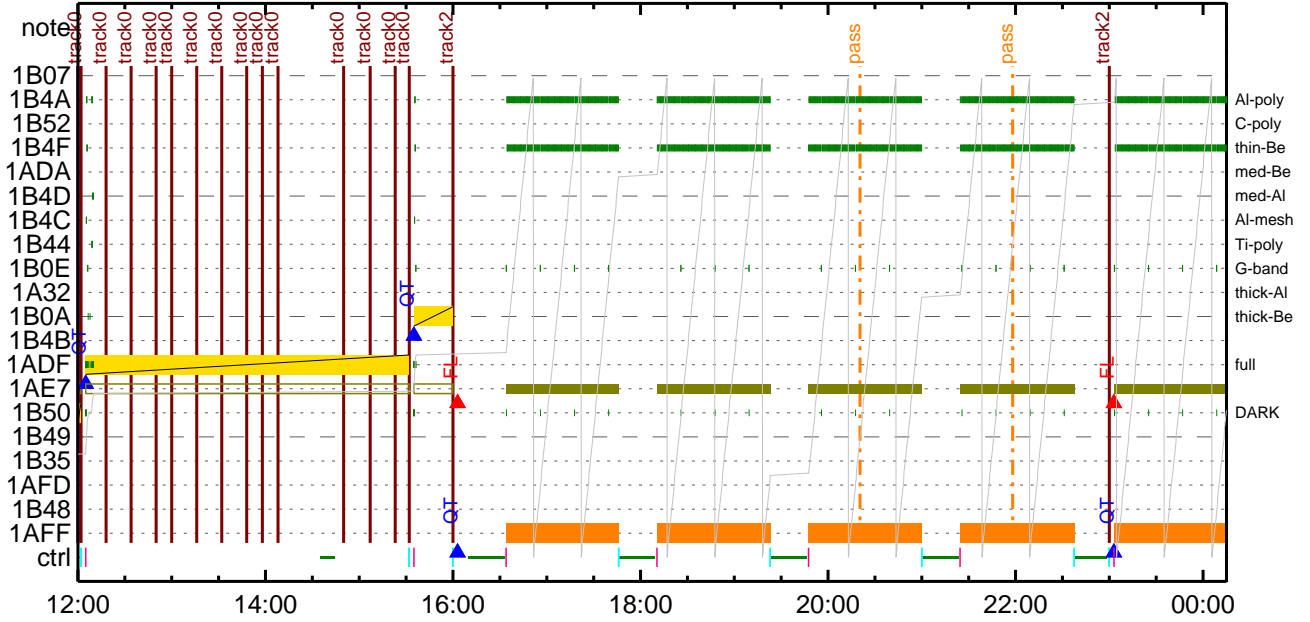
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FLD Patrol												
Term	Pointing (x, y)						Comment					
09/06 16:00:18 - 09/07 05:56:18	Track (102.1, -26.0) ^{09/06 16:00:00}						HOP320					
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50				30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

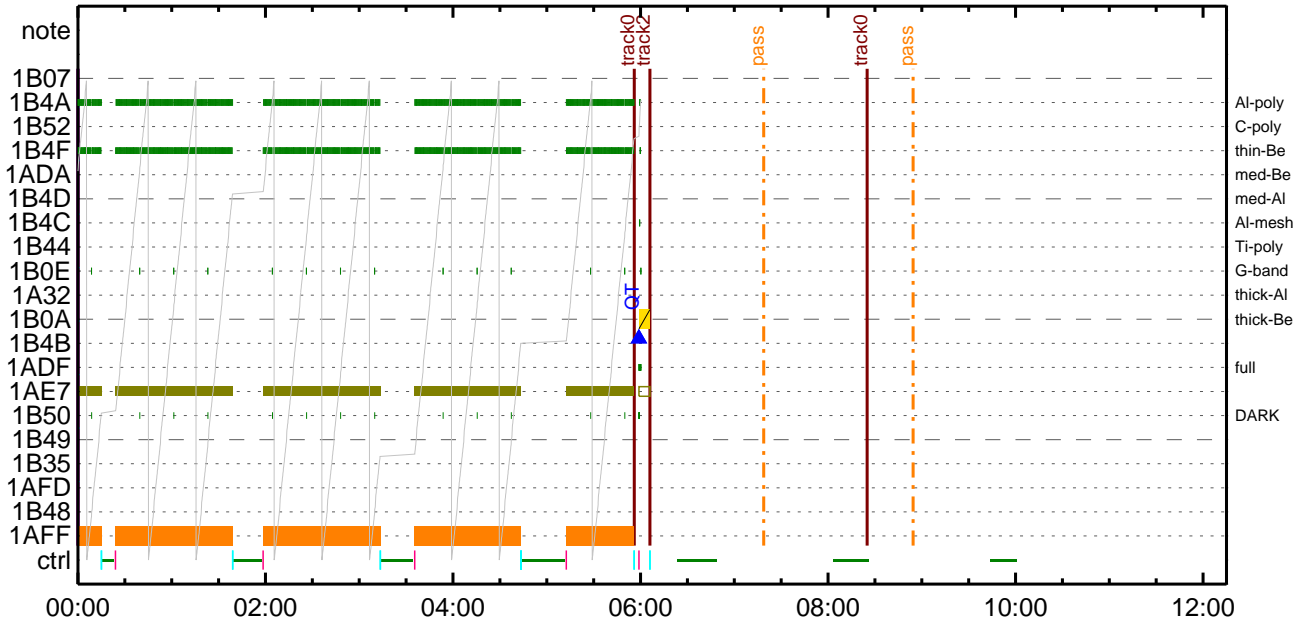
CMDI #0160 2016/09/06



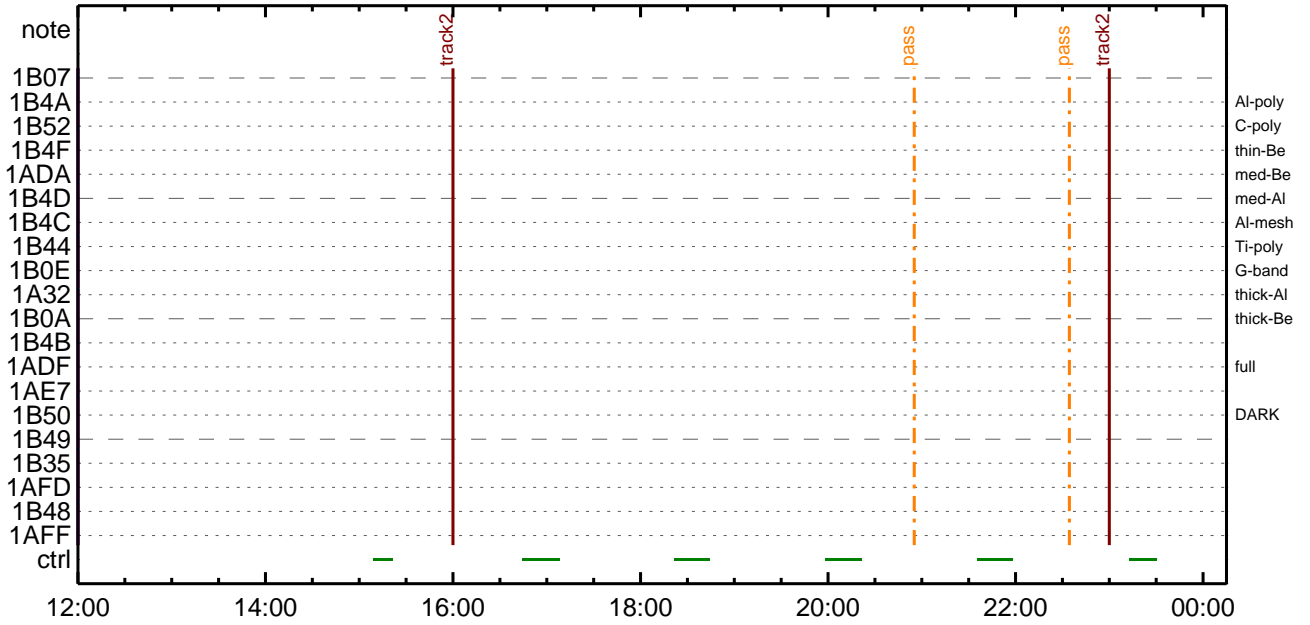
CMDI #0160 2016/09/06



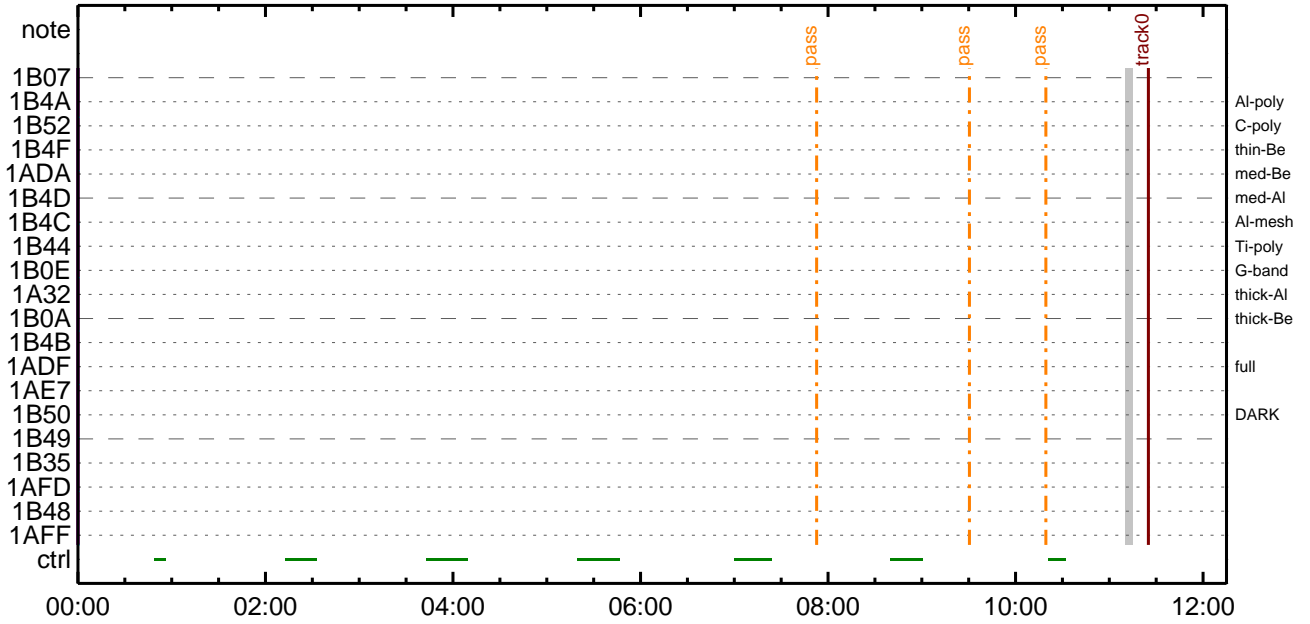
CMDI #0160 2016/09/07



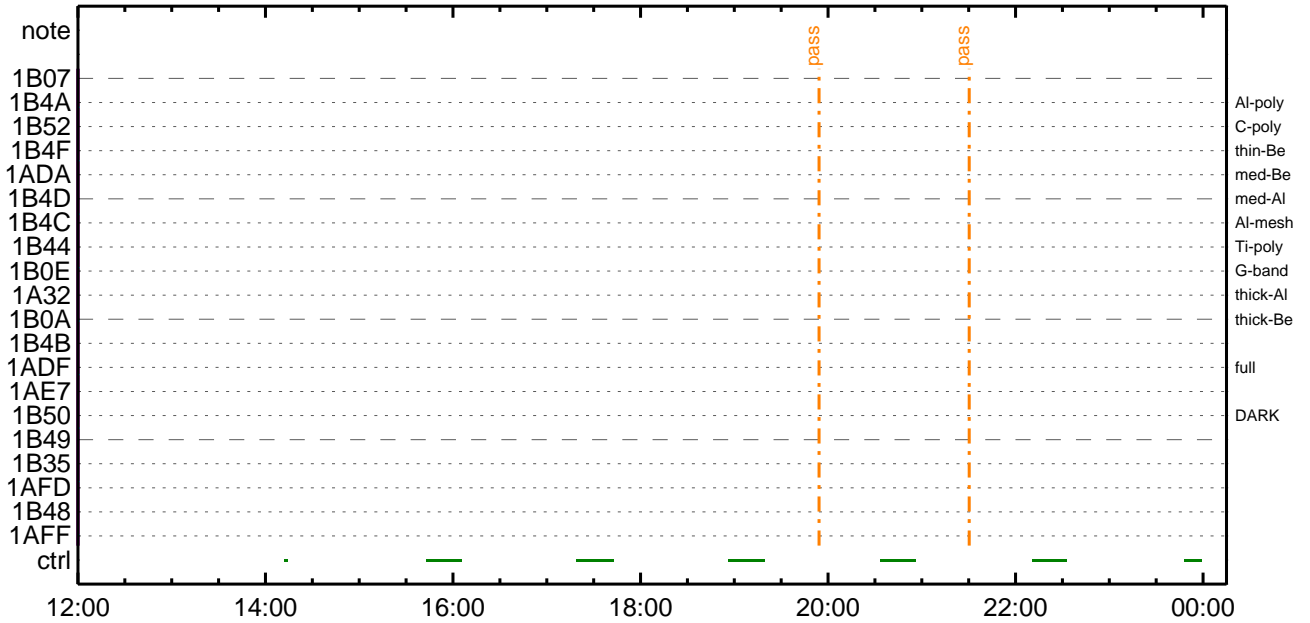
CMDI #0160 2016/09/07



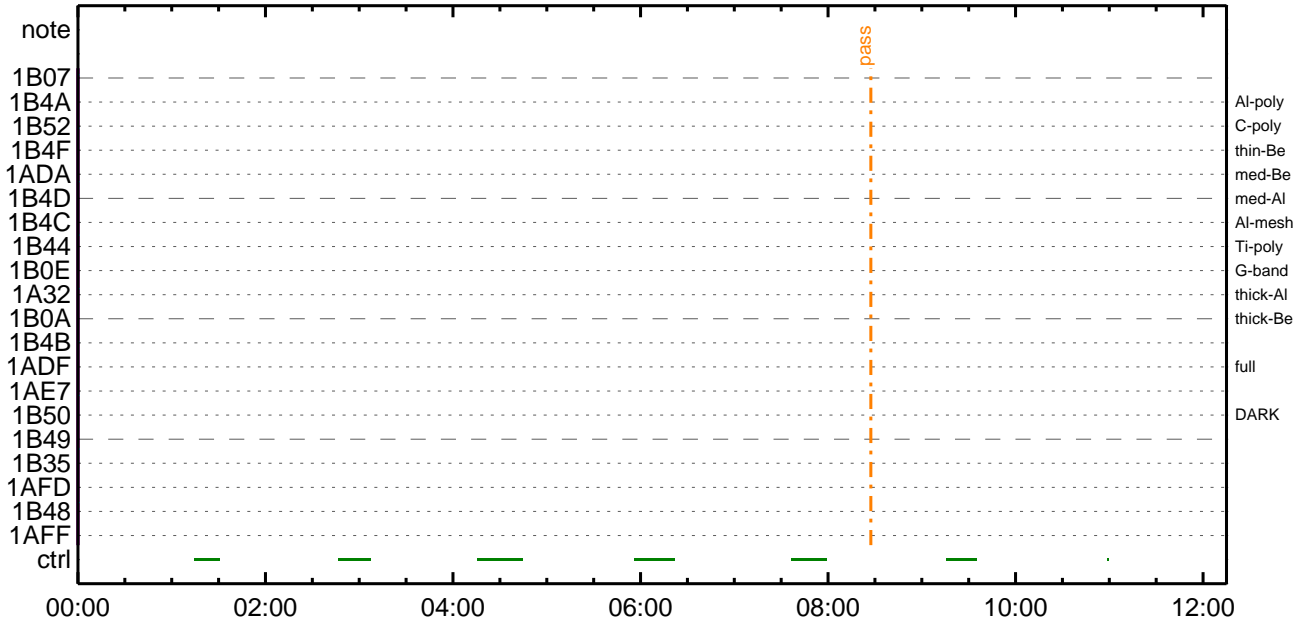
CMDI #0160 2016/09/08



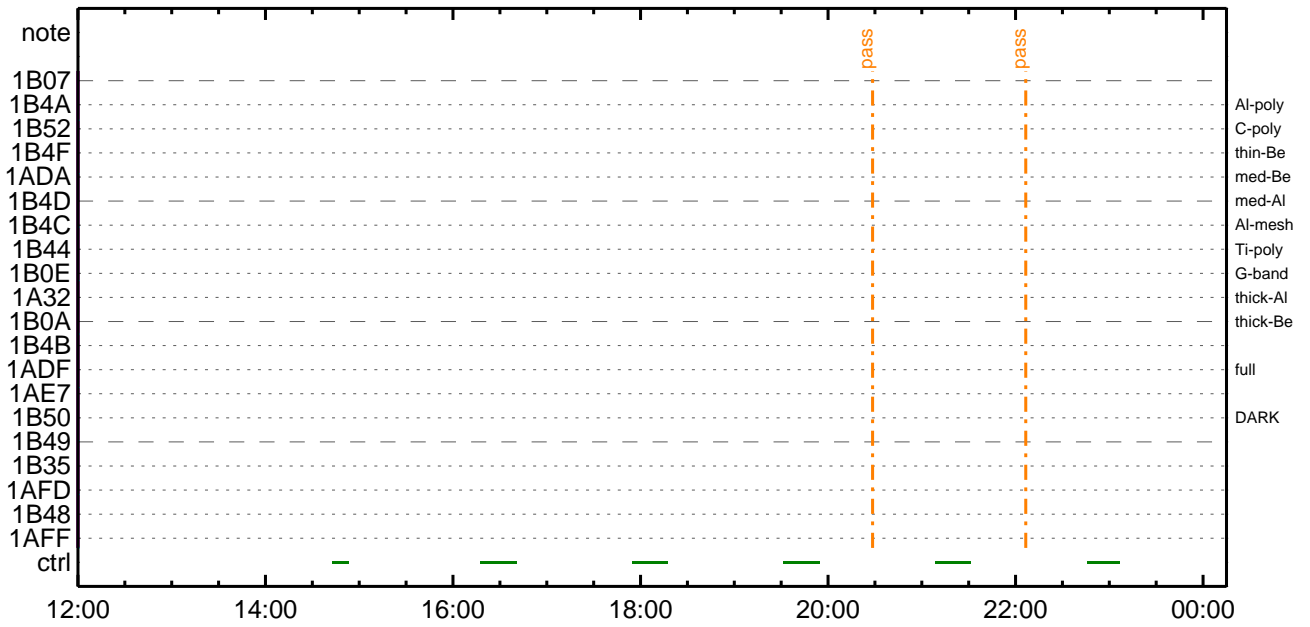
CMDI #0160 2016/09/08



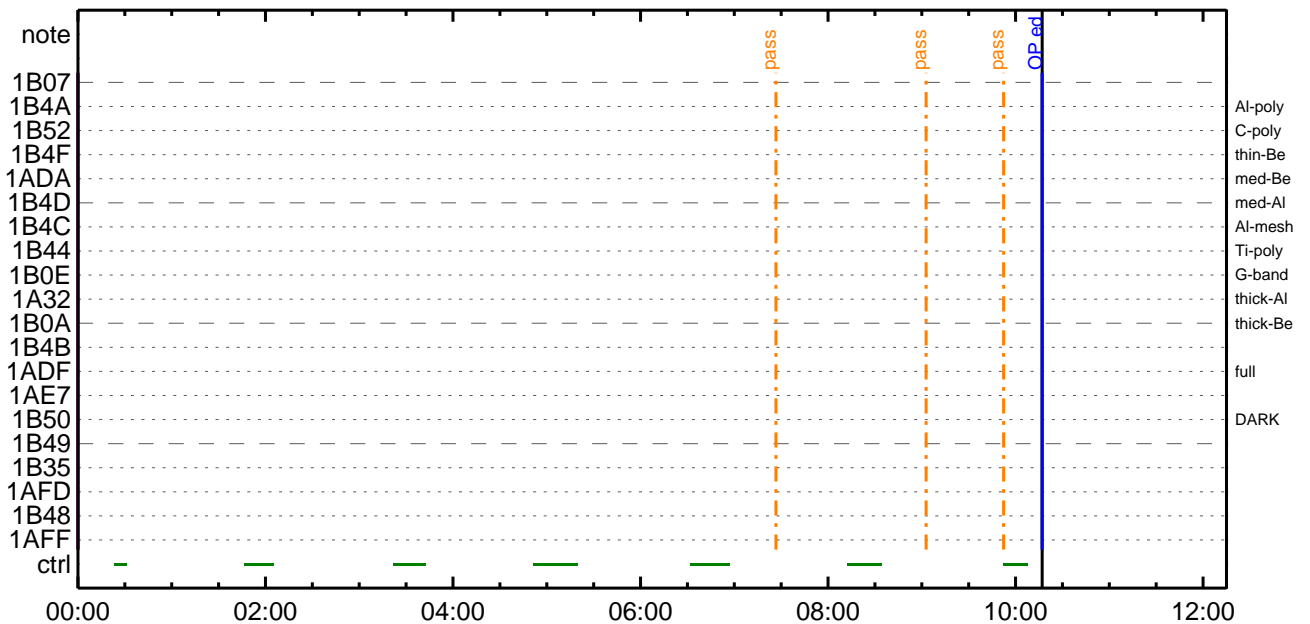
CMDI #0160 2016/09/09



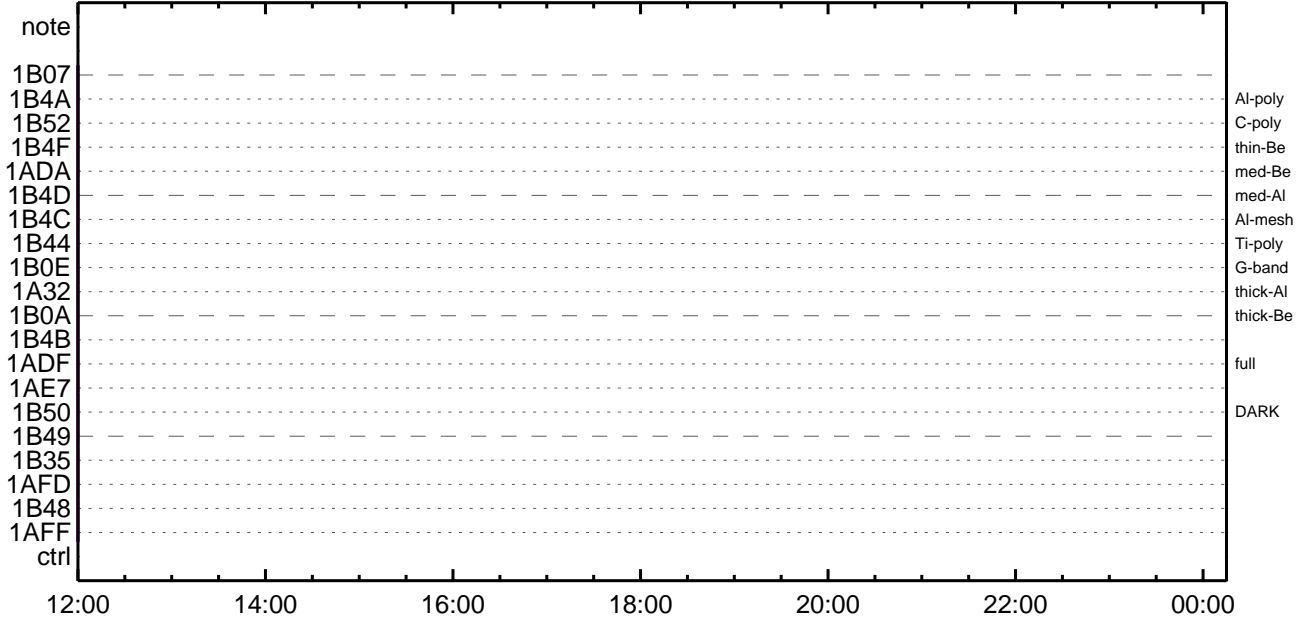
CMDI #0160 2016/09/09



CMDI #0160 2016/09/10



CMDI #0160 2016/09/10



(a) Spacecraft Operation Procedure (real-commands)

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main-097 2016-09-06 12:04:13 289 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSÝÁÝŞÝÄÝ-¼Ä»Û;ä
0005 C.
0006 C. ÝÀÝß;¼Ý³ÝÞÝóÝÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Éð¿ðÃð•µ°Æ»Í×ÃÇðÍÝÇÝÄÝ×ÝÍ;¼ÝÉ;ÉÈÈ¼µ•ííÉ;ÈðÈ¼°Çðð•ð¿¼í¹çðí;çÀ®, ùð¹ðÈðÞðÇÄ+¿®ð•ðÈððð³ðÈ;ß
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+¿µ;ON
0016 C. *****
0017 C. °ÆÀ, Í×ÈÝðÃLOSðÞçðí»P´Öðð¹íí, ð•; çÉÔÍ×ðÈXÃÓONðí¹ÖðÈðíðÈððð³ðÈ;ß
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 +. DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 +. DC 03-95 TCIA_XMOD_QPSK
0024 C.               çç[HK1_XPA_ON/OFF]                EQ        ON
0025 C.               çç[HK1_XPA_PWR_HI/LO]             EQ        HI
0026 C.               çç[HK1_XMOD_ON/OFF]                EQ        ON
0027 C.               çç[HK1_XMOD_QPSK/PM]                EQ        QPSK
0028 C.
0029 . C. XÝÐÝóÝÉÝíÝÄÝ-¾ÔÀÖð-°ÁÄÈð•ð¿ðé; ç°È²¼ðí°ÆÀ, ¼È¾çððð¼Á¹Öð¹ðÈ;ß
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°ÆÀ,
0033 C. *****
0034 C. ° RESTART;ÈPT1;Èð•ð¿ð¼í¹çðí; ç°È²¼ðí°ÆÀ¹Öð»ð°; çDCBC-150ðØ¿Èðà;ß
0035 C.
0036 . C. ;ãPT1°ÆÀ, ³«»í;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 +. DC 06-C8 DR_PT1_REP_SEL
0039 BC               (01 00)
0040 +. DC 06-B3 DR_REP_START
0041 +. DC 01-32 DHU_X_VC4_ON
0042 C.               çç[HK1_REP_PT_1/2]                EQ        PT1    (¼Á¹Ö, ;¼Ú)
0043 C.               çç[HK1_REP_STA/STP]                 EQ        START  (¼Á¹Ö, ;¼Ú)
0044 C.               çç[HK1_X_VC4_ON/OFF]                 EQ        ON     (¼Á¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãÝçÝóÝÉÝÄÚÁÖ;ÈÄ•Ä°²óÈð;È, áðí°ÆÀ, °Æ³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 +. DC 01-32 DHU_X_VC4_ON
0049 C.               çç[HK1_REP_PT_1/2]                EQ        PT1    (¼Á¹Ö, ;¼Ú)
0050 C.               çç[HK1_REP_STA/STP]                 EQ        START  (¼Á¹Ö, ;¼Ú)
0051 C.               çç[HK1_X_VC4_ON/OFF]                 EQ        ON     (¼Á¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÀ, ð-¼«Æ°Äá»ßð•ð¿, á; ç°È²¼ðí°ÆÀ¹Öð¹ðÈ;ß
0055 C. ÝçÝóÝÉÝÄÚÁÖðãÄ•Ä°²óÈðð-¾áð¼í¹çðí°í»ð¹ðÈðÞðÇÄÖðÄ;ß
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°ÆÀ,
0059 C. *****
0060 C. ° RESTART;ÈPT2;Èð•ð¿ð¼í¹çðí; ç°È²¼ðí°ÆÀ¹Öð»ð°; çDCBC-151ðØ¿Èðà;ß
0061 C.
0062 . C. ;ãPT2°ÆÀ, ³«»í;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 +. DC 06-C8 DR_PT2_REP_SEL
0065 BC               (02 00)
0066 +. DC 06-B3 DR_REP_START
0067 +. DC 01-32 DHU_X_VC4_ON
0068 C.               çç[HK1_REP_PT_1/2]                EQ        PT2    (¼Á¹Ö, ;¼Ú)
0069 C.               çç[HK1_REP_STA/STP]                 EQ        START  (¼Á¹Ö, ;¼Ú)
0070 C.               çç[HK1_X_VC4_ON/OFF]                 EQ        ON     (¼Á¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãÝçÝóÝÉÝÄÚÁÖ;ÈÄ•Ä°²óÈð;È, áðí°ÆÀ, °Æ³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 +. DC 01-32 DHU_X_VC4_ON
0075 C.               çç[HK1_REP_PT_1/2]                EQ        PT2    (¼Á¹Ö, ;¼Ú)
0076 C.               çç[HK1_REP_STA/STP]                 EQ        START  (¼Á¹Ö, ;¼Ú)
0077 C.               çç[HK1_X_VC4_ON/OFF]                 EQ        ON     (¼Á¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÀ, Äá»ß;çXÁ+¿µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÀ, Äá»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 +. DC 01-29 DHU_S/X_VC4_OFF
0086 C.               çç[HK1_REP_STA/STP]                 EQ        STOP
0087 C.               çç[HK1_S_VC4_ON/OFF]                 EQ        OFF
0088 C.               çç[HK1_X_VC4_ON/OFF]                 EQ        OFF
0089 C.
0090 . C. ;ãXÁ+¿µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 +. DC 03-B5 TCIA_XPA_OFF
0094 C.               çç[HK1_XMOD_ON/OFF]                 EQ        OFF
0095 C.               çç[HK1_XPA_ON/OFF]                   EQ        OFF
```



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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-097:OP
0104 ( )
0105 S. OG og-097:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½Ä´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOXx½ê½çòðÁÓÆòÇ¼ª°¬òE¼î¹çòÇòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yi;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½î¹ç;ç°E²¼òîTI-CMDÁ÷¿@²î½Á¹Ôª°¬E²ò³òE;f
0180 C. ²²ò¿;çSET²EEDUMP²î½±°îYÑY¹ç¹Ôª|²³òE;f
0181 C.
0182 C. TIY³Y²YóYÉòðÁDî¿(UT)
0183 +. TI 2016-09-06 11:05:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2016-09-06 11:05:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2016-09-06 11:05:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2016-09-06 11:09:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]             EQ       1COUNTUP
0198 C.
0199 C. °È²¼□İÄè%İİñ□İŷÄŷ§ŷÄŷ-¹àİŰ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]         EQ       ENA
0201 C.          çç[HK1_TI_CMD_NUM]             EQ       4
0202 C.          çç[HK1_NEXT_EXEC_PIM]         EQ       DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]         EQ       0xB3
0204 C.
0205 C. *****
0206 C. TIİİŧ°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]       EQ       07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]       EQ       2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]        EQ       3
0215 C.          çç[HK1_DMP_REPEAT_NUM]       EQ       0
0216 C.          çç[HK1_DMA_DMP_PIM]          EQ       DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]          EQ       7
0220 C.          çç[HK1_PKT_GEN_TIME]         EQ       0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]       EQ       32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]       EQ       4M
0223 C.          çç[HK1_DMP_CHK_FLG]         EQ       EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªİ»□ð³İÇ§
0226 C.          çç[HK1_DMP_CHK_FLG]         EQ       NON
0227 C.
0228 C. RAM ID=TI_TBL□İŷÈ¹ç•è²İOK□ð³İÇ§
0229 C.
0230 C. DHUŷâ;¼ŷÈ;È¼ŷ¼. ŷİ;¼ŷÈ;È□ðİä□
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]          EQ       2
0234 C.          çç[HK1_PKT_GEN_TIME]         EQ       0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]       EQ       32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]       EQ       4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2016-09-06 11:09:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC (41)
0245 C. -----
0246 C.   HK1_TI_CMD_NUM           = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2016-09-06 11:09:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC (21 02)
0258 +. TI 2016-09-06 11:09:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]         EQ       2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2016-09-06 11:09:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]         EQ       1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÄİäİ»ö¼ŷ□ÈÄð□ÈDCBC•x²è *****
0276 C. (¼á°İŷÖŷÄŷÈŷŷŷÈŷáŷçŷè□È¼□□¼Ä»Ű□è)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷĐŷ¹•İ Daily±çİñ□È´Ø□ÈDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼Ä»Ű;ä
0287 C.
0288 C. ***** LOS *****
0289 C.

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(a) Spacecraft Operation Procedure (real-commands)

```

main-099 2016-09-06 12:04:13 124 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY~¼Á»Ü;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. ÁíÊ¿ââ•µ°È»Í×ÁÇóÍYçYÁY×Yí;¼YÉ;ÉÈ%µ•ííÉ;ÈBÈ¼°ÇÔâ•â¿¼í¹çâÍ;çÁ®, ùâ¹ââBâçÁ+¿®â•âÉâââ³âÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop SP table >
0018 +. DC 07-F0 MDP_SP_CTRL_MANU
0019 BC (61)
0020 C. -----
0021 C. MDP_SP_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload SP Observation Table>
0025 . S. RAM ram-281:MDP_OBS_S
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_S >
0029 +. DC 07-F0 MDP_DUMP_SPTBL
0030 BC (83 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_S verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2016-09-06 11:09:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 . C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 +. DC 07-F0 MDP_XRT_CTRL_MANU
0052 BC (c1)
0053 +. DC 07-F0 MDP_XRT_MODE_STBY
0054 BC (c3)
0055 . C. ----- Success Verify ? OK / NG____
0056 C.
0057 C. XRT Obs. Table Upload
0058 . S. RAM ram-291:MDP_OBS_X
0059 ( )
0060 C.
0061 +. DC 07-F0 MDP_DUMP_XRTTBL
0062 BC (84 07 00 00 00 3a d4)
0063 . C. ----- Comparison Check ? OK / ERR _____
0064 C.
0065 C.
0066 +. DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 01 b1 b1 04 04)
0068 +. DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 02 b1 b1 08 08)
0070 +. DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 03 b1 b1 08 08)
0072 +. DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 04 b1 b1 06 06)
0074 +. DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 06 85 83 06 06)
0076 +. DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 07 80 80 20 20)
0078 +. DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 08 80 80 20 08)
0080 +. DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 09 80 80 08 20)
0082 +. DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 0f 80 80 06 06)
0084 +. DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 10 80 80 08 08)
0086 +. DC 07-F0 MDP_XRT_FLD_DIS
0087 BC (d9)
0088 +. DC 07-F0 MDP_XRT_FLRCTRL_DIS
0089 BC (c9)
0090 +. DC 07-F0 MDP_XRT_ARS_DIS
0091 BC (d5)
0092 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0093 BC (c4 14)
0094 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0095 BC (c5 07)

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0096 . C. ----- Success Verify ? OK / NG ____
0097 C.
0098 C.
0099 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0100 C.
0101 +. DC 07-F0 MDP_XRT_MODE_OBSV
0102 BC (c2)
0103 +. TI 2016-09-06 11:09:02.0
0104 DC 07-F0 MDP_XRT_MODE_OBSV
0105 BC (c2)
0106 . C. ----- Success Verify ? OK / NG ____
0107 C.
0108 C. ***** XRT END *****
0109 C.
0110 . C. ***** MDP 'úÃîâî»ö¼ÝðËÂð¹ñèDCBC•x²è *****
0111 C. (¼ã°îÿÓÿÄÿÈÿÞÿËÿÁÿçÿèñÈ¼ññ¼Ã»Û¹ñè)
0112 . S. DC-BC dcbc-402:DCBC
0113 (MDP_known_event)
0114 C.
0115 C.
0116 . C. ***** ÿÐÿ¹•Ï Daily±¿ÎÑñË'Ø¹ñèDCBC•x²è *****
0117 . S. DC-BC dcbc-153:DCBC
0118 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0119 C.
0120 C.
0121 . C. ;ãLOSÿÁÿ\$ÿÃÿ-¼Ã»Û;ã
0122 C.
0123 . C. ***** LOS *****
0124 C.

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*** OP Sequence for XRT ***

2016/09/06	11:19:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/09/06	11:19:56.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/09/06	11:19:58.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2016/09/06	11:20:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2016/09/06	11:20:18.0	XRT_FLD_DIS_406_OG [0x196]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2016/09/06	11:22:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2016/09/06	11:22:56.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/09/06	11:22:58.0	XRT_QT_PROG_SET_432_OG [0x1b0]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06		
2016/09/06	11:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/09/06	11:30:00.0	AOCS_Ore-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 ac cd		
2016/09/06	11:46:00.0	AOCS_Ore-point_Start_3_OG [0x099]					
		AOCU_NM	5	02-76	00 00 00 00 d6 67		
2016/09/06	12:01:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/09/06	12:01:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/09/06	12:01:58.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2016/09/06	12:02:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 00 00 00 00 00		
2016/09/06	12:02:18.0	XRT_FLD_DIS_406_OG [0x196]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2016/09/06	12:04:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2016/09/06	12:04:56.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/09/06	12:04:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08		
2016/09/06	12:05:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/09/06	12:18:00.0	AOCS_Ore-point_Start_4_OG [0x09a]					
		AOCU_NM	5	02-76	00 00 00 00 29 99		
2016/09/06	12:34:00.0	AOCS_Ore-point_Start_5_OG [0x09b]					
		AOCU_NM	5	02-76	00 00 00 00 53 33		
2016/09/06	12:50:00.0	AOCS_Ore-point_Start_6_OG [0x09c]					
		AOCU_NM	5	02-76	00 d6 36 b7 8e		
2016/09/06	13:00:00.0	AOCS_Ore-point_Start_7_OG [0x09d]					
		AOCU_NM	5	02-76	00 b4 b5 db 75		
2016/09/06	13:16:00.0	AOCS_Ore-point_Start_8_OG [0x09e]					
		AOCU_NM	5	02-76	00 ac 5b 00 00		
2016/09/06	13:32:00.0	AOCS_Ore-point_Start_9_OG [0x09f]					
		AOCU_NM	5	02-76	00 b4 b5 24 8b		
2016/09/06	13:48:00.0	AOCS_Ore-point_Start_10_OG [0x0a0]					
		AOCU_NM	5	02-76	00 d6 36 48 72		
2016/09/06	13:58:00.0	AOCS_Ore-point_Start_11_OG [0x0a1]					
		AOCU_NM	5	02-76	00 29 ca b7 8e		
2016/09/06	14:08:00.0	AOCS_Ore-point_Start_12_OG [0x0a2]					
		AOCU_NM	5	02-76	00 4b 4b db 75		
2016/09/06	14:50:00.0	AOCS_Ore-point_Start_13_OG [0x0a3]					
		AOCU_NM	5	02-76	00 53 a5 00 00		
2016/09/06	15:07:00.0	AOCS_Ore-point_Start_14_OG [0x0a4]					
		AOCU_NM	5	02-76	00 4b 4b 24 8b		
2016/09/06	15:23:00.0	AOCS_Ore-point_Start_15_OG [0x0a5]					
		AOCU_NM	5	02-76	00 29 db 48 72		
2016/09/06	15:31:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/09/06	15:31:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/09/06	15:31:58.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2016/09/06	15:32:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 00 00 00 00 00		
2016/09/06	15:32:18.0	XRT_FLD_DIS_406_OG [0x196]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2016/09/06	15:34:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2016/09/06	15:34:56.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/09/06	15:34:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a		
2016/09/06	15:35:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/09/06	15:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/09/06	15:59:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/09/06	15:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2016/09/06	16:00:00.0	AOCS_Ore-point_Start_16_OG [0x0a6]					
		AOCU_NM	5	02-76	02 00 00 00 00 00		
2016/09/06	16:00:18.0	XRT_FLD_ENA_411_OG [0x19b]					

2016/09/06	16:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/09/06	16:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/09/06	16:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/09/06	16:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/09/06	16:02:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/09/06	16:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
2016/09/06	16:33:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2016/09/06	16:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2016/09/06	17:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/09/06	17:46:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/09/06	17:46:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/09/06	17:46:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/09/06	17:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/09/06	18:09:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/09/06	18:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2016/09/06	19:23:00.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/09/06	19:23:03.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/09/06	19:23:06.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/09/06	19:23:08.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/09/06	19:26:16.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/09/06	19:46:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/09/06	19:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2016/09/06	21:00:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/09/06	21:00:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/09/06	21:00:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/09/06	21:00:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/09/06	21:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/09/06	21:23:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/09/06	21:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2016/09/06	22:37:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/09/06	22:37:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/09/06	22:37:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/09/06	22:37:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/09/06	22:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/09/06	22:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/09/06	22:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_CTRL_MANU_402_OG [0x192]				
2016/09/06	22:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/09/06	23:00:00.0	AOCS_Ore-point_Start_16_OG [0x0a6]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2016/09/06	23:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02	00 00 00 00
2016/09/06	23:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/09/06	23:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/09/06	23:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/09/06	23:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/09/06	23:02:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/09/06	23:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
2016/09/06	23:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2016/09/07	00:15:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_408_OG [0x198]				
2016/09/07	00:15:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
			MDP_XRT_CTRL_MANU	1	07-F0	c1	

2016/09/07	00:15:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/09/07	00:15:06.0	XRT_PREFLR_STRT_414_OG [0x19e]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/09/07	00:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/09/07	00:23:00.0	XRT_Custom_430_OG [0x1ae]						
2016/09/07	00:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/09/07	01:39:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/09/07	01:39:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/09/07	01:39:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/09/07	01:39:06.0	XRT_PREFLR_STRT_414_OG [0x19e]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/09/07	01:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/09/07	01:57:30.0	XRT_Custom_430_OG [0x1ae]						
2016/09/07	01:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/09/07	03:13:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/09/07	03:13:32.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/09/07	03:13:34.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/09/07	03:13:36.0	XRT_PREFLR_STRT_414_OG [0x19e]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/09/07	03:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/09/07	03:34:30.0	XRT_Custom_430_OG [0x1ae]						
2016/09/07	03:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/09/07	04:43:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/09/07	04:43:32.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/09/07	04:43:34.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/09/07	04:43:36.0	XRT_PREFLR_STRT_414_OG [0x19e]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/09/07	04:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/09/07	05:11:30.0	XRT_Custom_430_OG [0x1ae]						
2016/09/07	05:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/09/07	05:55:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/09/07	05:55:56.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/09/07	05:55:58.0	XRT_FOCUS_POSITION_403_OG [0x193]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2016/09/07	05:56:00.0	AOCS_Ore-point_Start_1_OG [0x097]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2016/09/07	05:56:18.0	XRT_FLD_DIS_406_OG [0x196]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2016/09/07	05:58:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2016/09/07	05:58:56.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/09/07	05:58:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a			
2016/09/07	05:59:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/09/07	06:06:00.0	AOCS_Ore-point_Start_16_OG [0x0a6]						
		AOCU_NM	5	02-76	02 00 00 00 00			
2016/09/07	06:06:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/09/07	06:06:04.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/09/07	06:07:00.0	XRT_TCIB_XRT_S_HTR_A_ENA_441_OG [0x1b9]						
		TCIB_XRT_S_HTR_A_ENA	0	04-BC				
2016/09/07	08:25:00.0	AOCS_Ore-point_Start_17_OG [0x0a7]						
		AOCU_NM	5	02-76	00 b3 8e 01 26			
2016/09/07	16:00:00.0	AOCS_Ore-point_Start_16_OG [0x0a6]						
		AOCU_NM	5	02-76	02 00 00 00 00			
2016/09/07	23:00:00.0	AOCS_Ore-point_Start_16_OG [0x0a6]						
		AOCU_NM	5	02-76	02 00 00 00 00			
2016/09/08	11:25:00.0	AOCS_Ore-point_Start_1_OG [0x097]						
		AOCU_NM	5	02-76	00 00 00 00 00			