

XRT Timeline to be uploaded on 2016/09/10

Period: 2016/09/10 10:17:00 - 2016/09/15 09:58:00

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Normal mode

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XOB #1AF1: CCD Monitor During Bakeout - G-band 3ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh(512ms), Al/Poly(1443ms) - w leak image-3ms

Term	Pointing (x, y)	Comment
09/10 12:03:00 - 09/10 12:09:54	Fixed (-528.4, -528.4)	XRT Quadrant pointing (1/4)
PROG= 13 1-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 86 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (1536, 1536)	Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (1536, 1536)	Q=90 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (1536, 1536)	Q=98 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (1536, 1536)	Q=98 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 55 2-time(s) 2.0sec		
└─ Open/Al-mesh	Open/Ti-poly close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Al-poly/Open	med-Be/Open close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 54 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024)	Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band close Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center)	Comp. AEC Buffer Interval

XOB #1AF2: CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh (512ms), Al/Poly (1443ms) - w leak image-3 ms

Term	Pointing (x, y)	Comment
09/10 12:13:00 - 09/10 12:19:54	Fixed (528.4, -528.4)	(2/4)
PROG= 03 1-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 15 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (512, 1536)	Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (512, 1536)	Q=90 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (512, 1536)	Q=98 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (512, 1536)	Q=98 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 55 2-time(s) 2.0sec		
└─ Open/Al-mesh	Open/Ti-poly close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Al-poly/Open	med-Be/Open close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 54 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024)	Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band close Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center)	Comp. AEC Buffer Interval

XOB #1AF3: CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - Q90 - 3rd Quadrant - Al/mesh (512ms), Al/Poly (1443ms) - w leak image-3 ms

Term	Pointing (x, y)	Comment
09/10 12:23:00 - 09/10 12:29:54	Fixed (528.4, 528.4)	(3/4)
PROG= 04 1-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 35 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (512, 512)	Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (512, 512)	Q=90 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (512, 512)	Q=98 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (512, 512)	Q=98 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 55 2-time(s) 2.0sec		
└─ Open/Al-mesh	Open/Ti-poly close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Al-poly/Open	med-Be/Open close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 54 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024)	Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band close Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center)	Comp. AEC Buffer Interval

XOB #1AF4: CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh (512ms), Al/Poly (1443ms) - w leak image-3 ms

Term	Pointing (x, y)	Comment
09/10 12:33:00 - 09/10 12:39:54	Fixed (-528.4, 528.4)	(4/4)
PROG= 05 1-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 3 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (1536, 512)	Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (1536, 512)	Q=90 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (1536, 512)	Q=98 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (1536, 512)	Q=98 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		

└─	Seqn= 55	2-time(s)	2.0sec																		
└─	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							
└─	Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							
└─	Subr= 3	2-time(s)	2.0sec																		
└─	Seqn= 54	1-time(s)	2.0sec																		
└─	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec							
└─	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval							

XOB #1B4D: HOP81/206 2-filter - Al/poly 8s, Al/mesh 6s 60s cadence, G-band - 384x384 3ms

Term	Pointing (x, y)	Comment												
09/10 12:43:30 - 09/10 18:59:54	Fixed (-18.0, 867.0)	HOP206 at N-pole												
PROG= 15	Inf.-time(s)													
└─	Subr= 1	1-time(s)	2.0sec											
└─	Seqn= 9	2-time(s)	2.0sec											
└─	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384	(1064, 1048)	DPCM	0	0	2.0sec
└─	Subr= 2	1-time(s)	2.0sec											
└─	Seqn= 7	1-time(s)	30.0sec											
└─	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384	(1064, 1048)	Q=90	0	0	2.0sec
└─	Subr= 3	30-time(s)	2.0sec											
└─	Seqn= 24	1-time(s)	60.0sec											
└─	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	5.66s	Obs	1x1	384x384	(1064, 1048)	Q=90	0	0	2.0sec
└─	Al-poly/Open	Al-poly/Open	close	Safe	Norm	8.00s	Obs	1x1	384x384	(1064, 1048)	Q=90	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval

XOB #1B0A: Synoptic Q95 2x2 - Al/mesh(12/181/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(24/362/1443) + T

Term	Pointing (x, y)	Comment
09/10 19:03:00 - 09/10 19:09:54	Fixed (0.0, 0.0)	synoptic, shifted manually
09/11 06:08:00 - 09/11 06:14:54	Fixed (0.0, 0.0)	synoptic, shifted 5.0 min
09/11 18:09:30 - 09/11 18:16:24	Fixed (0.0, 0.0)	synoptic, shifted 6.5 min
09/12 05:40:00 - 09/12 05:46:54	Fixed (0.0, 0.0)	synoptic, shifted -23.0 min
09/12 17:42:00 - 09/12 17:48:54	Fixed (0.0, 0.0)	synoptic, shifted -21.0 min
09/13 05:59:30 - 09/13 06:06:24	Fixed (0.0, 0.0)	synoptic, shifted -3.5 min

PROG= 10

└─	Subr= 1	1-time(s)	2.0sec											
└─	Seqn= 5	1-time(s)	2.0sec											
└─	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
└─	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
└─	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
└─	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512	(1024, 1024)	DPCM	0	0	2.0sec
└─	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048	(1024, 1024)	DPCM	0	0	2.0sec
└─	Seqn= 91	1-time(s)	2.0sec											
└─	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
└─	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
└─	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
└─	Seqn= 93	1-time(s)	2.0sec											
└─	Al-poly/Open	Al-poly/Open	close	Safe	Norm	24ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
└─	Al-poly/Open	Al-poly/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
└─	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
└─	Seqn= 77	1-time(s)	2.0sec											
└─	thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
└─	thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
└─	thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
└─	Seqn= 54	1-time(s)	2.0sec											
└─	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec
└─	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval

XOB #1B48: HOP316 (Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, and Med-Be context, with G-band (3ms/3ms VLS=CLS

Term	Pointing (x, y)	Comment												
09/10 19:13:00 - 09/10 23:59:54	Fixed (912.0, -150.0)	HOP316 at AR12584												
09/11 18:19:30 - 09/11 23:56:00	Fixed (910.0, 100.0)	HOP316 at AR12585												
09/12 17:52:00 - 09/12 23:59:54	Fixed (910.0, 100.0)	HOP316 at AR12585												
PROG= 02	Inf.-time(s)													
└─	Subr= 1	1-time(s)	2.0sec											
└─	Seqn= 56	1-time(s)	2.0sec											
└─	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384	(1064, 1048)	DPCM	0	0	2.0sec
└─	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384	(1064, 1048)	DPCM	0	0	2.0sec
└─	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384	(1064, 1048)	Q=98	0	0	2.0sec
└─	Subr= 2	5-time(s)	2.0sec											
└─	Seqn= 48	1-time(s)	2.0sec											
└─	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	2	0	2.0sec
└─	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	2.0sec
└─	med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1064, 1048)	Q=95	2	0	2.0sec
└─	med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	2.0sec
└─	Seqn= 96	5-time(s)	60.0sec											
└─	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384	(1064, 1048)	Q=95	1	0	2.0sec

thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	15.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	15.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1AFF: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
09/11 00:03:00 - 09/11 06:04:54	Track (839.3, 41.7) ^{Ⓢ 09/11 00:00:00}	AR12585 obs
09/13 00:03:00 - 09/13 05:56:24	Track (566.9, 129.5) ^{Ⓢ 09/13 00:00:00}	AR12589 obs
09/13 06:09:30 - 09/13 10:01:00	Track (609.2, 133.8) ^{Ⓢ 09/13 06:06:30}	AR12589 obs
PROG= 01 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 56 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Subr= 2 5-time(s) 2.0sec		
Seqn= 75 1-time(s) 2.0sec		
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Seqn= 96 4-time(s) 60.0sec		
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 15.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 15.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1B18: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
09/11 06:18:00 - 09/11 18:06:24	Track (864.5, 48.2) ^{Ⓢ 09/11 06:15:00}	AR12585 obs
09/12 00:07:00 - 09/12 01:21:30	Fixed (910.0, 100.0)	EIS spectral atlas at off-limb
09/12 01:40:00 - 09/12 05:36:54	Fixed (914.0, 56.0)	AR12585 obs(fixed pointing)
09/12 05:50:00 - 09/12 17:38:54	Fixed (914.0, 56.0)	AR12585 obs
PROG= 19 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 56 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Subr= 2 5-time(s) 2.0sec		
Seqn= 75 1-time(s) 2.0sec		
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Seqn= 50 4-time(s) 90.0sec		
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 25.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 25.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Flare mode

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XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
09/10 12:43:30 - 09/10 18:59:54	Fixed (-18.0, 867.0)	HOP206 at N-pole
09/10 19:13:00 - 09/10 23:59:54	Fixed (912.0, -150.0)	HOP316 at AR12584
09/11 00:03:00 - 09/11 06:04:54	Track (839.3, 41.7) ^{Ⓢ 09/11 00:00:00}	AR12585 obs
09/11 06:18:00 - 09/11 18:06:24	Track (864.5, 48.2) ^{Ⓢ 09/11 06:15:00}	AR12585 obs
09/11 18:19:30 - 09/11 23:56:00	Fixed (910.0, 100.0)	HOP316 at AR12585
09/12 00:07:00 - 09/12 01:21:30	Fixed (910.0, 100.0)	EIS spectral atlas at off-limb
09/12 01:40:00 - 09/12 05:36:54	Fixed (914.0, 56.0)	AR12585 obs(fixed pointing)
09/12 05:50:00 - 09/12 17:38:54	Fixed (914.0, 56.0)	AR12585 obs
09/12 17:52:00 - 09/12 23:59:54	Fixed (910.0, 100.0)	HOP316 at AR12585
09/13 00:03:00 - 09/13 05:56:24	Track (566.9, 129.5) ^{Ⓢ 09/13 00:00:00}	AR12589 obs
09/13 06:09:30 - 09/13 10:01:00	Track (609.2, 133.8) ^{Ⓢ 09/13 06:06:30}	AR12589 obs
PROG= 07 30-time(s)		
Subr= 1 20-time(s) 2.0sec		

Seqn= 11		1-time(s)		2.0sec																			
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec										
Seqn=100		1-time(s)		10.0sec																			
	thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec										
	med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec										
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec										
Subr= 2		1-time(s)		2.0sec																			
Seqn= 10		1-time(s)		2.0sec																			
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec										
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec										
Seqn= 11		1-time(s)		2.0sec																			
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec										
Seqn= 84		1-time(s)		2.0sec																			
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec										
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec										
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec										
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec										
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval											

* * * * *

Active Region Search

* * * * *

NOT USED

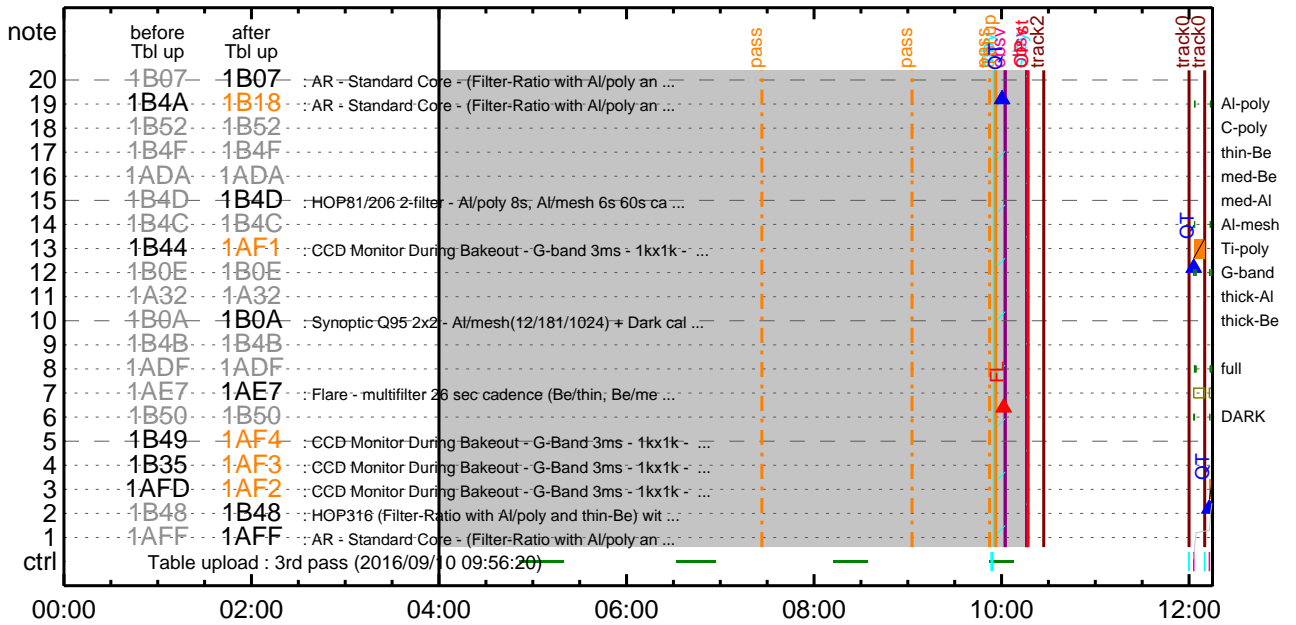
* * * * *

Flare Detection

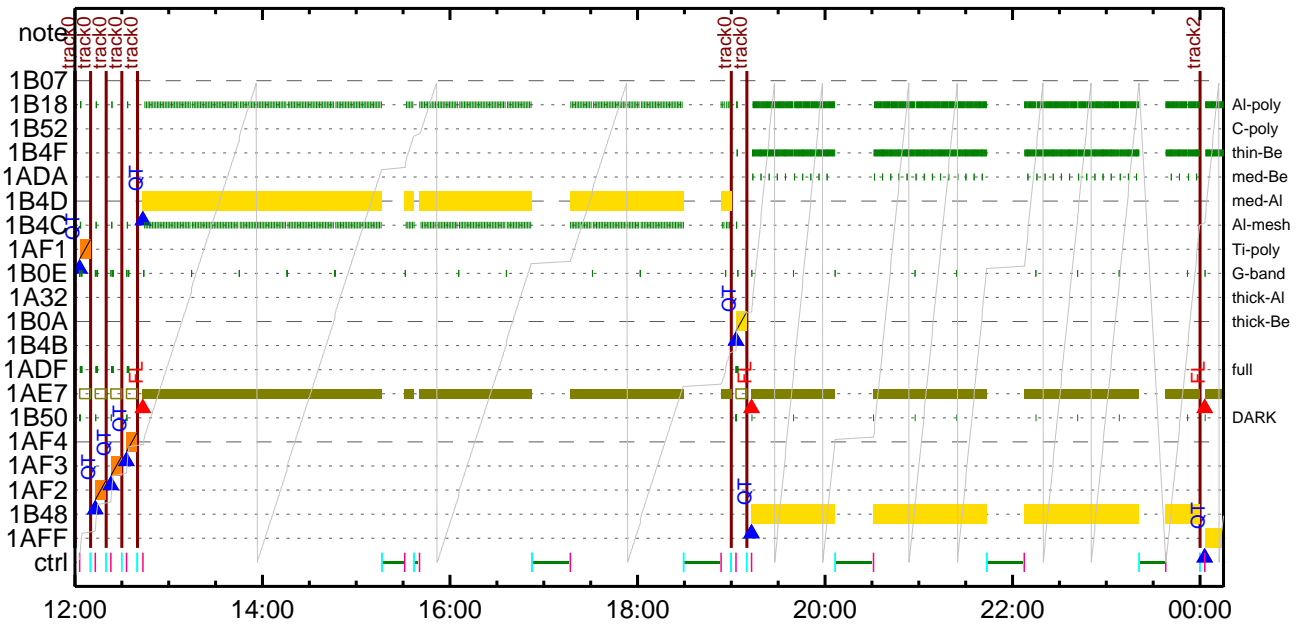
* * * * *

FLD Patrol																					
Term		Pointing (x, y)				Comment															
09/10 12:40:18 - 09/10 19:00:18		Fixed (-18.0, 867.0)				HOP206 at N-pole															
09/10 19:10:18 - 09/11 06:05:18		Fixed (912.0, -150.0)				HOP316 at AR12584															
09/11 06:15:18 - 09/11 18:06:48		Track (864.5, 48.2) @ 09/11 06:15:00				AR12585 obs															
09/11 18:16:48 - 09/12 05:37:18		Fixed (910.0, 100.0)				HOP316 at AR12585															
09/12 05:47:18 - 09/12 17:39:18		Fixed (914.0, 56.0)				AR12585 obs															
09/12 17:49:18 - 09/13 05:56:48		Fixed (910.0, 100.0)				HOP316 at AR12585															
09/13 06:06:48 - 09/15 09:58:00		Track (609.2, 133.8) @ 09/13 06:06:30				AR12589 obs															
Open/Ti-poly		Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50	30sec										
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval									

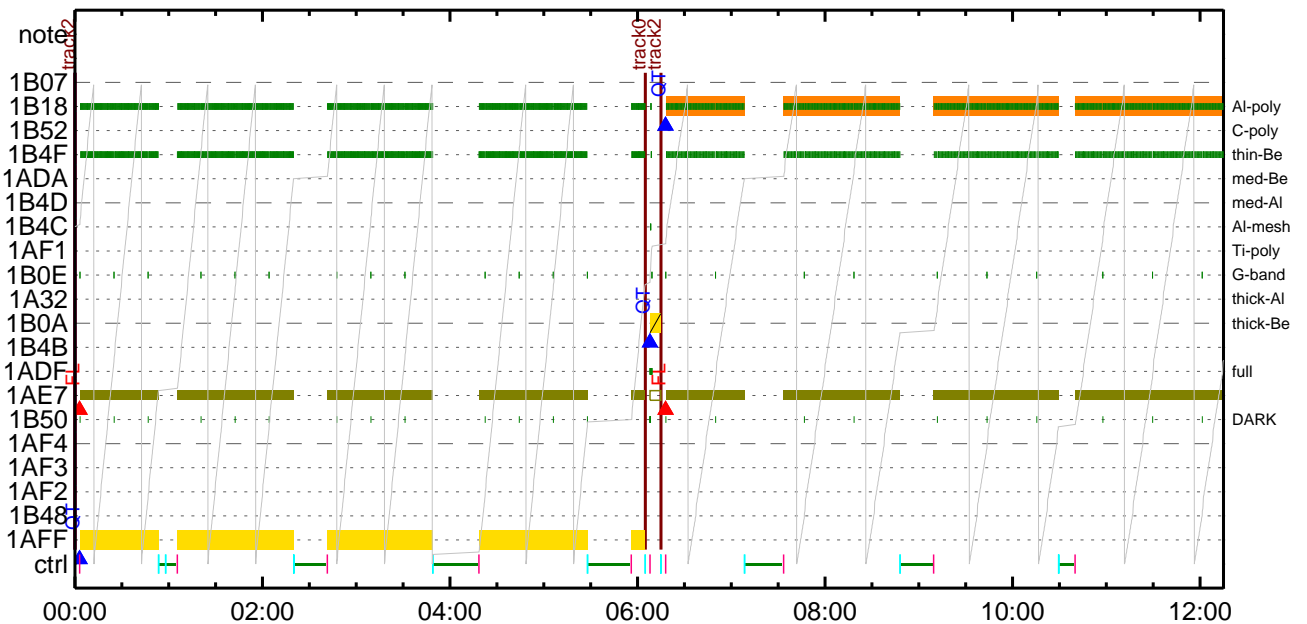
CMDI #0169 2016/09/10



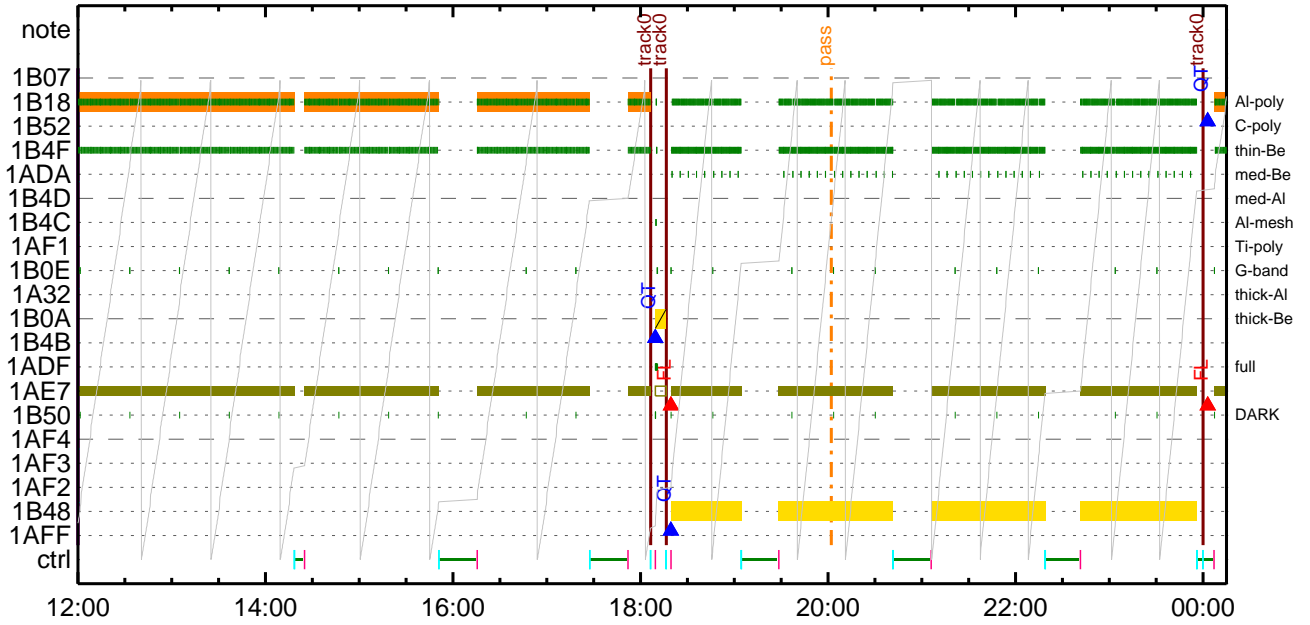
CMDI #0169 2016/09/10



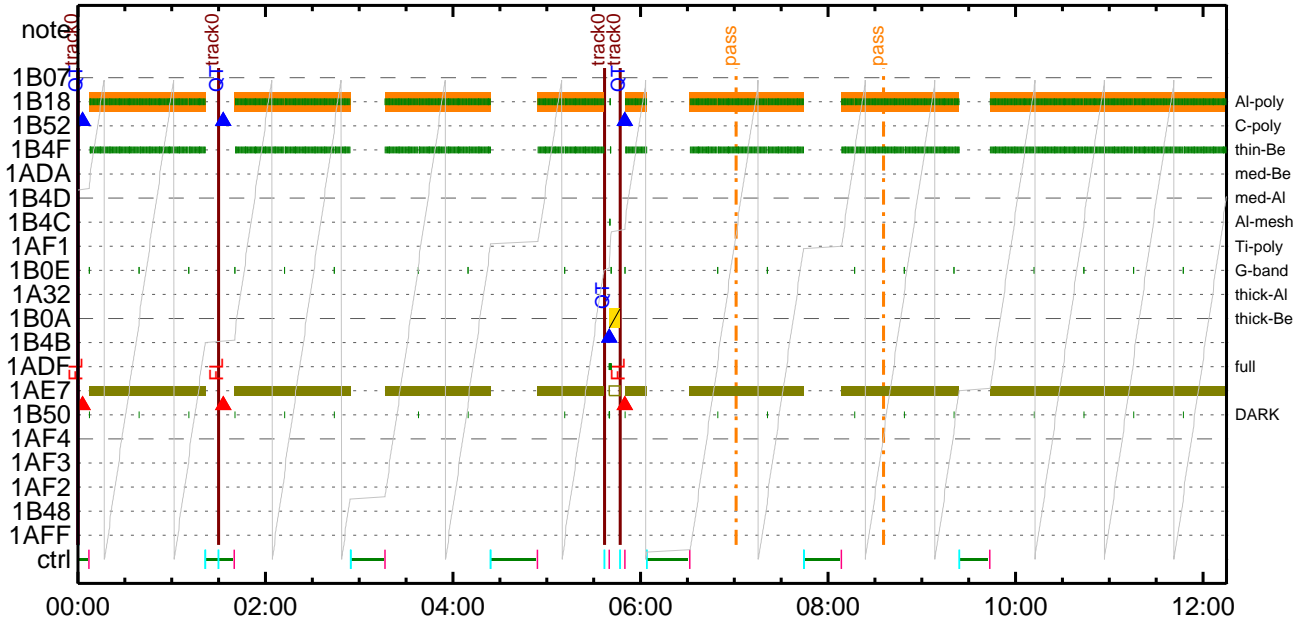
CMDI #0169 2016/09/11



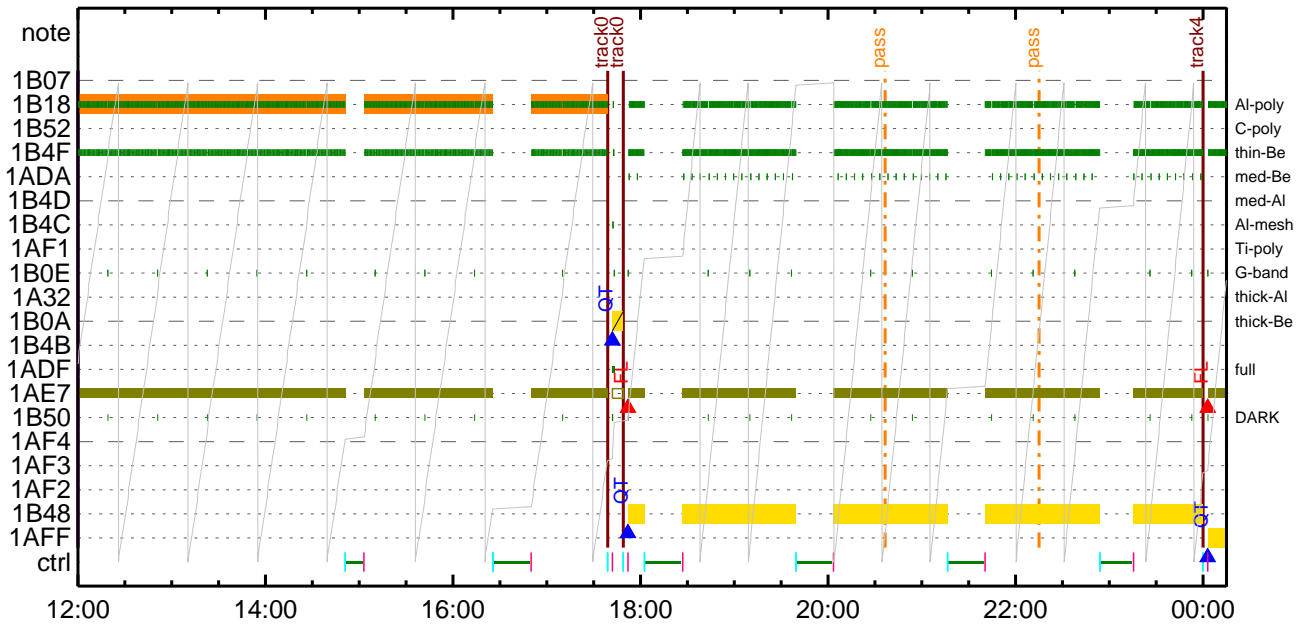
CMDI #0169 2016/09/11



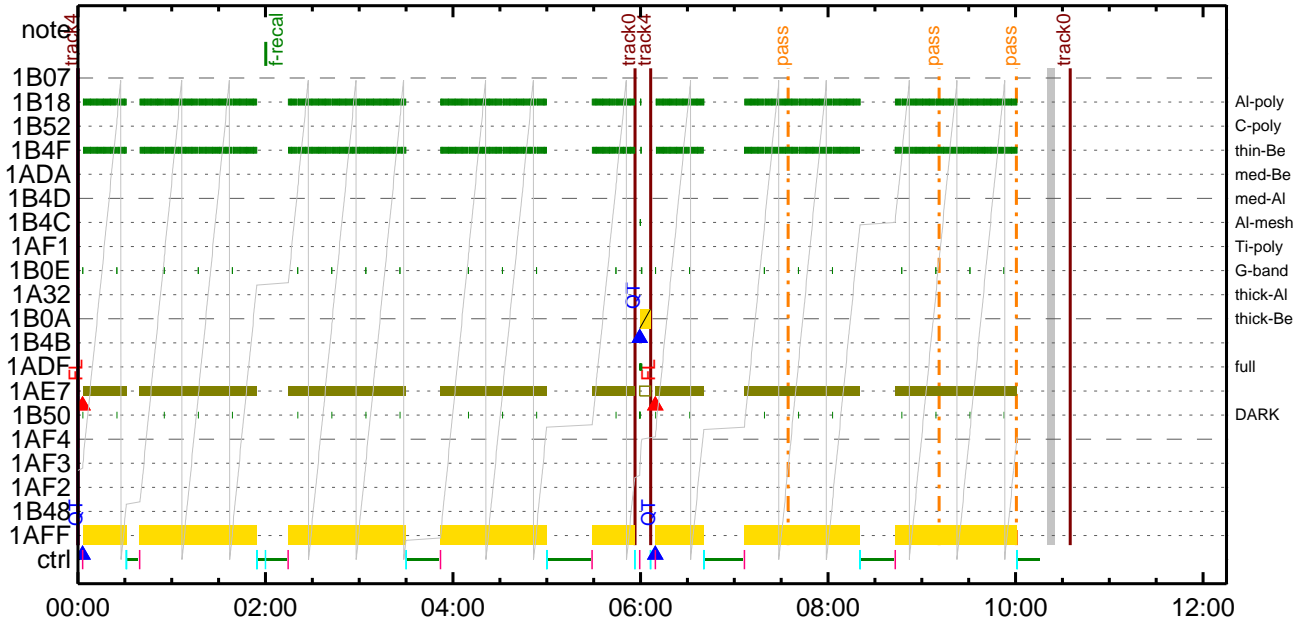
CMDI #0169 2016/09/12



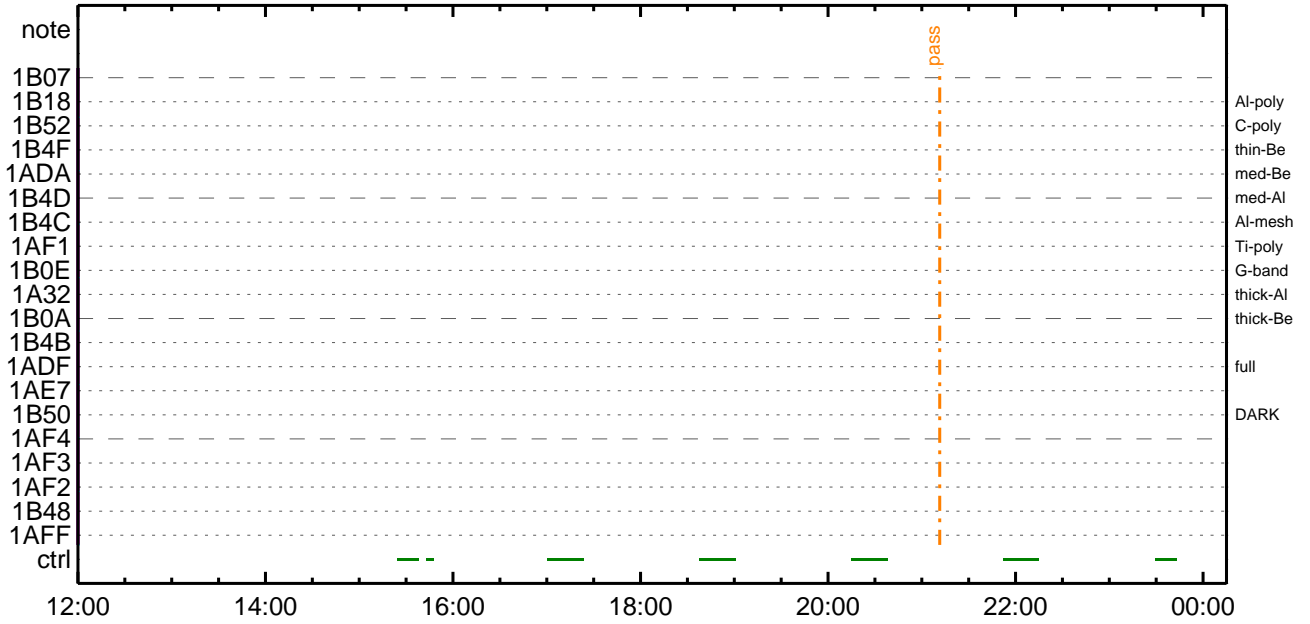
CMDI #0169 2016/09/12



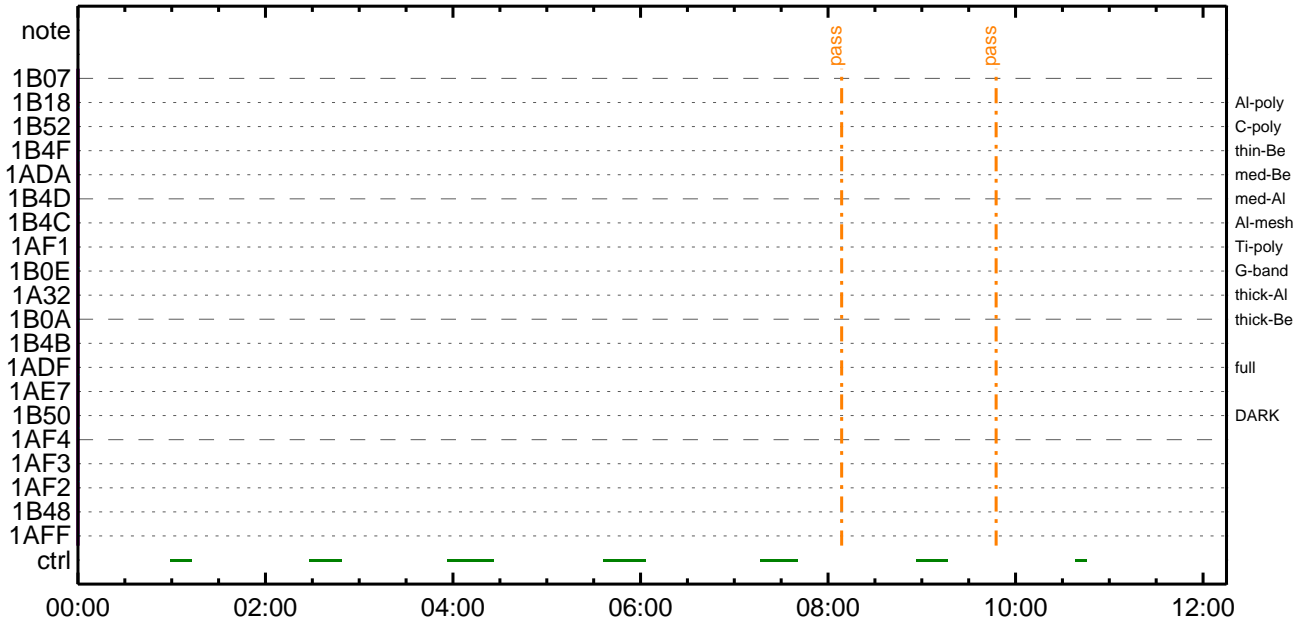
CMDI #0169 2016/09/13



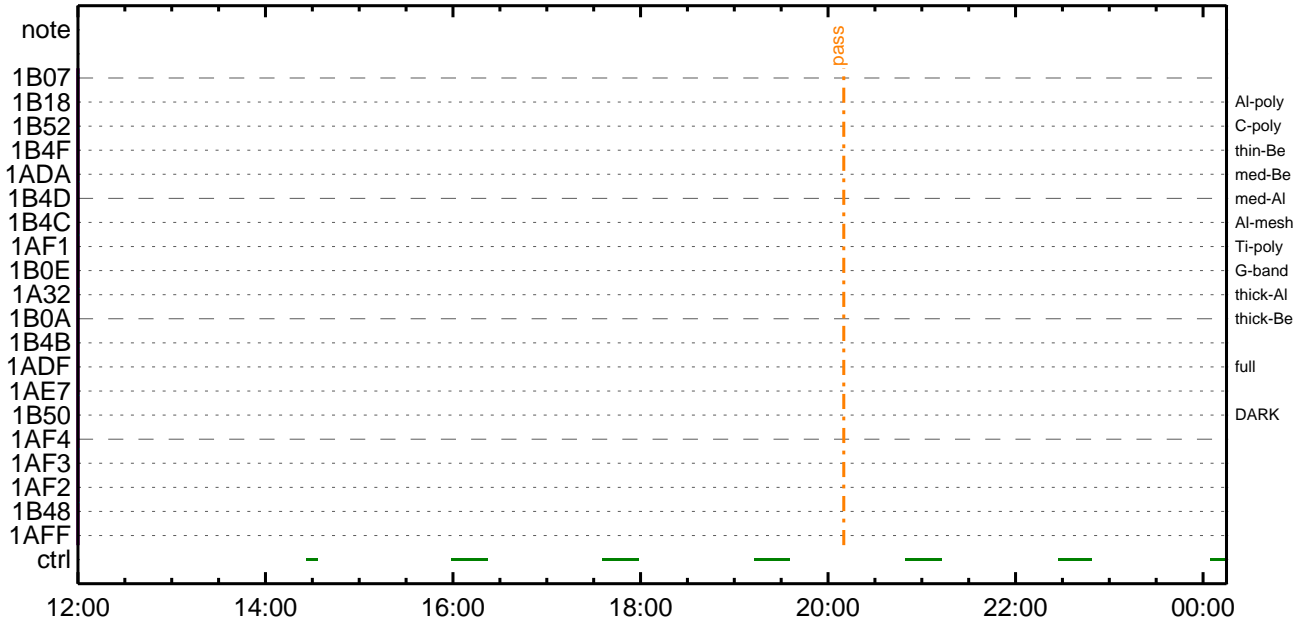
CMDI #0169 2016/09/13



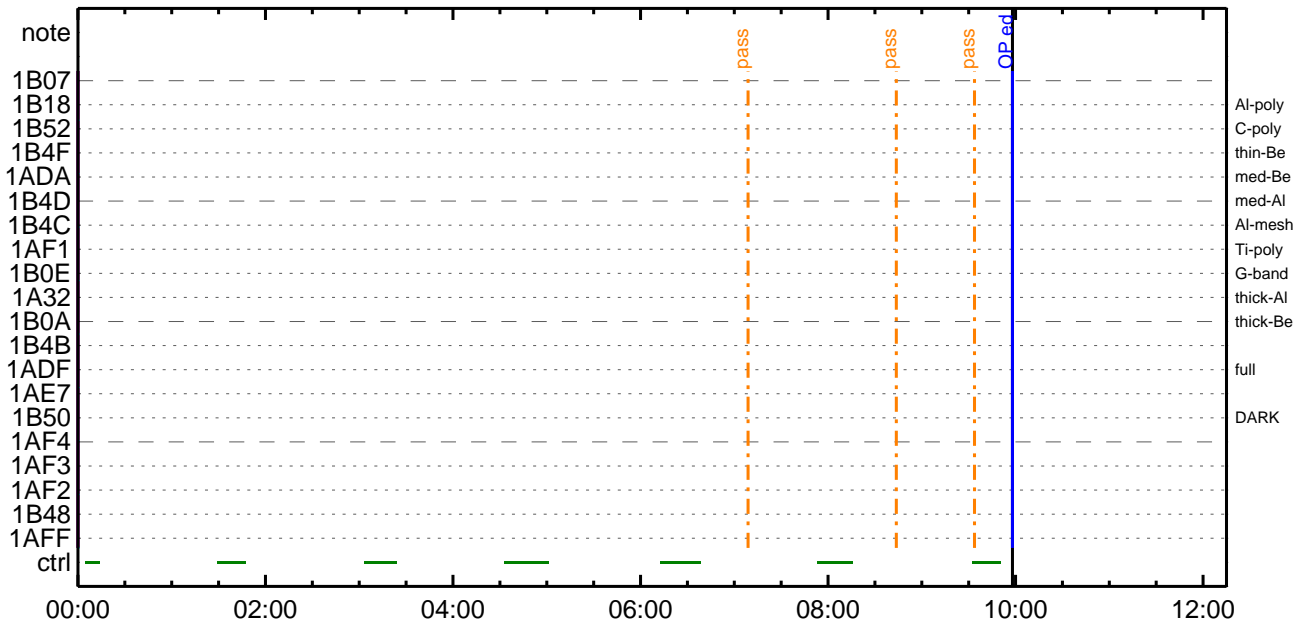
CMDI #0169 2016/09/14



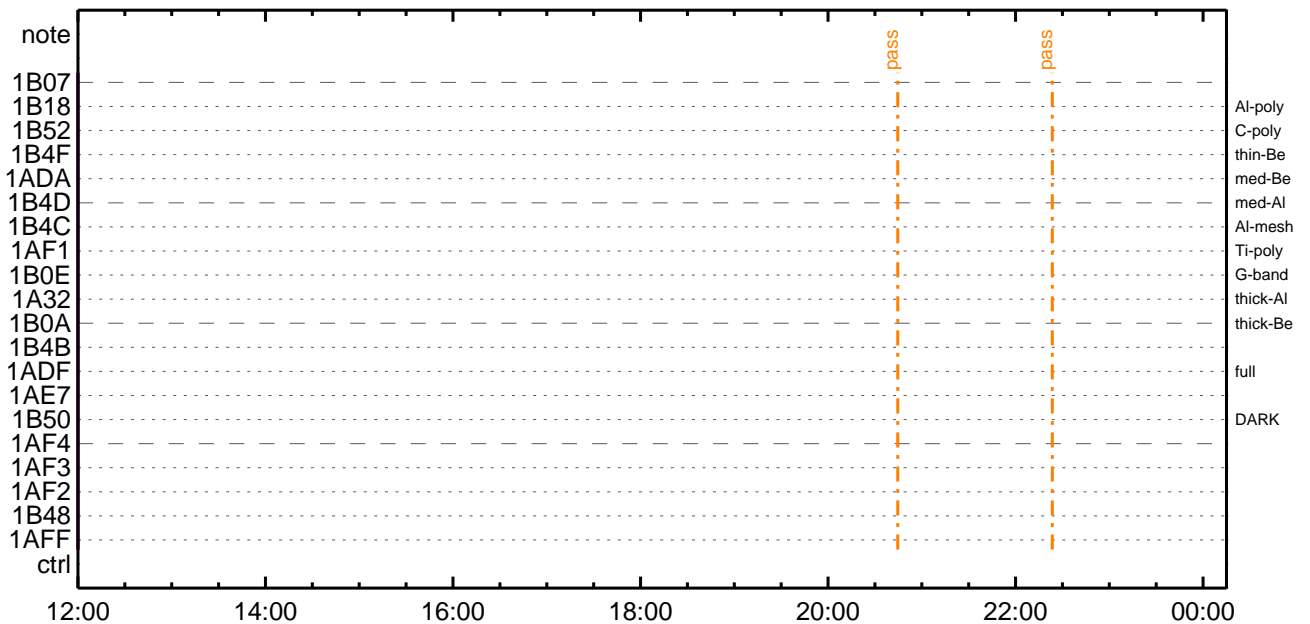
CMDI #0169 2016/09/14



CMDI #0169 2016/09/15



CMDI #0169 2016/09/15



(a) Spacecraft Operation Procedure (real-commands)

```
main-106 2016-09-10 13:56:04 278 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÄY-¼Ä»Û;ã
0005 C.
0006 C. YÄYß;¼Y³YÞYÓYÉÄ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;ÈçðÀá•µ°È»Í×ÁÇçÍYçYÄY×YÍ;¼YÉ;ÈÈÈ¼µ•íÉ;ÈÈÈ¼°ÇÖá•ç¼í¹ççí;çÄ®, ùá¹áèáèáèçÄ+ç®•áÈáááááè;ç
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+ç®µ;ON
0016 C. *****
0017 C. ç" °ÄÄ, Í×ÈYáÀLOSáèççí»p´Öáð¹íí, ç. ; çÉÖÍ×áÈXÁÖONáí¹ÖáÈáááááè;ç
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYÓYÉYÍYÄY-¾ÖÁÖá•çç;ç°È²¼áí°ÄÄ, ¼È¼çáð¼Ä¹Öá¹áè;ç
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°ÄÄ,
0033 C. *****
0034 C. ç" RESTART;ÈPT1;Èá•ççáç¼í¹ççí;ç°È²¼áí°ÄÄ»á°;çDCBC-150áçÈáá;ç
0035 C.
0036 . C. ;ãPT1°ÄÄ, ³«»Í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYçYÓYÉYÉÄÜÁÖ;ÈÄ•Á°²óÈð;È, ááí°ÄÄ, °È³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÄÄ, ç-¼«È°ÄÄ»ßá•çç;ç°È²¼áí°ÄÄ»á°;ç
0055 C. YçYÓYÉYÉÄÜÁÖáÄ•Á°²óÈðá•ççáç¼í¹ççí°í»á¹áèáèáèçÄÖá;ç
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°ÄÄ,
0059 C. *****
0060 C. ç" RESTART;ÈPT2;Èá•ççáç¼í¹ççí;ç°È²¼áí°ÄÄ»á°;çDCBC-151áçÈáá;ç
0061 C.
0062 . C. ;ãPT2°ÄÄ, ³«»Í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYçYÓYÉYÉÄÜÁÖ;ÈÄ•Á°²óÈð;È, ááí°ÄÄ, °È³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÄÄ, Äá»ß;çXÁ+ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÄÄ, Äá»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+ç®µ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YEi;YAYOYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YEi;ã
0103 S. OP op-106:OP
0104 (
0105 S. OG og-106:OG
0106 (
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOYx½ª î»ò³ îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG¤î½¤¹ç•ë² îOK¤ò³ îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOYx½ª î»ò³ îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG¤î½¤¹ç•ë² îOK¤ò³ îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOYx½ª î»ò³ îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP¤î½¤¹ç•ë² îOK¤ò³ îÇ§
0165 C.
0166 C. ***** °ê²¼¤î½¤¹ç•ë² îOK¤ò³ îÇ§ *****
0167 C. DHUYã;½YE;ê½Y½;Yi;½YE;ëòîã¤¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE | § OPOG UPLOAD¤-Á÷¿@NG¤î½¤¹ç•ë² îOK¤ò³ îÇ§
0180 C. ¤¤¤¤;çSET¤èDUMP¤î½¤¹ç•ë² îOK¤ò³ îÇ§
0181 C.
0182 C. TIY³YbYóYë¤òîÁDî¿(UT)
0183 +. TI 2016-09-10 10:12:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2016-09-10 10:12:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2016-09-10 10:12:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2016-09-10 10:16:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼αîÄë%îîñαîŷÄŷ§ŷÄŷ-¹àîŮ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]      EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]       EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîŮ°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]      EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]     EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]        EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]     EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]     EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]       EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»αò³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]       EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLαîŷÈ¹ç•è²îOKαò³îç§
0229 C.
0230 C. DHUŷâ;¼ŷÉ;È¼ŷ¼.ŷî;¼ŷÈ;Èαòîāα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]     EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]     EQ      4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2016-09-10 10:16:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2016-09-10 10:16:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.          [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.
0255 C. ***** XRT START *****
0256 C. Execute, after the success of OP upload.
0257 +. TI 2016-09-10 10:16:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC      (c3)
0260 C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0261 C.
0262 C. ***** XRT END *****
0263 C.
0264 C. ***** MDP `ûÄîαî»ò¼ŷαÈÄα¹αèDCBC•×²è *****
0265 C. (¼ª°îŷÖŷÄŷÈŷŷŷÈŷâŷçŷèè¼α¼Ä»Ůα¹αè)
0266 S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 C. ***** ŷĐŷ¹.İ Daily±;îñαÈ´Øα¹αèDCBC•×²è *****
0271 S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 C. ðäLOSŷÄŷŷŷÄŷ-¼Ä»Ů;ä
0276 C.
0277 C. ***** LOS *****
0278 C.

```



```
0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_CHG_ENA
0131 BC (20)
0132 . C. Verify EIS_MODE_CHG_FLG is ENA
0133 +. DC 07-FC EIS_MODE_MANU
0134 BC (21 02)
0135 . C. Verify EIS in MANUAL mode
0136 . C. Estimated OBSTBL upload time is 1ml9s
0137 C. *****
0138 C. EIS START OBSTBL LOAD
0139 C. *****
0140 . S. RAM ram-820:EIS_OBSTBL
0141 ( )
0142 +. DC 07-FC EIS_DUMP_OBSTBL
0143 BC (07 07 07 00 00 70 00)
0144 C.
0145 C. Execute, after the success of OBSTBL upload.
0146 C. Set EIS TI-commands
0147 +. TI 2016-09-10 10:16:50.0
0148 DC 07-FC EIS_MODE_CHG_ENA
0149 BC (20)
0150 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0151 C. *****
0152 C. EIS END OBSTBL LOAD
0153 C. *****
0154 C.
0155 . C. ***** MDP 'üÀîñî»ö¼ÝñÊÄðñ¹ñèDCBC•x²è *****
0156 C. (¼ã°îÝÖÝÁÝÈÝÞÝËÝäÝçÝèñÈ¾¼ñ¼Ä»Ûñ¹ñè)
0157 . S. DC-BC dcbc-402:DCBC
0158 (MDP_known_event)
0159 C.
0160 C.
0161 . C. ***** ÝÐÝ¹•Ï Daily±¿ÍÑñÈ´Øñ¹ñèDCBC•x²è *****
0162 . S. DC-BC dcbc-153:DCBC
0163 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164 C.
0165 C.
0166 . C. ;ãLOSÝÁÝ§ÝÄÝ¹¼Ä»Û;ã
0167 C.
0168 . C. ***** LOS *****
0169 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-108 2016-09-10 13:56:04 100 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY~¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YFÝÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;È¿µÁµ•µ°È»Í×ÁÇµíYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;ÈÈÈ¼°ÇÔµ•µ¿¼í¹çµí;çÀ®, ùµ¹µèµµµµÇÁ+¿®µ•µÈµµµ³µÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 +. DC 07-F0 MDP_XRT_CTRL_MANU
0020 BC (c1)
0021 + DC 07-F0 MDP_XRT_MODE_STBY
0022 BC (c3)
0023 . C. ----- Success Verify ? OK / NG_____
0024 C.
0025 C. XRT Obs. Table Upload
0026 . S. RAM ram-291:MDP_OBS_X
0027 ( )
0028 C.
0029 +. DC 07-F0 MDP_DUMP_XRTTBL
0030 BC (84 07 00 00 00 3a d4)
0031 . C. ----- Comparison Check ? OK / ERR _____
0032 C.
0033 C.
0034 +. DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 01 b1 b1 04 04)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 02 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 03 b1 b1 08 08)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 04 b1 b1 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 06 06)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 c0 c0 10 10)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 20 20)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 40 c0 10 10)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 40 40 10 10)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0b c0 40 10 10)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0c 80 80 20 08)
0056 + DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 0d 80 80 08 20)
0058 + DC 07-F0 MDP_XRT_ROI_SET
0059 BC (cd 0f 80 80 06 06)
0060 + DC 07-F0 MDP_XRT_ROI_SET
0061 BC (cd 10 80 80 08 08)
0062 + DC 07-F0 MDP_XRT_FLD_DIS
0063 BC (d9)
0064 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0065 BC (c9)
0066 + DC 07-F0 MDP_XRT_ARS_DIS
0067 BC (d5)
0068 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0069 BC (c4 14)
0070 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0071 BC (c5 07)
0072 . C. ----- Success Verify ? OK / NG _____
0073 C.
0074 C.
0075 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0076 C.
0077 +. DC 07-F0 MDP_XRT_MODE_OBSV
0078 BC (c2)
0079 +. TI 2016-09-10 10:16:02.0
0080 DC 07-F0 MDP_XRT_MODE_OBSV
0081 BC (c2)
0082 . C. ----- Success Verify ? OK / NG _____
0083 C.
0084 C. ***** XRT END *****
0085 C.
0086 . C. ***** MDP ´ûÃíµí»ò¼YµÈÁµ¹µèDCBC•x²è *****
0087 C. (¼á°íYÓYÁYÈYBÝYÉYáYçYèµÈ¼µµ¼Á»Ûµ¹µè)
0088 . S. DC-BC dcbc-402:DCBC
0089 (MDP_known_event)
0090 C.
0091 C.
0092 . C. ***** YBY¹•İ Daily±¿İÑµÈ´Øµ¹µèDCBC•x²è *****
0093 . S. DC-BC dcbc-153:DCBC
0094 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0095 C.
```

0096 C.
0097 . C. ;ãLOS¥Á¥§¥Ã¥-¼Á»Û;ã
0098 C.
0099 . C. ***** LOS *****
0100 C.

*** OP Sequence for XRT ***

```

2016/09/10 10:27:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM                    5 02-76 02 00 00 00 00
2016/09/10 11:59:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2016/09/10 11:59:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2016/09/10 11:59:58.0 XRT_FOCUS_POSITION_439_OG [0x1b7]
                        XRT_FOCUS_POSITION         4 07-F8 22 ff aa 00
2016/09/10 12:00:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM                    5 02-76 00 2e f9 2e f9
2016/09/10 12:00:18.0 XRT_FLD_DIS_428_OG [0x1ac]
                        MDP_XRT_FLD_DIS           1 07-F0 d9
2016/09/10 12:00:20.0 XRT_FLRCTRL_DIS_443_OG [0x1bb]
                        MDP_XRT_FLRCTRL_DIS        1 07-F0 c9
2016/09/10 12:02:56.0 XRT_ARS_DIS_445_OG [0x1bd]
                        MDP_XRT_ARS_DIS           1 07-F0 d5
2016/09/10 12:02:58.0 XRT_QT_PROG_SET_401_OG [0x191]
                        MDP_XRT_QT_PROG_SET        2 07-F0 c4 0d
2016/09/10 12:03:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO          1 07-F0 c0
2016/09/10 12:09:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2016/09/10 12:09:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2016/09/10 12:09:58.0 XRT_FOCUS_POSITION_439_OG [0x1b7]
                        XRT_FOCUS_POSITION         4 07-F8 22 ff aa 00
2016/09/10 12:10:00.0 AOCs_OrE-point_Start_3_OG [0x099]
                        AOCU_NM                    5 02-76 00 2e f9 d1 07
2016/09/10 12:10:18.0 XRT_FLD_DIS_428_OG [0x1ac]
                        MDP_XRT_FLD_DIS           1 07-F0 d9
2016/09/10 12:10:20.0 XRT_FLRCTRL_DIS_443_OG [0x1bb]
                        MDP_XRT_FLRCTRL_DIS        1 07-F0 c9
2016/09/10 12:12:56.0 XRT_ARS_DIS_445_OG [0x1bd]
                        MDP_XRT_ARS_DIS           1 07-F0 d5
2016/09/10 12:12:58.0 XRT_QT_PROG_SET_407_OG [0x197]
                        MDP_XRT_QT_PROG_SET        2 07-F0 c4 03
2016/09/10 12:13:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO          1 07-F0 c0
2016/09/10 12:19:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2016/09/10 12:19:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2016/09/10 12:19:58.0 XRT_FOCUS_POSITION_439_OG [0x1b7]
                        XRT_FOCUS_POSITION         4 07-F8 22 ff aa 00
2016/09/10 12:20:00.0 AOCs_OrE-point_Start_4_OG [0x09a]
                        AOCU_NM                    5 02-76 00 d1 07 d1 07
2016/09/10 12:20:18.0 XRT_FLD_DIS_428_OG [0x1ac]
                        MDP_XRT_FLD_DIS           1 07-F0 d9
2016/09/10 12:20:20.0 XRT_FLRCTRL_DIS_443_OG [0x1bb]
                        MDP_XRT_FLRCTRL_DIS        1 07-F0 c9
2016/09/10 12:22:56.0 XRT_ARS_DIS_445_OG [0x1bd]
                        MDP_XRT_ARS_DIS           1 07-F0 d5
2016/09/10 12:22:58.0 XRT_QT_PROG_SET_418_OG [0x1a2]
                        MDP_XRT_QT_PROG_SET        2 07-F0 c4 04
2016/09/10 12:23:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO          1 07-F0 c0
2016/09/10 12:29:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2016/09/10 12:29:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2016/09/10 12:29:58.0 XRT_FOCUS_POSITION_439_OG [0x1b7]
                        XRT_FOCUS_POSITION         4 07-F8 22 ff aa 00
2016/09/10 12:30:00.0 AOCs_OrE-point_Start_5_OG [0x09b]
                        AOCU_NM                    5 02-76 00 d1 07 2e f9
2016/09/10 12:30:18.0 XRT_FLD_DIS_428_OG [0x1ac]
                        MDP_XRT_FLD_DIS           1 07-F0 d9
2016/09/10 12:30:20.0 XRT_FLRCTRL_DIS_443_OG [0x1bb]
                        MDP_XRT_FLRCTRL_DIS        1 07-F0 c9
2016/09/10 12:32:56.0 XRT_ARS_DIS_445_OG [0x1bd]
                        MDP_XRT_ARS_DIS           1 07-F0 d5
2016/09/10 12:32:58.0 XRT_QT_PROG_SET_413_OG [0x19d]
                        MDP_XRT_QT_PROG_SET        2 07-F0 c4 05
2016/09/10 12:33:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO          1 07-F0 c0
2016/09/10 12:39:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2016/09/10 12:39:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2016/09/10 12:39:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION         4 07-F8 22 fe 97 00
2016/09/10 12:40:00.0 AOCs_OrE-point_Start_6_OG [0x09c]
                        AOCU_NM                    5 02-76 00 b2 f2 01 99
2016/09/10 12:40:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA           1 07-F0 d8
2016/09/10 12:40:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA        1 07-F0 c8
2016/09/10 12:40:22.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET          1 07-F0 d0
2016/09/10 12:40:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS           1 07-F0 d5
2016/09/10 12:40:26.0 XRT_FLD_RESET_438_OG [0x1b6]

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XRT_OGLIST_0169.chk

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2016/09/10	12:43:26.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/09/10	12:43:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f			
2016/09/10	12:43:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2016/09/10	15:16:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/09/10	15:16:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/10	15:16:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/10	15:16:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/09/10	15:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/09/10	15:30:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/09/10	15:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]							
2016/09/10	15:37:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]							
2016/09/10	15:37:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/09/10	15:37:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/10	15:37:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/10	15:39:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/09/10	15:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/09/10	15:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/09/10	16:52:30.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]							
2016/09/10	16:52:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/09/10	16:52:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/10	16:52:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/10	16:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/09/10	17:16:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/09/10	17:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/09/10	18:29:30.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_Custom_430_OG [0x1ae]							
2016/09/10	18:29:32.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_CTRL_AUTO_424_OG [0x1a8]							
2016/09/10	18:29:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/09/10	18:29:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/10	18:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/10	18:52:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/09/10	18:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/09/10	18:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/09/10	18:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_Custom_430_OG [0x1ae]							
2016/09/10	18:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_CTRL_AUTO_424_OG [0x1a8]							
2016/09/10	19:00:00.0	AOCS_Ore-point_Start_7_OG [0x09d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/09/10	19:00:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/10	19:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/10	19:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/09/10	19:02:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/09/10	19:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/09/10	19:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_QT_PROG_SET_427_OG [0x1ab]							
2016/09/10	19:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2016/09/10	19:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/09/10	19:10:00.0	AOCS_Ore-point_Start_8_OG [0x09e]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/10	19:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/10	19:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	XRT_FOCUS_POSITION_410_OG [0x19a]							
2016/09/10	19:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00			
2016/09/10	19:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	AOCS_Ore-point_Start_8_OG [0x09e]							
			AOCS_Ore-point_Start_8_OG [0x09e]							
			MDP_XRT_AEC_RESET	1	07-F0	d0				
			MDP_XRT_ARS_DIS	1	07-F0	d5				

2016/09/10	19:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/09/10	19:12:56.0	XRT_QT_PROG_SET_431_OG [0x1af]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	02			
2016/09/10	19:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2016/09/10	19:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/09/10	20:06:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/10	20:06:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/10	20:06:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/09/10	20:06:36.0	XRT_PREFLR_STRT_414_OG [0x19e]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/09/10	20:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/09/10	20:30:00.0	XRT_Custom_430_OG [0x1ae]							
2016/09/10	20:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/09/10	21:43:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/10	21:43:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/10	21:43:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/09/10	21:43:36.0	XRT_PREFLR_STRT_414_OG [0x19e]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/09/10	21:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/09/10	22:06:30.0	XRT_Custom_430_OG [0x1ae]							
2016/09/10	22:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/09/10	23:21:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/10	23:21:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/10	23:21:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/09/10	23:21:06.0	XRT_PREFLR_STRT_414_OG [0x19e]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/09/10	23:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/09/10	23:37:00.0	XRT_Custom_430_OG [0x1ae]							
2016/09/10	23:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/09/10	23:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/10	23:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/10	23:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2016/09/11	00:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02	00	00	00	00
2016/09/11	00:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/09/11	00:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/09/11	00:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/09/11	00:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/09/11	00:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/09/11	00:02:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	01			
2016/09/11	00:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2016/09/11	00:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/09/11	00:53:30.5	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/11	00:53:32.5	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/11	00:53:34.5	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/09/11	00:53:36.5	XRT_PREFLR_STRT_414_OG [0x19e]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/09/11	00:56:44.5	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/09/11	00:58:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/11	00:58:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/11	00:58:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/09/11	00:58:06.0	XRT_PREFLR_STRT_414_OG [0x19e]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/09/11	01:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/09/11	01:04:30.0	XRT_Custom_430_OG [0x1ae]							
2016/09/11	01:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							

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2016/09/11	02:20:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/11	02:20:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/11	02:20:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/09/11	02:20:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/09/11	02:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/09/11	02:40:30.0	XRT_Custom_430_OG [0x1ae]				
2016/09/11	02:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/09/11	03:49:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/11	03:49:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/11	03:49:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/09/11	03:49:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/09/11	03:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/09/11	04:17:30.0	XRT_Custom_430_OG [0x1ae]				
2016/09/11	04:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/09/11	05:28:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/11	05:28:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/11	05:28:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/09/11	05:28:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/09/11	05:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/09/11	05:55:00.0	XRT_Custom_430_OG [0x1ae]				
2016/09/11	05:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/09/11	06:04:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/11	06:04:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/11	06:04:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/09/11	06:05:00.0	AOCS_OrE-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 00 00 00 00
2016/09/11	06:05:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9
2016/09/11	06:07:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/09/11	06:07:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/09/11	06:07:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2016/09/11	06:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/09/11	06:14:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/11	06:14:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/11	06:14:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/09/11	06:15:00.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 00 00 00 00
2016/09/11	06:15:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/09/11	06:15:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/09/11	06:15:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/09/11	06:15:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/09/11	06:15:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/09/11	06:17:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2016/09/11	06:17:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/09/11	06:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/09/11	07:08:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/11	07:08:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/11	07:08:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/09/11	07:08:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/09/11	07:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/09/11	07:32:30.0	XRT_Custom_430_OG [0x1ae]				

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2016/09/11	07:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/09/11	08:48:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/09/11	08:48:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/09/11	08:48:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/09/11	08:48:06.0	XRT_PREFLR_STRT_414_OG [0x19e]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/09/11	08:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/09/11	09:08:30.0	XRT_Custom_430_OG [0x1ae]						
2016/09/11	09:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/09/11	10:29:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/09/11	10:29:32.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/09/11	10:29:34.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/09/11	10:29:36.0	XRT_PREFLR_STRT_414_OG [0x19e]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/09/11	10:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/09/11	10:39:00.0	XRT_Custom_430_OG [0x1ae]						
2016/09/11	10:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/09/11	14:18:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/09/11	14:18:32.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/09/11	14:18:34.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/09/11	14:18:36.0	XRT_PREFLR_STRT_414_OG [0x19e]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/09/11	14:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/09/11	14:24:00.0	XRT_Custom_430_OG [0x1ae]						
2016/09/11	14:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/09/11	15:51:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/09/11	15:51:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/09/11	15:51:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/09/11	15:51:06.0	XRT_PREFLR_STRT_414_OG [0x19e]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/09/11	15:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/09/11	16:14:30.0	XRT_Custom_430_OG [0x1ae]						
2016/09/11	16:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/09/11	17:27:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/09/11	17:27:32.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/09/11	17:27:34.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/09/11	17:27:36.0	XRT_PREFLR_STRT_414_OG [0x19e]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/09/11	17:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/09/11	17:51:00.0	XRT_Custom_430_OG [0x1ae]						
2016/09/11	17:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/09/11	18:06:24.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/09/11	18:06:26.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/09/11	18:06:28.0	XRT_FOCUS_POSITION_403_OG [0x193]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2016/09/11	18:06:30.0	AOCS_OrE-point_Start_7_OG [0x09d]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2016/09/11	18:06:48.0	XRT_FLD_DIS_406_OG [0x196]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2016/09/11	18:09:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2016/09/11	18:09:26.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/09/11	18:09:28.0	XRT_QT_PROG_SET_427_OG [0x1ab]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a			
2016/09/11	18:09:30.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/09/11	18:16:24.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/09/11	18:16:26.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/09/11	18:16:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2016/09/11	18:16:30.0	AOCS_OrE-point_Start_9_OG [0x09f]						
		AOCU_NM	5	02-76	00 f7 1b af 1b			

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2016/09/11	18:16:48.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2016/09/11	18:16:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/09/11	18:16:52.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2016/09/11	18:16:54.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2016/09/11	18:16:56.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/09/11	18:19:26.0	XRT_QT_PROG_SET_431_OG [0x1af]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2016/09/11	18:19:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/09/11	18:19:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/09/11	19:04:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/11	19:04:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/11	19:04:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/09/11	19:04:36.0	XRT_PREFLR_STRT_414_OG [0x19e]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/09/11	19:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/09/11	19:27:30.0	XRT_Custom_430_OG [0x1ae]			
2016/09/11	19:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/09/11	20:41:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/11	20:41:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/11	20:41:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/09/11	20:41:36.0	XRT_PREFLR_STRT_414_OG [0x19e]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/09/11	20:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/09/11	21:05:00.0	XRT_Custom_430_OG [0x1ae]			
2016/09/11	21:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/09/11	22:19:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/11	22:19:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/11	22:19:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/09/11	22:19:06.0	XRT_PREFLR_STRT_414_OG [0x19e]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/09/11	22:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/09/11	22:40:30.5	XRT_Custom_430_OG [0x1ae]			
2016/09/11	22:41:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/09/11	23:56:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/11	23:56:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/11	23:56:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/09/11	23:56:06.0	XRT_PREFLR_STRT_414_OG [0x19e]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/09/11	23:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/09/11	23:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/11	23:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/11	23:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/09/12	00:00:00.0	AOCS_OrE-point_Start_9_OG [0x09f]			
		AOCU_NM	5	02-76	00 f7 1b af 1b
2016/09/12	00:00:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2016/09/12	00:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/09/12	00:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2016/09/12	00:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2016/09/12	00:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/09/12	00:02:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2016/09/12	00:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/09/12	00:06:00.0	XRT_Custom_430_OG [0x1ae]			
2016/09/12	00:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/09/12	01:21:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/12	01:21:32.0	XRT_CTRL_MANU_402_OG [0x192]			

2016/09/12	01:21:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/09/12	01:21:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/09/12	01:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/09/12	01:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/09/12	01:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/09/12	01:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/09/12	01:30:00.0	AOCS_OrE-point_Start_10_OG [0x0a0]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2016/09/12	01:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 fb 03 ae c1	
2016/09/12	01:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/09/12	01:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/09/12	01:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/09/12	01:30:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/09/12	01:32:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/09/12	01:32:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13	
2016/09/12	01:39:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2016/09/12	01:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2016/09/12	02:54:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/09/12	02:54:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/09/12	02:54:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/09/12	02:54:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/09/12	02:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/09/12	03:15:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/09/12	03:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2016/09/12	04:24:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/09/12	04:24:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/09/12	04:24:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/09/12	04:24:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/09/12	04:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/09/12	04:53:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/09/12	04:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2016/09/12	05:36:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/09/12	05:36:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/09/12	05:36:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/09/12	05:37:00.0	AOCS_OrE-point_Start_7_OG [0x09d]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2016/09/12	05:37:18.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00 00 00 00 00	
2016/09/12	05:39:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2016/09/12	05:39:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2016/09/12	05:39:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/09/12	05:40:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a	
2016/09/12	05:46:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/09/12	05:46:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/09/12	05:46:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/09/12	05:47:00.0	AOCS_OrE-point_Start_10_OG [0x0a0]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2016/09/12	05:47:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 fb 03 ae c1	
2016/09/12	05:47:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/09/12	05:47:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/09/12	05:47:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/09/12	05:47:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5	

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2016/09/12	05:49:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/09/12	05:49:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2016/09/12	05:50:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/09/12	06:04:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/09/12	06:04:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/12	06:04:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/12	06:04:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/09/12	06:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/09/12	06:30:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/09/12	06:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	e9
2016/09/12	07:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/09/12	07:44:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/12	07:44:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/12	07:44:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/09/12	07:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/09/12	08:07:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/09/12	08:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	e9
2016/09/12	09:24:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/09/12	09:24:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/12	09:24:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/12	09:24:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/09/12	09:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/09/12	09:42:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/09/12	09:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	e9
2016/09/12	14:51:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/09/12	14:51:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/12	14:51:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/12	14:51:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/09/12	14:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/09/12	15:02:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/09/12	15:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	e9
2016/09/12	16:25:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/09/12	16:25:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/12	16:25:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/12	16:25:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/09/12	16:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/09/12	16:49:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/09/12	16:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	e9
2016/09/12	17:38:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/09/12	17:38:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/12	17:38:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/12	17:39:00.0	AOCS_OrE-point_Start_7_OG [0x09d]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/09/12	17:39:18.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00 00 00 00 00
2016/09/12	17:41:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2016/09/12	17:41:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/09/12	17:41:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/09/12	17:42:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2016/09/12	17:48:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/09/12	17:48:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1

2016/09/12	17:48:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/12	17:49:00.0	AOCS_OrE-point_Start_9_OG [0x09f]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/09/12	17:49:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 f7 1b af 1b
2016/09/12	17:49:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/09/12	17:49:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/09/12	17:49:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/09/12	17:49:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/09/12	17:51:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/09/12	17:51:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2016/09/12	17:52:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/09/12	18:02:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/09/12	18:02:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/12	18:02:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/12	18:02:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/09/12	18:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/09/12	18:26:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/09/12	18:27:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2016/09/12	19:39:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/09/12	19:39:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/12	19:39:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/12	19:39:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/09/12	19:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/09/12	20:02:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/09/12	20:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2016/09/12	21:16:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/09/12	21:16:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/12	21:16:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/12	21:16:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/09/12	21:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/09/12	21:39:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/09/12	21:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2016/09/12	22:54:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/09/12	22:54:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/12	22:54:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/12	22:54:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/09/12	22:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/09/12	23:14:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/09/12	23:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2016/09/12	23:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/09/12	23:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/12	23:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/13	00:00:00.0	AOCS_OrE-point_Start_11_OG [0x0a1]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/09/13	00:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	04 00 00 00 00
2016/09/13	00:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/09/13	00:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/09/13	00:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/09/13	00:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/09/13	00:02:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 01

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2016/09/13	00:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2016/09/13	00:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/09/13	00:31:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/13	00:31:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/13	00:31:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/09/13	00:31:06.0	XRT_PREFLR_STRT_414_OG [0x19e]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/09/13	00:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/09/13	00:38:30.0	XRT_Custom_430_OG [0x1ae]							
2016/09/13	00:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/09/13	01:54:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/13	01:54:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/13	01:54:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/09/13	01:54:36.0	XRT_PREFLR_STRT_414_OG [0x19e]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/09/13	01:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/09/13	02:00:00.0	XRT_CTRL_MANU_409_OG [0x199]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/13	02:00:10.0	XRT_FOCUS_RECALIBRATE_416_OG [0x1a0]							
		XRT_FOCUS_RECAL	2	07-F8	78	00			
2016/09/13	02:04:10.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2016/09/13	02:13:30.0	XRT_Custom_430_OG [0x1ae]							
2016/09/13	02:14:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/09/13	03:30:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/13	03:30:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/13	03:30:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/09/13	03:30:06.0	XRT_PREFLR_STRT_414_OG [0x19e]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/09/13	03:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/09/13	03:51:00.0	XRT_Custom_430_OG [0x1ae]							
2016/09/13	03:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/09/13	05:00:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/13	05:00:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/13	05:00:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/09/13	05:00:06.0	XRT_PREFLR_STRT_414_OG [0x19e]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/09/13	05:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/09/13	05:28:00.0	XRT_Custom_430_OG [0x1ae]							
2016/09/13	05:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/09/13	05:56:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/13	05:56:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/13	05:56:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2016/09/13	05:56:30.0	AOCS_Ore-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	00	00	00	00
2016/09/13	05:56:48.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/09/13	05:59:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/09/13	05:59:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/09/13	05:59:28.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2016/09/13	05:59:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/09/13	06:06:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/13	06:06:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/09/13	06:06:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2016/09/13	06:06:30.0	AOCS_Ore-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	04	00	00	00	00
2016/09/13	06:06:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/09/13	06:06:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/09/13	06:06:52.0	XRT_AEC_RESET_448_OG [0x1c0]							

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2016/09/13	06:06:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/09/13	06:06:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/09/13	06:09:26.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/09/13	06:09:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2016/09/13	06:09:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/09/13	06:40:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/09/13	06:40:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/13	06:40:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/13	06:40:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/09/13	06:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/09/13	07:05:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/09/13	07:06:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2016/09/13	08:20:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/09/13	08:20:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/13	08:20:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/13	08:20:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/09/13	08:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/09/13	08:42:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/09/13	08:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2016/09/13	10:01:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/09/13	10:01:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/13	10:01:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/09/13	10:01:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/09/13	10:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/09/13	10:35:00.0	AOCS_Ore-point_Start_7_OG [0x09d]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
		AOCU_NM		5	02-76	00 00 00 00 00