

XRT Timeline to be uploaded on 2016/11/15

Period: 2016/11/15 10:01:00 - 2016/11/19 11:02:00

* * * * *

Normal mode

* * * * *

XOB #1ADA: CME watch - 4x4 - AEC 2/3 - Be-thin (Be-Med) - Long/Short Pairs - 300s cad			
Term	Pointing (x, y)	Comment	
11/15 10:14:00 - 11/15 10:54:24	Fixed (-22.0, -978.0)	# OP start + 10min, HOP79 1/20	
11/15 10:57:30 - 11/15 11:24:24	Fixed (-22.0, -878.0)	2/20	
11/15 11:57:30 - 11/15 12:24:24	Fixed (-22.0, -678.0)	4/20	
11/15 12:27:30 - 11/15 12:54:24	Fixed (-22.0, -578.0)	5/20	
11/15 12:57:30 - 11/15 13:24:24	Fixed (-22.0, -478.0)	6/20	
11/15 13:57:30 - 11/15 14:24:24	Fixed (-22.0, -278.0)	8/20	
11/15 14:27:30 - 11/15 15:02:00	Fixed (-22.0, -178.0)	9/20	
11/15 15:18:00 - 11/15 15:44:54	Fixed (-22.0, -78.0)	10/20	
11/15 16:00:38 - 11/15 16:37:00	Fixed (-22.0, 12.0)	11/20 and synoptic	
11/15 17:04:30 - 11/15 17:31:24	Fixed (-22.0, 112.0)	12/20	
11/15 17:34:30 - 11/15 18:14:00	Fixed (-22.0, 212.0)	13/20	
11/15 18:41:30 - 11/15 19:08:24	Fixed (-22.0, 312.0)	14/20	
11/15 20:18:00 - 11/15 20:44:54	Fixed (-22.0, 512.0)	16/20	
11/15 20:48:00 - 11/15 21:28:00	Fixed (-22.0, 612.0)	17/20	
11/15 22:25:00 - 11/15 23:05:30	Fixed (-22.0, 812.0)	19/20	
11/15 23:29:30 - 11/15 23:56:24	Fixed (-22.0, 912.0)	20/20	

PROG= 15 Inf-time(s)

Subr= 1		1-time(s)		300.0sec											
Seqn= 81		1-time(s)		4.0sec											
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048	(1024, 1024)	Q=98	2	0	2.0sec		
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048	(1024, 1024)	Q=98	3	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval			

XOB #1B57: CME watch - 4x4 - AEC 2/3 - Be-thin (Be-Med) - Long/Short Pairs -G-band (2x2,1ms) - Leak (2x2,1ms) - 300s cad			
Term	Pointing (x, y)	Comment	
11/15 11:27:30 - 11/15 11:54:24	Fixed (-22.0, -778.0)	3/20	
11/15 13:27:30 - 11/15 13:54:24	Fixed (-22.0, -378.0)	7/20	
11/15 19:11:30 - 11/15 19:51:00	Fixed (-22.0, 412.0)	15/20	
11/15 21:55:00 - 11/15 22:21:54	Fixed (-22.0, 712.0)	18/20	

PROG= 19 Inf-time(s)

Subr= 1		1-time(s)		2.0sec											
Seqn= 26		1-time(s)		2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	2x2	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec		
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Subr= 2		24-time(s)		300.0sec											
Seqn= 81		1-time(s)		2.0sec											
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048	(1024, 1024)	Q=98	2	0	2.0sec		
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048	(1024, 1024)	Q=98	3	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval			

XOB #1B15: Synoptic 7 Filter w/ Al-mesh(24/256/2897), Al-poly(45/512/4096), Thin-Be(181/2048/11571) - Thick-Be(65536), Al-poly+Ti-poly(512/4096), Med-Al			
Term	Pointing (x, y)	Comment	
11/15 15:48:00 - 11/15 16:00:00	Fixed (-22.0, 12.0)	11/20 and synoptic	

PROG= 18 1-time(s)

Subr= 1		1-time(s)		2.0sec											
Seqn= 5		1-time(s)		2.0sec											
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec		
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec		
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec		
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512	(1024, 1024)	DPCM	0	0	2.0sec		
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048	(1024, 1024)	DPCM	0	0	2.0sec		
Seqn= 1		1-time(s)		2.0sec											
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	24ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Seqn= 99		1-time(s)		2.0sec											
Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Seqn= 67		1-time(s)		2.0sec											
thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Seqn= 54		1-time(s)		4.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec		
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Subr= 2		1-time(s)		2.0sec											
Seqn= 46		2-time(s)		2.0sec											
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec		
Seqn= 58		2-time(s)		2.0sec											

Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 71 2-time(s) 2.0sec												
med-Al/Open	med-Al/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1AFE: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
11/15 23:59:30 - 11/16 05:59:54	Track (161.9, 164.8) ^{® 11/15 23:56:30}	AR12610 obs
11/16 21:03:00 - 11/17 05:48:00	Track (347.4, 168.8) ^{® 11/16 21:00:00}	AR12610 obs
11/17 06:37:30 - 11/17 09:08:00	Track (427.4, 171.1) ^{® 11/17 06:34:30}	# Cont,

PROG= 17 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec												
Seqn= 56 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2 5-time(s) 2.0sec												
Seqn= 75 1-time(s) 2.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 53 4-time(s) 180.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	55.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	55.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1B14: Synoptic Q95 2x2 - Al/mesh(24/256/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(45/512/4096) + Ti

Term	Pointing (x, y)	Comment
11/16 06:03:00 - 11/16 06:09:54	Fixed (0.0, 0.0)	synoptic
11/16 16:33:00 - 11/16 16:39:54	Fixed (0.0, 0.0)	synoptic, shifted manually
11/17 06:27:30 - 11/17 06:34:24	Fixed (0.0, 0.0)	synoptic, shifted 24.5 min

PROG= 14 1-time(s)

Subr= 1 1-time(s) 2.0sec												
Seqn= 5 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 1 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 99 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 67 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1B58: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, Al/Poly context, with G-band (3ms/3ms)

Term	Pointing (x, y)	Comment
11/16 06:13:00 - 11/16 10:13:00	Track (217.8, 165.8) ^{® 11/16 06:10:00}	# HOP306 on AR12610

PROG= 13 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec												
Seqn= 56 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 42 4-time(s) 2.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 25 80-time(s) 45.0sec												

thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	6.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	16.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1B3F: CME watch - 4x4 - AEC 2/3 - 2-filter (Be-thin, Al-poly) - G-band (2x2,1ms) - Leak (2x2,1ms) - 90s cad (G-band/Leak first)

Term	Pointing (x, y)	Comment
11/16 10:33:00 - 11/16 16:29:54	Track (374.6, -200.8) @ 11/16 10:30:00	EIS deep expts obs w/ IRIS
PROG= 09 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 26 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Subr= 2 40-time(s) 90.0sec		
Seqn= 8 1-time(s) 2.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
Seqn= 6 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/Open close	Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1B02: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
11/16 16:43:00 - 11/16 20:59:54	Track (737.3, -220.4) @ 11/16 16:40:00	# AR12609 w/ IRIS
PROG= 11 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 56 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Subr= 2 5-time(s) 2.0sec		
Seqn= 75 1-time(s) 2.0sec		
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Seqn= 79 4-time(s) 120.0sec		
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 34.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 34.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

* * * * *

Flare mode

* * * * *

XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
11/15 10:14:00 - 11/15 10:54:24	Fixed (-22.0, -978.0)	# OP start + 10min, HOP79 1/20
11/15 10:57:30 - 11/15 11:24:24	Fixed (-22.0, -878.0)	2/20
11/15 11:27:30 - 11/15 11:54:24	Fixed (-22.0, -778.0)	3/20
11/15 11:57:30 - 11/15 12:24:24	Fixed (-22.0, -678.0)	4/20
11/15 12:27:30 - 11/15 12:54:24	Fixed (-22.0, -578.0)	5/20
11/15 12:57:30 - 11/15 13:24:24	Fixed (-22.0, -478.0)	6/20
11/15 13:27:30 - 11/15 13:54:24	Fixed (-22.0, -378.0)	7/20
11/15 13:57:30 - 11/15 14:24:24	Fixed (-22.0, -278.0)	8/20
11/15 14:27:30 - 11/15 15:02:00	Fixed (-22.0, -178.0)	9/20
11/15 15:18:00 - 11/15 15:44:54	Fixed (-22.0, -78.0)	10/20
11/15 16:00:38 - 11/15 16:37:00	Fixed (-22.0, 12.0)	11/20 and synoptic
11/15 17:04:30 - 11/15 17:31:24	Fixed (-22.0, 112.0)	12/20
11/15 17:34:30 - 11/15 18:14:00	Fixed (-22.0, 212.0)	13/20
11/15 18:41:30 - 11/15 19:08:24	Fixed (-22.0, 312.0)	14/20
11/15 19:11:30 - 11/15 19:51:00	Fixed (-22.0, 412.0)	15/20
11/15 20:18:00 - 11/15 20:44:54	Fixed (-22.0, 512.0)	16/20
11/15 20:48:00 - 11/15 21:28:00	Fixed (-22.0, 612.0)	17/20
11/15 21:55:00 - 11/15 22:21:54	Fixed (-22.0, 712.0)	18/20
11/15 22:25:00 - 11/15 23:05:30	Fixed (-22.0, 812.0)	19/20
11/15 23:29:30 - 11/15 23:56:24	Fixed (-22.0, 912.0)	20/20
11/15 23:59:30 - 11/16 05:59:54	Track (161.9, 164.8) @ 11/15 23:56:30	AR12610 obs
11/16 06:13:00 - 11/16 10:13:00	Track (217.8, 165.8) @ 11/16 06:10:00	# HOP306 on AR12610
11/16 10:33:00 - 11/16 16:29:54	Track (374.6, -200.8) @ 11/16 10:30:00	EIS deep expts obs w/ IRIS
11/16 16:43:00 - 11/16 20:59:54	Track (737.3, -220.4) @ 11/16 16:40:00	# AR12609 w/ IRIS
11/16 21:03:00 - 11/17 05:48:00	Track (347.4, 168.8) @ 11/16 21:00:00	AR12610 obs
11/17 06:37:30 - 11/17 09:08:00	Track (427.4, 171.1) @ 11/17 06:34:30	# Cont,

PROG= 07 30-time(s)													
Subr= 1 20-time(s) 2.0sec													
Seqn= 11 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn=100 1-time(s) 10.0sec													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
	med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 10 1-time(s) 2.0sec													
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 84 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * *

Active Region Search

* * * * *

NOT USED

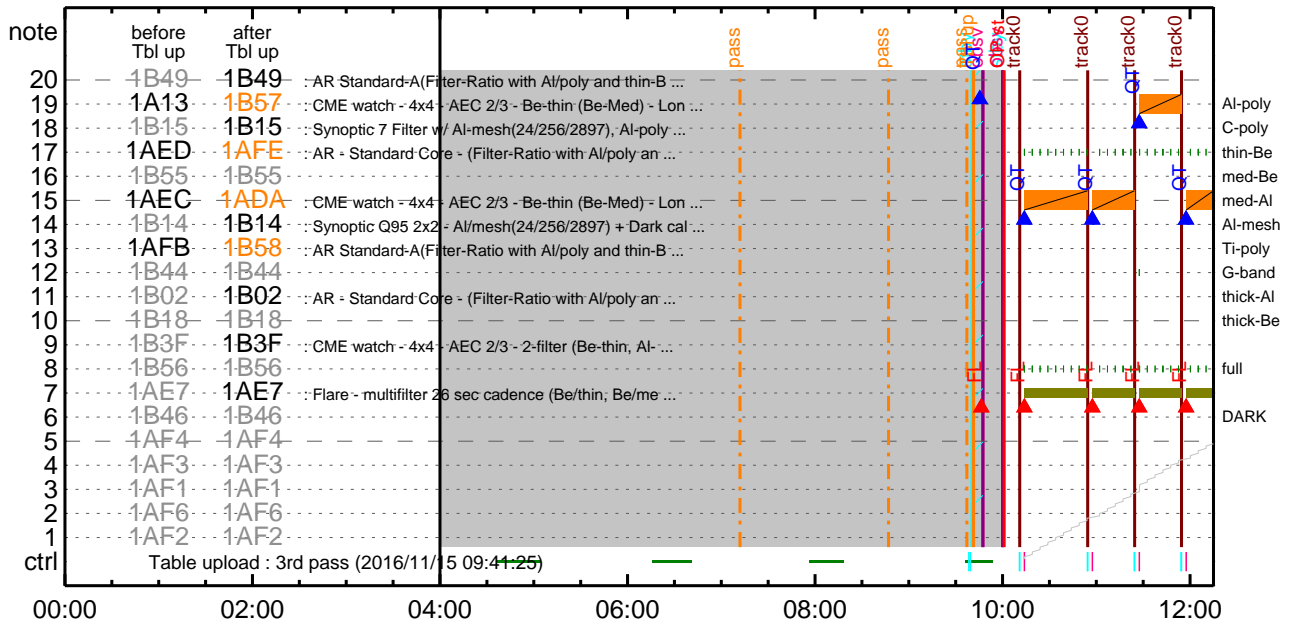
* * * * *

Flare Detection

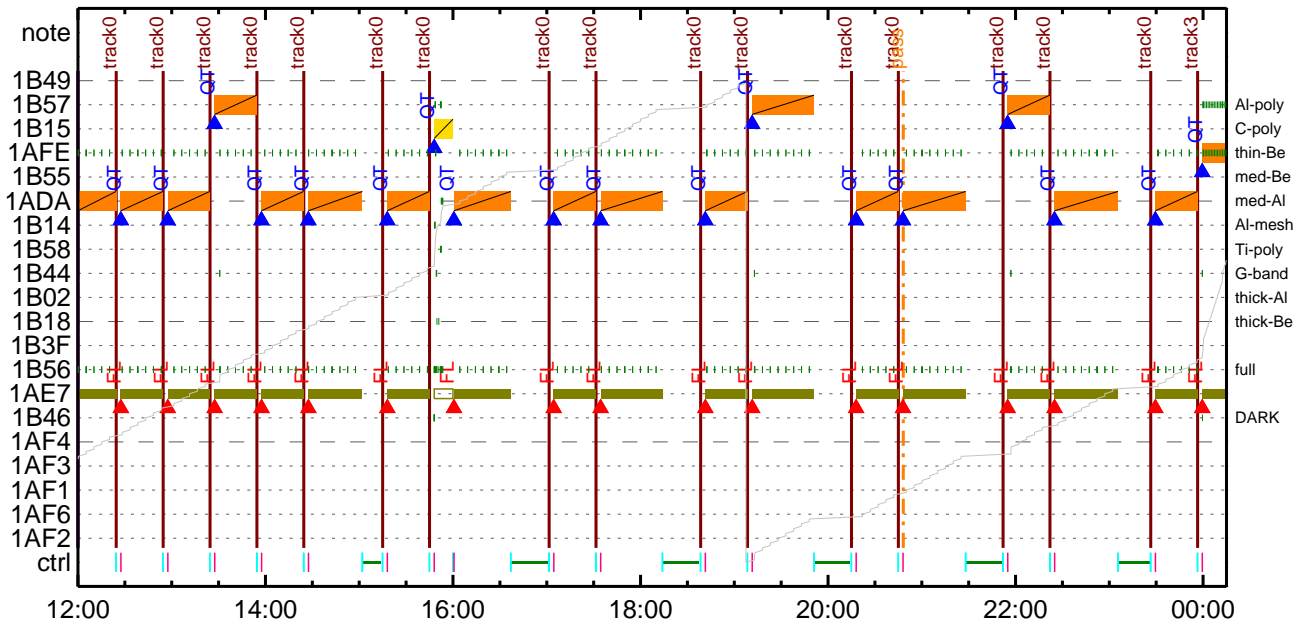
* * * * *

FLD Patrol												
Term												
Pointing (x, y)												
Comment												
11/15 16:00:24 - 11/16 06:00:18	Fixed (-22.0, 12.0)		11/20 and synoptic									
11/16 06:10:18 - 11/16 16:30:18	Track (217.8, 165.8) @ 11/16 06:10:00		# HOP306 on AR12610									
11/16 16:40:18 - 11/17 06:24:48	Track (737.3, -220.4) @ 11/16 16:40:00		# AR12609 w/ IRIS									
11/17 06:34:48 - 11/19 11:02:00	Track (427.4, 171.1) @ 11/17 06:34:30		# Cont,									
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

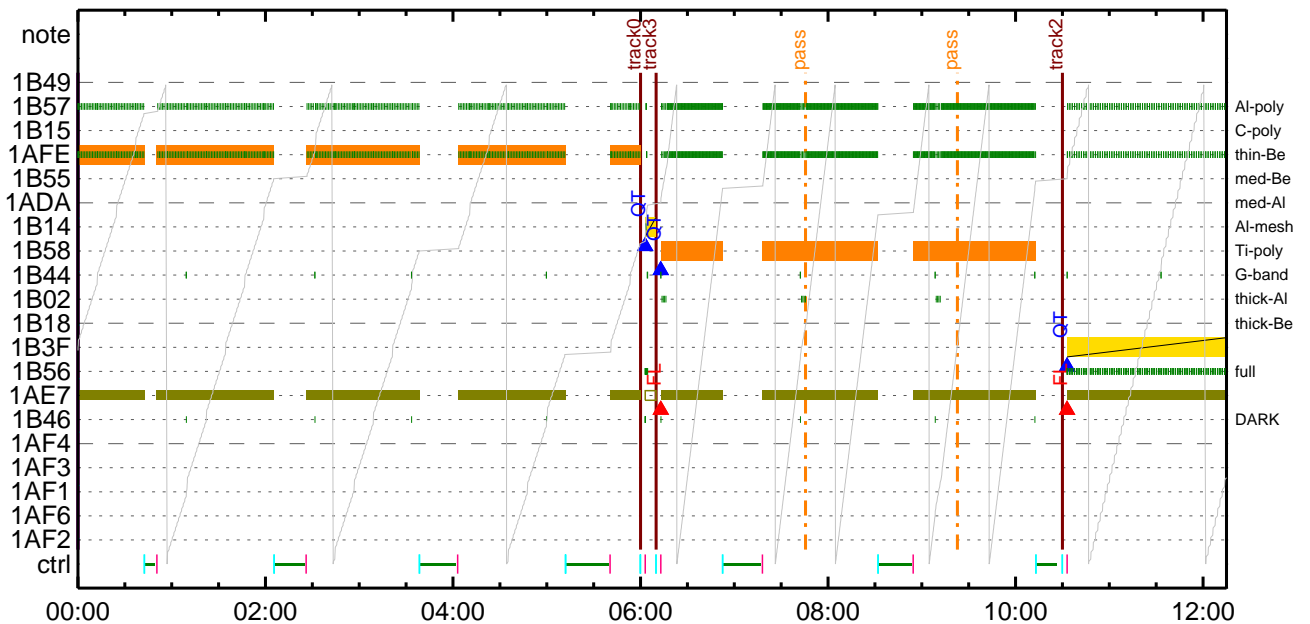
CMDI #0311 2016/11/15



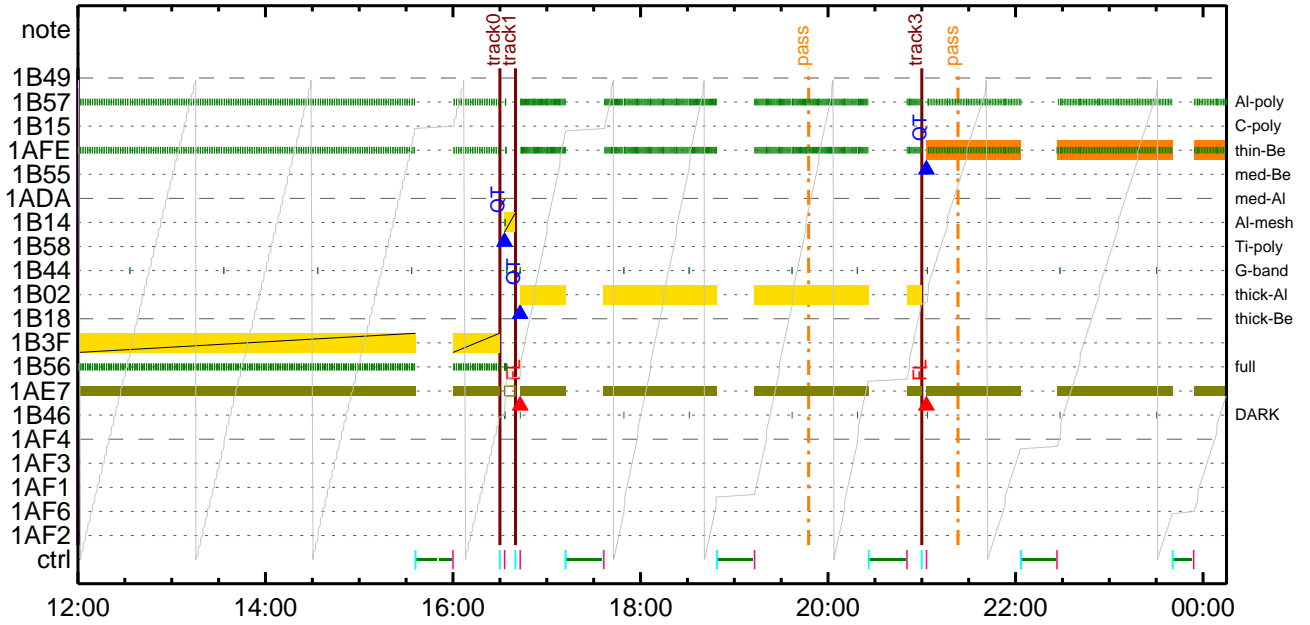
CMDI #0311 2016/11/15



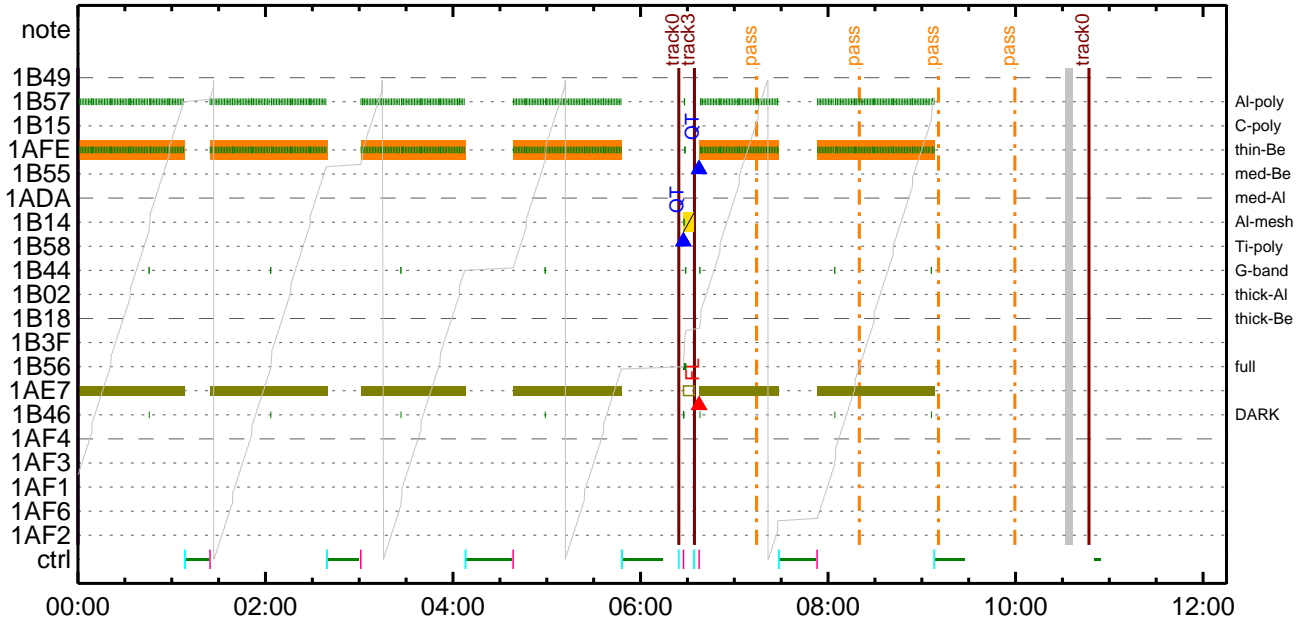
CMDI #0311 2016/11/16



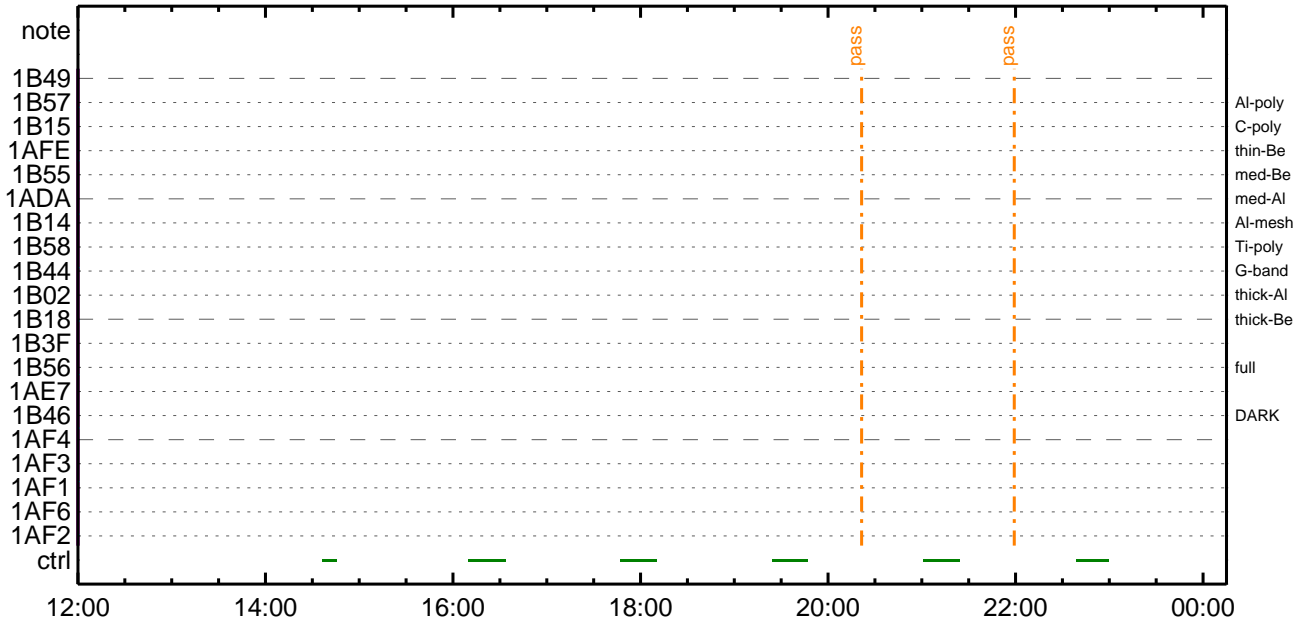
CMDI #0311 2016/11/16



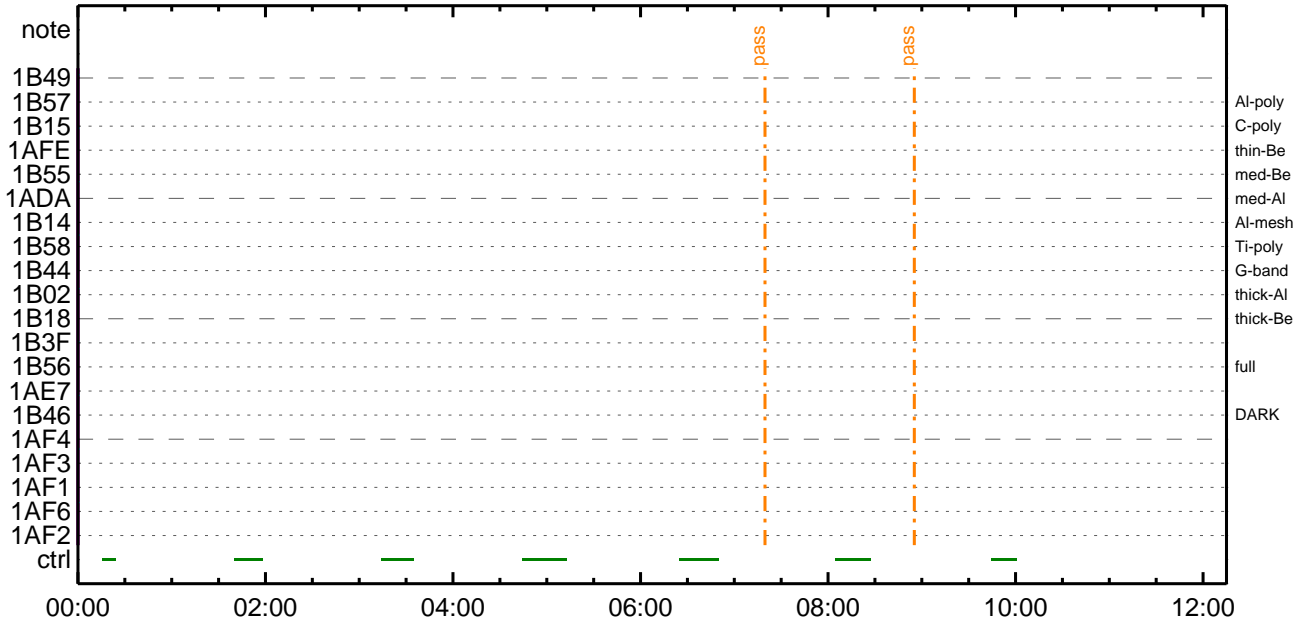
CMDI #0311 2016/11/17



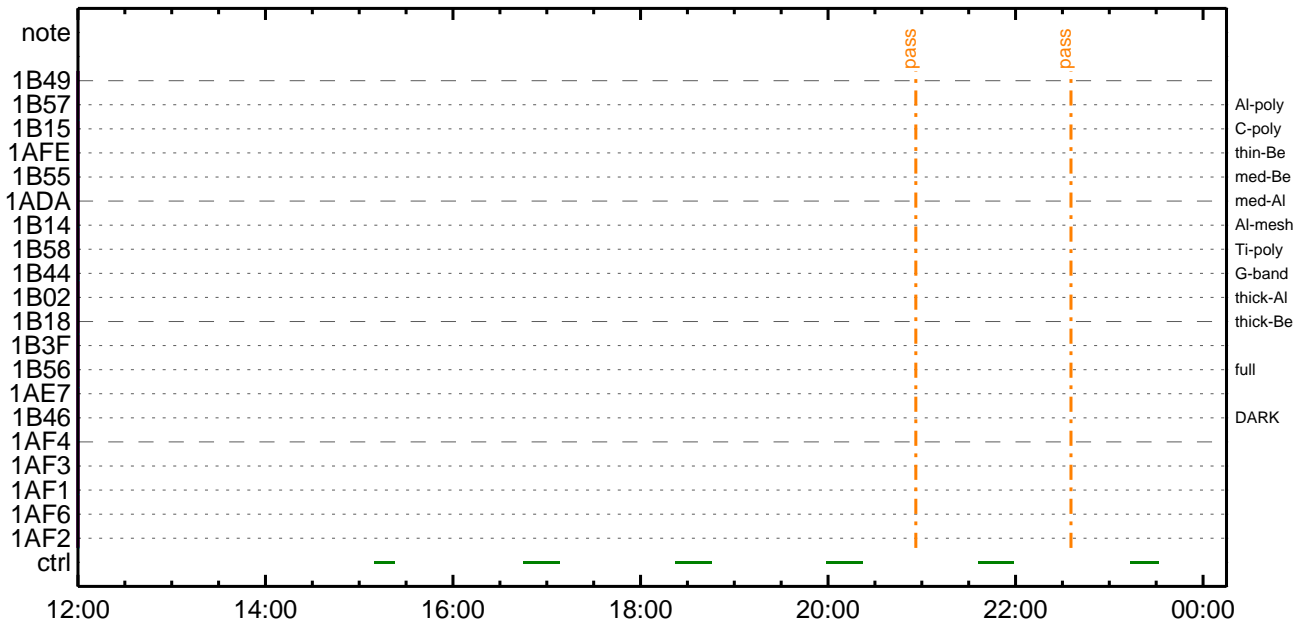
CMDI #0311 2016/11/17



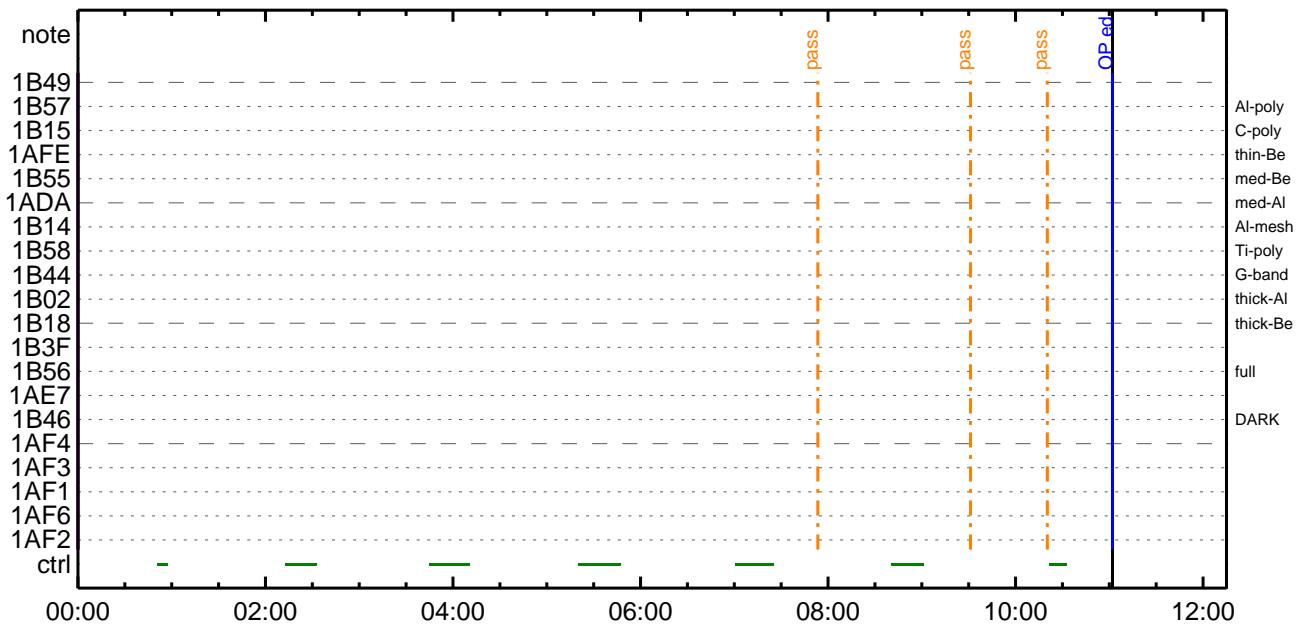
CMDI #0311 2016/11/18



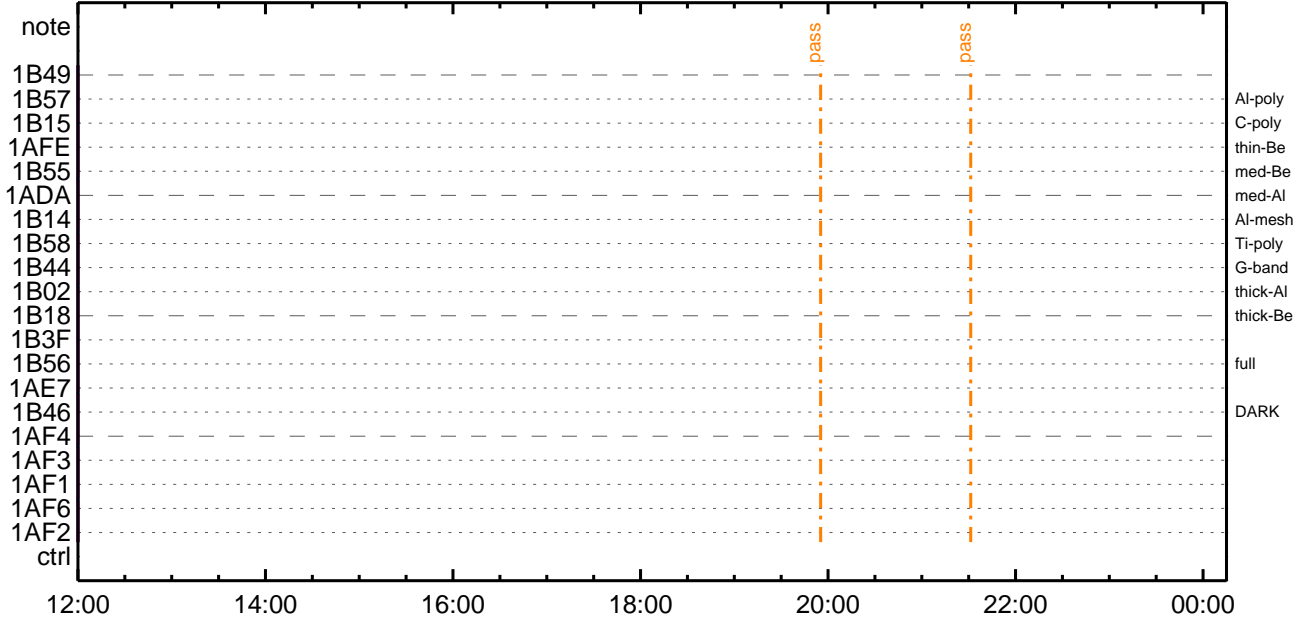
CMDI #0311 2016/11/18



CMDI #0311 2016/11/19



CMDI #0311 2016/11/19




```
0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ã
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-240:OP
0104 ( )
0105 S. OG og-240:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPïï°èãYôYx;ã
0109 C. NMOG(0x200000-0x207FFF;s 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YÀYôYx½ªÏ»ð³ÏÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGαî½Ë¹Ç•ë²ÏOKαð³ÏÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;s 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YÀYôYx½ªÏ»ð³ÏÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGαî½Ë¹Ç•ë²ÏOKαð³ÏÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;s 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YÀYôYx½ªÏ»ð³ÏÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPαî½Ë¹Ç•ë²ÏOKαð³ÏÇ§
0165 C.
0166 C. ***** òË²¼αî½Ë¹Ç•ë²ÏOKαð³ÏÇ§ *****
0167 C. DHUã½Ë¹Ç•ë²ÏOKαð³ÏÇ§
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOADα-Á÷ç@NGuî½Ë¹Ç•ë²ÏOKαð³ÏÇ§
0180 C. αð³ÏÇ§;çSETαEDUMPAÏ±±çÏNÏ¹ç¹Ïα|α³αË;ç
0181 C.
0182 C. TIÏ³ÏYôYx½ªÏ»ð³ÏÇ§(UT)
0183 +. TI 2016-11-15 09:56:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2016-11-15 09:56:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2016-11-15 09:56:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
```

0194 C.
0195 +. TI 2016-11-15 10:00:59.5
0196 DC 01-B2 DHU_OP_START
0197 C. ÷÷[HK1_TI_CMD_NUM] EQ 1COUNTUP
0198 C.
0199 C. °Ë²¼ºîÄè%îîñºîî¥Ä¥§¥Ä¥~¹àîÜ
0200 C. ÷÷[HK1_TI_CMD_ENA/DIS] EQ ENA
0201 C. ÷÷[HK1_TI_CMD_NUM] EQ 4
0202 C. ÷÷[HK1_NEXT_EXEC_PIM] EQ DHU
0203 C. ÷÷[HK1_NEXT_EXEC_DC] EQ 0xB3
0204 C.
0205 . C. *****
0206 C. TIîî°è¥Ä¥Ö¥×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC (03 ab 03 01 02)
0212 C. ÷÷[HK1_DMP_TOP_ADRS_1] EQ 07
0213 C. ÷÷[HK1_DMP_TOP_ADRS_0] EQ 2B
0214 C. ÷÷[HK1_DMP_BLOCK_NUM] EQ 3
0215 C. ÷÷[HK1_DMP_REPEAT_NUM] EQ 0
0216 C. ÷÷[HK1_DMA_DMP_PIM] EQ DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC (07 0b f8)
0219 C. ÷÷[HK1_PKT_FORM_NO] EQ 7
0220 C. ÷÷[HK1_PKT_GEN_TIME] EQ 0.25 s
0221 C. ÷÷[HK1_S_TLM_BIT_RATE] EQ 32k
0222 C. ÷÷[HK1_X_TLM_BIT_RATE] EQ 4M
0223 C. ÷÷[HK1_DMP_CHK_FLG] EQ EXEC
0224 C.
0225 . C. ¥Ä¥Ö¥×½ªî»º³îç§
0226 C. ÷÷[HK1_DMP_CHK_FLG] EQ NON
0227 C.
0228 . C. RAM ID=TI_TBLºîî²Ë¹çºè²îOKºº³îç§
0229 C.
0230 . C. DHU¥â;¼¥Ë;Ë¼¥¼. ¥î;¼¥Ë;Ëººîäº¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC (02 0a f8)
0233 C. ÷÷[HK1_PKT_FORM_NO] EQ 2
0234 C. ÷÷[HK1_PKT_GEN_TIME] EQ 0.5S
0235 C. ÷÷[HK1_S_TLM_BIT_RATE] EQ 32K
0236 C. ÷÷[HK1_X_TLM_BIT_RATE] EQ 4M
0237 C.
0238 . C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2016-11-15 10:00:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC (21 02)
0247 +. TI 2016-11-15 10:00:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC (22)
0250 . C. [] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.
0255 C. ***** XRT START *****
0256 C. Execute, after the success of OP upload.
0257 +. TI 2016-11-15 10:00:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC (c3)
0260 . C. [] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0261 C.
0262 C. ***** XRT END *****
0263 C.
0264 . C. ***** MDP `ûÄîºî»º¼¥ºèÄºº¹ºèDCBC·º²è *****
0265 C. (¼ªºî¥Ö¥Ä¥Ë¥Ë¥Ë¥ä¥ç¥èºè¼ºº¼ª»Üº¹ºè)
0266 . S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 . C. ***** ¥D¥¹·î Daily±;îîºè'ºº¹ºèDCBC·º²è *****
0271 . S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 . C. ¡äLOS¥Ä¥§¥Ä¥~¼ª»Ü;ä
0276 C.
0277 . C. ***** LOS *****
0278 C.


```

0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. ***** AOCS Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 + DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 1106638.6 +- 1.0 (s) [ ]
0136 C.
0137 . C.
0138 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139 +. DC 07-FC EIS_MODE_CHG_ENA
0140 BC (20)
0141 . C. Verify EIS_MODE_CHG_FLG is ENA
0142 +. DC 07-FC EIS_MODE_MANU
0143 BC (21 02)
0144 . C. Verify EIS in MANUAL mode
0145 . C. Estimated OBSTBL upload time is lml6s
0146 C. *****
0147 C. EIS START OBSTBL LOAD
0148 C. *****
0149 . S. RAM ram-820:EIS_OBSTBL
0150 ( )
0151 +. DC 07-FC EIS_DUMP_OBSTBL
0152 BC (07 07 07 00 00 70 00)
0153 C.
0154 C. Execute, after the success of OBSTBL upload.
0155 C. Set EIS TI-commands
0156 +. TI 2016-11-15 10:00:50.0
0157 DC 07-FC EIS_MODE_CHG_ENA
0158 BC (20)
0159 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0160 C. *****
0161 C. EIS END OBSTBL LOAD
0162 C. *****
0163 C.
0164 . C. ***** MDP 'úÃîî»ö¼ÝðËÂð¹ñèDCBC•x²è *****
0165 C. (%á°ìÿÓÿÄÿËÿÏÿÑÿÒÿÓÿÔÿÕÿÖÿ×ÿ¸ÿ¹ÿºÿ»ÿ¼ÿ½ÿ¾ÿ¿ÿÀÿÃÿÄÿÅÿ
0166 . S. DC-BC dcbc-402:DCBC
0167 (MDP_known_event)
0168 C.
0169 C.
0170 . C. ***** ¥Ð¥¹•İ Daily±;îÑñË´Øñ¹èDCBC•x²è *****
0171 . S. DC-BC dcbc-153:DCBC
0172 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0173 C.
0174 C.
0175 . C. ;ãLOS¥Á¥S¥Ä¥-¼Å»Ü;ã
0176 C.
0177 . C. ***** LOS *****
0178 C.

```


0096 C.
0097 . C. ;ãLOS¥Á¥§¥Ã¥-¼Á»Û;ã
0098 C.
0099 . C. ***** LOS *****
0100 C.

*** OP Sequence for XRT ***

2016/11/15	10:10:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/11/15	10:10:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/11/15	10:10:58.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2016/11/15	10:11:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 56 f1 01 f3		
2016/11/15	10:11:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2016/11/15	10:11:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/11/15	10:11:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/11/15	10:11:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/11/15	10:11:26.0	XRT_FLD_RESET_433_OG [0x1b1]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2016/11/15	10:13:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f		
2016/11/15	10:13:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07		
2016/11/15	10:14:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/11/15	10:54:24.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/11/15	10:54:26.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/11/15	10:54:28.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2016/11/15	10:54:30.0	AOCS_Ore-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 4e 0c 01 f3		
2016/11/15	10:54:48.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2016/11/15	10:54:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/11/15	10:54:52.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/11/15	10:54:54.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/11/15	10:54:56.0	XRT_FLD_RESET_433_OG [0x1b1]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2016/11/15	10:57:26.0	XRT_QT_PROG_SET_426_OG [0x1aa]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f		
2016/11/15	10:57:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07		
2016/11/15	10:57:30.5	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/11/15	11:24:24.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/11/15	11:24:26.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/11/15	11:24:28.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2016/11/15	11:24:30.0	AOCS_Ore-point_Start_3_OG [0x099]					
		AOCU_NM	5	02-76	00 45 26 01 f3		
2016/11/15	11:24:48.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2016/11/15	11:24:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/11/15	11:24:52.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/11/15	11:24:54.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/11/15	11:24:56.0	XRT_FLD_RESET_433_OG [0x1b1]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2016/11/15	11:27:26.0	XRT_QT_PROG_SET_444_OG [0x1bc]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13		
2016/11/15	11:27:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07		
2016/11/15	11:27:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/11/15	11:54:24.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/11/15	11:54:26.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/11/15	11:54:28.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2016/11/15	11:54:30.0	AOCS_Ore-point_Start_4_OG [0x09a]					
		AOCU_NM	5	02-76	00 3c 41 01 f3		
2016/11/15	11:54:48.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2016/11/15	11:54:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/11/15	11:54:52.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/11/15	11:54:54.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/11/15	11:54:56.0	XRT_FLD_RESET_433_OG [0x1b1]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2016/11/15	11:57:26.0	XRT_QT_PROG_SET_426_OG [0x1aa]					

Nov 15, 16 12:42

XRT_OGLIST_0311.chk

Page 2/11

2016/11/15	11:57:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2016/11/15	11:57:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/11/15	12:24:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/11/15	12:24:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/11/15	12:24:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2016/11/15	12:24:30.0	AOCS_Ore-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00	33 64 01 f3
2016/11/15	12:24:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/11/15	12:24:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/11/15	12:24:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/11/15	12:24:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/11/15	12:24:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/11/15	12:27:26.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f
2016/11/15	12:27:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2016/11/15	12:27:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/11/15	12:54:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/11/15	12:54:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/11/15	12:54:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2016/11/15	12:54:30.0	AOCS_Ore-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00	2a 7e 01 f3
2016/11/15	12:54:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/11/15	12:54:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/11/15	12:54:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/11/15	12:54:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/11/15	12:54:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/11/15	12:57:26.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f
2016/11/15	12:57:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2016/11/15	12:57:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/11/15	13:24:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/11/15	13:24:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/11/15	13:24:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2016/11/15	13:24:30.0	AOCS_Ore-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00	21 99 01 f3
2016/11/15	13:24:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/11/15	13:24:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/11/15	13:24:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/11/15	13:24:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/11/15	13:24:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/11/15	13:27:26.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	13
2016/11/15	13:27:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2016/11/15	13:27:30.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/11/15	13:54:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/11/15	13:54:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/11/15	13:54:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2016/11/15	13:54:30.0	AOCS_Ore-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00	18 b4 01 f3
2016/11/15	13:54:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/11/15	13:54:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/11/15	13:54:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/11/15	13:54:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	

2016/11/15	13:54:56.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/11/15	13:57:26.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f			
2016/11/15	13:57:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2016/11/15	13:57:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/11/15	14:24:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/11/15	14:24:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/11/15	14:24:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2016/11/15	14:24:30.0	AOCS_Ore-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00	0f	ce	01	f3
2016/11/15	14:24:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/11/15	14:24:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/11/15	14:24:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/11/15	14:24:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/11/15	14:24:56.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/11/15	14:27:26.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f			
2016/11/15	14:27:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2016/11/15	14:27:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/11/15	15:02:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/11/15	15:02:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/11/15	15:02:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/11/15	15:02:06.0	XRT_PREFLR_STRT_414_OG [0x19e]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/11/15	15:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/11/15	15:14:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/11/15	15:14:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/11/15	15:14:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2016/11/15	15:15:00.0	AOCS_Ore-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00	06	f1	01	f3
2016/11/15	15:15:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/11/15	15:15:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/11/15	15:15:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/11/15	15:15:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/11/15	15:15:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/11/15	15:17:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f			
2016/11/15	15:17:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2016/11/15	15:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/11/15	15:44:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/11/15	15:44:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/11/15	15:44:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2016/11/15	15:45:00.0	AOCS_Ore-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00	fe	f2	01	f3
2016/11/15	15:45:18.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/11/15	15:47:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/11/15	15:47:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/11/15	15:47:58.0	XRT_QT_PROG_SET_442_OG [0x1ba]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2016/11/15	15:48:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/11/15	16:00:00.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/11/15	16:00:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/11/15	16:00:04.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2016/11/15	16:00:24.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/11/15	16:00:26.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							

2016/11/15	16:00:28.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/11/15	16:00:30.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/11/15	16:00:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/11/15	16:00:34.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/11/15	16:00:36.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f	
2016/11/15	16:00:38.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07	
2016/11/15	16:37:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/11/15	16:37:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/11/15	16:37:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/11/15	16:37:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/11/15	16:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/11/15	17:01:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/11/15	17:01:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/11/15	17:01:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/11/15	17:01:30.0	AOCS_Ore-point_Start_12_OG [0x0a2]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2016/11/15	17:01:48.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00	f6 0d 01 f3	
2016/11/15	17:01:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2016/11/15	17:01:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/11/15	17:01:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/11/15	17:01:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/11/15	17:04:26.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/11/15	17:04:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f	
2016/11/15	17:04:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07	
2016/11/15	17:31:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/11/15	17:31:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/11/15	17:31:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/11/15	17:31:30.0	AOCS_Ore-point_Start_13_OG [0x0a3]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2016/11/15	17:31:48.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00	ed 27 01 f3	
2016/11/15	17:31:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2016/11/15	17:31:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/11/15	17:31:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/11/15	17:31:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/11/15	17:34:26.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/11/15	17:34:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f	
2016/11/15	17:34:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07	
2016/11/15	18:14:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/11/15	18:14:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/11/15	18:14:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/11/15	18:14:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/11/15	18:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/11/15	18:38:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/11/15	18:38:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/11/15	18:38:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/11/15	18:38:30.0	AOCS_Ore-point_Start_14_OG [0x0a4]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2016/11/15	18:38:48.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00	e4 42 01 f3	
2016/11/15	18:38:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		

Nov 15, 16 12:42

XRT_OGLIST_0311.chk

Page 5/11

2016/11/15	18:38:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/11/15	18:38:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/11/15	18:38:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/15	18:41:26.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2016/11/15	18:41:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/11/15	18:41:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/15	19:08:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/15	19:08:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/15	19:08:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/11/15	19:08:30.0	AOCS_OrE-point_Start_15_OG [0x0a5]	AOCU_NM	5	02-76	00 db 65 01 f3
2016/11/15	19:08:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/11/15	19:08:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/11/15	19:08:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/11/15	19:08:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/11/15	19:08:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/15	19:11:26.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2016/11/15	19:11:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/11/15	19:11:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/15	19:51:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/15	19:51:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/15	19:51:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/15	19:51:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/11/15	19:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/11/15	20:14:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/15	20:14:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/15	20:14:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/11/15	20:15:00.0	AOCS_OrE-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	00 d2 7f 01 f3
2016/11/15	20:15:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/11/15	20:15:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/11/15	20:15:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/11/15	20:15:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/11/15	20:15:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/15	20:17:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2016/11/15	20:17:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/11/15	20:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/15	20:44:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/15	20:44:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/15	20:44:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/11/15	20:45:00.0	AOCS_OrE-point_Start_17_OG [0x0a7]	AOCU_NM	5	02-76	00 c9 9a 01 f3
2016/11/15	20:45:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/11/15	20:45:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/11/15	20:45:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/11/15	20:45:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/11/15	20:45:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/15	20:47:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2016/11/15	20:47:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/11/15	20:48:00.0	XRT_CTRL_AUTO_408_OG [0x198]				

Nov 15, 16 12:42

XRT_OGLIST_0311.chk

Page 6/11

2016/11/15	21:28:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/11/15	21:28:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/11/15	21:28:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2016/11/15	21:28:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/11/15	21:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/11/15	21:51:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/11/15	21:51:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/11/15	21:51:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2016/11/15	21:52:00.0	AOCS_Ore-point_Start_18_OG [0x0a8]	AOCU_NM	5	02-76	00 c0 b5 01 f3			
			AOCU_NM	5	02-76	00 c0 b5 01 f3			
2016/11/15	21:52:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
			MDP_XRT_FLD_ENA	1	07-F0	d8			
2016/11/15	21:52:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2016/11/15	21:52:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
			MDP_XRT_AEC_RESET	1	07-F0	d0			
2016/11/15	21:52:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/11/15	21:52:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2016/11/15	21:54:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13			
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 13			
2016/11/15	21:54:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07			
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 07			
2016/11/15	21:55:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/11/15	22:21:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/11/15	22:21:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/11/15	22:21:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2016/11/15	22:22:00.0	AOCS_Ore-point_Start_19_OG [0x0a9]	AOCU_NM	5	02-76	00 b7 cf 01 f3			
			AOCU_NM	5	02-76	00 b7 cf 01 f3			
2016/11/15	22:22:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
			MDP_XRT_FLD_ENA	1	07-F0	d8			
2016/11/15	22:22:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2016/11/15	22:22:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
			MDP_XRT_AEC_RESET	1	07-F0	d0			
2016/11/15	22:22:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/11/15	22:22:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2016/11/15	22:24:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f			
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f			
2016/11/15	22:24:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07			
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 07			
2016/11/15	22:25:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/11/15	23:05:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/11/15	23:05:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/11/15	23:05:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2016/11/15	23:05:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/11/15	23:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/11/15	23:26:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/11/15	23:26:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/11/15	23:26:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2016/11/15	23:26:30.0	AOCS_Ore-point_Start_20_OG [0x0aa]	AOCU_NM	5	02-76	00 ae f2 01 f3			
			AOCU_NM	5	02-76	00 ae f2 01 f3			
2016/11/15	23:26:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
			MDP_XRT_FLD_ENA	1	07-F0	d8			
2016/11/15	23:26:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2016/11/15	23:26:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
			MDP_XRT_AEC_RESET	1	07-F0	d0			
2016/11/15	23:26:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/11/15	23:26:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2016/11/15	23:29:26.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f			
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f			
2016/11/15	23:29:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07			
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 07			
2016/11/15	23:29:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			

Nov 15, 16 12:42

XRT_OGLIST_0311.chk

Page 7/11

2016/11/15	23:56:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/15	23:56:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/15	23:56:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/11/15	23:56:30.0	AOCS_OrE-point_Start_21_OG [0x0ab]	AOCU_NM	5	02-76	03 00 00 00 00
2016/11/15	23:56:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/11/15	23:56:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/11/15	23:56:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/11/15	23:56:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/11/15	23:56:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/15	23:59:26.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2016/11/15	23:59:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/11/15	23:59:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/16	00:42:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/16	00:42:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/16	00:42:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/16	00:42:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/11/16	00:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/11/16	00:49:30.0	XRT_Custom_430_OG [0x1ae]				
2016/11/16	00:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/16	02:05:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/16	02:05:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/16	02:05:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/16	02:05:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/11/16	02:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/11/16	02:25:00.5	XRT_Custom_430_OG [0x1ae]				
2016/11/16	02:26:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/16	03:38:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/16	03:38:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/16	03:38:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/16	03:38:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/11/16	03:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/11/16	04:02:00.0	XRT_Custom_430_OG [0x1ae]				
2016/11/16	04:03:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/16	05:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/16	05:12:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/16	05:12:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/16	05:12:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/11/16	05:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/11/16	05:39:30.5	XRT_Custom_430_OG [0x1ae]				
2016/11/16	05:40:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/16	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/16	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/16	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/11/16	06:00:00.0	AOCS_OrE-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	00 00 00 00 00
2016/11/16	06:00:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9
2016/11/16	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/11/16	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/11/16	06:02:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2016/11/16	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]				

2016/11/16	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/16	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/16	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/16	06:10:00.0	AOCS_OrE-point_Start_21_OG [0x0ab]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/11/16	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 00 00 00 00
2016/11/16	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/11/16	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/11/16	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/11/16	06:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/11/16	06:12:56.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/16	06:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2016/11/16	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/11/16	06:52:30.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/16	06:52:33.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/16	06:52:35.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/16	06:52:37.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/16	06:55:45.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/11/16	07:17:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/11/16	07:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2016/11/16	08:32:00.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/16	08:32:03.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/16	08:32:05.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/16	08:32:07.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/16	08:35:15.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/11/16	08:53:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/11/16	08:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2016/11/16	10:13:00.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/16	10:13:03.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/16	10:13:05.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/16	10:13:07.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/16	10:16:15.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/11/16	10:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/11/16	10:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_CTRL_MANU_402_OG [0x192]			
2016/11/16	10:29:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/16	10:30:00.0	AOCS_OrE-point_Start_23_OG [0x0ad]	MDP_XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/11/16	10:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02 00 00 00 00
2016/11/16	10:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/11/16	10:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/11/16	10:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/11/16	10:30:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/11/16	10:32:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/16	10:32:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2016/11/16	10:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/11/16	15:36:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/16	15:36:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/16	15:36:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/16	15:36:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/16			MDP_XRT_PREFLR_STRT	1	07-F0	e8

Nov 15, 16 12:42

XRT_OGLIST_0311.chk

Page 9/11

2016/11/16	15:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/11/16	15:59:00.0	XRT_Custom_430_OG [0x1ae]							
2016/11/16	16:00:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/11/16	16:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/11/16	16:29:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/11/16	16:29:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/11/16	16:30:00.0	AOCS_Ore-point_Start_22_OG [0x0ac]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2016/11/16	16:30:18.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/11/16	16:32:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/11/16	16:32:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/11/16	16:32:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2016/11/16	16:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/11/16	16:39:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/11/16	16:39:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/11/16	16:39:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/11/16	16:40:00.0	AOCS_Ore-point_Start_24_OG [0x0ae]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2016/11/16	16:40:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/11/16	16:40:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/11/16	16:40:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/11/16	16:40:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/11/16	16:40:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/11/16	16:42:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2016/11/16	16:42:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/11/16	16:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/11/16	17:12:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/11/16	17:12:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/11/16	17:12:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/11/16	17:12:06.0	XRT_PREFLR_STRT_414_OG [0x19e]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/11/16	17:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/11/16	17:35:30.0	XRT_Custom_430_OG [0x1ae]							
2016/11/16	17:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/11/16	18:49:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/11/16	18:49:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/11/16	18:49:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/11/16	18:49:06.0	XRT_PREFLR_STRT_414_OG [0x19e]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/11/16	18:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/11/16	19:12:00.5	XRT_Custom_430_OG [0x1ae]							
2016/11/16	19:13:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/11/16	20:26:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/11/16	20:26:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/11/16	20:26:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/11/16	20:26:06.0	XRT_PREFLR_STRT_414_OG [0x19e]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/11/16	20:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/11/16	20:49:30.0	XRT_Custom_430_OG [0x1ae]							
2016/11/16	20:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/11/16	20:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/11/16	20:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/11/16	20:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/11/16	21:00:00.0	AOCS_ORe-point_Start_21_OG [0x0ab]							

2016/11/16	21:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76 03 00 00 00 00
			MDP_XRT_FLD_ENA	1	07-F0 d8
2016/11/16	21:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		1	07-F0 c8
			MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2016/11/16	21:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]		1	07-F0 d0
			MDP_XRT_AEC_RESET	1	07-F0 d0
2016/11/16	21:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]		1	07-F0 d5
			MDP_XRT_ARS_DIS	1	07-F0 d5
2016/11/16	21:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]		1	07-F0 da
			MDP_XRT_FLD_RESET	1	07-F0 da
2016/11/16	21:02:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]		2	07-F0 c4 11
			MDP_XRT_QT_PROG_SET	2	07-F0 c4 11
2016/11/16	21:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]		2	07-F0 c5 07
			MDP_XRT_FL_PROG_SET	2	07-F0 c5 07
2016/11/16	21:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0 c0
			MDP_XRT_CTRL_AUTO	1	07-F0 c0
2016/11/16	22:03:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0 c1
			MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/11/16	22:03:32.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0 c1
			MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/11/16	22:03:34.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0 da
			MDP_XRT_FLD_RESET	1	07-F0 da
2016/11/16	22:03:36.0	XRT_PREFLR_STRT_414_OG [0x19e]		1	07-F0 e8
			MDP_XRT_PREFLR_STRT	1	07-F0 e8
2016/11/16	22:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0 e9
			MDP_XRT_PREFLR_STOP	1	07-F0 e9
2016/11/16	22:25:30.0	XRT_Custom_430_OG [0x1ae]			
2016/11/16	22:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0 c0
			MDP_XRT_CTRL_AUTO	1	07-F0 c0
2016/11/16	23:40:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0 c1
			MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/11/16	23:40:32.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0 c1
			MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/11/16	23:40:34.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0 da
			MDP_XRT_FLD_RESET	1	07-F0 da
2016/11/16	23:40:36.0	XRT_PREFLR_STRT_414_OG [0x19e]		1	07-F0 e8
			MDP_XRT_PREFLR_STRT	1	07-F0 e8
2016/11/16	23:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0 e9
			MDP_XRT_PREFLR_STOP	1	07-F0 e9
2016/11/16	23:53:00.0	XRT_Custom_430_OG [0x1ae]			
2016/11/16	23:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0 c0
			MDP_XRT_CTRL_AUTO	1	07-F0 c0
2016/11/17	01:08:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0 c1
			MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/11/17	01:08:32.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0 c1
			MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/11/17	01:08:34.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0 da
			MDP_XRT_FLD_RESET	1	07-F0 da
2016/11/17	01:08:36.0	XRT_PREFLR_STRT_414_OG [0x19e]		1	07-F0 e8
			MDP_XRT_PREFLR_STRT	1	07-F0 e8
2016/11/17	01:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0 e9
			MDP_XRT_PREFLR_STOP	1	07-F0 e9
2016/11/17	01:23:30.0	XRT_Custom_430_OG [0x1ae]			
2016/11/17	01:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0 c0
			MDP_XRT_CTRL_AUTO	1	07-F0 c0
2016/11/17	02:39:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0 c1
			MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/11/17	02:39:32.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0 c1
			MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/11/17	02:39:34.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0 da
			MDP_XRT_FLD_RESET	1	07-F0 da
2016/11/17	02:39:36.0	XRT_PREFLR_STRT_414_OG [0x19e]		1	07-F0 e8
			MDP_XRT_PREFLR_STRT	1	07-F0 e8
2016/11/17	02:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0 e9
			MDP_XRT_PREFLR_STOP	1	07-F0 e9
2016/11/17	03:00:00.0	XRT_Custom_430_OG [0x1ae]			
2016/11/17	03:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0 c0
			MDP_XRT_CTRL_AUTO	1	07-F0 c0
2016/11/17	04:08:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0 c1
			MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/11/17	04:08:02.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0 c1
			MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/11/17	04:08:04.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0 da
			MDP_XRT_FLD_RESET	1	07-F0 da
2016/11/17	04:08:06.0	XRT_PREFLR_STRT_414_OG [0x19e]		1	07-F0 e8
			MDP_XRT_PREFLR_STRT	1	07-F0 e8
2016/11/17	04:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0 e9
			MDP_XRT_PREFLR_STOP	1	07-F0 e9
2016/11/17	04:37:30.0	XRT_Custom_430_OG [0x1ae]			
2016/11/17	04:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0 c0
			MDP_XRT_CTRL_AUTO	1	07-F0 c0
2016/11/17	05:48:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0 c1
			MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/11/17	05:48:02.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0 c1
			MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/11/17	05:48:04.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0 da
			MDP_XRT_FLD_RESET	1	07-F0 da
2016/11/17	05:48:06.0	XRT_PREFLR_STRT_414_OG [0x19e]		1	07-F0 e8
			MDP_XRT_PREFLR_STRT	1	07-F0 e8
2016/11/17	05:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0 e9
			MDP_XRT_PREFLR_STOP	1	07-F0 e9
2016/11/17	06:24:24.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0 c1

Nov 15, 16 12:42

XRT_OGLIST_0311.chk

Page 11/11

2016/11/17	06:24:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/17	06:24:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/17	06:24:30.0	AOCS_Orе-point_Start_22_OG [0x0ac]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/11/17	06:24:48.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00 00 00 00 00
2016/11/17	06:27:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2016/11/17	06:27:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/11/17	06:27:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/11/17	06:27:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2016/11/17	06:34:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/17	06:34:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/17	06:34:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/17	06:34:30.0	AOCS_Orе-point_Start_21_OG [0x0ab]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/11/17	06:34:48.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 00 00 00 00
2016/11/17	06:34:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/11/17	06:34:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/11/17	06:34:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/11/17	06:34:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/11/17	06:37:26.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/17	06:37:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2016/11/17	06:37:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/11/17	07:28:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/17	07:28:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/17	07:28:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/17	07:28:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/17	07:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/11/17	07:52:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/11/17	07:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2016/11/17	09:08:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/17	09:08:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/17	09:08:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/17	09:08:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/17	09:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/11/17	10:47:00.0	AOCS_Orе-point_Start_22_OG [0x0ac]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
		AOCU_NM	AOCS_Orе-point_Start_22_OG [0x0ac]	5	02-76	00 00 00 00 00