

XRT Timeline to be uploaded on 2016/12/22

Period: 2016/12/22 11:08:00 - 2016/12/27 09:44:00

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Normal mode

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XOB #1B61: AR (Filter-Ratio with Al/poly and thin-Be) with PFB, 512x512 at 1240 984, thick-Al context, with G-band (3ms/3ms leak), 60s cad

Term	Pointing (x, y)	Comment
12/22 11:21:00 - 12/22 12:39:54	Track (722.1, -399.0) @ 12/22 11:18:00	# OP start + 10min, new AR 12620 observation
12/22 17:53:00 - 12/22 20:25:30	Track (766.1, -379.4) @ 12/22 17:50:00	# HOP 329 with ALMA (AR 12620)
12/22 20:50:00 - 12/23 06:24:24	Track (763.2, -400.6) @ 12/22 20:30:00	AR 12620 tracking
12/23 06:37:30 - 12/23 17:46:30	Track (801.6, -402.6) @ 12/23 06:34:30	# AR 12620 observation
12/23 18:23:00 - 12/24 05:56:24	Track (837.4, -405.3) @ 12/23 18:20:00	# AR 12620 tracking
12/24 06:09:30 - 12/24 09:44:00	Fixed (863.0, -408.0)	# AR 12620 observation

PROG= 17 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec																		
Seqn= 56	1-time(s)	2.0sec																		
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384	(1064, 1048)	DPCM	0	0	2.0sec							
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384	(1064, 1048)	DPCM	0	0	2.0sec							
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384	(1064, 1048)	Q=98	0	0	2.0sec							
Seqn= 51	3-time(s)	2.0sec																		
Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	512x512	(1064, 1048)	Q=98	3	0	2.0sec							
Subr= 2	120-time(s)	60.0sec																		
Seqn= 17	1-time(s)	30.0sec																		
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	512x512	(1240, 984)	Q=95	2	0	2.0sec							
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512	(1240, 984)	Q=95	2	0	14.0sec							
Seqn= 21	1-time(s)	2.0sec																		
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	1	2.0sec							
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1064, 1048)	Q=95	3	1	2.0sec							

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1B5F: AR (Filter-Ratio with Al/poly and thin-Be) with PFB, 512x512 at 1064 1048, thick-Al context, with G-band (3ms/3ms leak), 40s cad

Term	Pointing (x, y)	Comment
12/22 12:43:00 - 12/22 16:59:54	Track (743.0, -378.5) @ 12/22 12:40:00	HOP 328 with ALMA (AR 12620)

PROG= 04 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec																		
Seqn= 56	1-time(s)	2.0sec																		
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384	(1064, 1048)	DPCM	0	0	2.0sec							
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384	(1064, 1048)	DPCM	0	0	2.0sec							
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384	(1064, 1048)	Q=98	0	0	2.0sec							
Seqn= 51	3-time(s)	2.0sec																		
Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	512x512	(1064, 1048)	Q=98	3	0	2.0sec							
Seqn= 16	120-time(s)	40.0sec																		
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	512x512	(1064, 1048)	Q=98	3	0	2.0sec							
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512	(1064, 1048)	Q=98	3	0	14.0sec							
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1064, 1048)	Q=95	3	1	2.0sec							
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	1	2.0sec							

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1B14: Synoptic Q95 2x2 - Al/mesh(24/256/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(45/512/4096) + T

Term	Pointing (x, y)	Comment
12/22 17:03:00 - 12/22 17:49:54	Fixed (0.0, 0.0)	synoptic + SP flat field, shifted 1 H
12/23 06:27:30 - 12/23 06:34:24	Fixed (0.0, 0.0)	synoptic, shifted 24.5 min
12/23 18:13:00 - 12/23 18:19:54	Fixed (0.0, 0.0)	synoptic, shifted 10.0 min
12/24 05:59:30 - 12/24 06:06:24	Fixed (0.0, 0.0)	synoptic, shifted -3.5 min

PROG= 14 1-time(s)

Subr= 1	1-time(s)	2.0sec																		
Seqn= 5	1-time(s)	2.0sec																		
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec							
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec							
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec							
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512	(1024, 1024)	DPCM	0	0	2.0sec							
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048	(1024, 1024)	DPCM	0	0	2.0sec							
Seqn= 1	1-time(s)	2.0sec																		
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	24ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							
Seqn= 99	1-time(s)	2.0sec																		
Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							
Seqn= 67	1-time(s)	2.0sec																		
thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							
thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							
Seqn= 54	1-time(s)	2.0sec																		
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec							
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							

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Flare mode

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XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
12/22 11:21:00 - 12/22 12:39:54	Track (722.1, -399.0) @ 12/22 11:18:00	# OP start + 10min, new AR 12620 observation
12/22 12:43:00 - 12/22 16:59:54	Track (743.0, -378.5) @ 12/22 12:40:00	HOP 328 with ALMA (AR 12620)
12/22 17:53:00 - 12/22 20:25:30	Track (766.1, -379.4) @ 12/22 17:50:00	# HOP 329 with ALMA (AR 12620)
12/22 20:50:00 - 12/23 06:24:24	Track (763.2, -400.6) @ 12/22 20:30:00	AR 12620 tracking
12/23 06:37:30 - 12/23 17:46:30	Track (801.6, -402.6) @ 12/23 06:34:30	# AR 12620 observation
12/23 18:23:00 - 12/24 05:56:24	Track (837.4, -405.3) @ 12/23 18:20:00	# AR 12620 tracking
12/24 06:09:30 - 12/24 09:44:00	Fixed (863.0, -408.0)	# AR 12620 observation

PROG= 07 30-time(s)

Subr= 1	20-time(s)	2.0sec										
Seqn= 11	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/thick-Al close	Safe Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec		
Seqn=100	1-time(s)	10.0sec										
thin-Be/Open	med-Be/Open close	Safe Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec		
med-Be/Open	Open/thick-Al close	Safe Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
Open/thick-Al	Open/thick-Be close	Safe Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
Subr= 2	1-time(s)	2.0sec										
Seqn= 10	1-time(s)	2.0sec										
med-Al/Open	med-Al/thick-Al close	Safe Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
Open/thick-Be	Open/thick-Be close	Safe Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
Seqn= 11	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/thick-Al close	Safe Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec		
Seqn= 84	1-time(s)	2.0sec										
Open/G-band	Open/G-band open	Safe Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
Open/G-band	Open/G-band close	Safe Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
Open/thick-Al	Open/thick-Al close	Safe Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
Open/thick-Al	Open/thick-Al close	Safe Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec		

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Active Region Search

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NOT USED

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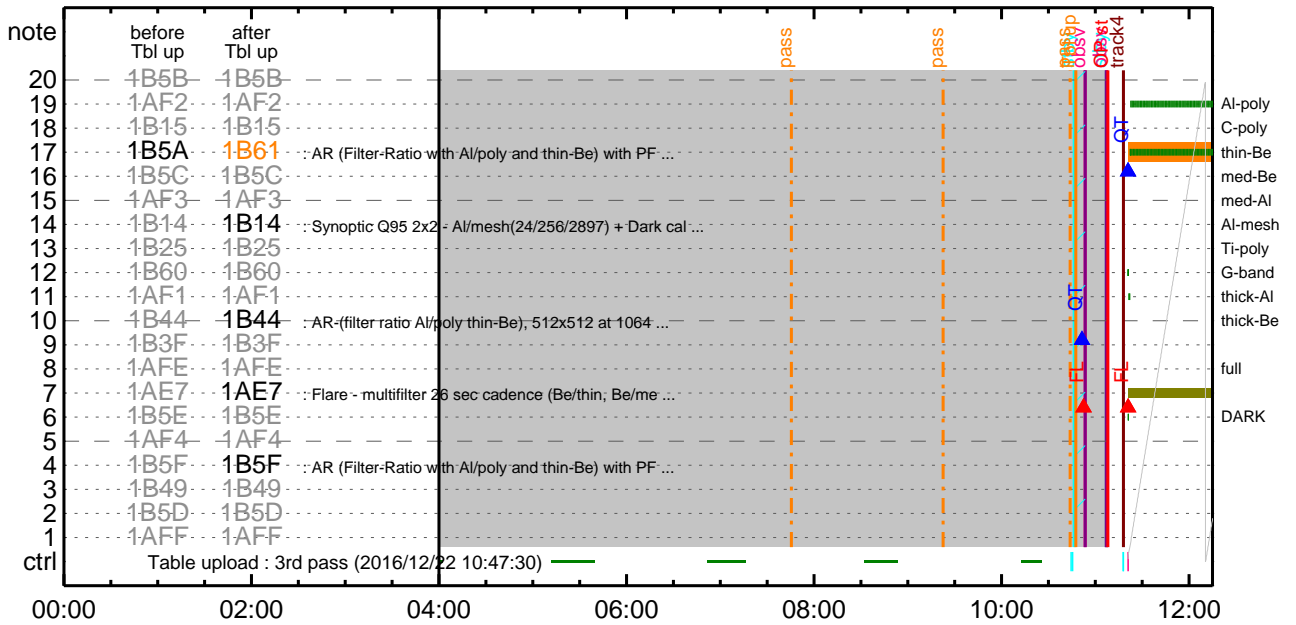
Flare Detection

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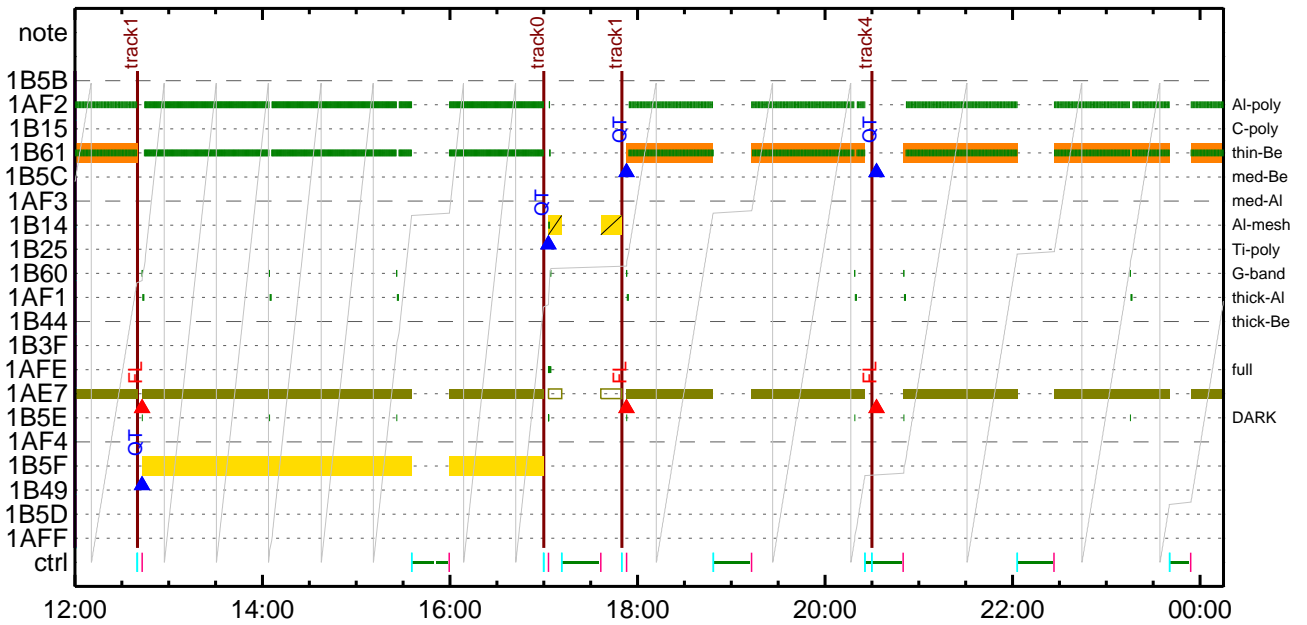
FLD Patrol

Term	Pointing (x, y)	Comment						
12/22 17:50:18 - 12/23 06:24:48	Track (766.1, -379.4) @ 12/22 17:50:00	# HOP 329 with ALMA (AR 12620)						
12/23 06:34:48 - 12/23 18:10:18	Track (801.6, -402.6) @ 12/23 06:34:30	# AR 12620 observation						
12/23 18:20:18 - 12/24 05:56:48	Track (837.4, -405.3) @ 12/23 18:20:00	# AR 12620 tracking						
12/24 06:06:48 - 12/27 09:44:00	Fixed (863.0, -408.0)	# AR 12620 observation						
Open/Ti-poly	Open/thick-Al close	Safe Norm	8ms	Obs	8x8	Q=50	30sec	

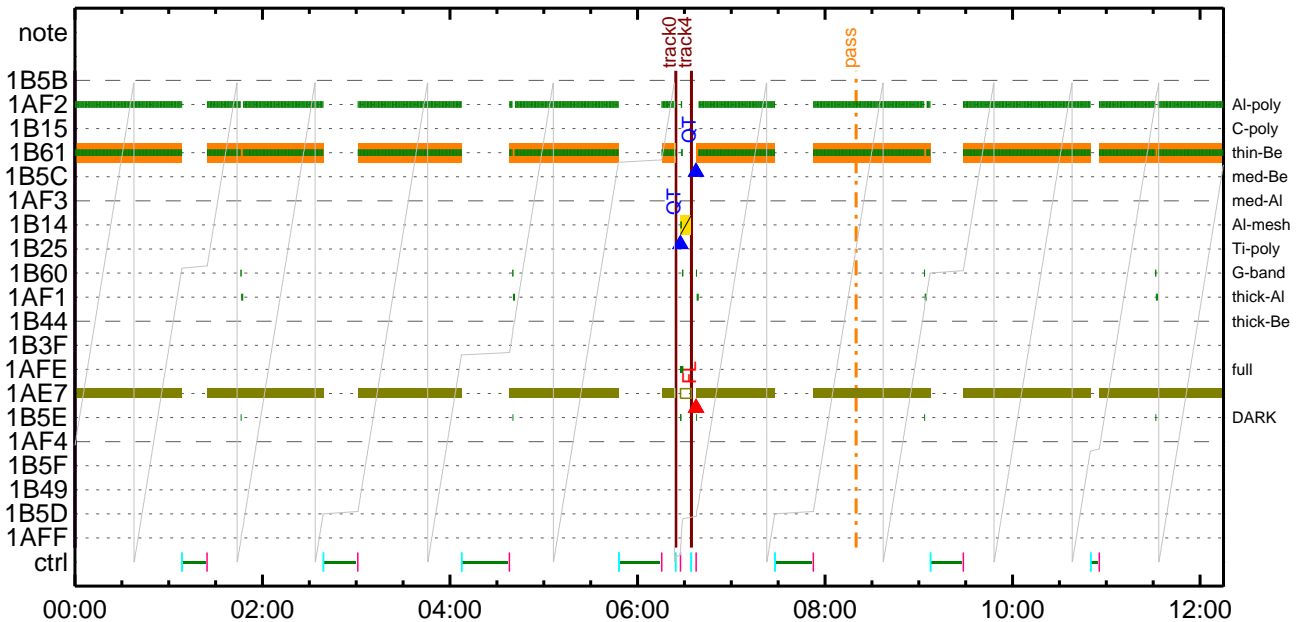
CMDI #0413 2016/12/22



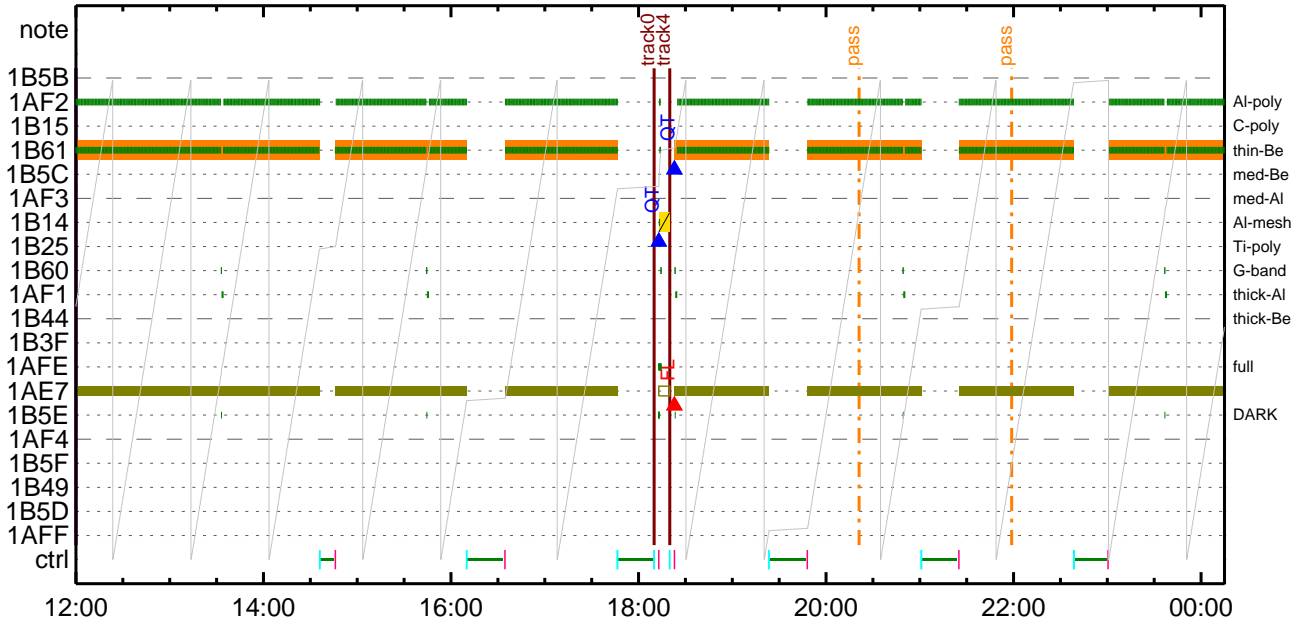
CMDI #0413 2016/12/22



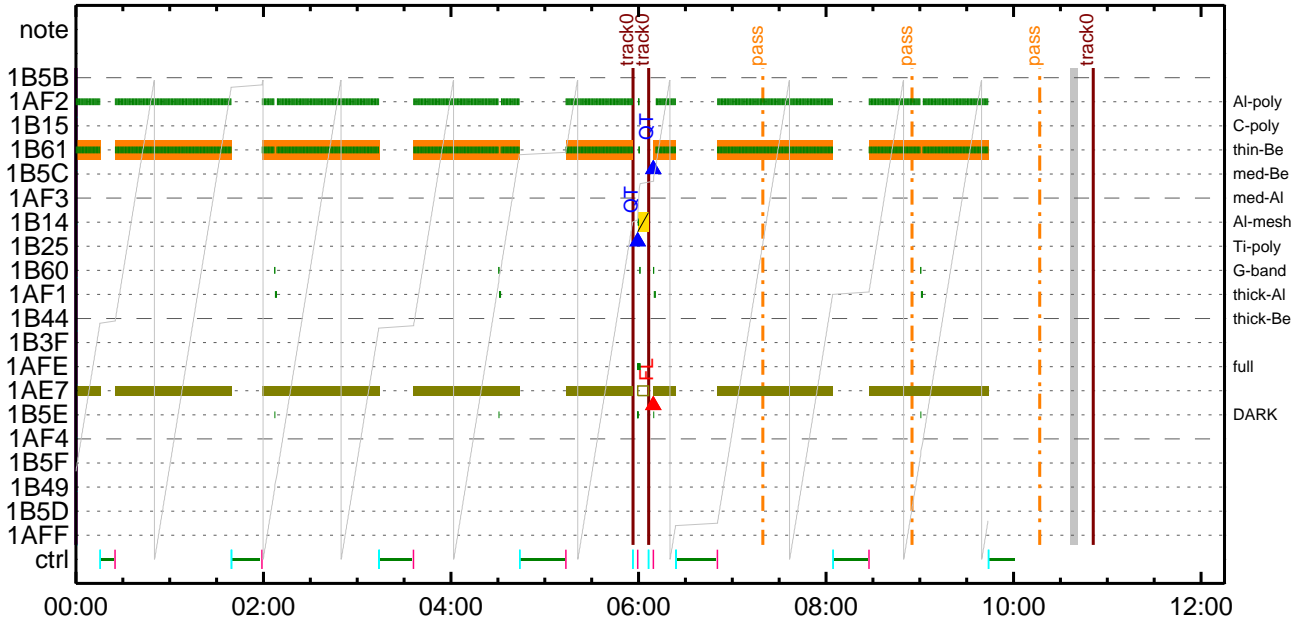
CMDI #0413 2016/12/23



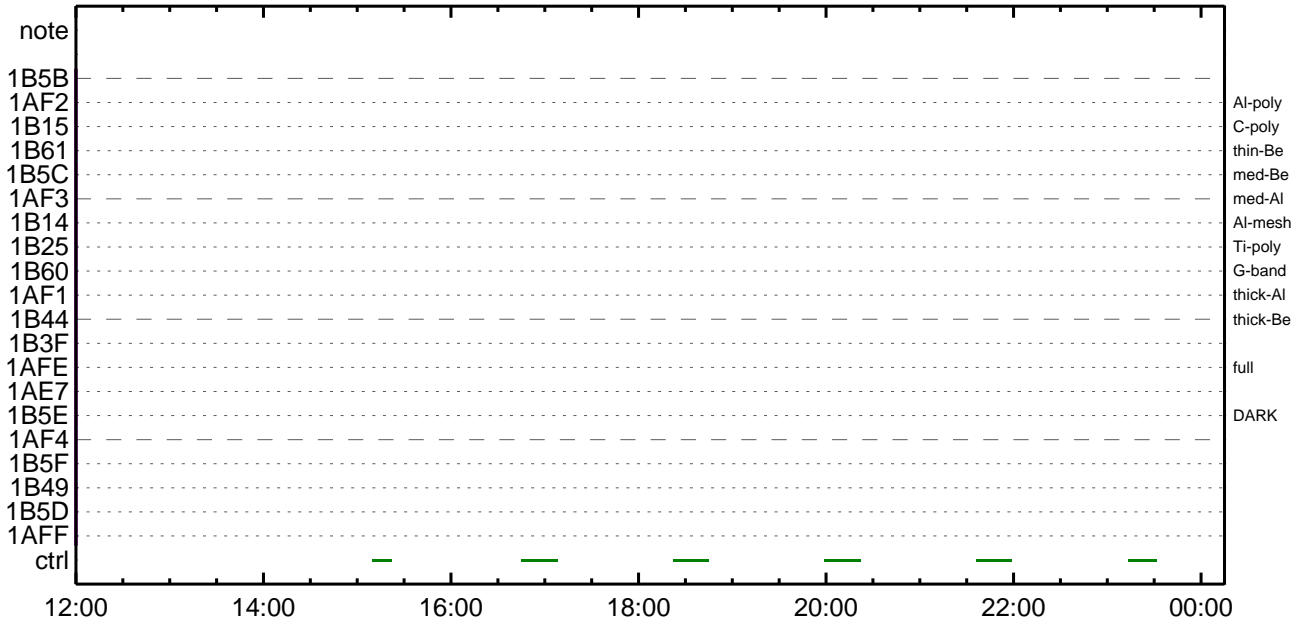
CMDI #0413 2016/12/23



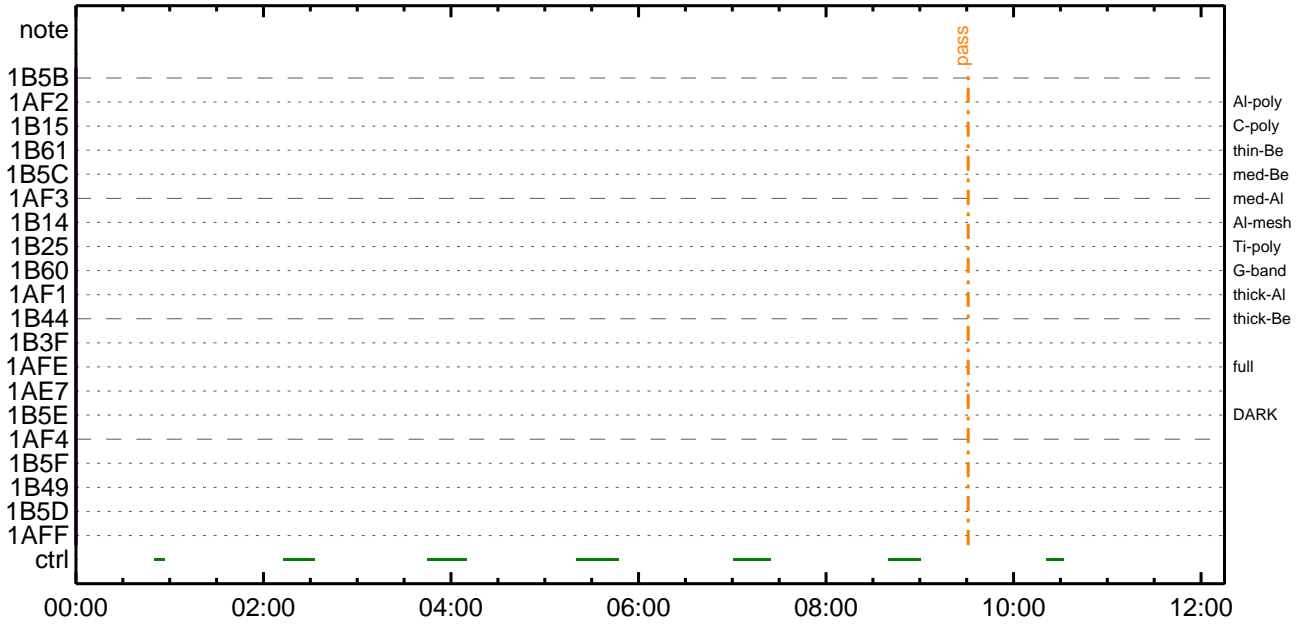
CMDI #0413 2016/12/24



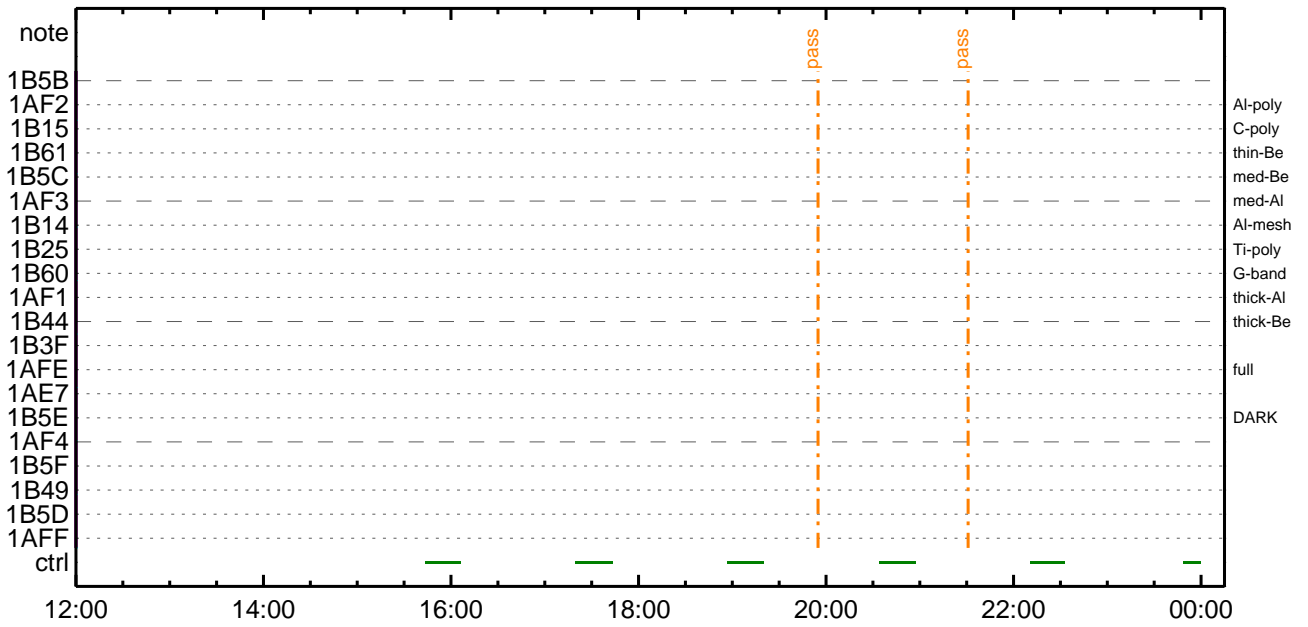
CMDI #0413 2016/12/24



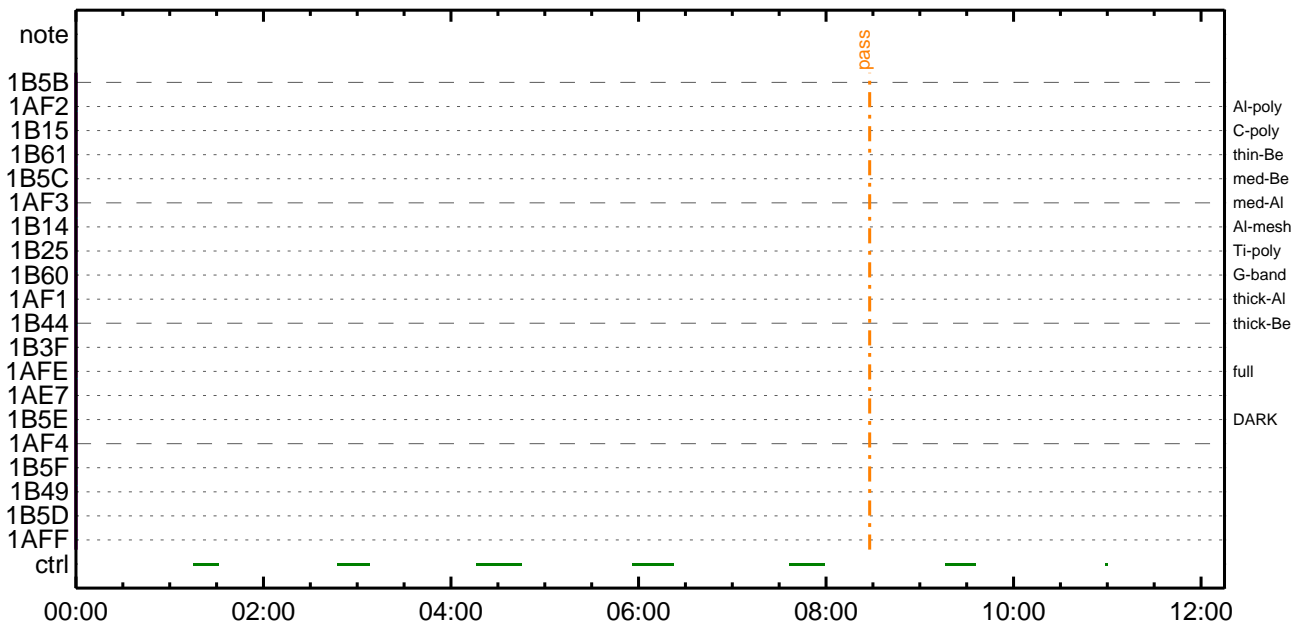
CMDI #0413 2016/12/25



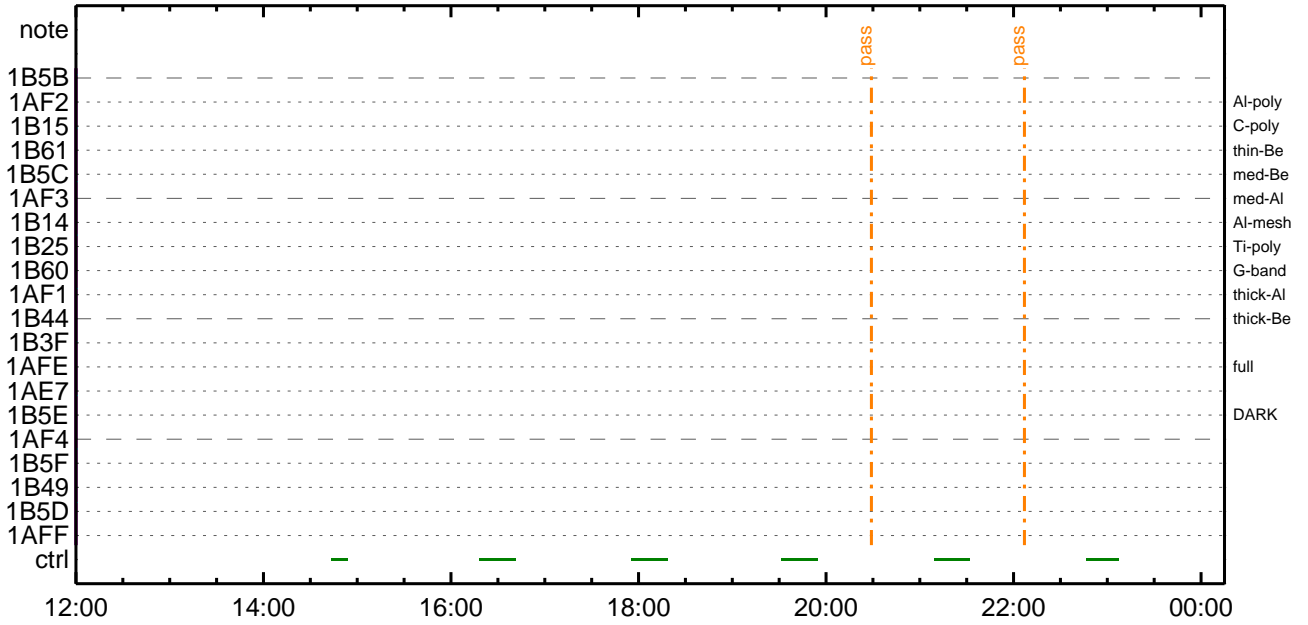
CMDI #0413 2016/12/25



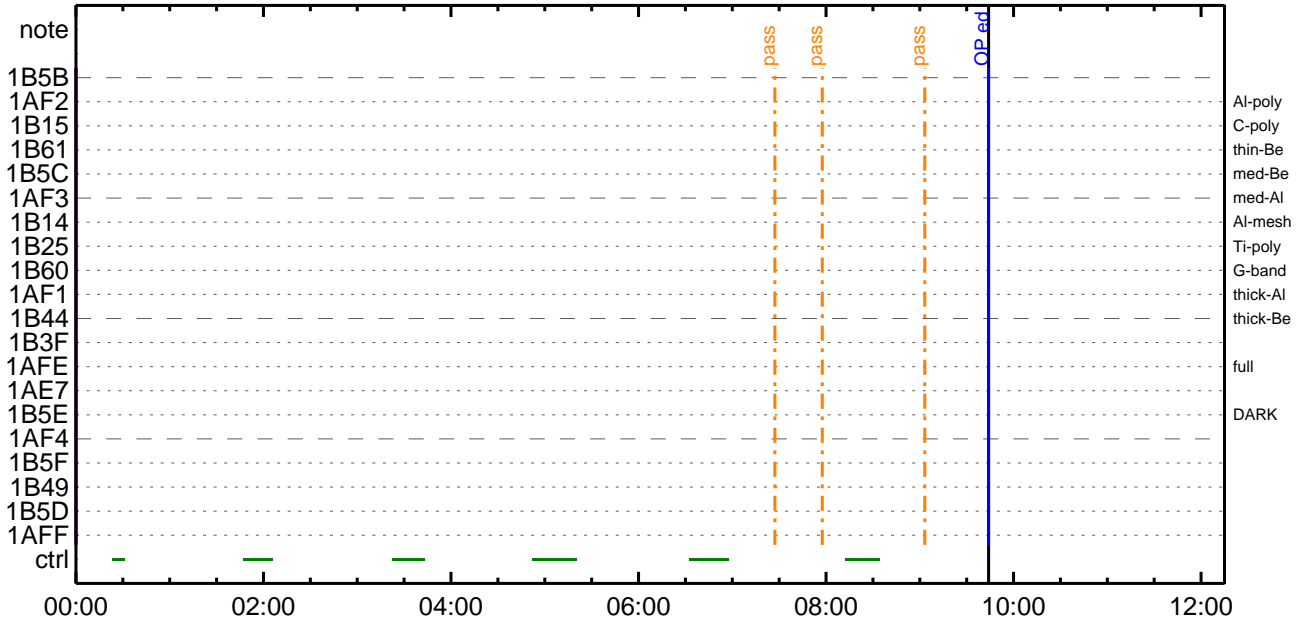
CMDI #0413 2016/12/26



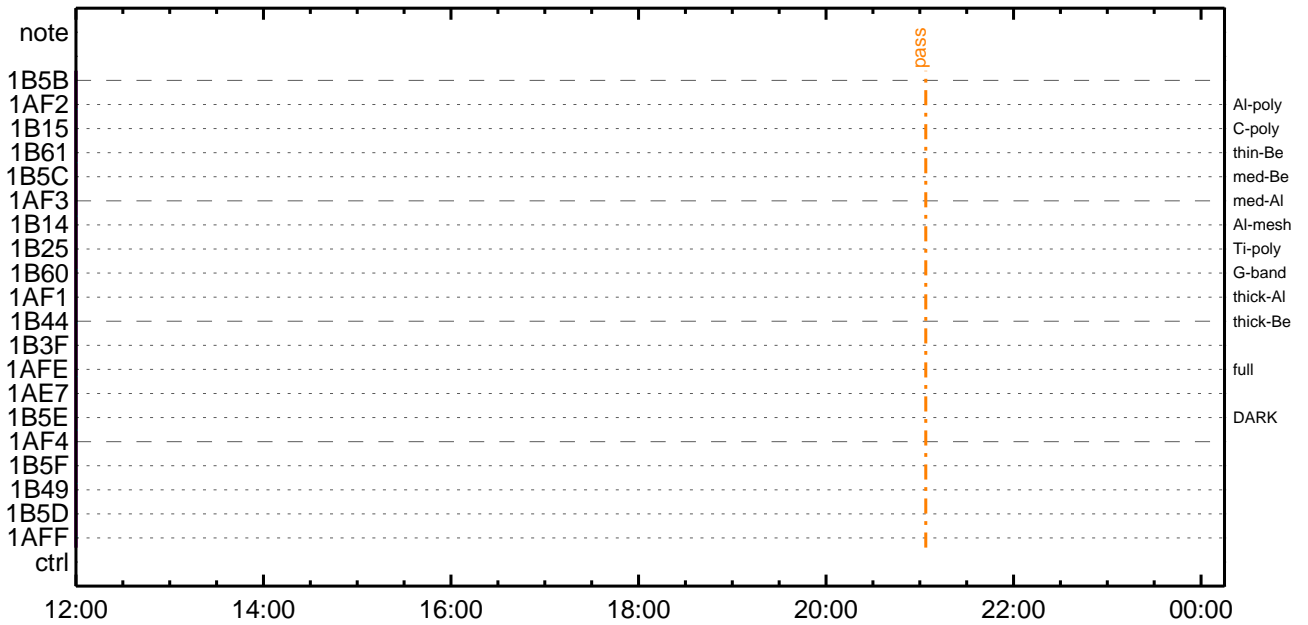
CMDI #0413 2016/12/26



CMDI #0413 2016/12/27



CMDI #0413 2016/12/27




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;YÉi;YAYóYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;YÉi;ã
0103 S. OP op-317:OP
0104 ( )
0105 S. OG og-317:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPîî°èYAYóYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYóYx½ª î»ò³ ÎÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGªî¼Ë¹ç•ë² ÎOKªò³ ÎÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYóYx½ª î»ò³ ÎÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGªî¼Ë¹ç•ë² ÎOKªò³ ÎÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYóYx½ª î»ò³ ÎÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPªî¼Ë¹ç•ë² ÎOKªò³ ÎÇ§
0165 C.
0166 C. ***** °Ë²¼ª î¼Ë¹ç•ë² ÎOKªò³ ÎÇ§ *****
0167 C. DHUYâ;¼YÉi;Ë¼Y½;Yi;¼YÉi;Ëòîãª¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ; § OPOG UPLOADª-Á÷ç@NGªî¼Ë¹ç•ë² ÎOKªò³ ÎÇ§ *****
0180 C. çç[HK1_DMP_CHK_FLG] EQ NON
0181 C.
0182 C. TIY³YpYóYÉªòîÁDîç(UT)
0183 +. TI 2016-12-22 11:03:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2016-12-22 11:03:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2016-12-22 11:03:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2016-12-22 11:07:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          φφ[HK1_TI_CMD_NUM]                 EQ          1COUNTUP
0198 C.
0199 C.   °È²¼õİÄë%ĩíÑõİŶÄŶ$ŶÄŶ¹àİÜ
0200 C.          φφ[HK1_TI_CMD_ENA/DIS]             EQ          ENA
0201 C.          φφ[HK1_TI_CMD_NUM]                 EQ          4
0202 C.          φφ[HK1_NEXT_EXEC_PIM]             EQ          DHU
0203 C.          φφ[HK1_NEXT_EXEC_DC]              EQ          0xB3
0204 C.
0205 C.   *****
0206 C.   TIİŶ°èŶÄŶÖŶ×
0207 C.   *****
0208 C.
0209 C.   TI_TBL(0x03AB00-0x03AEFF;$ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          φφ[HK1_DMP_TOP_ADRS_1]           EQ          07
0213 C.          φφ[HK1_DMP_TOP_ADRS_0]           EQ          2B
0214 C.          φφ[HK1_DMP_BLOCK_NUM]            EQ          3
0215 C.          φφ[HK1_DMP_REPEAT_NUM]           EQ          0
0216 C.          φφ[HK1_DMA_DMP_PIM]             EQ          DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          φφ[HK1_PKT_FORM_NO]             EQ          7
0220 C.          φφ[HK1_PKT_GEN_TIME]             EQ          0.25 s
0221 C.          φφ[HK1_S_TLM_BIT_RATE]          EQ          32k
0222 C.          φφ[HK1_X_TLM_BIT_RATE]          EQ          4M
0223 C.          φφ[HK1_DMP_CHK_FLG]             EQ          EXEC
0224 C.
0225 C.   ŶÄŶÖŶ×½ªİ»õ³İÇ§
0226 C.          φφ[HK1_DMP_CHK_FLG]             EQ          NON
0227 C.
0228 C.   RAM ID=TI_TBLõİ½È¹ç•è²İOKõ³İÇ§
0229 C.
0230 C.   DHUŶã;¼ŶÈ;È¼Ŷ½. Ŷİ;¼ŶÈ;Èõðİãõ¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          φφ[HK1_PKT_FORM_NO]             EQ          2
0234 C.          φφ[HK1_PKT_GEN_TIME]             EQ          0.5S
0235 C.          φφ[HK1_S_TLM_BIT_RATE]          EQ          32K
0236 C.          φφ[HK1_X_TLM_BIT_RATE]          EQ          4M
0237 C.
0238 C.   Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C.   ***** Start EIS operation (TI set) *****
0242 C.   Execute, after the success of OP upload.
0243 C.   Set EIS TI-commands
0244 +. TI 2016-12-22 11:07:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2016-12-22 11:07:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.          [ ] [HK1_TI_CMD_NUM]            EQ          2 COUNTUP
0251 C.   ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.
0255 C.   ***** XRT START *****
0256 C.   Execute, after the success of OP upload.
0257 +. TI 2016-12-22 11:07:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC      (c3)
0260 C.          [ ] [HK1_TI_CMD_NUM]            EQ          1COUNTUP
0261 C.
0262 C.   ***** XRT END *****
0263 C.
0264 C.   ***** MDP `ûÃİõİ»õ¼ŶõÈÄõ¹õèDCBC•×²è *****
0265 C.   (¼ª°İŶÖŶÄŶĐŶŶŶÈŶãŶçŶèõÈ¼õ¼ª»Ûõ¹õè)
0266 S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 C.   ***** ŶĐŶ¹.İ Daily±;İÑõÈ¹õèDCBC•×²è *****
0271 S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 C.   ĩãLOŠŶÄŶ$ŶÄŶ¹¼ª»Û;ã
0276 C.
0277 C.   ***** LOS *****
0278 C.
```


(a) Spacecraft Operation Procedure (real-commands)

```

main-319 2016-12-22 12:44:11 102 33 SOLAR-B MAIN //
0001  C.
0002  . C. ***** AOS *****
0003  C.
0004  . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005  C.
0006  C. YÀYB;¼Y³YFÿÓYÉÁ+¿@
0007  +. DC 00-00 NULL_DUMMY_CMD
0008  C.
0009  . C. ***** AOCs : Reload orbital element (send every contact) *****
0010  C.  Āí;Ēò¿òÀò•µ°E»Í×ÁÇòÍYÇYÁY×Yí;¼YÉj;ĒÈèµ•ííĒ;ĒòĒ¼°ÇÒò•ò¿¼i¹çòĪ;çĀ@, ùò¹òĒòĒòçĀ+¿@ò•òĒòòò³òĒ;Ē
0011  +. DC 02-8E AOCU_ORB_UPD
0012  C.
0013  C.
0014  C.
0015  C. ***** XRT START *****
0016  C.
0017  +. DC 07-F0 MDP_XRT_CTRL_MANU
0018  BC (c1)
0019  +. DC 07-F0 MDP_XRT_CTRL_MANU
0020  BC (c1)
0021  +. DC 07-F0 MDP_XRT_MODE_STBY
0022  BC (c3)
0023  . C. ----- Success Verify ?                OK / NG____
0024  C.
0025  C. XRT Obs. Table Upload
0026  . S. RAM ram-291:MDP_OBS_X
0027  ( )
0028  C.
0029  +. DC 07-F0 MDP_DUMP_XRTTBL
0030  BC (84 07 00 00 00 3a d4)
0031  . C. ----- Comparison Check ?                OK / ERR ____
0032  C.
0033  C.
0034  +. DC 07-F0 MDP_XRT_ROI_SET
0035  BC (cd 01 b1 b1 04 04)
0036  + DC 07-F0 MDP_XRT_ROI_SET
0037  BC (cd 02 b1 b1 08 08)
0038  + DC 07-F0 MDP_XRT_ROI_SET
0039  BC (cd 03 b1 b1 08 08)
0040  + DC 07-F0 MDP_XRT_ROI_SET
0041  BC (cd 04 b1 b1 06 06)
0042  + DC 07-F0 MDP_XRT_ROI_SET
0043  BC (cd 05 85 83 06 06)
0044  + DC 07-F0 MDP_XRT_ROI_SET
0045  BC (cd 06 85 83 08 08)
0046  + DC 07-F0 MDP_XRT_ROI_SET
0047  BC (cd 07 85 83 06 06)
0048  + DC 07-F0 MDP_XRT_ROI_SET
0049  BC (cd 08 9b 7b 08 08)
0050  + DC 07-F0 MDP_XRT_ROI_SET
0051  BC (cd 09 80 80 20 20)
0052  + DC 07-F0 MDP_XRT_ROI_SET
0053  BC (cd 0a 80 80 20 08)
0054  + DC 07-F0 MDP_XRT_ROI_SET
0055  BC (cd 0b 80 80 08 20)
0056  + DC 07-F0 MDP_XRT_ROI_SET
0057  BC (cd 0f 80 80 06 06)
0058  + DC 07-F0 MDP_XRT_ROI_SET
0059  BC (cd 10 80 80 08 08)
0060  + DC 07-F0 MDP_XRT_FLD_ENA
0061  BC (d8)
0062  + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0063  BC (c8)
0064  + DC 07-F0 MDP_XRT_ARS_DIS
0065  BC (d5)
0066  + DC 07-F0 MDP_XRT_AEC_RESET
0067  BC (d0)
0068  + DC 07-F0 MDP_XRT_FLD_RESET
0069  BC (da)
0070  + DC 07-F0 MDP_XRT_QT_PROG_SET
0071  BC (c4 0a)
0072  + DC 07-F0 MDP_XRT_FL_PROG_SET
0073  BC (c5 07)
0074  . C. ----- Success Verify ?                OK / NG ____
0075  C.
0076  C.
0077  . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0078  C.
0079  +. DC 07-F0 MDP_XRT_MODE_OBSV
0080  BC (c2)
0081  +. TI 2016-12-22 11:07:02.0
0082  DC 07-F0 MDP_XRT_MODE_OBSV
0083  BC (c2)
0084  . C. ----- Success Verify ?                OK / NG ____
0085  C.
0086  C. ***** XRT END *****
0087  C.
0088  . C. ***** MDP `úĀĪòĪ»ò¼YòĒĀò¹òĒDCBC•x²è *****
0089  C. (%ā°īYŌYÁYĒYÿYáYçYèòĒE¼òò¼Ā»Ûò¹òĒ)
0090  . S. DC-BC dcbc-402:DCBC
0091  (MDP_known_event)
0092  C.
0093  C.
0094  . C. ***** YĐY¹.Ī Daily+¿ĪÑòĒ´Ÿò¹òĒDCBC•x²è *****
0095  . S. DC-BC dcbc-153:DCBC

```

```
0096 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0097 C.
0098 C.
0099 . C. ;äLOSŸÁŸSŸÄŸ-¼Ä»Û;ä
0100 C.
0101 . C. ***** LOS *****
0102 C.
```

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*** OP Sequence for XRT ***

2016/12/22	11:17:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/22	11:17:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/22	11:17:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/12/22	11:18:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2016/12/22	11:18:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/12/22	11:18:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/12/22	11:18:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/12/22	11:18:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/12/22	11:18:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/12/22	11:20:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2016/12/22	11:20:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/12/22	11:21:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/12/22	12:39:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/22	12:39:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/22	12:39:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/12/22	12:40:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2016/12/22	12:40:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/12/22	12:40:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/12/22	12:40:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/12/22	12:40:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/12/22	12:40:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/12/22	12:42:56.0	XRT_QT_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2016/12/22	12:42:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/12/22	12:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/12/22	15:35:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/22	15:35:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/22	15:35:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/12/22	15:35:36.0	XRT_PREFLR_STRT_414_OG [0x19e]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/12/22	15:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/12/22	15:58:30.0	XRT_Custom_430_OG [0x1ae]							
2016/12/22	15:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/12/22	16:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/22	16:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/22	16:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/12/22	17:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2016/12/22	17:00:18.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/12/22	17:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/12/22	17:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/12/22	17:02:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2016/12/22	17:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/12/22	17:11:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/22	17:11:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/12/22	17:11:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/12/22	17:11:36.0	XRT_PREFLR_STRT_414_OG [0x19e]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/12/22	17:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/12/22	17:35:30.0	XRT_Custom_430_OG [0x1ae]							
2016/12/22	17:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							

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2016/12/22	17:49:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/12/22	17:49:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/12/22	17:49:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97 00
2016/12/22	17:50:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01	00	00 00 00
2016/12/22	17:50:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2016/12/22	17:50:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/12/22	17:50:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/12/22	17:50:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/12/22	17:50:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/12/22	17:52:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11	
2016/12/22	17:52:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07	
2016/12/22	17:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/12/22	18:48:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/12/22	18:48:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/12/22	18:48:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/12/22	18:48:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/12/22	18:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/12/22	19:12:00.0	XRT_Custom_430_OG [0x1ae]						
2016/12/22	19:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/12/22	20:25:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/12/22	20:25:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/12/22	20:25:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/12/22	20:25:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/12/22	20:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/12/22	20:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/12/22	20:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/12/22	20:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97 00
2016/12/22	20:30:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04	00	00 00 00
2016/12/22	20:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2016/12/22	20:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/12/22	20:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/12/22	20:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/12/22	20:30:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/12/22	20:32:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11	
2016/12/22	20:32:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07	
2016/12/22	20:49:00.0	XRT_Custom_430_OG [0x1ae]						
2016/12/22	20:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/12/22	22:03:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/12/22	22:03:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/12/22	22:03:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/12/22	22:03:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/12/22	22:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/12/22	22:25:30.0	XRT_Custom_430_OG [0x1ae]						
2016/12/22	22:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/12/22	23:40:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/12/22	23:40:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/12/22	23:40:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/12/22	23:40:36.0	XRT_PREFLR_STRT_414_OG [0x19e]						

2016/12/22	23:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/12/22	23:53:00.0	XRT_Custom_430_OG [0x1ae]					
2016/12/22	23:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/12/23	01:08:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/12/23	01:08:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/12/23	01:08:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2016/12/23	01:08:36.0	XRT_PREFLR_STRT_414_OG [0x19e]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/12/23	01:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/12/23	01:23:30.0	XRT_Custom_430_OG [0x1ae]					
2016/12/23	01:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/12/23	02:39:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/12/23	02:39:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/12/23	02:39:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2016/12/23	02:39:06.0	XRT_PREFLR_STRT_414_OG [0x19e]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/12/23	02:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/12/23	03:00:00.0	XRT_Custom_430_OG [0x1ae]					
2016/12/23	03:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/12/23	04:07:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/12/23	04:07:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/12/23	04:07:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2016/12/23	04:07:36.0	XRT_PREFLR_STRT_414_OG [0x19e]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/12/23	04:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/12/23	04:37:00.0	XRT_Custom_430_OG [0x1ae]					
2016/12/23	04:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/12/23	05:48:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/12/23	05:48:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/12/23	05:48:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2016/12/23	05:48:06.0	XRT_PREFLR_STRT_414_OG [0x19e]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/12/23	05:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/12/23	06:14:30.5	XRT_Custom_430_OG [0x1ae]					
2016/12/23	06:15:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/12/23	06:24:24.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/12/23	06:24:26.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/12/23	06:24:28.0	XRT_FOCUS_POSITION_403_OG [0x193]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2016/12/23	06:24:30.0	AOCS_OrE-point_Start_3_OG [0x099]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2016/12/23	06:24:48.0	XRT_FLD_DIS_406_OG [0x196]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2016/12/23	06:27:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2016/12/23	06:27:26.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/12/23	06:27:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e	
2016/12/23	06:27:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/12/23	06:34:24.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/12/23	06:34:26.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/12/23	06:34:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2016/12/23	06:34:30.0	AOCS_OrE-point_Start_1_OG [0x097]					
			AOCU_NM	5	02-76	04 00 00 00 00	
2016/12/23	06:34:48.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/12/23	06:34:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/12/23	06:34:52.0	XRT_AEC_RESET_448_OG [0x1c0]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/12/23	06:34:54.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/12/23	06:34:56.0	XRT_FLD_RESET_433_OG [0x1b1]					

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2016/12/23	06:37:26.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/12/23	06:37:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2016/12/23	06:37:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/12/23	07:28:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/12/23	07:28:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/23	07:28:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/23	07:28:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/12/23	07:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/12/23	07:51:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/12/23	07:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0
2016/12/23	09:07:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/12/23	09:07:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/23	09:07:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/23	09:07:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/12/23	09:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/12/23	09:27:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/12/23	09:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0
2016/12/23	10:50:01.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/12/23	10:50:03.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/23	10:50:05.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/23	10:50:07.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/12/23	10:53:15.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/12/23	10:54:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/12/23	10:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0
2016/12/23	14:36:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/12/23	14:36:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/23	14:36:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/23	14:36:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/12/23	14:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/12/23	14:45:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/12/23	14:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0
2016/12/23	16:10:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/12/23	16:10:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/23	16:10:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/23	16:10:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/12/23	16:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/12/23	16:33:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/12/23	16:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0
2016/12/23	17:46:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/12/23	17:46:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/23	17:46:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/23	17:46:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/12/23	17:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/12/23	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/12/23	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/23	18:09:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/23	18:10:00.0	AOCS_OrE-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/12/23	18:10:18.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00 00 00 00 00
2016/12/23	18:12:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9

2016/12/23	18:12:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/12/23	18:12:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/12/23	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2016/12/23	18:19:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/12/23	18:19:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/23	18:19:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/23	18:20:00.0	AOCS_OrE-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/12/23	18:20:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	04 00 00 00 00
2016/12/23	18:20:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/12/23	18:20:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/12/23	18:20:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/12/23	18:20:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/12/23	18:22:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/12/23	18:22:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2016/12/23	18:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/12/23	19:23:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/12/23	19:23:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/23	19:23:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/23	19:23:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/12/23	19:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/12/23	19:47:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/12/23	19:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2016/12/23	21:01:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/12/23	21:01:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/23	21:01:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/23	21:01:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/12/23	21:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/12/23	21:24:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/12/23	21:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2016/12/23	22:38:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/12/23	22:38:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/23	22:38:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/23	22:38:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/12/23	22:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/12/23	22:59:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/12/23	23:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2016/12/24	00:15:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/12/24	00:15:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/24	00:15:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/24	00:15:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/12/24	00:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/12/24	00:24:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/12/24	00:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2016/12/24	01:39:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/12/24	01:39:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/24	01:39:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/12/24	01:39:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/12/24	01:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
			MDP_XRT_PREFLR_STOP	1	07-F0	e9

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2016/12/24	01:58:00.0	XRT_Custom_430_OG [0x1ae]						
2016/12/24	01:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/12/24	03:14:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/12/24	03:14:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/12/24	03:14:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/12/24	03:14:06.0	XRT_PREFLR_STRT_414_OG [0x19e]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/12/24	03:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/12/24	03:35:00.0	XRT_Custom_430_OG [0x1ae]						
2016/12/24	03:36:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/12/24	04:44:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/12/24	04:44:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/12/24	04:44:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/12/24	04:44:06.0	XRT_PREFLR_STRT_414_OG [0x19e]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/12/24	04:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/12/24	05:12:30.0	XRT_Custom_430_OG [0x1ae]						
2016/12/24	05:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/12/24	05:56:24.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/12/24	05:56:26.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/12/24	05:56:28.0	XRT_FOCUS_POSITION_403_OG [0x193]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2016/12/24	05:56:30.0	AOCS_Ore-point_Start_3_OG [0x099]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2016/12/24	05:56:48.0	XRT_FLD_DIS_406_OG [0x196]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2016/12/24	05:59:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2016/12/24	05:59:26.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/12/24	05:59:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e			
2016/12/24	05:59:30.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/12/24	06:06:24.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/12/24	06:06:26.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/12/24	06:06:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2016/12/24	06:06:30.0	AOCS_Ore-point_Start_4_OG [0x09a]						
		AOCU_NM	5	02-76	00 24 41 b3 4c			
2016/12/24	06:06:48.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2016/12/24	06:06:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2016/12/24	06:06:52.0	XRT_AEC_RESET_448_OG [0x1c0]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2016/12/24	06:06:54.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/12/24	06:06:56.0	XRT_FLD_RESET_433_OG [0x1b1]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/12/24	06:09:26.0	XRT_QT_PROG_SET_435_OG [0x1b3]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11			
2016/12/24	06:09:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07			
2016/12/24	06:09:30.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/12/24	06:24:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/12/24	06:24:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/12/24	06:24:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/12/24	06:24:06.0	XRT_PREFLR_STRT_414_OG [0x19e]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/12/24	06:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/12/24	06:49:30.0	XRT_Custom_430_OG [0x1ae]						
2016/12/24	06:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/12/24	08:04:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/12/24	08:04:32.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/12/24	08:04:34.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/12/24	08:04:36.0	XRT_PREFLR_STRT_414_OG [0x19e]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/12/24	08:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						

2016/12/24	08:26:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2016/12/24	08:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]									
2016/12/24	09:44:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2016/12/24	09:44:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1					
2016/12/24	09:44:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1					
2016/12/24	09:44:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da					
2016/12/24	09:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2016/12/24	10:51:00.0	AOCS_ORe-point_Start_3_OG [0x099]	MDP_XRT_PREFLR_STOP	1	07-F0	e9					
		AOCU_NM		5	02-76	00 00 00 00 00					