

# XRT Timeline to be uploaded on 2017/01/24

Period: 2017/01/24 10:28:00 - 2017/02/01 10:48:00

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Normal mode

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## XOB #1B3F: CME watch - 4x4 - AEC 2/3 - 2-filter (Be-thin, Al-poly) - G-band (2x2,1ms) - Leak (2x2,1ms) - 90s cad (G-band/Leak first)

Term	Pointing (x, y)	Comment
01/24 10:41:00 - 01/24 16:37:54	Track ( -62.5, -843.7) @ 01/24 10:38:00	# OP start + 10min, EIS polar plume
01/27 06:13:00 - 01/27 07:39:54	Track ( -6.5, -315.9) @ 01/27 06:10:00	# EIS spectral atlas

**PROG= 16 Inf.-time(s)**

<b>Subr= 1</b>		<b>1-time(s)</b>	<b>2.0sec</b>										
<b>Seqn= 26</b>		<b>1-time(s)</b>	<b>2.0sec</b>										
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	2x2	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
<b>Subr= 2</b>		<b>40-time(s)</b>	<b>90.0sec</b>										
<b>Seqn= 8</b>		<b>1-time(s)</b>	<b>2.0sec</b>										
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048	(1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048	(1024, 1024)	DPCM	2	0	2.0sec
<b>Seqn= 6</b>		<b>1-time(s)</b>	<b>2.0sec</b>										
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048	(1024, 1024)	DPCM	2	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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## XOB #1B49: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, Al/Poly context, with G-band (3ms/3ms)

Term	Pointing (x, y)	Comment
01/24 16:41:00 - 01/24 17:59:54	Track ( 10.1, 258.5) @ 01/24 16:38:00	AR 12628
01/25 06:28:30 - 01/25 17:38:00	Track ( 138.0, 258.3) @ 01/25 06:25:30	cont.
01/25 18:14:30 - 01/26 05:47:24	Track ( 245.2, 256.8) @ 01/25 18:11:30	cont.
01/26 06:00:30 - 01/26 17:49:24	Track ( 349.2, 254.1) @ 01/26 05:57:30	cont.
01/26 18:02:30 - 01/27 05:59:54	Track ( 450.9, 250.1) @ 01/26 17:59:30	cont.
01/27 07:43:00 - 01/27 18:05:54	Track ( 558.8, 244.1) @ 01/27 07:40:00	AR 12628
01/27 18:19:00 - 01/28 06:23:24	Track ( 635.8, 238.4) @ 01/27 18:16:00	cont.
01/28 06:36:30 - 01/28 17:46:00	Track ( 716.6, 230.7) @ 01/28 06:33:30	cont.
01/28 18:26:30 - 01/29 05:55:24	Track ( 784.4, 222.4) @ 01/28 18:19:30	cont.

**PROG= 03 Inf.-time(s)**

<b>Subr= 1</b>		<b>1-time(s)</b>	<b>2.0sec</b>										
<b>Seqn= 56</b>		<b>1-time(s)</b>	<b>2.0sec</b>										
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384	(1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384	(1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384	(1064, 1048)	Q=98	0	0	2.0sec
<b>Seqn= 42</b>		<b>4-time(s)</b>	<b>2.0sec</b>										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512	(1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	2.0sec
<b>Seqn= 28</b>		<b>30-time(s)</b>	<b>60.0sec</b>										
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1064, 1048)	Q=95	3	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1064, 1048)	Q=95	3	2	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	2	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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## XOB #1B64: Synoptic Q95 2x2 - Al/mesh(64/512/2048) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(45/512/4096) + Ti-poly(45/512/4096)

Term	Pointing (x, y)	Comment
01/24 18:03:00 - 01/24 18:09:54	Fixed ( 0.0, 0.0)	synoptic
01/25 18:04:30 - 01/25 18:11:24	Fixed ( 0.0, 0.0)	synoptic, shifted 1.5 min
01/26 05:50:30 - 01/26 05:57:24	Fixed ( 0.0, 0.0)	synoptic, shifted -12.5 min
01/26 17:52:30 - 01/26 17:59:24	Fixed ( 0.0, 0.0)	synoptic, shifted -10.5 min
01/27 06:03:00 - 01/27 06:09:54	Fixed ( 0.0, 0.0)	synoptic
01/27 18:09:00 - 01/27 18:15:54	Fixed ( 0.0, 0.0)	synoptic, shifted 6.0 min
01/28 18:12:30 - 01/28 18:19:24	Fixed ( 0.0, 0.0)	synoptic, shifted 9.5 min
01/29 05:58:30 - 01/29 06:20:00	Fixed ( 0.0, 0.0)	synoptic, shifted -4.5 min

**PROG= 06 1-time(s)**

<b>Subr= 1</b>		<b>1-time(s)</b>	<b>2.0sec</b>										
<b>Seqn= 5</b>		<b>1-time(s)</b>	<b>2.0sec</b>										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512	(1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048	(1024, 1024)	DPCM	0	0	2.0sec
<b>Seqn= 27</b>		<b>1-time(s)</b>	<b>2.0sec</b>										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	63ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 99</b>		<b>1-time(s)</b>	<b>2.0sec</b>										
Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec

Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 85</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
thin-Be/Open	thin-Be/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	16.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 54</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1B44: AR-(filter ratio Al/poly thin-Be), 512x512 at 1064 1048, with G-band 3ms, PFB, 60s cad**

Term	Pointing (x, y)	Comment
01/24 18:13:00 - 01/25 06:15:24	Track ( 24.4, 258.5) @ 01/24 18:10:00	cont.
<b>PROG= 14 Inf-time(s)</b>		
<b>Subr= 1</b>		<b>1-time(s)</b> <b>2.0sec</b>
<b>Seqn= 78</b>		<b>1-time(s)</b> <b>2.0sec</b>
Open/G-band	Open/G-band	open Safe Norm 3ms Obs 1x1 512x512 (1064, 1048) DPCM 0 0 2.0sec
Open/G-band	Open/G-band	close Safe Norm 3ms Obs 1x1 512x512 (1064, 1048) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Dark 16.0s Obs 1x1 512x512 (1064, 1048) Q=98 0 0 2.0sec
<b>Subr= 2</b>		<b>1-time(s)</b> <b>2.0sec</b>
<b>Seqn= 66</b>		<b>60-time(s)</b> <b>60.0sec</b>
thin-Be/Open	med-Be/Open	close Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
Al-poly/Open	thin-Be/Open	close Safe Norm 500ms Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open	close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
Al-poly/Open	thin-Be/Open	close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
thin-Be/Open	med-Be/Open	close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
Al-poly/Open	thin-Be/Open	close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

**XOB #1B66: Synoptic 7 Filter w/ Al-mesh(64/512/2897), Al-poly(45/512/4096), Thin-Be(512/8192/23142) - Thick-Be(65536), Al-poly+Ti-poly(256/5795), Med-Al**

Term	Pointing (x, y)	Comment
01/25 06:18:30 - 01/25 06:25:24	Fixed ( 0.0, 0.0)	synoptic, shifted 15.5 min
01/28 06:26:30 - 01/28 06:33:24	Fixed ( 0.0, 0.0)	synoptic, shifted 23.5 min
<b>PROG= 12 1-time(s)</b>		
<b>Subr= 1</b>		<b>1-time(s)</b> <b>2.0sec</b>
<b>Seqn= 5</b>		<b>1-time(s)</b> <b>2.0sec</b>
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
<b>Seqn= 1</b>		<b>1-time(s)</b> <b>2.0sec</b>
Open/Al-mesh	Open/Al-mesh	close Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh	close Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh	close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
<b>Seqn= 99</b>		<b>1-time(s)</b> <b>2.0sec</b>
Al-poly/Open	Al-poly/Open	close Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/Open	close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/thick-Al	close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
<b>Seqn= 94</b>		<b>1-time(s)</b> <b>2.0sec</b>
thin-Be/Open	thin-Be/Open	close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open	close Safe Norm 8.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open	close Safe Norm 22.6s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
<b>Seqn= 54</b>		<b>1-time(s)</b> <b>4.0sec</b>
Open/G-band	Open/G-band	open Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band	close Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
<b>Subr= 2</b>		<b>1-time(s)</b> <b>2.0sec</b>
<b>Seqn= 46</b>		<b>1-time(s)</b> <b>2.0sec</b>
Open/thick-Be	Open/thick-Be	close Safe Norm 64.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
<b>Seqn= 20</b>		<b>1-time(s)</b> <b>2.0sec</b>
med-Al/Open	med-Al/Open	close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
med-Al/Open	med-Al/Open	close Safe Norm 64.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
<b>Seqn= 40</b>		<b>1-time(s)</b> <b>2.0sec</b>
Al-poly/Ti-poly	Al-poly/thick-Al	close Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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**Flare mode**

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**XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512)**

Term	Pointing (x, y)	Comment
01/24 10:41:00 - 01/24 16:37:54	Track ( -62.5, -843.7) @ 01/24 10:38:00	# OP start + 10min, EIS polar plume
01/24 16:41:00 - 01/24 17:59:54	Track ( 10.1, 258.5) @ 01/24 16:38:00	AR 12628
01/24 18:13:00 - 01/25 06:15:24	Track ( 24.4, 258.5) @ 01/24 18:10:00	cont.
01/25 06:28:30 - 01/25 17:38:00	Track ( 138.0, 258.3) @ 01/25 06:25:30	cont.
01/25 18:14:30 - 01/26 05:47:24	Track ( 245.2, 256.8) @ 01/25 18:11:30	cont.

01/26 06:00:30 - 01/26 17:49:24 Track ( 349.2, 254.1) @ 01/26 05:57:30 cont.  
 01/26 18:02:30 - 01/27 05:59:54 Track ( 450.9, 250.1) @ 01/26 17:59:30 cont.  
 01/27 06:13:00 - 01/27 07:39:54 Track ( -6.5, -315.9) @ 01/27 06:10:00 # EIS spectral atlas  
 01/27 07:43:00 - 01/27 18:05:54 Track ( 558.8, 244.1) @ 01/27 07:40:00 AR 12628  
 01/27 18:19:00 - 01/28 06:23:24 Track ( 635.8, 238.4) @ 01/27 18:16:00 cont.  
 01/28 06:36:30 - 01/28 17:46:00 Track ( 716.6, 230.7) @ 01/28 06:33:30 cont.  
 01/28 18:26:30 - 01/29 05:55:24 Track ( 784.4, 222.4) @ 01/28 18:19:30 cont.

**PROG= 07 30-time(s)**

Subr= 1		20-time(s)		2.0sec											
Seqn= 11		1-time(s)		2.0sec											
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512	(1024, 1024)	Q=95	2	0	2.0sec		
Seqn=100		1-time(s)		10.0sec											
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384	(1024, 1024)	Q=95	2	0	2.0sec		
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec		
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec		
Subr= 2		1-time(s)		2.0sec											
Seqn= 10		1-time(s)		2.0sec											
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec		
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec		
Seqn= 11		1-time(s)		2.0sec											
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512	(1024, 1024)	Q=95	2	0	2.0sec		
Seqn= 84		1-time(s)		2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384	(1024, 1024)	Q=98	0	0	2.0sec		
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384	(1024, 1024)	Q=98	0	0	2.0sec		
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384	(1024, 1024)	Q=98	0	0	2.0sec		
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512	(1024, 1024)	Q=98	0	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval			

\* \* \* \* \*

**Active Region Search**

\* \* \* \* \*

NOT USED

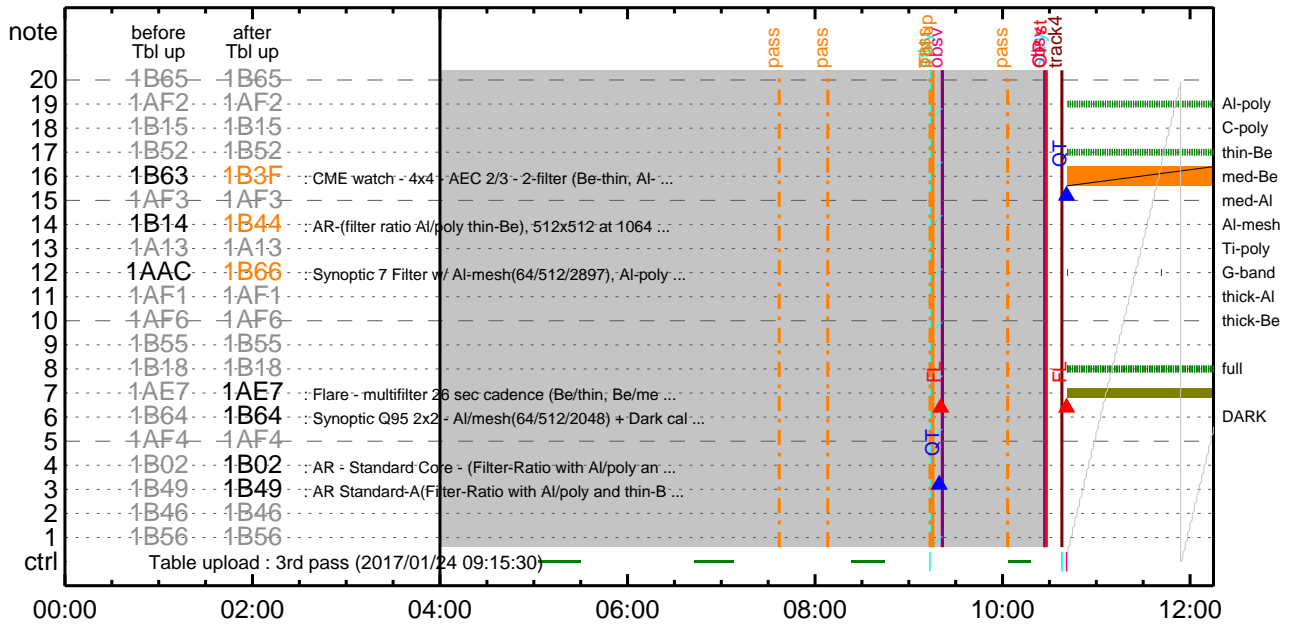
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**Flare Detection**

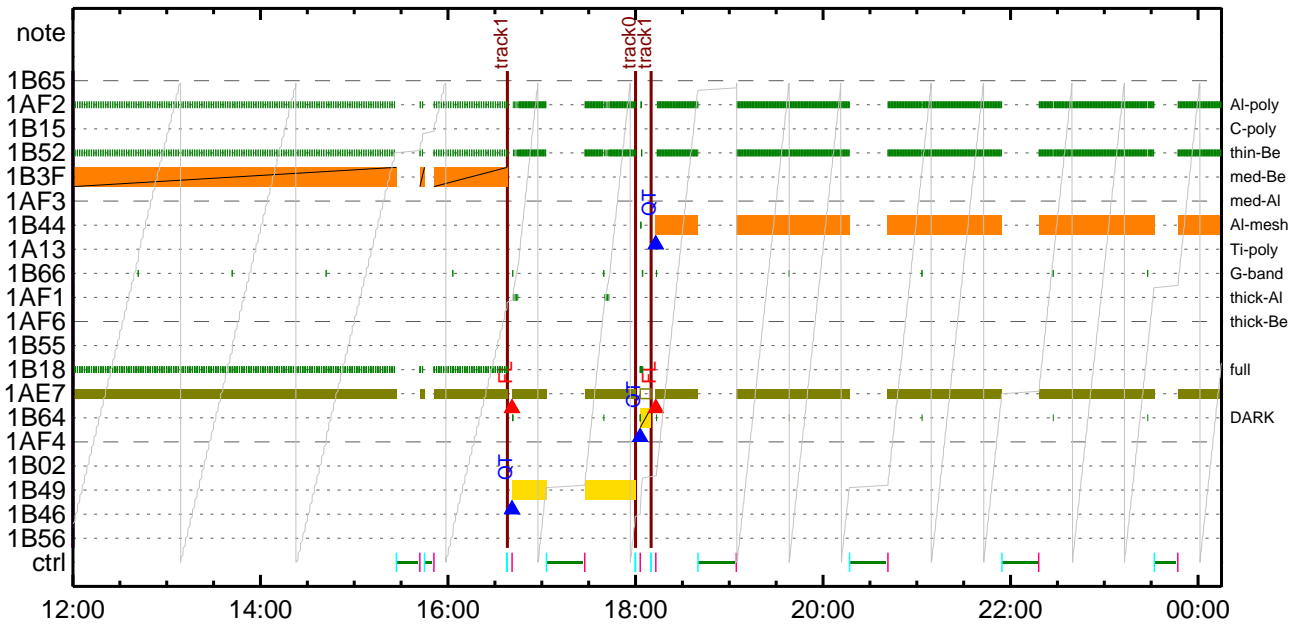
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<b>FLD Patrol</b>												
Term		Pointing (x, y)					Comment					
01/24 18:10:18 - 01/25 06:15:48	Track ( 24.4, 258.5)	@ 01/24 18:10:00				cont.						
01/25 06:25:48 - 01/25 18:01:48	Track ( 138.0, 258.3)	@ 01/25 06:25:30				cont.						
01/25 18:11:48 - 01/26 05:47:48	Track ( 245.2, 256.8)	@ 01/25 18:11:30				cont.						
01/26 05:57:48 - 01/26 17:49:48	Track ( 349.2, 254.1)	@ 01/26 05:57:30				cont.						
01/26 17:59:48 - 01/27 06:00:18	Track ( 450.9, 250.1)	@ 01/26 17:59:30				cont.						
01/27 06:10:18 - 01/27 18:06:18	Track ( -6.5, -315.9)	@ 01/27 06:10:00				# EIS spectral atlas						
01/27 18:16:18 - 01/28 06:23:48	Track ( 635.8, 238.4)	@ 01/27 18:16:00				cont.						
01/28 06:33:48 - 01/28 18:09:48	Track ( 716.6, 230.7)	@ 01/28 06:33:30				cont.						
01/28 18:23:48 - 01/29 05:55:48	Track ( 784.4, 222.4)	@ 01/28 18:19:30				cont.						
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50		30sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval

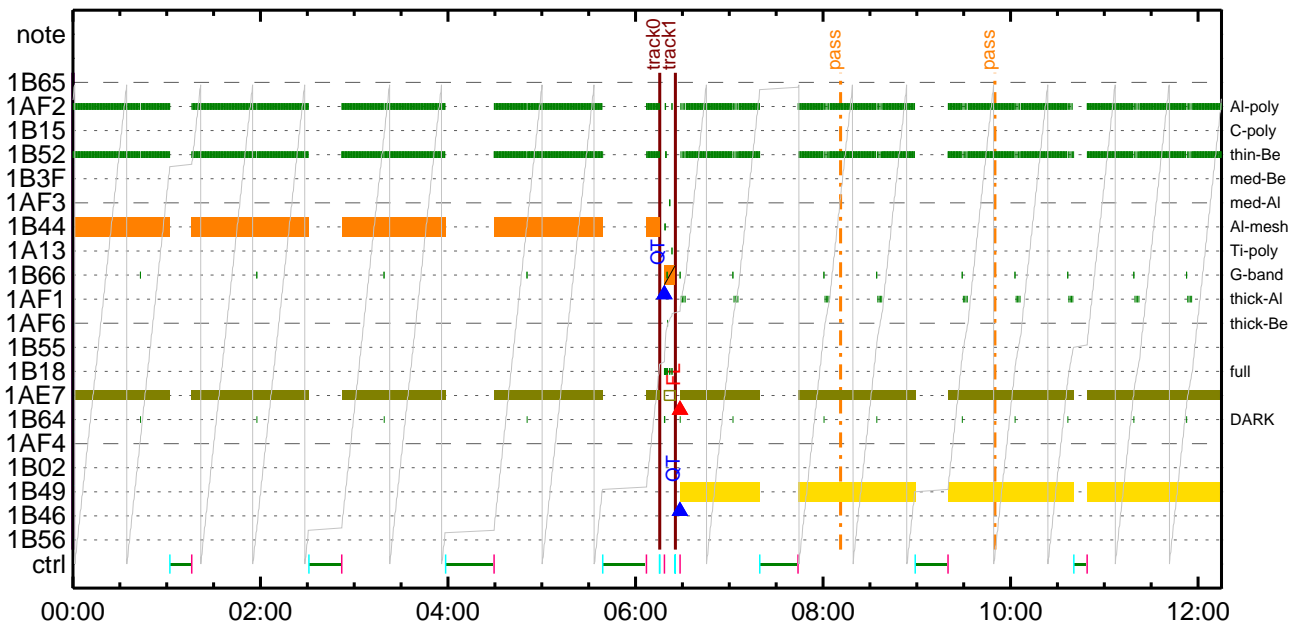
### CMDI #0504 2017/01/24



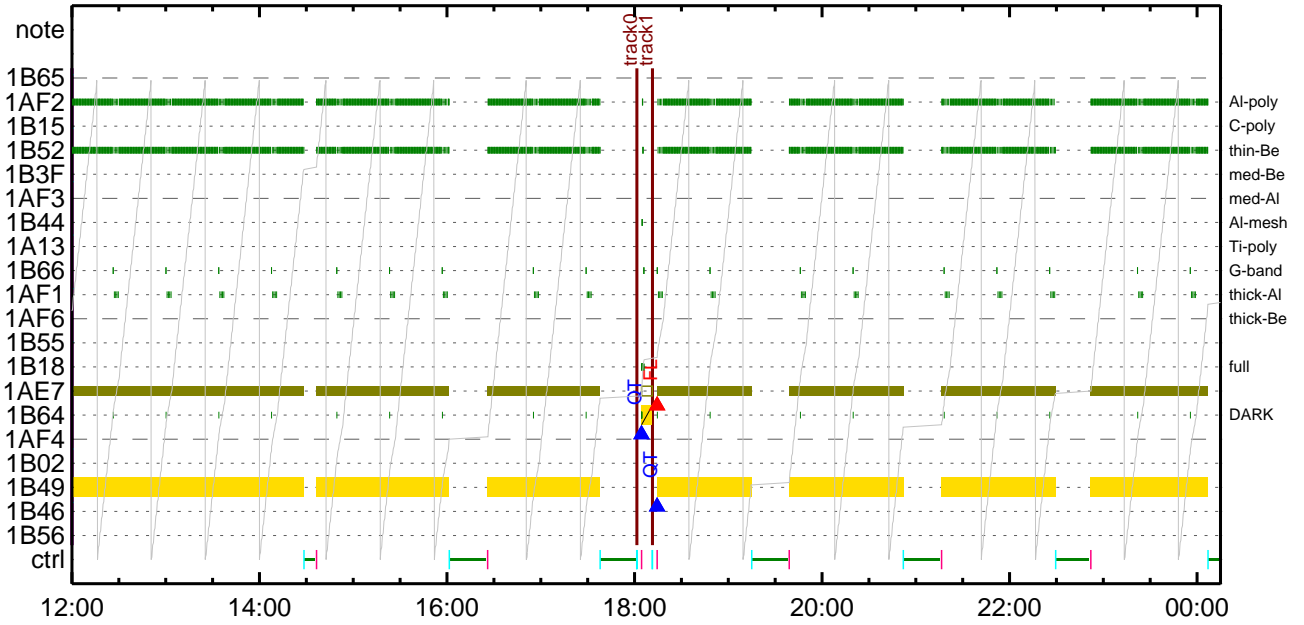
### CMDI #0504 2017/01/24



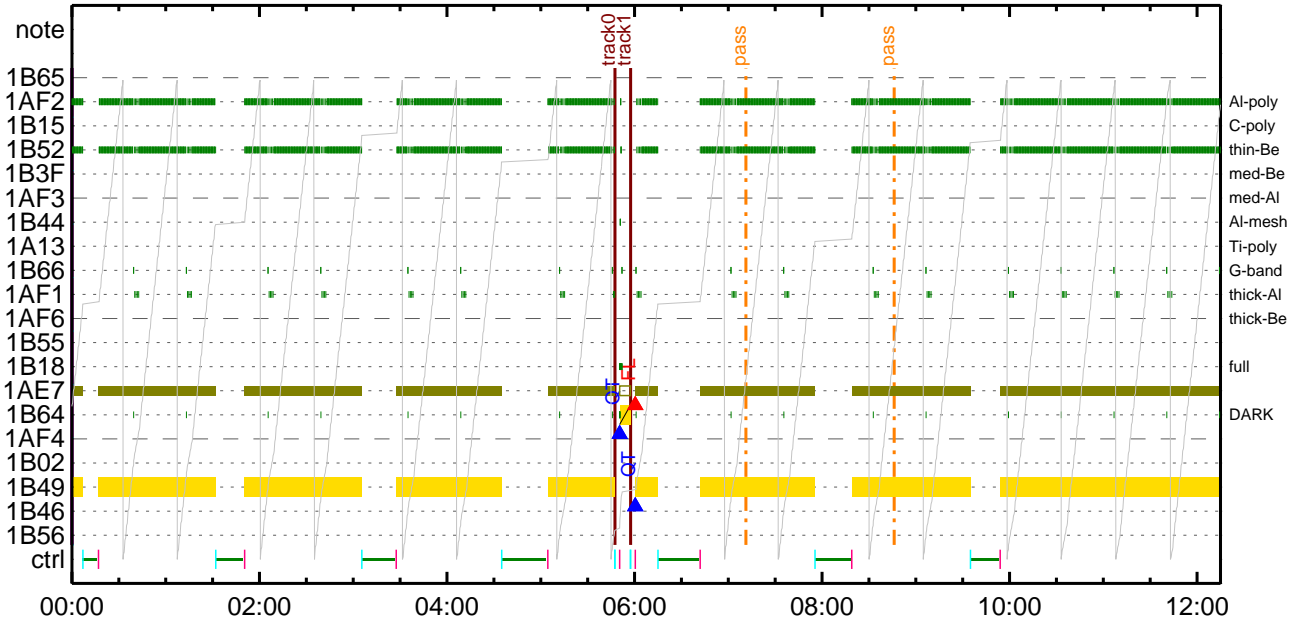
### CMDI #0504 2017/01/25



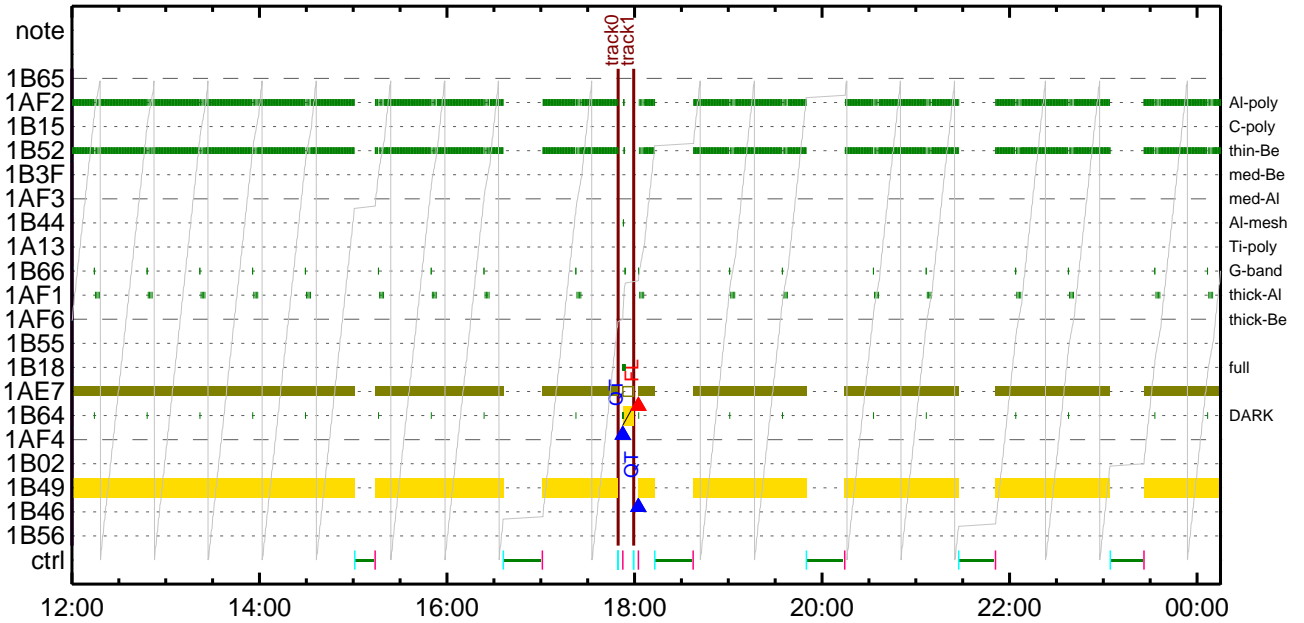
CMDI #0504 2017/01/25



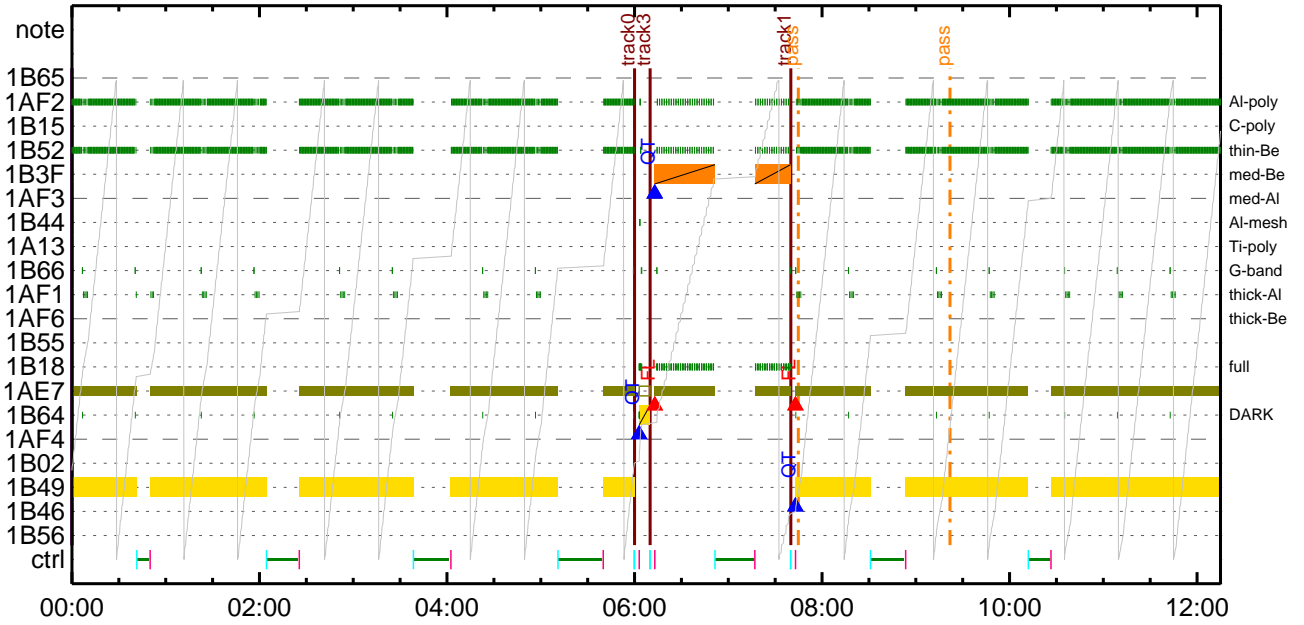
CMDI #0504 2017/01/26



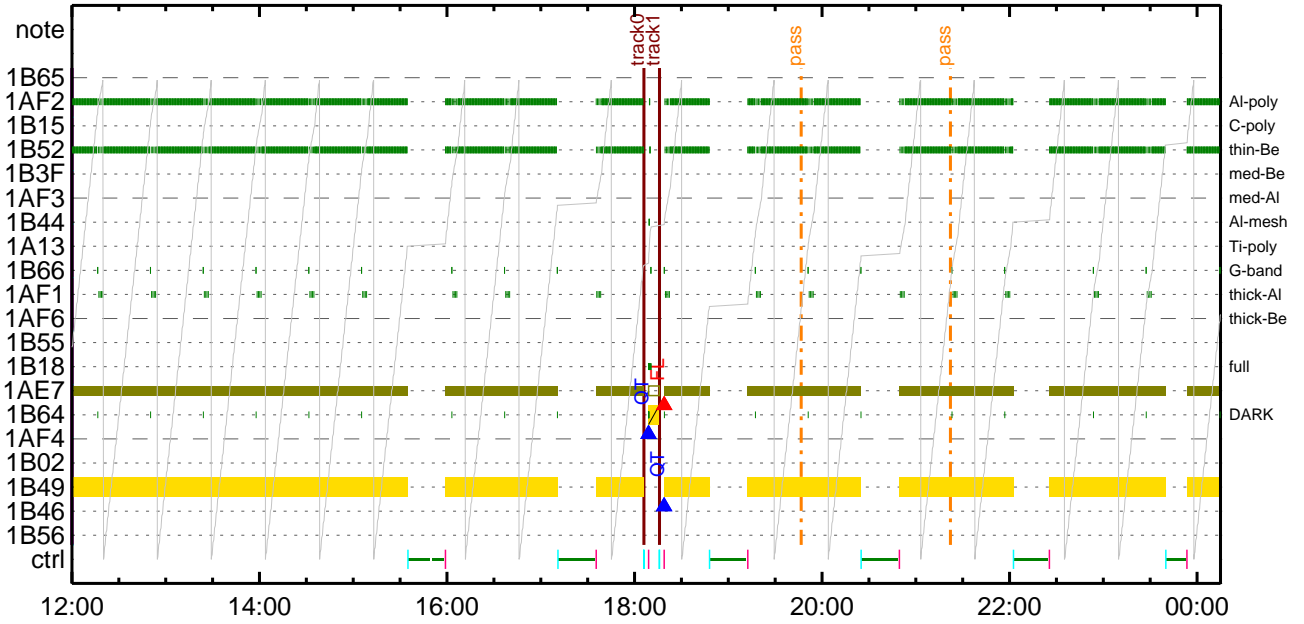
CMDI #0504 2017/01/26



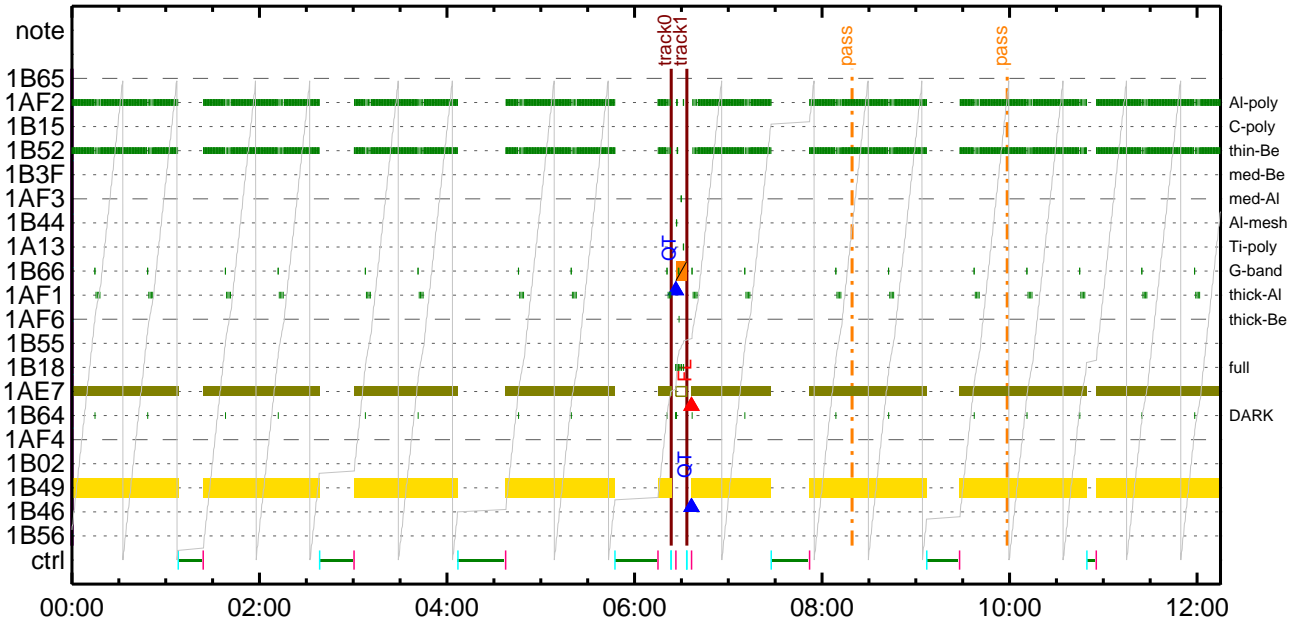
CMDI #0504 2017/01/27



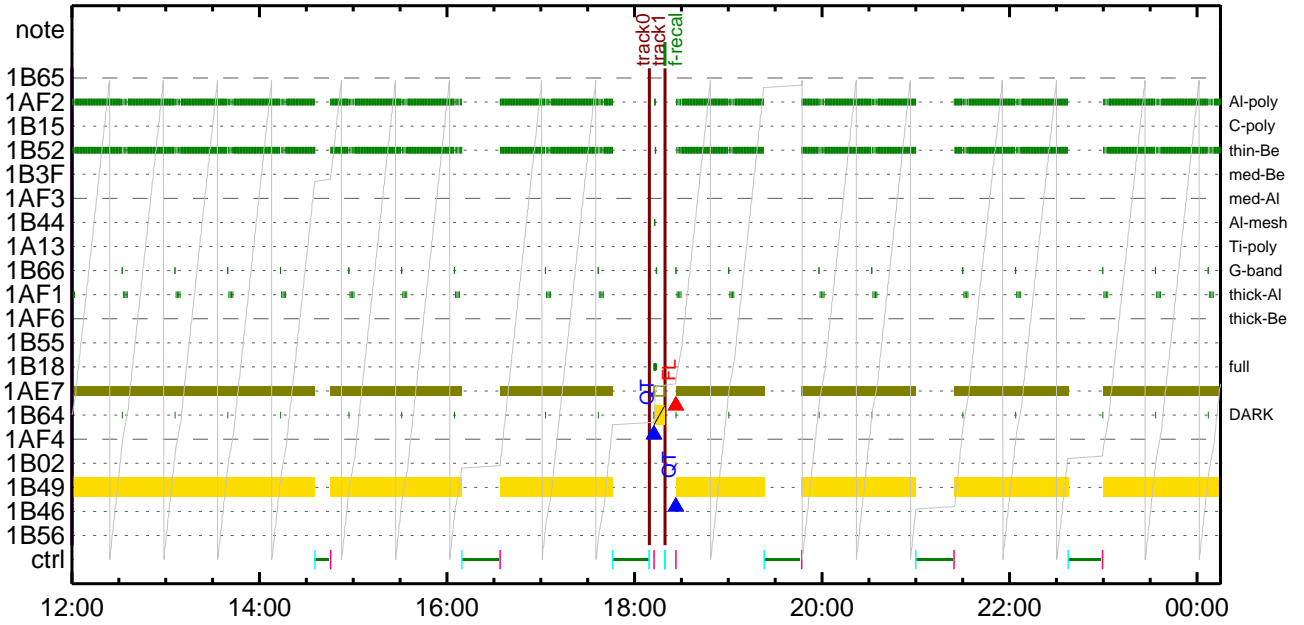
CMDI #0504 2017/01/27



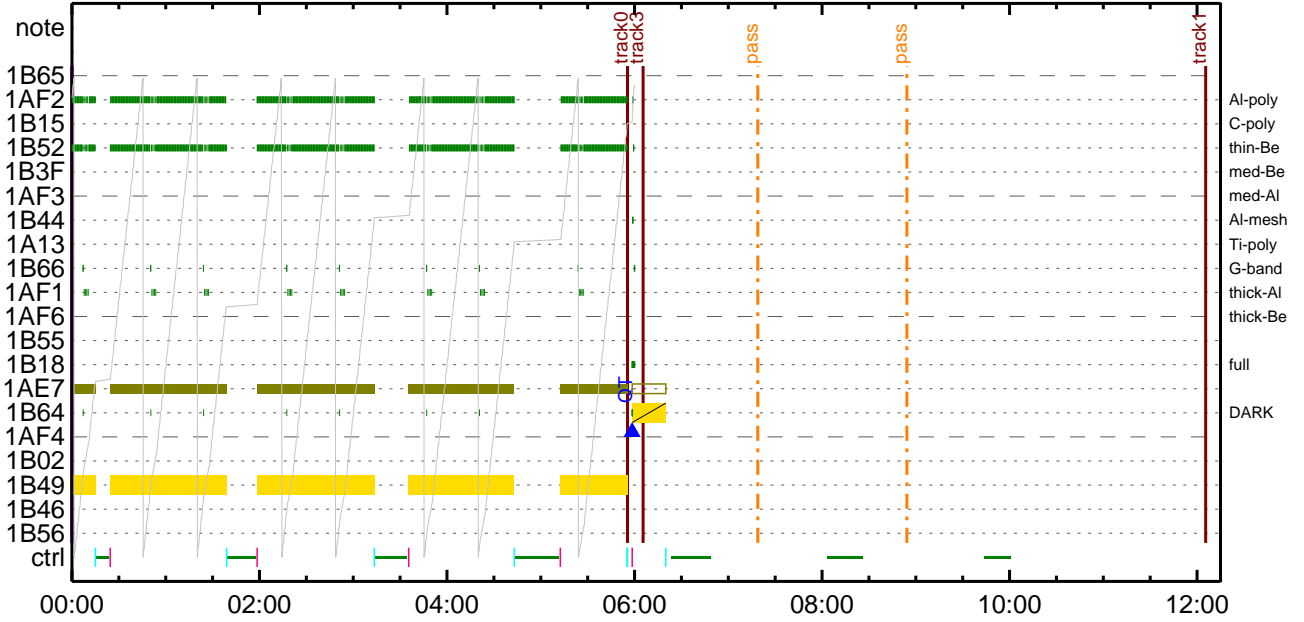
CMDI #0504 2017/01/28



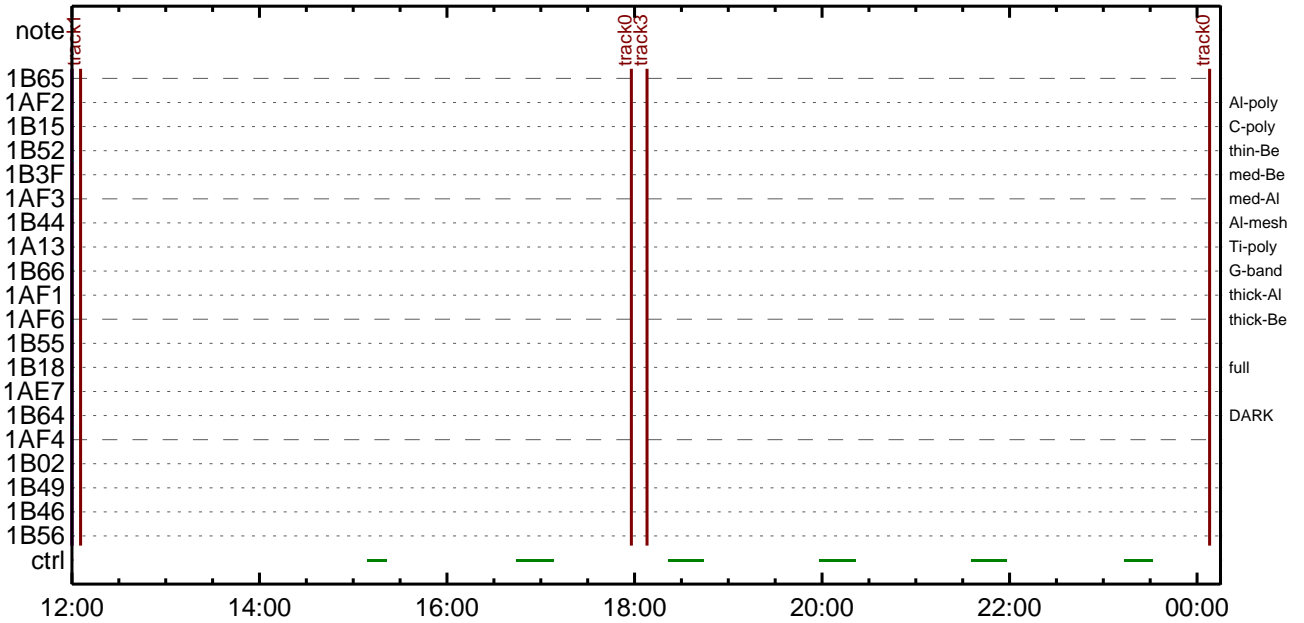
CMDI #0504 2017/01/28



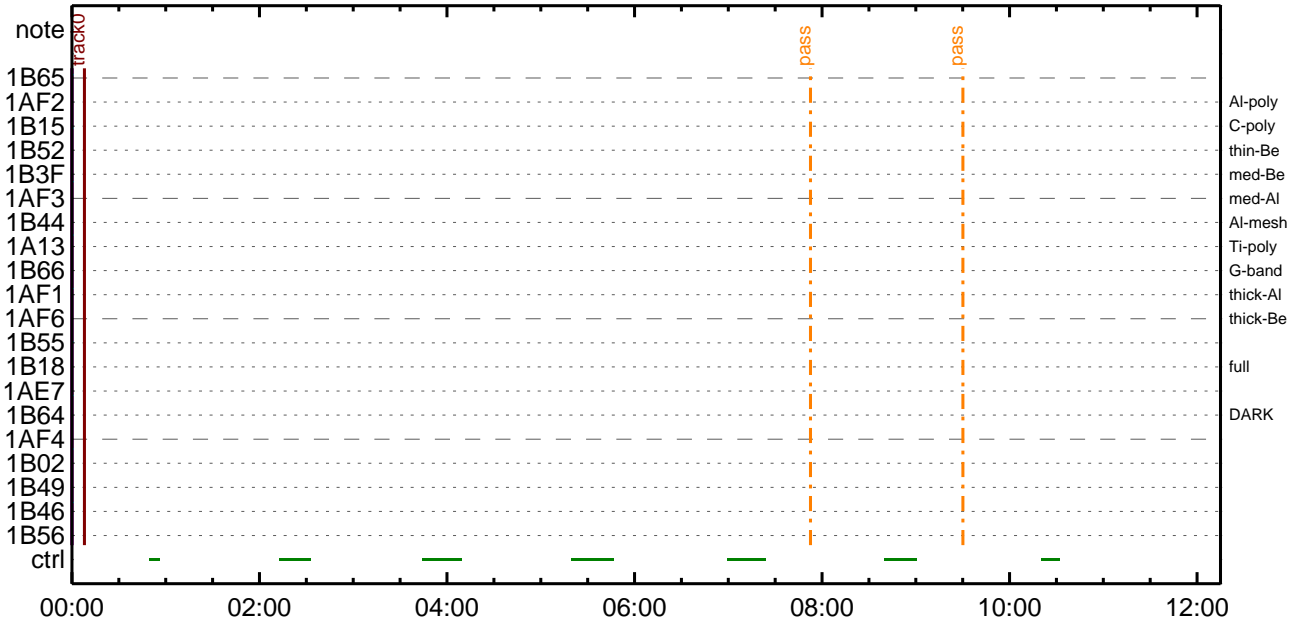
CMDI #0504 2017/01/29



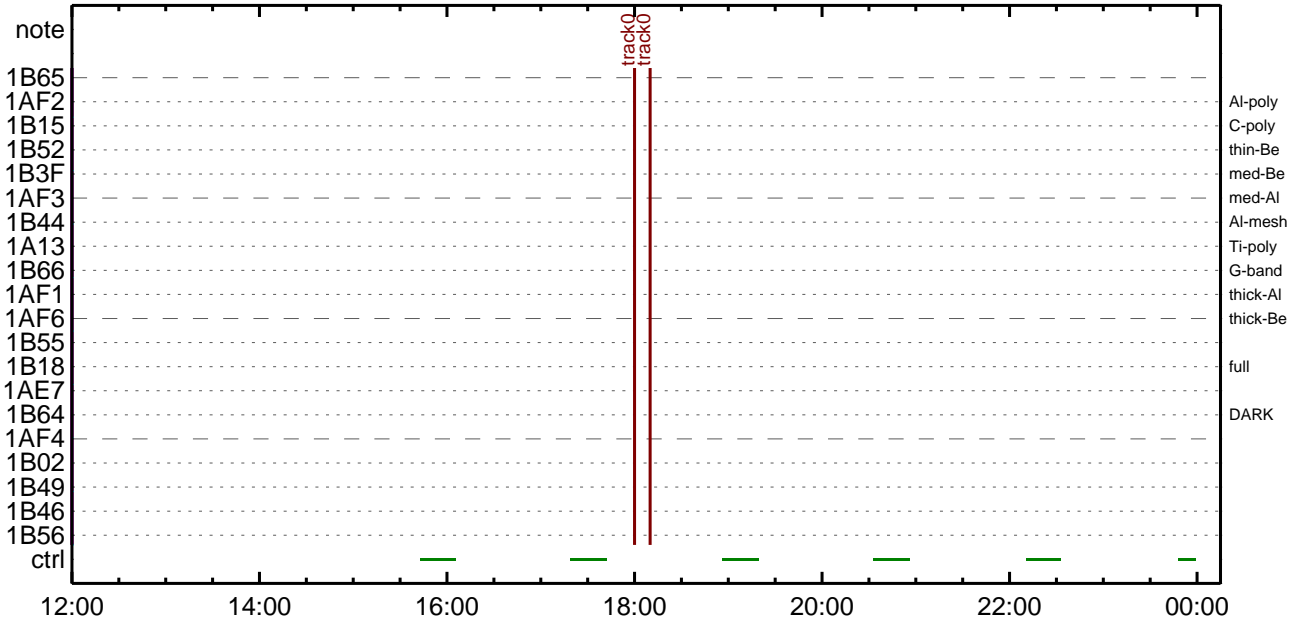
CMDI #0504 2017/01/29



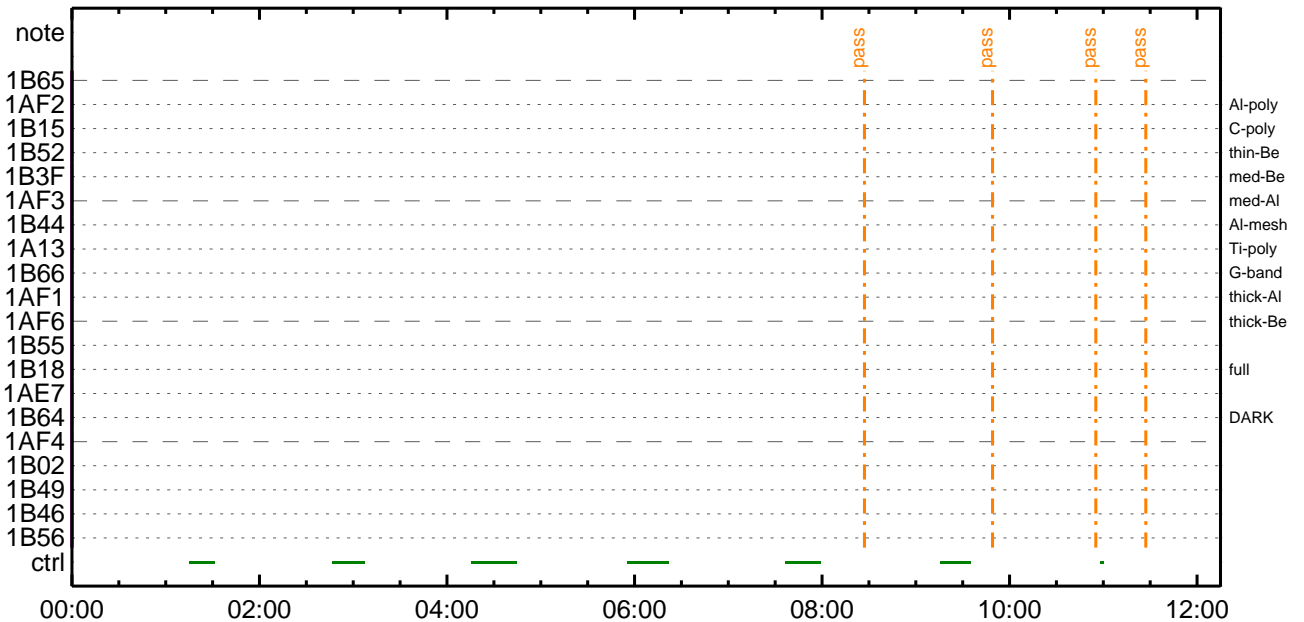
CMDI #0504 2017/01/30



CMDI #0504 2017/01/30

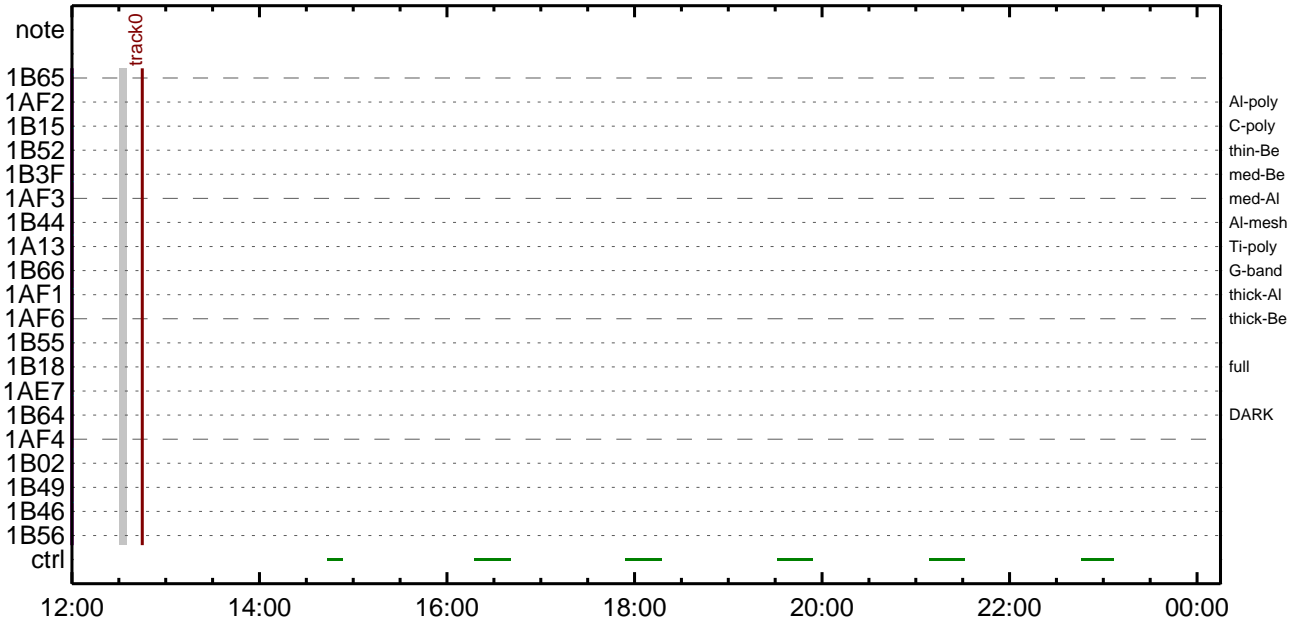


CMDI #0504 2017/01/31

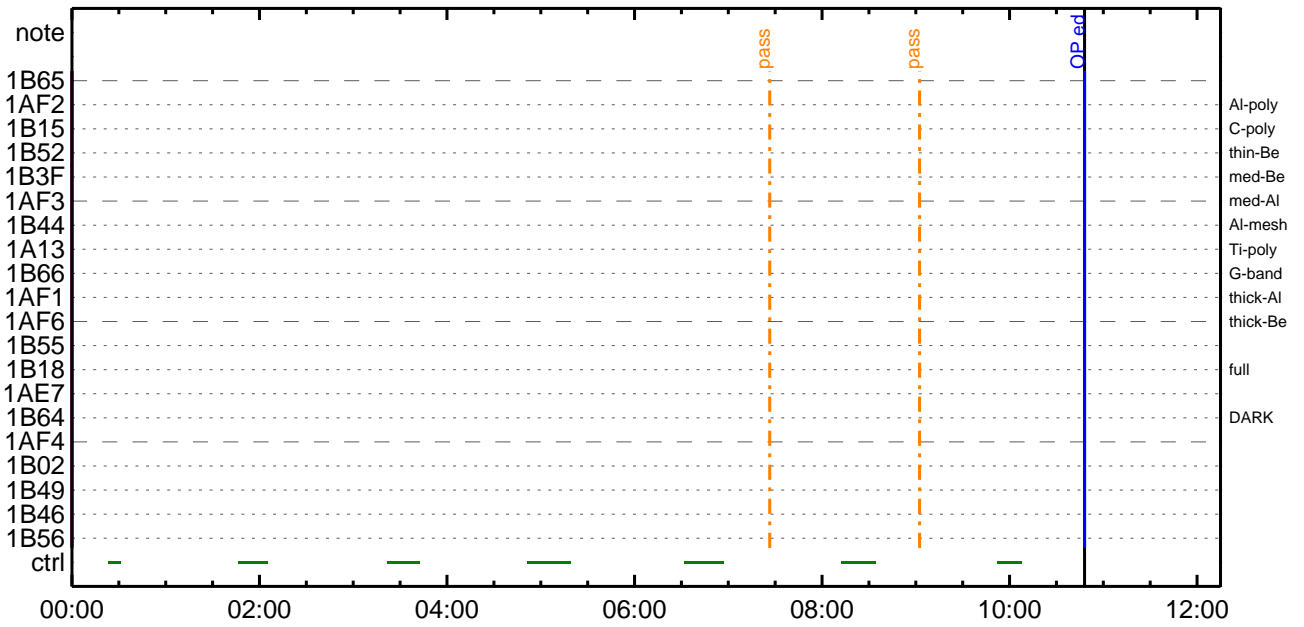




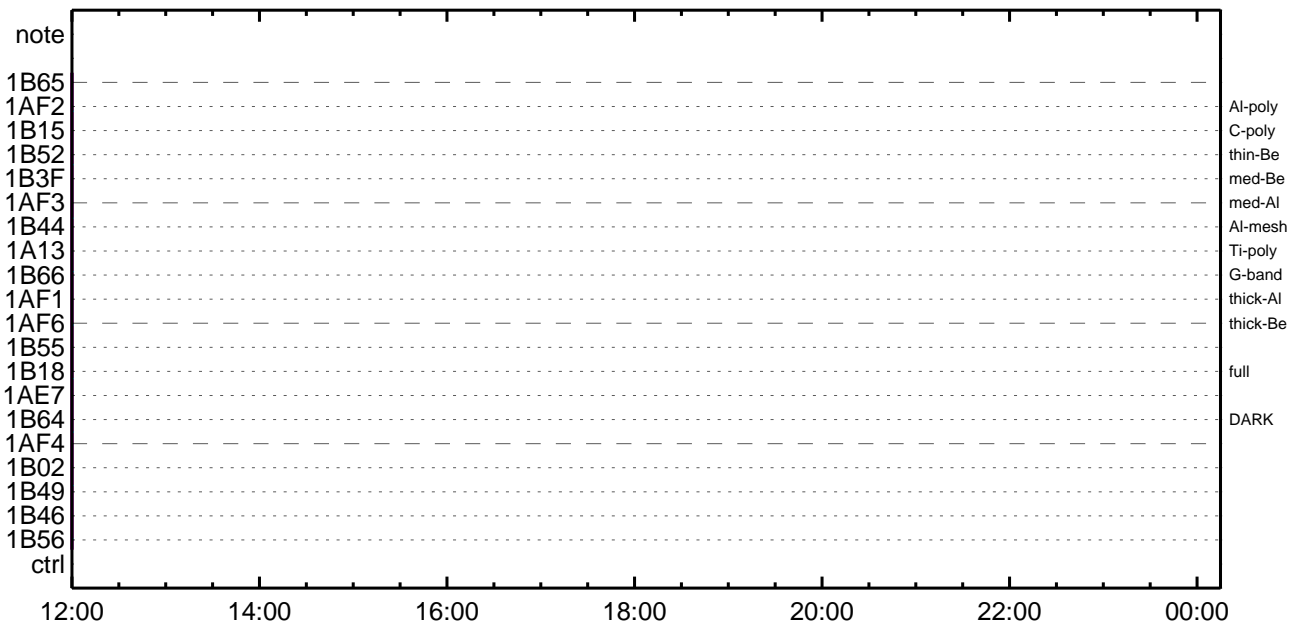
CMDI #0504 2017/01/31



CMDI #0504 2017/02/01



CMDI #0504 2017/02/01





```
0096 . C.
0097 . C.
0098 . C. *****
0099 . C. OP/OGY1;½YE;à;½AYÓYx
0100 . C. *****
0101 . C.
0102 . C. ;ãOP/OGY1;½YE;à
0103 . S. OP      op-379:OP
0104 . (
0105 . S. OG      og-379:OG
0106 . (
0107 . C.
0108 . C. ;ãNMOG&OPíî°è½AYÓYx;ã
0109 . C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC      (20 00 7f 01 02)
0112 . C.           ¢¢[HK1_DMP_TOP_ADRS_1]           EQ      40
0113 . C.           ¢¢[HK1_DMP_TOP_ADRS_0]           EQ      0
0114 . C.           ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0115 . C.           ¢¢[HK1_DMP_REPEAT_NUM]          EQ      0
0116 . C.           ¢¢[HK1_DMA_DMP_PIM]              EQ     DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC      (07 0b f8)
0119 . C.           ¢¢[HK1_PKT_FORM_NO]              EQ      7
0120 . C.           ¢¢[HK1_PKT_GEN_TIME]             EQ     0.25 s
0121 . C.           ¢¢[HK1_S_TLM_BIT_RATE]           EQ     32k
0122 . C.           ¢¢[HK1_X_TLM_BIT_RATE]          EQ      4M
0123 . C.           ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0124 . C. ½AYÓYx½ªª î»ðð³ îÇ§
0125 . C.           ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0126 . C. RAM ID=NMOGαî½E¹ Ç. è² îOKαð³ îÇ§
0127 . C.
0128 . C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC      (20 80 7f 01 02)
0131 . C.           ¢¢[HK1_DMP_TOP_ADRS_1]           EQ     41
0132 . C.           ¢¢[HK1_DMP_TOP_ADRS_0]           EQ      0
0133 . C.           ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0134 . C.           ¢¢[HK1_DMP_REPEAT_NUM]          EQ      0
0135 . C.           ¢¢[HK1_DMA_DMP_PIM]              EQ     DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC      (07 0b f8)
0138 . C.           ¢¢[HK1_PKT_FORM_NO]              EQ      7
0139 . C.           ¢¢[HK1_PKT_GEN_TIME]             EQ     0.25 s
0140 . C.           ¢¢[HK1_S_TLM_BIT_RATE]           EQ     32k
0141 . C.           ¢¢[HK1_X_TLM_BIT_RATE]          EQ      4M
0142 . C.           ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0143 . C. ½AYÓYx½ªª î»ðð³ îÇ§
0144 . C.           ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0145 . C. RAM ID=NMOGαî½E¹ Ç. è² îOKαð³ îÇ§
0146 . C.
0147 . C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC      (21 00 41 01 02)
0150 . C.           ¢¢[HK1_DMP_TOP_ADRS_1]           EQ     42
0151 . C.           ¢¢[HK1_DMP_TOP_ADRS_0]           EQ      0
0152 . C.           ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0153 . C.           ¢¢[HK1_DMP_REPEAT_NUM]          EQ      0
0154 . C.           ¢¢[HK1_DMA_DMP_PIM]              EQ     DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC      (07 0b f8)
0157 . C.           ¢¢[HK1_PKT_FORM_NO]              EQ      7
0158 . C.           ¢¢[HK1_PKT_GEN_TIME]             EQ     0.25 s
0159 . C.           ¢¢[HK1_S_TLM_BIT_RATE]           EQ     32k
0160 . C.           ¢¢[HK1_X_TLM_BIT_RATE]          EQ      4M
0161 . C.           ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0162 . C. ½AYÓYx½ªª î»ðð³ îÇ§
0163 . C.           ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0164 . C. RAM ID=NMOG, RAM ID=OPαî½E¹ Ç. è² îOKαð³ îÇ§
0165 . C.
0166 . C. ***** òE²¼α î½Á' ¶Á° òEĒ-α òÁ÷¿@ (½âµ-½AYÓYx½E½ ÇαðÀŌÅæαÇªª α" òE½i¹ ÇαÇαâ) *****
0167 . C. DHU½â;½YE;Ē½Y½, ½i;½YE;Ēαðîãα¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC      (02 0a f8)
0170 . C.           ¢¢[HK1_PKT_FORM_NO]              EQ      2
0171 . C.           ¢¢[HK1_PKT_GEN_TIME]             EQ     0.5S
0172 . C.           ¢¢[HK1_S_TLM_BIT_RATE]           EQ     32K
0173 . C.           ¢¢[HK1_X_TLM_BIT_RATE]          EQ      4M
0174 . C.
0175 . C. *****
0176 . C. TI-CMD SET (OPOG STOP/COPY/START)
0177 . C. *****
0178 . C.
0179 . C. NOTICE | § OPOG UPLOADα-Á÷¿@NGµî½i¹ Ç; ¢°E²¼α îTI-CMDÁ÷¿@αî½Á¹ Ōα. òEααα³ òE; f
0180 . C.           αPα¿; ¢SETαĒDUMPαî½E±° òE½Y¹ òÇ¹ Ōα | α³ òE; f
0181 . C.
0182 . C. TIY³ YP½YÓYĒ òðĀĬ¿ (UT)
0183 +. TI 2017-01-24 10:23:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 . C.           ¢¢[HK1_TI_CMD_NUM]                 EQ     1COUNTUP
0186 . C.
0187 +. TI 2017-01-24 10:23:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 . C.           ¢¢[HK1_TI_CMD_NUM]                 EQ     1COUNTUP
0190 . C.
0191 +. TI 2017-01-24 10:23:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 . C.           ¢¢[HK1_TI_CMD_NUM]                 EQ     1COUNTUP
```

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0194 C.
0195 +. TI 2017-01-24 10:27:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          꺺꺺[HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0198 C.
0199 C.          °Ê²¼ºíÄê%îíÑºîîŷÄŷ$ŷÄŷ-¹àîŰ
0200 C.          꺺꺺[HK1_TI_CMD_ENA/DIS]                  EQ      ENA
0201 C.          꺺꺺[HK1_TI_CMD_NUM]                      EQ      4
0202 C.          꺺꺺[HK1_NEXT_EXEC_PIM]                   EQ      DHU
0203 C.          꺺꺺[HK1_NEXT_EXEC_DC]                    EQ      0xB3
0204 C.
0205 . C.          *****
0206 C.          TÍîŰ°èŷÄŷÓŷ×
0207 C.          *****
0208 C.
0209 C.          TI_TBL(0x03AB00-0x03AEFF;$ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC          (03 ab 03 01 02)
0212 C.          꺺꺺[HK1_DMP_TOP_ADRS_1]                  EQ      07
0213 C.          꺺꺺[HK1_DMP_TOP_ADRS_0]                  EQ      2B
0214 C.          꺺꺺[HK1_DMP_BLOCK_NUM]                   EQ      3
0215 C.          꺺꺺[HK1_DMP_REPEAT_NUM]                 EQ      0
0216 C.          꺺꺺[HK1_DMA_DMP_PIM]                    EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC          (07 0b f8)
0219 C.          꺺꺺[HK1_PKT_FORM_NO]                    EQ      7
0220 C.          꺺꺺[HK1_PKT_GEN_TIME]                    EQ      0.25 s
0221 C.          꺺꺺[HK1_S_TLM_BIT_RATE]                 EQ      32k
0222 C.          꺺꺺[HK1_X_TLM_BIT_RATE]                 EQ      4M
0223 C.          꺺꺺[HK1_DMP_CHK_FLG]                    EQ      EXEC
0224 C.
0225 . C.          ŷÄŷÓŷ×½ªî»º³ÍÇ$
0226 C.          꺺꺺[HK1_DMP_CHK_FLG]                    EQ      NON
0227 C.
0228 . C.          RAM ID=TI_TBLºîî¾È¹ç•è²ìOKº³ÍÇ$
0229 C.
0230 . C.          DHUŷª;¼ŷÈ;È¼ŷ¼.ŷî;¼ŷÈ;Èº³Íª¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC          (02 0a f8)
0233 C.          꺺꺺[HK1_PKT_FORM_NO]                    EQ      2
0234 C.          꺺꺺[HK1_PKT_GEN_TIME]                    EQ      0.5S
0235 C.          꺺꺺[HK1_S_TLM_BIT_RATE]                 EQ      32K
0236 C.          꺺꺺[HK1_X_TLM_BIT_RATE]                 EQ      4M
0237 C.
0238 . C.          Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C.          ***** Start EIS operation (TI set) *****
0242 C.          Execute, after the success of OP upload.
0243 C.          Set EIS TI-commands
0244 +. TI 2017-01-24 10:27:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC          (21 02)
0247 +. TI 2017-01-24 10:27:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC          (22)
0250 . C.          [ ] [HK1_TI_CMD_NUM]                  EQ      2 COUNTUP
0251 C.          ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.
0255 C.          ***** XRT START *****
0256 C.          Execute, after the success of OP upload.
0257 +. TI 2017-01-24 10:27:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC          (c3)
0260 . C.          [ ] [HK1_TI_CMD_NUM]                  EQ      1COUNTUP
0261 C.
0262 C.          ***** XRT END *****
0263 C.
0264 . C.          ***** MDP ´ûÄîºî»º¼ŷºèÄºº¹ºèDCBC•x²è *****
0265 C.          (¼ªºîŷÓŷÄŷÈŷŰŷÈŷáŷçŷèºè¼ºº¼ª»Űº¹ºè)
0266 . S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 . C.          ***** ŷĐŷ¹.İ Daily±;îÑºè´ºº¹ºèDCBC•x²è *****
0271 . S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 . C.          ;äLOSŷÄŷ$ŷÄŷ-¼ª»Ű;ª
0276 C.
0277 . C.          ***** LOS *****
0278 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-380 2017-01-24 13:13:19 94 33 SOLAR-B MAIN //
0001 . C.
0002 . C. ***** AOS *****
0003 . C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 . C.
0006 . C. YÁYB;¼Y³YFÖYÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 . C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 . C. ÁÍ;È±¿±Á±·μ°Ë»Í×ÁÇ±ÍY¿YÁY×YÍ;¼YÉ;ÈÈÈ¾μ·ÍÍÉ;È±È¼°ÇÔ±·±¿¼Í¹Ç±Í;¿Á®, ù±¹±±±±±±ÇÁ+¿®±·±È±±±³±È;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 . C.
0013 . C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 . C. Upload the Orbit Element and the Target Attitude
0016 . C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 . C.
0019 . C.
0020 . C.
0021 . C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 . BC (07 00 00 00 18 00)
0024 . C.
0025 . C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0026 . C.
0027 . C.
0028 . C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 . BC (04 0b f8)
0031 . C.
0032 . C. Wait for AOCSDUMP to end
0033 . C.
0034 . C. Check the dump memory
0035 . C.
0036 . C. Result = OK [ ]
0037 . C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 . BC (02 0a f8)
0040 . C.
0041 . C. <A_***>[TLM STS] FMT = 2 [ ]
0042 . C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. ***** AOCS Commands (Orbital Element Update) *****
0046 . C. Update the orbital element
0047 +. DC 02-50 AOCU_ORB_PRPGT_START
0048 . BC (16)
0049 +. DC 02-8E AOCU_ORB_UPD
0050 . C.
0051 . C. <A_ORB>[ORBIT] EPC = 7107882.4 +- 1.0 (s) [ ]
0052 . C.
0053 . C.
0054 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0055 +. DC 07-FC EIS_MODE_CHG_ENA
0056 . BC (20)
0057 . C. Verify EIS_MODE_CHG_FLG is ENA
0058 +. DC 07-FC EIS_MODE_MANU
0059 . BC (21 02)
0060 . C. Verify EIS in MANUAL mode
0061 . C. Estimated OBSTBL upload time is 1m45s
0062 . C. *****
0063 . C. EIS START OBSTBL LOAD
0064 . C. *****
0065 . S. RAM ram-820:EIS_OBSTBL
0066 . C.
0067 +. DC 07-FC EIS_DUMP_OBSTBL
0068 . BC (07 07 07 00 00 70 00)
0069 . C.
0070 . C. Execute, after the success of OBSTBL upload.
0071 . C. Set EIS TI-commands
0072 +. TI 2017-01-24 10:27:50.0
0073 . DC 07-FC EIS_MODE_CHG_ENA
0074 . BC (20)
0075 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0076 . C. *****
0077 . C. EIS END OBSTBL LOAD
0078 . C. *****
0079 . C.
0080 . C. ***** MDP `ûÁÎ±Í»ö¼Y±ÉÄ±¹±±DCBC·×²è *****
0081 . C. (¼±°ÍYÖYÁYÉY¿YÉYáY¿Yé±±E¾¼±±¼Á»Û¹±±è)
0082 . S. DC-BC dcbc-402:DCBC
0083 . C. (MDP_known_event)
0084 . C.
0085 . C.
0086 . C. ***** YD¥¹·Í Daily±¿ÍÑ±É´Ø±¹±±DCBC·×²è *****
0087 . S. DC-BC dcbc-153:DCBC
0088 . C. (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0089 . C.
0090 . C.
0091 . C. ;ãLOSÁY$YÁY-¼Á»Û;ã
0092 . C.
0093 . C. ***** LOS *****
0094 . C.
```



```

0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG ____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 80 80 20 20)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 85 83 08 08)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 20 08)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 80 80 08 20)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0f 80 80 06 06)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 10 80 80 08 08)
0140 + DC 07-F0 MDP_XRT_FLD_ENA
0141 BC (d8)
0142 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0143 BC (c8)
0144 + DC 07-F0 MDP_XRT_ARS_DIS
0145 BC (d5)
0146 + DC 07-F0 MDP_XRT_AEC_RESET
0147 BC (d0)
0148 + DC 07-F0 MDP_XRT_FLD_RESET
0149 BC (da)
0150 + DC 07-F0 MDP_XRT_QT_PROG_SET
0151 BC (c4 04)
0152 + DC 07-F0 MDP_XRT_FL_PROG_SET
0153 BC (c5 07)
0154 . C. ----- Success Verify ? OK / NG ____
0155 C.
0156 C.
0157 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0158 C.
0159 +. DC 07-F0 MDP_XRT_MODE_OBSV
0160 BC (c2)
0161 +. TI 2017-01-24 10:27:02.0
0162 DC 07-F0 MDP_XRT_MODE_OBSV
0163 BC (c2)
0164 . C. ----- Success Verify ? OK / NG ____
0165 C.
0166 C. ***** XRT END *****
0167 C.
0168 . C. ***** MDP `úÃîï»ö%ÝðËÄð¹ðèDCBC•x²è *****
0169 C. (%á°îÿÓYÁYÈYÏYÉYáYçYèðÉ%¼ð¼Á»Û¹ðè)
0170 . S. DC-BC dcbc-402:DCBC
0171 (MDP_known_event)
0172 C.
0173 C.
0174 . C. ***** ¥D¥¹•İ Daily±¿İÑðË'Ø¹ðèDCBC•x²è *****
0175 . S. DC-BC dcbc-153:DCBC
0176 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0177 C.
0178 C.
0179 . C. ;ãLOS¥Á¥$¥Ã¥¹¼Á»Û;ã
0180 C.
0181 . C. ***** LOS *****
0182 C.

```





Jan 24, 17 13:13

XRT\_OGLIST\_0504.chk

Page 1/13

\*\*\* OP Sequence for XRT \*\*\*

2017/01/24	10:37:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/24	10:37:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/24	10:37:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2017/01/24	10:38:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2017/01/24	10:38:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2017/01/24	10:38:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2017/01/24	10:38:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2017/01/24	10:38:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2017/01/24	10:38:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/01/24	10:40:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2017/01/24	10:40:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2017/01/24	10:41:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/01/24	15:27:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/24	15:27:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/01/24	15:27:04.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/01/24	15:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/01/24	15:41:00.0	XRT_Custom_430_OG [0x1ae]							
2017/01/24	15:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/01/24	15:45:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/24	15:45:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/01/24	15:45:04.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/01/24	15:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/01/24	15:50:00.0	XRT_Custom_430_OG [0x1ae]							
2017/01/24	15:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/01/24	16:37:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/24	16:37:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/24	16:37:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2017/01/24	16:38:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2017/01/24	16:38:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2017/01/24	16:38:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2017/01/24	16:38:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2017/01/24	16:38:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2017/01/24	16:38:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/01/24	16:40:56.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2017/01/24	16:40:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2017/01/24	16:41:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/01/24	17:03:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/24	17:03:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/01/24	17:03:04.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/01/24	17:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/01/24	17:26:30.0	XRT_Custom_430_OG [0x1ae]							
2017/01/24	17:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/01/24	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/24	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/24	17:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2017/01/24	18:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2017/01/24	18:00:18.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				

Jan 24, 17 13:13

## XRT\_OGLIST\_0504.chk

Page 2/13

2017/01/24	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2017/01/24	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2017/01/24	18:02:58.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	06
2017/01/24	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/01/24	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/01/24	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/01/24	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2017/01/24	18:10:00.0	AOCs_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01	00 00 00 00
2017/01/24	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2017/01/24	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2017/01/24	18:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2017/01/24	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2017/01/24	18:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/01/24	18:12:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e
2017/01/24	18:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2017/01/24	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/01/24	18:40:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/01/24	18:40:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/01/24	18:40:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/01/24	18:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/01/24	19:03:31.0	XRT_Custom_430_OG [0x1ae]					
2017/01/24	19:04:31.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/01/24	20:17:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/01/24	20:17:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/01/24	20:17:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/01/24	20:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/01/24	20:40:30.0	XRT_Custom_430_OG [0x1ae]					
2017/01/24	20:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/01/24	21:54:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/01/24	21:54:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/01/24	21:54:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/01/24	21:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/01/24	22:17:00.5	XRT_Custom_430_OG [0x1ae]					
2017/01/24	22:18:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/01/24	23:32:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/01/24	23:32:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/01/24	23:32:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/01/24	23:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/01/24	23:46:00.0	XRT_Custom_430_OG [0x1ae]					
2017/01/24	23:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/01/25	01:02:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/01/25	01:02:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/01/25	01:02:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/01/25	01:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/01/25	01:15:00.5	XRT_Custom_430_OG [0x1ae]					
2017/01/25	01:16:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/01/25	02:31:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/01/25	02:31:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/01/25	02:31:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	

2017/01/25	02:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/01/25	02:51:00.0	XRT_Custom_430_OG [0x1ae]							
2017/01/25	02:52:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/01/25	03:58:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/25	03:58:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/01/25	03:58:34.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/01/25	04:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/01/25	04:28:30.5	XRT_Custom_430_OG [0x1ae]							
2017/01/25	04:29:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/01/25	05:39:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/25	05:39:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/01/25	05:39:04.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/01/25	05:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/01/25	06:06:00.0	XRT_Custom_430_OG [0x1ae]							
2017/01/25	06:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/01/25	06:15:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/25	06:15:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/25	06:15:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2017/01/25	06:15:30.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2017/01/25	06:15:48.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2017/01/25	06:18:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2017/01/25	06:18:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2017/01/25	06:18:28.0	XRT_QT_PROG_SET_429_OG [0x1ad]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2017/01/25	06:18:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/01/25	06:25:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/25	06:25:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/25	06:25:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2017/01/25	06:25:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2017/01/25	06:25:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2017/01/25	06:25:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2017/01/25	06:25:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2017/01/25	06:25:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2017/01/25	06:25:56.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/01/25	06:28:26.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2017/01/25	06:28:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2017/01/25	06:28:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/01/25	07:19:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/25	07:19:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/01/25	07:19:34.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/01/25	07:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/01/25	07:43:00.5	XRT_Custom_430_OG [0x1ae]							
2017/01/25	07:44:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/01/25	08:59:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/25	08:59:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/01/25	08:59:04.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/01/25	09:02:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/01/25	09:19:00.0	XRT_Custom_430_OG [0x1ae]							
2017/01/25	09:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/01/25	10:40:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				

2017/01/25	10:40:32.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da	
2017/01/25	10:40:34.0	XRT_PREFLR_STRT_431_OG [0x1af] MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/01/25	10:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/01/25	10:48:00.0	XRT_Custom_430_OG [0x1ae]				
2017/01/25	10:49:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/01/25	14:28:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/01/25	14:28:32.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da	
2017/01/25	14:28:34.0	XRT_PREFLR_STRT_431_OG [0x1af] MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/01/25	14:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/01/25	14:35:30.5	XRT_Custom_430_OG [0x1ae]				
2017/01/25	14:36:30.5	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/01/25	16:01:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/01/25	16:01:32.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da	
2017/01/25	16:01:34.0	XRT_PREFLR_STRT_431_OG [0x1af] MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/01/25	16:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/01/25	16:25:00.0	XRT_Custom_430_OG [0x1ae]				
2017/01/25	16:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/01/25	17:38:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/01/25	17:38:02.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da	
2017/01/25	17:38:04.0	XRT_PREFLR_STRT_431_OG [0x1af] MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/01/25	17:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/01/25	18:01:24.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/01/25	18:01:26.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/01/25	18:01:28.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2017/01/25	18:01:30.0	AOCS_Ore-point_Start_3_OG [0x099] AOCU_NM	5	02-76	00 00 00 00 00	
2017/01/25	18:01:48.0	XRT_FLD_DIS_406_OG [0x196] MDP_XRT_FLD_DIS	1	07-F0	d9	
2017/01/25	18:04:24.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2017/01/25	18:04:26.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5	
2017/01/25	18:04:28.0	XRT_QT_PROG_SET_432_OG [0x1b0] MDP_XRT_QT_PROG_SET	2	07-F0	c4 06	
2017/01/25	18:04:30.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/01/25	18:11:24.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/01/25	18:11:26.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/01/25	18:11:28.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2017/01/25	18:11:30.0	AOCS_Ore-point_Start_2_OG [0x098] AOCU_NM	5	02-76	01 00 00 00 00	
2017/01/25	18:11:48.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8	
2017/01/25	18:11:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2017/01/25	18:11:52.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0	
2017/01/25	18:11:54.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5	
2017/01/25	18:11:56.0	XRT_FLD_RESET_433_OG [0x1b1] MDP_XRT_FLD_RESET	1	07-F0	da	
2017/01/25	18:14:26.0	XRT_QT_PROG_SET_407_OG [0x197] MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2017/01/25	18:14:28.0	XRT_FL_PROG_SET_436_OG [0x1b4] MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2017/01/25	18:14:30.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/01/25	19:15:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/01/25	19:15:02.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da	
2017/01/25	19:15:04.0	XRT_PREFLR_STRT_431_OG [0x1af] MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/01/25	19:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/01/25	19:38:00.0	XRT_Custom_430_OG [0x1ae]				
2017/01/25	19:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/01/25	20:52:00.0	XRT_CTRL_MANU_400_OG [0x190]				

2017/01/25	20:52:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/01/25	20:52:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da		
2017/01/25	20:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2017/01/25	21:15:30.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2017/01/25	21:16:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/01/25	22:29:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/01/25	22:29:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2017/01/25	22:29:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2017/01/25	22:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2017/01/25	22:51:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/01/25	22:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/01/26	00:07:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/01/26	00:07:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2017/01/26	00:07:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2017/01/26	00:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2017/01/26	00:16:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/01/26	00:17:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/01/26	01:32:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/01/26	01:32:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2017/01/26	01:32:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2017/01/26	01:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2017/01/26	01:49:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/01/26	01:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/01/26	03:05:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/01/26	03:05:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2017/01/26	03:05:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2017/01/26	03:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2017/01/26	03:26:30.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/01/26	03:27:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/01/26	04:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/01/26	04:35:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2017/01/26	04:35:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2017/01/26	04:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2017/01/26	05:03:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/01/26	05:04:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/01/26	05:47:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/01/26	05:47:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/01/26	05:47:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2017/01/26	05:47:30.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00		
2017/01/26	05:47:48.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2017/01/26	05:50:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2017/01/26	05:50:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2017/01/26	05:50:28.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06		
2017/01/26	05:50:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/01/26	05:57:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/01/26	05:57:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/01/26	05:57:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2017/01/26	05:57:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01 00 00 00 00		
2017/01/26	05:57:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		

2017/01/26	05:57:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2017/01/26	05:57:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2017/01/26	05:57:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2017/01/26	05:57:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/01/26	06:00:26.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
2017/01/26	06:00:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2017/01/26	06:00:30.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/01/26	06:15:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/01/26	06:15:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/01/26	06:15:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/01/26	06:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/01/26	06:41:00.0	XRT_Custom_430_OG [0x1ae]					
2017/01/26	06:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/01/26	07:55:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/01/26	07:55:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/01/26	07:55:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/01/26	07:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/01/26	08:18:00.5	XRT_Custom_430_OG [0x1ae]					
2017/01/26	08:19:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/01/26	09:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/01/26	09:35:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/01/26	09:35:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/01/26	09:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/01/26	09:53:00.5	XRT_Custom_430_OG [0x1ae]					
2017/01/26	09:54:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/01/26	15:01:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/01/26	15:01:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/01/26	15:01:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/01/26	15:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/01/26	15:13:00.0	XRT_Custom_430_OG [0x1ae]					
2017/01/26	15:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/01/26	16:36:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/01/26	16:36:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/01/26	16:36:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/01/26	16:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/01/26	17:00:01.0	XRT_Custom_430_OG [0x1ae]					
2017/01/26	17:01:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/01/26	17:49:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/01/26	17:49:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/01/26	17:49:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2017/01/26	17:49:30.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00 00 00 00
2017/01/26	17:49:48.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2017/01/26	17:52:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2017/01/26	17:52:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2017/01/26	17:52:28.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	06
2017/01/26	17:52:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/01/26	17:59:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/01/26	17:59:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/01/26	17:59:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00

2017/01/26	17:59:30.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01	00	00	00	00
2017/01/26	17:59:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2017/01/26	17:59:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2017/01/26	17:59:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2017/01/26	17:59:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2017/01/26	17:59:56.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/01/26	18:02:26.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2017/01/26	18:02:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2017/01/26	18:02:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/01/26	18:13:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/26	18:13:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/01/26	18:13:04.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/01/26	18:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/01/26	18:36:30.0	XRT_Custom_430_OG [0x1ae]							
2017/01/26	18:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/01/26	19:50:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/26	19:50:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/01/26	19:50:04.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/01/26	19:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/01/26	20:13:30.0	XRT_Custom_430_OG [0x1ae]							
2017/01/26	20:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/01/26	21:27:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/26	21:27:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/01/26	21:27:34.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/01/26	21:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/01/26	21:50:00.5	XRT_Custom_430_OG [0x1ae]							
2017/01/26	21:51:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/01/26	23:04:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/26	23:04:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/01/26	23:04:34.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/01/26	23:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/01/26	23:25:00.0	XRT_Custom_430_OG [0x1ae]							
2017/01/26	23:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/01/27	00:41:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/27	00:41:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/01/27	00:41:34.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/01/27	00:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/01/27	00:49:00.0	XRT_Custom_430_OG [0x1ae]							
2017/01/27	00:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/01/27	02:04:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/27	02:04:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/01/27	02:04:34.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/01/27	02:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/01/27	02:24:30.0	XRT_Custom_430_OG [0x1ae]							
2017/01/27	02:25:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/01/27	03:38:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/27	03:38:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/01/27	03:38:34.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/01/27	03:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/01/27	04:01:30.0	XRT_Custom_430_OG [0x1ae]							

Jan 24, 17 13:13

## XRT\_OGLIST\_0504.chk

Page 8/13

2017/01/27	04:02:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/01/27	05:11:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/27	05:11:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/01/27	05:11:04.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/01/27	05:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/01/27	05:39:00.0	XRT_Custom_430_OG [0x1ae]							
2017/01/27	05:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/01/27	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/27	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/27	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2017/01/27	06:00:00.0	AOCS_Or-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2017/01/27	06:00:18.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2017/01/27	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2017/01/27	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2017/01/27	06:02:58.0	XRT_QT_PROG_SET_432_OG [0x1b0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2017/01/27	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/01/27	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/27	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/27	06:09:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2017/01/27	06:10:00.0	AOCS_Or-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2017/01/27	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2017/01/27	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2017/01/27	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2017/01/27	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2017/01/27	06:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/01/27	06:12:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2017/01/27	06:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2017/01/27	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/01/27	06:51:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/27	06:51:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/01/27	06:51:34.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/01/27	06:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/01/27	07:16:00.0	XRT_Custom_430_OG [0x1ae]							
2017/01/27	07:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/01/27	07:39:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/27	07:39:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/27	07:39:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2017/01/27	07:40:00.0	AOCS_Or-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2017/01/27	07:40:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2017/01/27	07:40:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2017/01/27	07:40:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2017/01/27	07:40:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2017/01/27	07:40:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/01/27	07:42:56.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2017/01/27	07:42:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2017/01/27	07:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/01/27	08:31:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/27	08:31:02.0	XRT_FLD_RESET_415_OG [0x19f]							



2017/01/27	08:31:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da			
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2017/01/27	08:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2017/01/27	08:52:30.0	XRT_Custom_430_OG [0x1ae]							
2017/01/27	08:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2017/01/27	10:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/01/27	10:12:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2017/01/27	10:12:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2017/01/27	10:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2017/01/27	10:25:30.0	XRT_Custom_430_OG [0x1ae]							
2017/01/27	10:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2017/01/27	15:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/01/27	15:35:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2017/01/27	15:35:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2017/01/27	15:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2017/01/27	15:58:00.0	XRT_Custom_430_OG [0x1ae]							
2017/01/27	15:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2017/01/27	17:11:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/01/27	17:11:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2017/01/27	17:11:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2017/01/27	17:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2017/01/27	17:34:30.0	XRT_Custom_430_OG [0x1ae]							
2017/01/27	17:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2017/01/27	18:05:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/01/27	18:05:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/01/27	18:05:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2017/01/27	18:06:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00			
2017/01/27	18:06:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2017/01/27	18:08:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2017/01/27	18:08:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2017/01/27	18:08:58.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06			
2017/01/27	18:09:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2017/01/27	18:15:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/01/27	18:15:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/01/27	18:15:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2017/01/27	18:16:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01 00 00 00 00			
2017/01/27	18:16:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2017/01/27	18:16:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2017/01/27	18:16:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2017/01/27	18:16:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2017/01/27	18:16:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da			
2017/01/27	18:18:56.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03			
2017/01/27	18:18:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07			
2017/01/27	18:19:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2017/01/27	18:48:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/01/27	18:48:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2017/01/27	18:48:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2017/01/27	18:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2017/01/27	19:11:30.0	XRT_Custom_430_OG [0x1ae]							
2017/01/27	19:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							

Jan 24, 17 13:13

## XRT\_OGLIST\_0504.chk

Page 10/13

2017/01/27	20:25:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/27	20:25:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2017/01/27	20:25:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/01/27	20:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/01/27	20:48:30.0	XRT_Custom_430_OG [0x1ae]								
2017/01/27	20:49:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/01/27	22:02:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/27	22:02:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2017/01/27	22:02:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/01/27	22:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/01/27	22:24:30.0	XRT_Custom_430_OG [0x1ae]								
2017/01/27	22:25:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/01/27	23:40:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/27	23:40:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2017/01/27	23:40:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/01/27	23:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/01/27	23:52:30.0	XRT_Custom_430_OG [0x1ae]								
2017/01/27	23:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/01/28	01:08:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/28	01:08:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2017/01/28	01:08:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/01/28	01:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/01/28	01:23:00.0	XRT_Custom_430_OG [0x1ae]								
2017/01/28	01:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/01/28	02:38:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/28	02:38:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2017/01/28	02:38:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/01/28	02:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/01/28	02:59:30.0	XRT_Custom_430_OG [0x1ae]								
2017/01/28	03:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/01/28	04:07:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/28	04:07:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2017/01/28	04:07:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/01/28	04:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/01/28	04:36:30.0	XRT_Custom_430_OG [0x1ae]								
2017/01/28	04:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/01/28	05:47:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/28	05:47:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2017/01/28	05:47:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/01/28	05:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/01/28	06:14:00.0	XRT_Custom_430_OG [0x1ae]								
2017/01/28	06:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/01/28	06:23:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/28	06:23:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/01/28	06:23:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2017/01/28	06:23:30.0	AOCS_Or-e-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00				
2017/01/28	06:23:48.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2017/01/28	06:26:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2017/01/28	06:26:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2017/01/28	06:26:28.0	XRT_QT_PROG_SET_429_OG [0x1ad]								

2017/01/28	06:26:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/01/28	06:33:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/01/28	06:33:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/01/28	06:33:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2017/01/28	06:33:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01	00 00 00 00
2017/01/28	06:33:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2017/01/28	06:33:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2017/01/28	06:33:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2017/01/28	06:33:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2017/01/28	06:33:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/01/28	06:36:26.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
2017/01/28	06:36:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2017/01/28	06:36:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/01/28	07:27:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/01/28	07:27:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/01/28	07:27:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/01/28	07:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/01/28	07:51:00.0	XRT_Custom_430_OG [0x1ae]					
2017/01/28	07:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/01/28	09:07:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/01/28	09:07:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/01/28	09:07:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/01/28	09:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/01/28	09:27:00.0	XRT_Custom_430_OG [0x1ae]					
2017/01/28	09:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/01/28	10:49:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/01/28	10:49:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/01/28	10:49:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/01/28	10:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/01/28	10:54:30.0	XRT_Custom_430_OG [0x1ae]					
2017/01/28	10:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/01/28	14:35:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/01/28	14:35:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/01/28	14:35:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/01/28	14:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/01/28	14:44:30.0	XRT_Custom_430_OG [0x1ae]					
2017/01/28	14:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/01/28	16:09:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/01/28	16:09:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/01/28	16:09:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/01/28	16:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/01/28	16:33:00.0	XRT_Custom_430_OG [0x1ae]					
2017/01/28	16:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/01/28	17:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/01/28	17:46:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/01/28	17:46:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/01/28	17:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/01/28	18:09:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/01/28	18:09:26.0	XRT_CTRL_MANU_402_OG [0x192]					

2017/01/28	18:09:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/01/28	18:09:30.0	AOCs_OrE-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2017/01/28	18:09:48.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00 00 00 00 00
2017/01/28	18:12:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2017/01/28	18:12:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2017/01/28	18:12:28.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_ARS_DIS	1	07-F0	d5
2017/01/28	18:12:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2017/01/28	18:19:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/01/28	18:19:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/01/28	18:19:28.0	XRT_FOCUS_RECALIBRATE_416_OG [0x1a0]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/01/28	18:19:30.0	AOCs_OrE-point_Start_2_OG [0x098]	XRT_FOCUS_RECAL	2	07-F8	78 00
2017/01/28	18:23:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	AOCU_NM	5	02-76	01 00 00 00 00
2017/01/28	18:23:48.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2017/01/28	18:23:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2017/01/28	18:23:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2017/01/28	18:23:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2017/01/28	18:23:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2017/01/28	18:26:26.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/01/28	18:26:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2017/01/28	18:26:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2017/01/28	19:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/01/28	19:23:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/01/28	19:23:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/01/28	19:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/01/28	19:46:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/01/28	19:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2017/01/28	21:00:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/01/28	21:00:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/01/28	21:00:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/01/28	21:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/01/28	21:23:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/01/28	21:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2017/01/28	22:37:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/01/28	22:37:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/01/28	22:37:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/01/28	22:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/01/28	22:58:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/01/28	22:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2017/01/29	00:15:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/01/29	00:15:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/01/29	00:15:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/01/29	00:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/01/29	00:23:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/01/29	00:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2017/01/29	01:39:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/01/29	01:39:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/01/29	01:39:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/01/29	01:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
			MDP_XRT_PREFLR_STOP	1	07-F0	e9

2017/01/29	01:57:30.0	XRT_Custom_430_OG [0x1ae]			
2017/01/29	01:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/01/29	03:13:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/01/29	03:13:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/01/29	03:13:34.0	XRT_PREFLR_STRT_431_OG [0x1af]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/01/29	03:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/01/29	03:34:30.5	XRT_Custom_430_OG [0x1ae]			
2017/01/29	03:35:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/01/29	04:43:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/01/29	04:43:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/01/29	04:43:04.0	XRT_PREFLR_STRT_431_OG [0x1af]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/01/29	04:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/01/29	05:11:30.5	XRT_Custom_430_OG [0x1ae]			
2017/01/29	05:12:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/01/29	05:55:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/01/29	05:55:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/01/29	05:55:28.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2017/01/29	05:55:30.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2017/01/29	05:55:48.0	XRT_FLD_DIS_406_OG [0x196]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2017/01/29	05:58:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2017/01/29	05:58:26.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2017/01/29	05:58:28.0	XRT_QT_PROG_SET_432_OG [0x1b0]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2017/01/29	05:58:30.5	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/01/29	06:05:30.0	AOCS_ORe-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	03 00 00 00 00
2017/01/29	06:20:00.0	XRT_CTRL_MANU_447_OG [0x1bf]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/01/29	06:20:30.0	XRT_TCIB_XRT_S_HTR_A_ENA_441_OG [0x1b9]			
		TCIB_XRT_S_HTR_A_ENA	0	04-BC	
2017/01/29	12:05:30.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	01 00 00 00 00
2017/01/29	17:58:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2017/01/29	18:08:00.0	AOCS_ORe-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	03 00 00 00 00
2017/01/30	00:08:00.0	AOCS_ORe-point_Start_5_OG [0x09b]			
		AOCU_NM	5	02-76	00 ee 7f af cf
2017/01/30	18:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2017/01/30	18:10:00.0	AOCS_ORe-point_Start_6_OG [0x09c]			
		AOCU_NM	5	02-76	00 ef e8 af cf
2017/01/31	12:45:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00