XRT Timeline to be uploaded on 2017/03/14

Period: 2017/03/14 09:56:00 - 2017/03/18 10:19:00

* * * * * **Normal mode** * * * * *

XOB #1AF4: CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh (512ms), Al/Poly (1443ms) - w leak image-3 ms

Term	Pointing (x, y)	Comment
03/15 12:13:00 - 03/15 12:19:54	Fixed (-528.4, 528.4)	Post-Bakeout Quadrant Pointing #1

PROG= 19 1-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 3 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 55 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 3 2-time(s) 2.0sec

Seqn= 54 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1AF2: CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh (512ms), Al/Poly (1443ms) - w leak image-3 ms

Term	Pointing (x, y)	Comment
03/15 12:23:00 - 03/15 12:29:54	Fixed (528.4, -538.4)	Post-Bakeout Quadrant Pointing #2

PROG= 13 1-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 15 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 55 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 3 2-time(s) 2.0sec

Seqn= 54 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1AF3: CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - Q90 - 3rd Quadrant - Al/mesh (512ms), Al/Poly (1443ms) - w leak image-3 ms

Term	Pointing (x, y)	Comment
03/15 12:33:00 - 03/15 12:39:54	Fixed (528.4, 528.4)	Post-Bakeout Quadrant Pointing #3

PROG= 02 1-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 35 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 55 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 3 2-time(s) 2.0sec

Seqn= 54 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1AF1: CCD Monitor During Bakeout - G-band 3ms - 1kx1k - Q90 - 1st Quadrant -Al/mesh(512ms), Al/Poly(1443ms) - w leak image-3ms

Term	Pointing (x, y)	Comment
03/15 12:43:00 - 03/15 12:49:54	Fixed (-528.4, -528.4)	Post-Bakeout Quadrant Pointing #4

PROG= 08 1-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 86 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 55 2-time(s) 2.0sec													
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 3 2-time(s) 2.0sec													
Seqn= 54 1-time(s) 2.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval	

XOB #1B4D: HOP81/206 2-filter - Al/poly 8s, Al/mesh 6s 60s cadence, G-band - 384x384 3ms

Term	Pointing (x, y)	Comment
03/15 12:53:00 - 03/15 19:09:54	Fixed (-22.0, -953.0)	HOP 206 S-Pole
03/15 19:43:00 - 03/16 06:19:24	Track (188.2, 765.7) @ 03/15 19:40:00	IRIS support CH boundary (near N-Pole)

PROG= 16 Inf.-time(s)													
Subr= 1 1-time(s) 2.0sec													
Seqn= 9 2-time(s) 2.0sec													
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 7 1-time(s) 30.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Subr= 3 30-time(s) 2.0sec													
Seqn= 24 1-time(s) 60.0sec													
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	8.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval	

XOB #1B6D: Synoptic Q95 2x2 - Al/mesh(512/2048/4096) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(512/4096/8192

Term	Pointing (x, y)	Comment
03/15 19:13:00 - 03/15 19:39:54	Fixed (0.0, 0.0)	Synoptic + SOT focus test
03/16 06:22:30 - 03/16 06:29:24	Fixed (0.0, 0.0)	synoptic, shifted 19.5 min

PROG= 14 1-time(s)													
Subr= 1 1-time(s) 2.0sec													
Seqn= 5 1-time(s) 2.0sec													
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 73 1-time(s) 2.0sec													
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 44 1-time(s) 2.0sec													
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 76 1-time(s) 2.0sec													
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	16.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54 1-time(s) 2.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval	

XOB #1B6F: CME watch - 4x4 - AEC 2/3 - 2-filter (Be-thin, Al-poly) - G-band (2x2,1ms) - Leak (2x2,1ms) - 60s cad (G-band/Leak first)

Term	Pointing (x, y)	Comment
03/16 06:32:30 - 03/16 10:21:30	Fixed (-930.0, 200.0)	Pointing for an upcoming AR on the E-limb

PROG= 03 Inf.-time(s)													
Subr= 1 1-time(s) 2.0sec													
Seqn= 26 1-time(s) 2.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 80-time(s) 60.0sec													
Seqn= 8 1-time(s) 2.0sec													
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 6 1-time(s) 2.0sec													
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval	

* * * * * **Flare mode** * * * * *

XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment

03/15 12:53:00 - 03/15 19:09:54	Fixed (-22.0, -953.0)	HOP 206 S-Pole
03/15 19:43:00 - 03/16 06:19:24	Track (188.2, 765.7) @ 03/15 19:40:00	IRIS support CH boundary (near N-Pole)
03/16 06:32:30 - 03/16 10:21:30	Fixed (-930.0, 200.0)	Pointing for an upcoming AR on the E-limb

PROG= 07 30-time(s)

Subr= 1 20-time(s) 2.0sec

Seqn= 11 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec

Seqn=100 1-time(s) 10.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 10 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Seqn= 11 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec

Seqn= 84 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec

* * * * * Active Region Search * * * * *

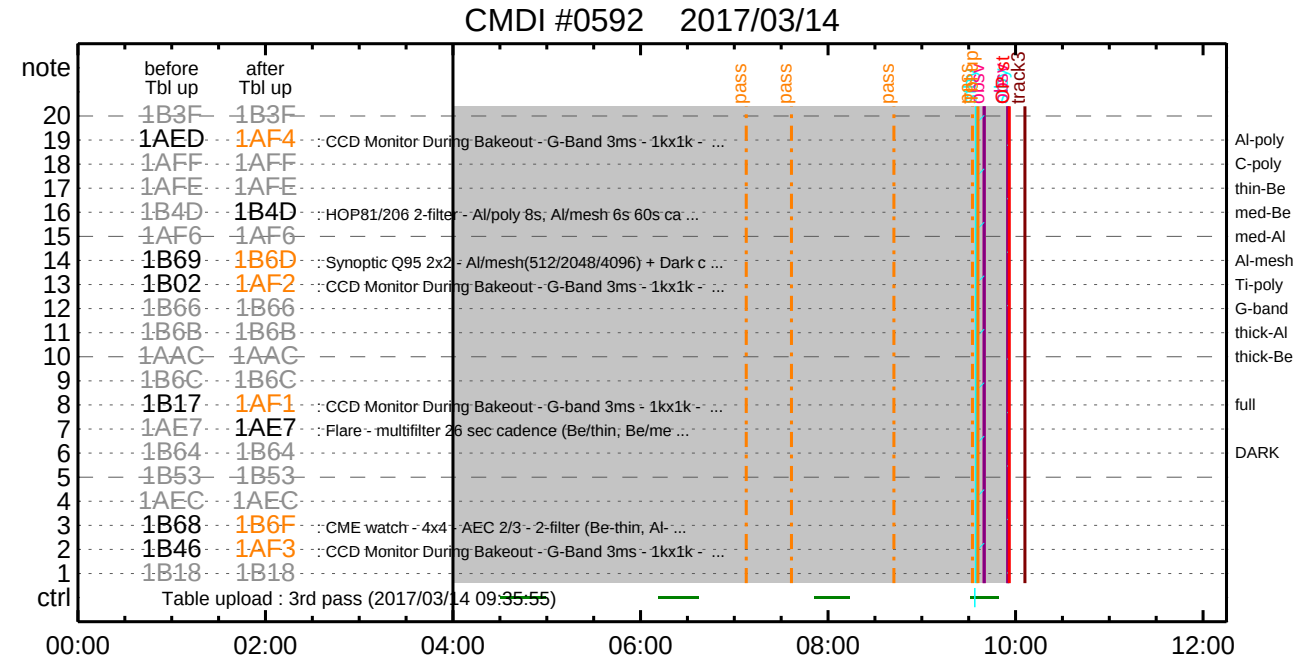
NOT USED

* * * * * Flare Detection * * * * *

FLD Patrol

Term	Pointing (x, y)	Comment
03/15 12:50:18 - 03/15 19:10:18	Fixed (-22.0, -953.0)	HOP 206 S-Pole
03/15 19:40:18 - 03/16 06:19:48	Track (188.2, 765.7) @ 03/15 19:40:00	IRIS support CH boundary (near N-Pole)
03/16 06:29:48 - 03/18 10:19:00	Fixed (-930.0, 200.0)	Pointing for an upcoming AR on the E-limb

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec


CMDI #0592 2017/03/14
note
before Tbl up
after Tbl up
20 1B3F 1B3F
19 1AED 1AF4 : CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - ...
18 1AFF 1AFF
17 1AFE 1AFE
16 1B4D 1B4D : HOP81/206 2-filter - Al/poly 8s, Al/mesh 6s 60s ca ...
15 1AF6 1AF6
14 1B69 1B6D : Synoptic Q95 2x2 - Al/mesh(512/2048/4096) + Dark c ...
13 1B02 1AF2 : CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - ...
12 1B66 1B66
11 1B6B 1B6B
10 1AAC 1AAC
9 1B6C 1B6C
8 1B17 1AF1 : CCD Monitor During Bakeout - G-band 3ms - 1kx1k - ...
7 1AE7 1AE7 : Flare - multifilter 26 sec cadence (Be/thin, Be/me ...
6 1B64 1B64
5 1B53 1B53
4 1AEC 1AEC
3 1B68 1B6F : CME watch - 4x4 - AEC 2/3 - 2-filter (Be-thin, Al- ...
2 1B46 1AF3 : CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - ...
1 1B18 1B18
ctrl
Table upload : 3rd pass (2017/03/14 09:35:55)
pass
pass
pass
track3
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
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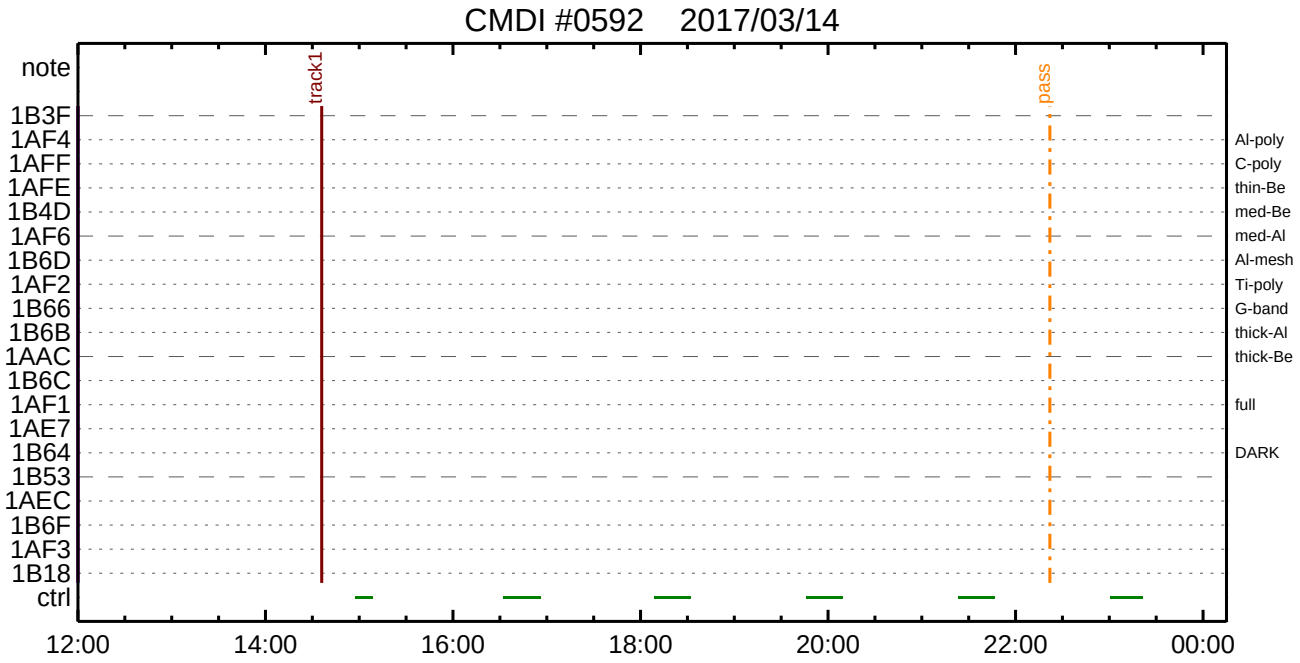
08:00
10:00
12:00


CMDI #0592 2017/03/14
note
1B3F
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1AFF
1AFE
1B4D
1AF6
1B6D
1AF2
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1B6B
1AAC
1B6C
1AF1
1AE7
1B64
1B53
1AEC
1B6F
1AF3
1B18
ctrl
track1
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
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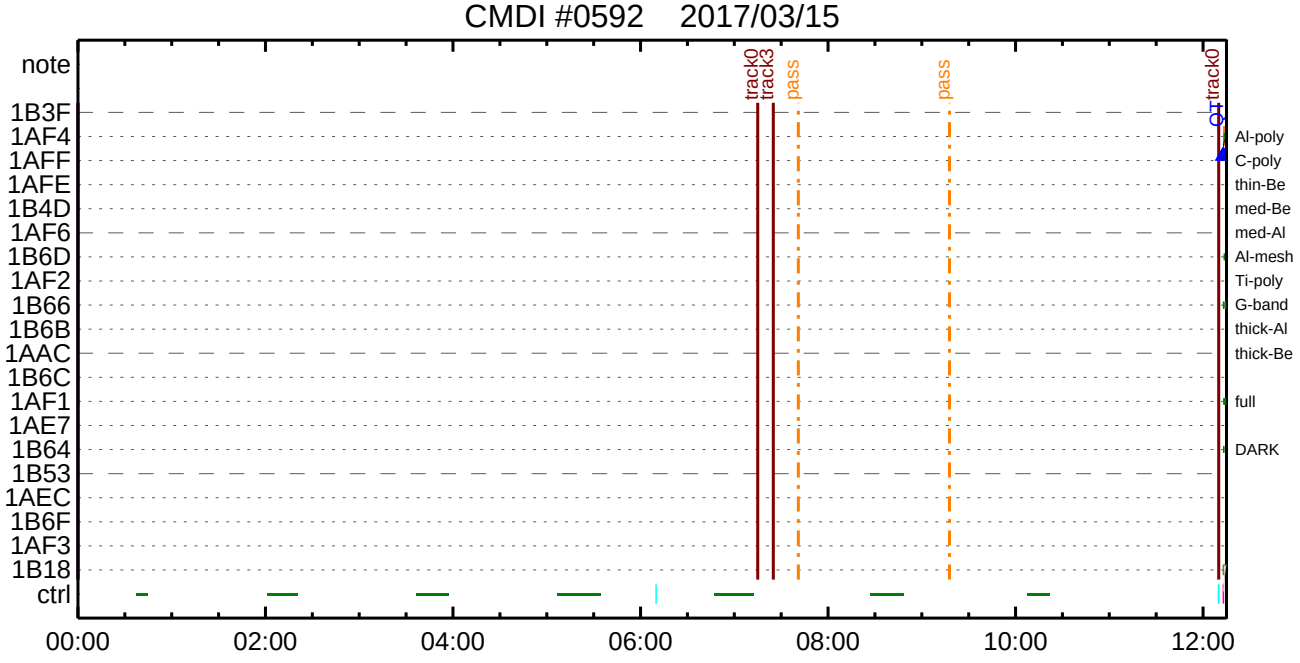
CMDI #0592 2017/03/15
note
1B3F
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1B4D
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1B6D
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1B6F
1AF3
1B18
ctrl
track0
track3
pass
pass
track0
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
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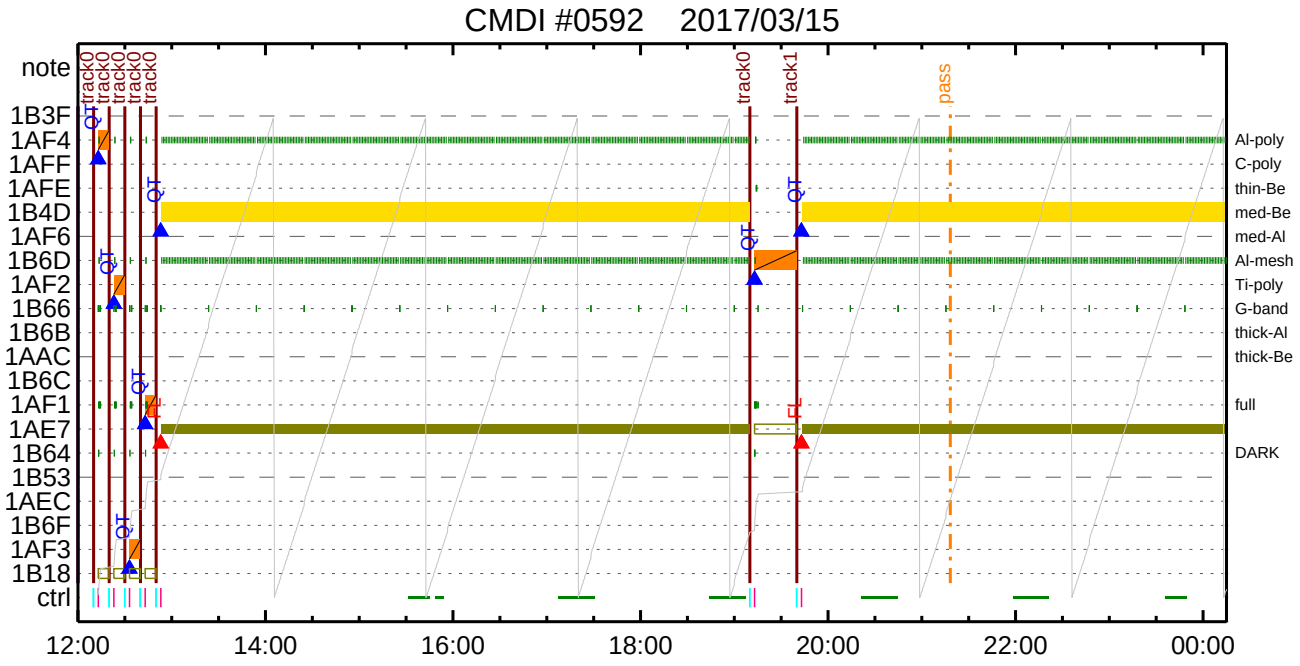
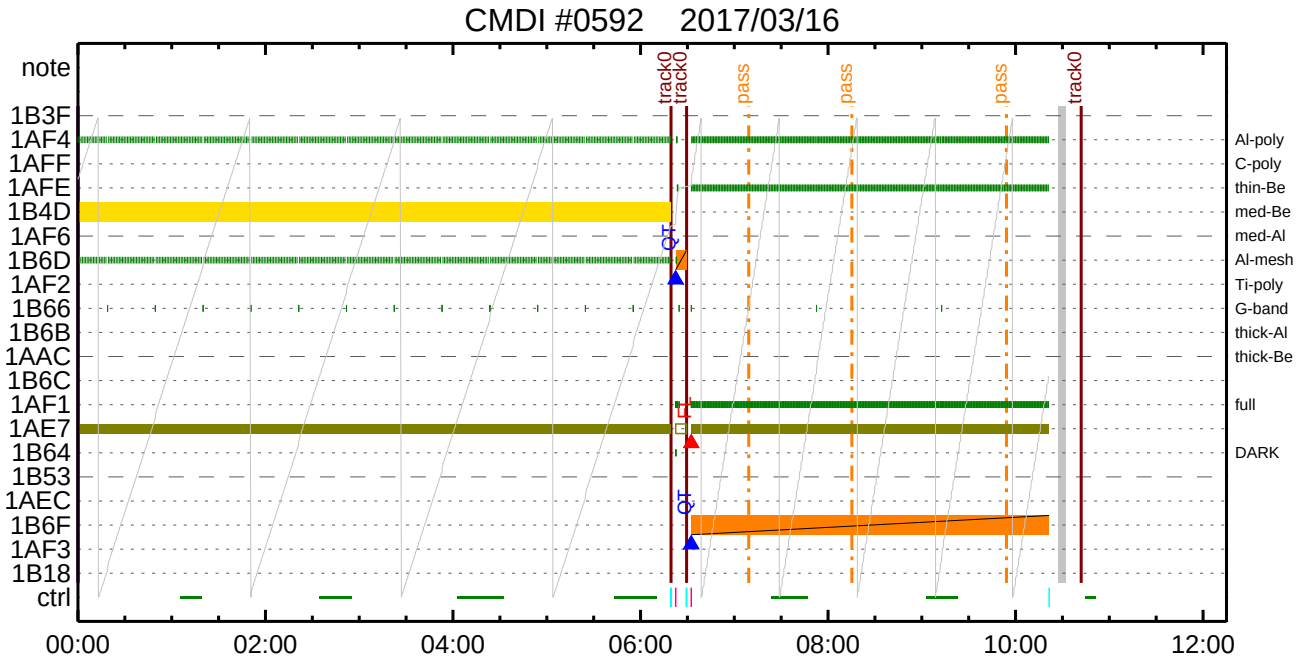
10:00
12:00


CMDI #0592 2017/03/15
note
1B3F
1AF4
1AFF
1AFE
1B4D
1AF6
1B6D
1AF2
1B66
1B6B
1AAC
1B6C
1AF1
1AE7
1B64
1B53
1AEC
1B6F
1AF3
1B18
ctrl
track0
track1
pass
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0592 2017/03/16
note
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1AFE
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1AF6
1B6D
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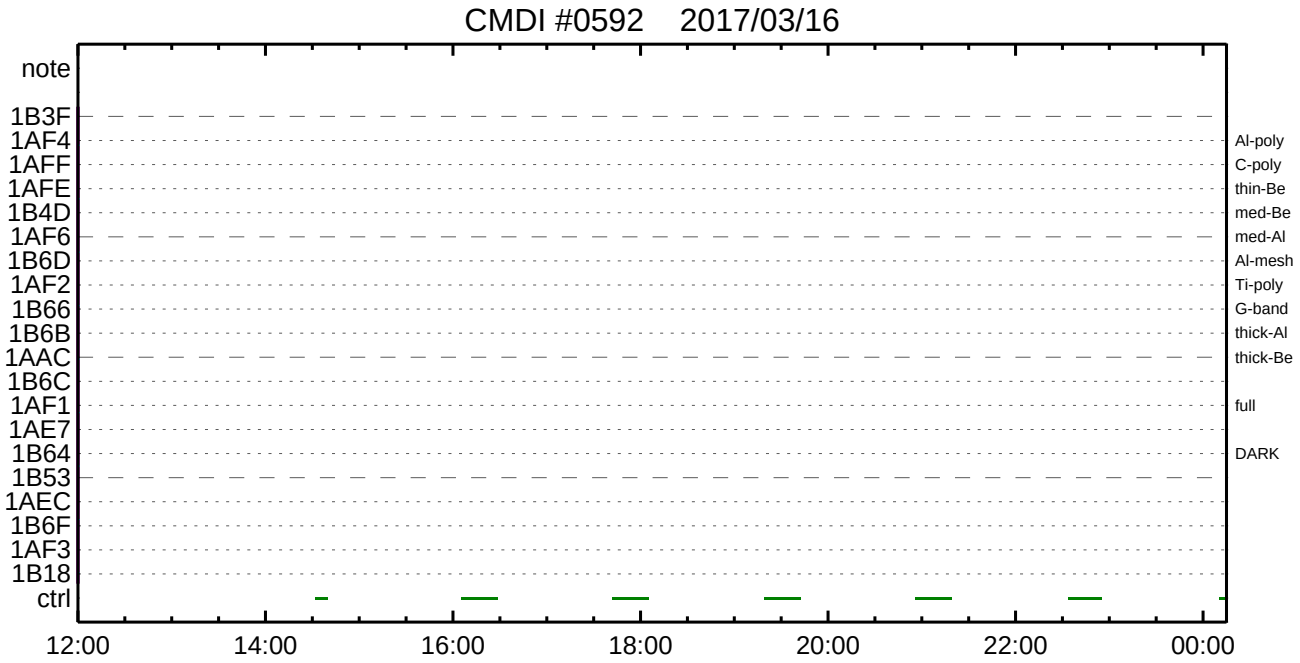
1B6C
1AF1
1AE7
1B64
1B53
1AEC
1B6F
1AF3
1B18
ctrl
track0
pass
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0592 2017/03/16
note
1B3F
1AF4
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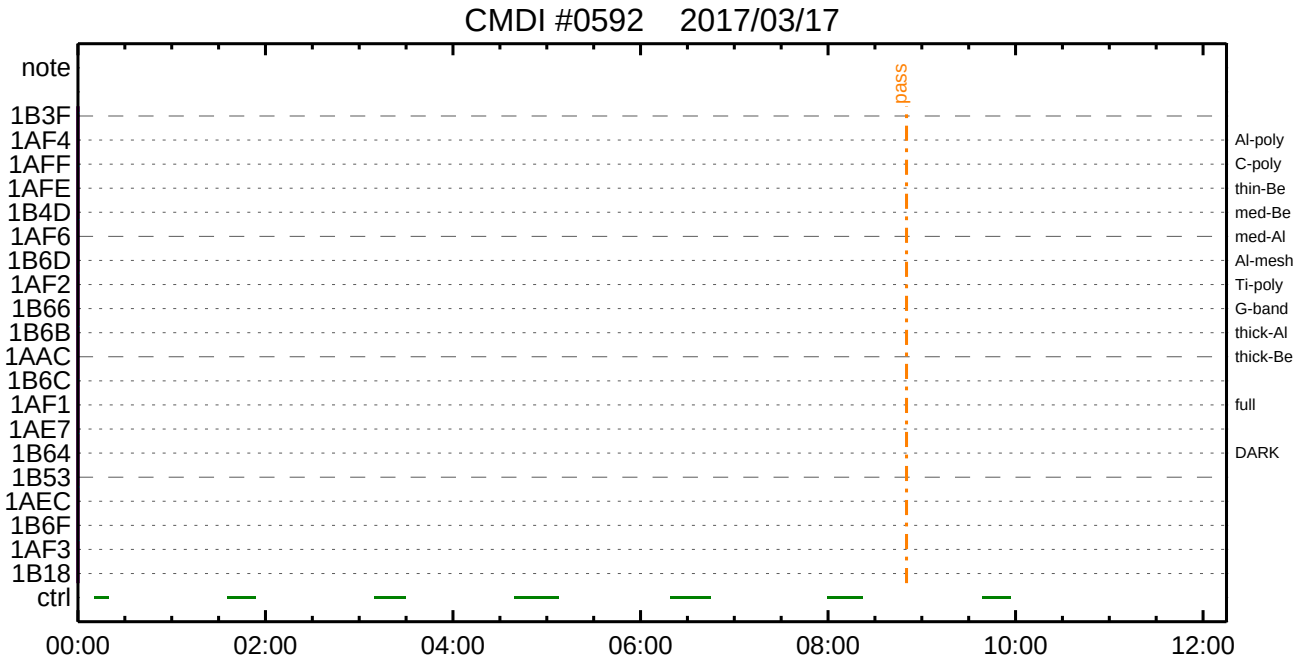
1AF2
1B66
1B6B
1AAC
1B6C
1AF1
1AE7
1B64
1B53
1AEC
1B6F
1AF3
1B18
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0592 2017/03/17
note
1B3F
1AF4
1AFF
1AFE
1B4D
1AF6
1B6D
1AF2
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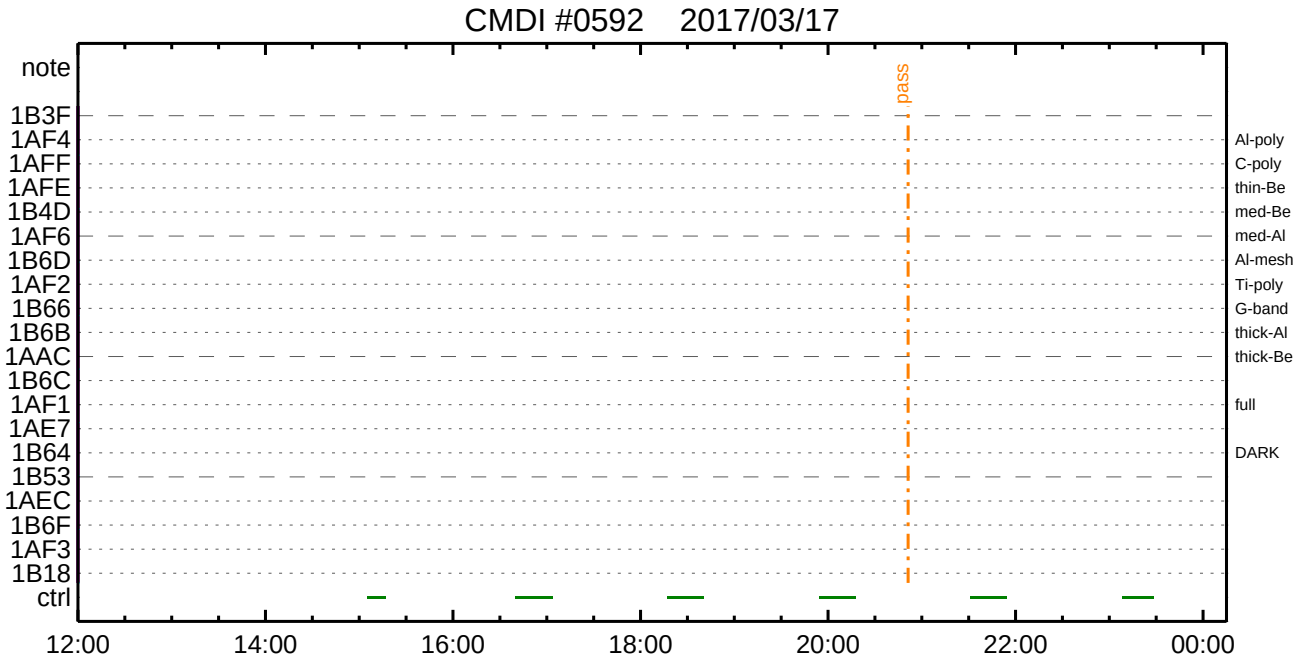
1B6B
1AAC
1B6C
1AF1
1AE7
1B64
1B53
1AEC
1B6F
1AF3
1B18
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0592 2017/03/17
note
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1AF4
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1AF1
1AE7
1B64
1B53
1AEC
1B6F
1AF3
1B18
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
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thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
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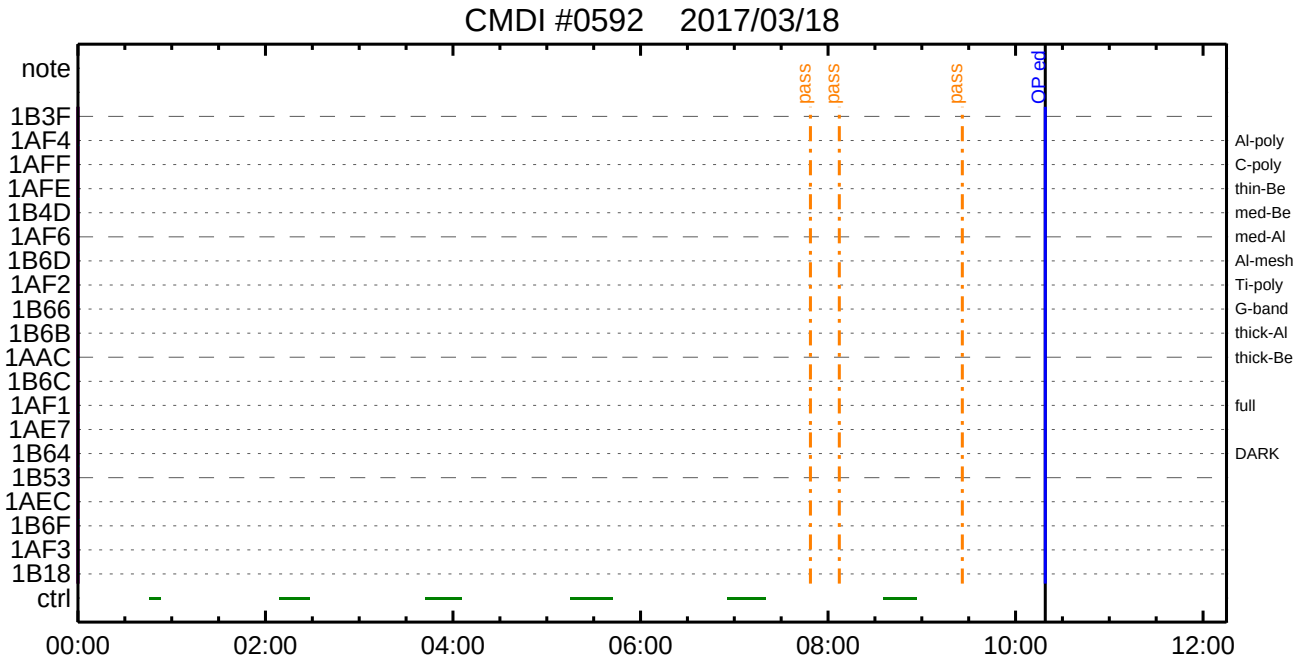
CMDI #0592 2017/03/18
note
1B3F
1AF4
1AFF
1AFE
1B4D
1AF6
1B6D
1AF2
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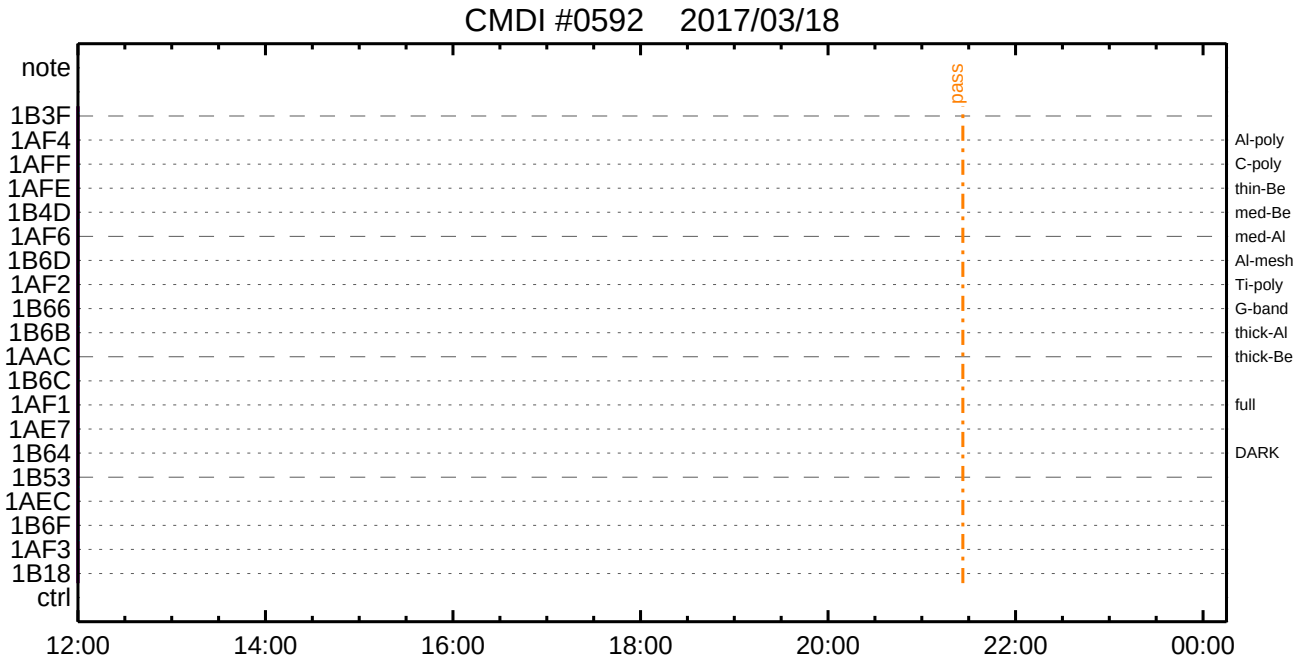
1AAC
1B6C
1AF1
1AE7
1B64
1B53
1AEC
1B6F
1AF3
1B18
ctrl
pass
pass
pass
OP end
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0592 2017/03/18
note
1B3F
1AF4
1AFF
1AFE
1B4D
1AF6
1B6D
1AF2
1B66
1B6B
1AAC
1B6C
1AF1
1AE7
1B64
1B53
1AEC
1B6F
1AF3
1B18
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

```
(a) Spacecraft Operation Procedure (real-commands)

main-476 2017-03-14 13:25:53 205 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0016     C. ****************************
0017     C.
0018   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0019   . S. OP    op-476:OP
0020     ()
0021   . S. OG    og-476:OG
0022     ()
0023     C.
0024   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0025     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0026   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027     BC       (20 00 7f 01 02)
0028     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0029     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0030     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0031     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0032     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0033   +. DC 01-22 DHU_MODE_CHNG
0034     BC       (07 0b f8)
0035     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0036     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0037     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0038     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0039     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0040   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0041     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0042   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0043     C.
0044     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0045   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046     BC       (20 80 7f 01 02)
0047     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0048     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0049     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0050     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0051     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0052   +. DC 01-22 DHU_MODE_CHNG
0053     BC       (07 0b f8)
0054     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0055     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0056     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0057     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0058     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0059   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0060     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0061   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0062     C.
0063     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065     BC       (21 00 41 01 02)
0066     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0067     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0068     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0069     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0070     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0071   +. DC 01-22 DHU_MODE_CHNG
0072     BC       (07 0b f8)
0073     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0074     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0075     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0076     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0077     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0078   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0079     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0080   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0081     C.
0082   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0083     C. DHU¥â¡¼¥É¡Ê¼ý¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0084   +. DC 01-22 DHU_MODE_CHNG
0085     BC       (02 0a f8)
0086     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     2
0087     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0088     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0089     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0090     C.
0091   . C. ********************************
0092     C. TI-CMD SET (OPOG STOP/COPY/START)
0093     C. ********************************
0094     C.
0095   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
```

```
0096     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¹¤Ç¹Î¤¦¤³¤È¡£
0097     C.
0098   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
0099   +. TI 2017-03-14 09:51:00.0
0100     DC 01-B3 DHU_OP_STOP
0101     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0102     C.
0103   +. TI 2017-03-14 09:51:01.0
0104     DC 01-B4 DHU_OP_COPY
0105     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0106     C.
0107   +. TI 2017-03-14 09:51:01.0
0108     DC 01-B5 DHU_OPOG_COPY
0109     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0110     C.
0111   +. TI 2017-03-14 09:55:59.5
0112     DC 01-B2 DHU_OP_START
0113     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0114     C.
0115     C. °Ê²¼¤ÏÄê¼ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0116     C.                      ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0117     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     4
0118     C.                      ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0119     C.                      ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0120     C.
0121   . C. ***************************
0122     C. TIÎÎ°è¥À¥ó¥×
0123     C. ***************************
0124     C.
0125     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0126   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127     BC       (03 ab 03 01 02)
0128     C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0129     C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0130     C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0131     C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0132     C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0133   +. DC 01-22 DHU_MODE_CHNG
0134     BC       (07 0b f8)
0135     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
0136     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0137     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0138     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0139     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0140     C.
0141   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0142     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0143     C.
0144   . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0145     C.
0146   . C. DHU¥â¡¼¥É¡Ê¼ý¸¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0147   +. DC 01-22 DHU_MODE_CHNG
0148     BC       (02 0a f8)
0149     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     2
0150     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0151     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0152     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0153     C.
0154     C. ********************************
0155     C.  SOT TI command set
0156     C. ********************************
0157     C. Execute, after the success of OP upload.
0158   +. TI 2017-03-14 09:55:16.0
0159     DC 07-F0 MDP_SOT_MODE_STBY
0160     BC       (41)
0161   . C. ------------------------------------
0162     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0163     C. ------------------------------------
0164     C. ************ SOT END ************
0165   . C. Stop EIS observation and temporarily disable EIS mode changes
0166     C.
0167     C.
0168     C. ********** Start EIS operation (TI set) **********
0169     C. Execute, after the success of OP upload.
0170     C. Set EIS TI-commands
0171   +. TI 2017-03-14 09:55:30.0
0172     DC 07-FC EIS_MODE_MANU
0173     BC       (21 02)
0174   +. TI 2017-03-14 09:55:40.0
0175     DC 07-FC EIS_MODE_CHG_DIS
0176     BC       (22)
0177   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0178     C. ************ End EIS operation (TI set) ************
0179     C.
0180     C.
0181     C.
0182     C. ************ XRT START ************
0183     C. Execute, after the success of OP upload.
0184   +. TI 2017-03-14 09:55:00.0
0185     DC 07-F0 MDP_XRT_MODE_STBY
0186     BC       (c3)
0187   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0188     C.
0189     C. ************ XRT END ************
0190     C.
0191   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0192     C. (¾å°Ì¥Ó¥Ã¥Ë¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0193   . S. DC-BC dcbc-402:DCBC
```

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0194     (MDP_known_event)
0195     C.
0196     C.
0197   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0198   . S. DC-BC dcbc-153:DCBC
0199     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200     C.
0201     C.
0202   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0203     C.
0204   . C. ********** LOS **********
0205     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-477 2017-03-14 13:25:53 178 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0099     C. Upload the Orbit Element and the Target Attitude
0100     C. RAM-ID:TARGET_ATT
0101   . S. RAM   ram-150:TARGET_ATT
0102     ()
0103     C.
0104     C.
0105     C. Set the dump memory area of TARGET_ATT
0106   +. DC 02-48 AOCU_DUMP_SET
0107     BC       (07 00 00 00 18 00)
0108     C.
0109     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110     C.
0111     C.
0112     C. Change the TLMFormatNo for the AOCS Dump Format
0113   +. DC 01-22 DHU_MODE_CHNG
0114     BC       (04 0b f8)
0115     C.
0116     C. Wait for AOCSDUMP to end
0117     C.
0118   . C. Check the dump memory
0119     C.
0120     C.                                      Result = OK [ ]
0121     C.
0122   +. DC 01-22 DHU_MODE_CHNG
0123     BC       (02 0a f8)
0124     C.
0125     C.                         <A_***>[TLM STS] FMT = 2 [ ]
0126     C.
0127   +. DC 02-8E AOCU_ORB_UPD
0128    . C.
0129    . C. ***** AOCS Commands (Orbital Element Update) *****
0130     C. Update the orbital element
0131   +. DC 02-50 AOCU_ORB_PRPGT_START
0132     BC       (16)
0133   +  DC 02-8E AOCU_ORB_UPD
0134     C.
0135     C.       <A_ORB>[ORBIT] EPC = 2992506.3 +- 1.0 (s) [ ]
0136     C.
0137    . C.
0138    . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139   +. DC 07-FC EIS_MODE_CHG_ENA
0140     BC       (20)
0141    . C. Verify EIS_MODE_CHG_FLG is ENA
0142   +. DC 07-FC EIS_MODE_MANU
0143     BC       (21 02)
0144    . C. Verify EIS in MANUAL mode
0145    . C. Estimated OBSTBL upload time is 32s
0146     C. ********************************
0147     C. EIS START OBSTBL LOAD
0148     C. ********************************
0149    . S. RAM   ram-820:EIS_OBSTBL
0150     ()
0151   +. DC 07-FC EIS_DUMP_OBSTBL
0152     BC       (07 07 07 00 00 70 00)
0153     C.
0154     C. Execute, after the success of OBSTBL upload.
0155     C. Set EIS TI-commands
0156   +. TI 2017-03-14 09:55:50.0
0157     DC 07-FC EIS_MODE_CHG_ENA
0158     BC       (20)
0159    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0160     C. ********************************
0161     C. EIS END OBSTBL LOAD
0162     C. ********************************
0163     C.
0164    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0165     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0166    . S. DC-BC dcbc-402:DCBC
0167     (MDP_known_event)
0168     C.
0169     C.
0170    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0171    . S. DC-BC dcbc-153:DCBC
0172     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0173     C.
0174     C.
0175    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0176     C.
0177    . C. ********** LOS **********
0178     C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-478 2017-03-14 13:25:53 130 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¼É¡ÊËè½µ•îÍË¡Ë¤¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ********************************
0015     C.  SOT table upload
0016     C. ********************************
0017   . C. < Stop SP table >
0018   +. DC 07-F0 MDP_SP_CTRL_MANU
0019     BC       (61)
0020     C. ------------------------------------
0021     C.  MDP_SP_CTRL_MODE      = MANU    [ ]
0022     C. ------------------------------------
0023     C.
0024   . C. <Upload SP Observation Table>
0025   . S. RAM   ram-284:MDP_OBS_S
0026     ()
0027     C.
0028   . C. < Dump RAMID=MDP_OBS_S >
0029   +. DC 07-F0 MDP_DUMP_SPTBL
0030     BC       (83 07 00 00 00 38 b8)
0031     C. ------------------------------------
0032     C.  MDP_OBS_S verify = OK/NG        [ ]
0033     C. ------------------------------------
0034     C.
0035     C. ********************************
0036     C.  SOT TI command set
0037     C. ********************************
0038     C. Execute, after the success of TBL upload.
0039   +. TI 2017-03-14 09:55:18.0
0040     DC 07-F0 MDP_SOT_MODE_OBSV
0041     BC       (40)
0042   . C. ------------------------------------
0043     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0044     C. ------------------------------------
0045     C.
0046     C.
0047     C. ************ XRT START ************
0048     C.
0049   +. DC 07-F0 MDP_XRT_CTRL_MANU
0050     BC       (c1)
0051   +  DC 07-F0 MDP_XRT_MODE_STBY
0052     BC       (c3)
0053   . C. ----------- Success Verify ?             OK / NG____
0054     C.
0055     C. XRT Obs. Table Upload
0056   . S. RAM   ram-291:MDP_OBS_X
0057     ()
0058     C.
0059   +. DC 07-F0 MDP_DUMP_XRTTBL
0060     BC       (84 07 00 00 00 3a d4)
0061   . C. ----------- Comparison Check ?                      OK / ERR ____
0062     C.
0063     C.
0064   +. DC 07-F0 MDP_XRT_ROI_SET
0065     BC       (cd 01 b1 b1 04 04)
0066   +  DC 07-F0 MDP_XRT_ROI_SET
0067     BC       (cd 02 b1 b1 08 08)
0068   +  DC 07-F0 MDP_XRT_ROI_SET
0069     BC       (cd 03 b1 b1 08 08)
0070   +  DC 07-F0 MDP_XRT_ROI_SET
0071     BC       (cd 04 b1 b1 06 06)
0072   +  DC 07-F0 MDP_XRT_ROI_SET
0073     BC       (cd 06 c0 40 10 10)
0074   +  DC 07-F0 MDP_XRT_ROI_SET
0075     BC       (cd 07 80 80 20 20)
0076   +  DC 07-F0 MDP_XRT_ROI_SET
0077     BC       (cd 08 40 c0 10 10)
0078   +  DC 07-F0 MDP_XRT_ROI_SET
0079     BC       (cd 09 40 40 10 10)
0080   +  DC 07-F0 MDP_XRT_ROI_SET
0081     BC       (cd 0a c0 c0 10 10)
0082   +  DC 07-F0 MDP_XRT_ROI_SET
0083     BC       (cd 0b 85 83 06 06)
0084   +  DC 07-F0 MDP_XRT_ROI_SET
0085     BC       (cd 0c 80 80 20 08)
0086   +  DC 07-F0 MDP_XRT_ROI_SET
0087     BC       (cd 0d 80 80 08 20)
0088   +  DC 07-F0 MDP_XRT_ROI_SET
0089     BC       (cd 0f 80 80 06 06)
0090   +  DC 07-F0 MDP_XRT_ROI_SET
0091     BC       (cd 10 80 80 08 08)
0092   +  DC 07-F0 MDP_XRT_FLD_ENA
0093     BC       (d8)
0094   +  DC 07-F0 MDP_XRT_FLRCTRL_ENA
0095     BC       (c8)
```

```
0096  +  DC 07-F0 MDP_XRT_ARS_DIS
0097     BC       (d5)
0098  +  DC 07-F0 MDP_XRT_AEC_RESET
0099     BC       (d0)
0100  +  DC 07-F0 MDP_XRT_FLD_RESET
0101     BC       (da)
0102   . C. ---------- Success Verify ?            OK / NG ____
0103     C.
0104     C.
0105   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0106     C.
0107  +. DC 07-F0 MDP_XRT_MODE_OBSV
0108     BC       (c2)
0109  +. TI 2017-03-14 09:55:02.0
0110     DC 07-F0 MDP_XRT_MODE_OBSV
0111     BC       (c2)
0112   . C. ---------- Success Verify ?            OK / NG ____
0113     C.
0114     C. ************ XRT END ***********
0115     C.
0116   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0117     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¾Â»Ü¤¹¤ë)
0118   . S. DC-BC dcbc-402:DCBC
0119     (MDP_known_event)
0120     C.
0121     C.
0122   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0123   . S. DC-BC dcbc-153:DCBC
0124     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0125     C.
0126     C.
0127   . C. ¡ãLOS¥Á¥§¥Ã¥¯¾Â»Ü¡ä
0128     C.
0129   . C. *********** LOS ***********
0130     C.
```

```
*** OP Sequence for XRT ***

2017/03/14  10:06:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 03 00 00 00 00
2017/03/14  14:36:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 01 00 00 00 00
2017/03/15  06:10:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2017/03/15  06:10:02.0  XRT_TCIB_XRT_S_HTR_A_DIS_404_OG [0x194]
                                TCIB_XRT_S_HTR_A_DIS 0 04-C0
2017/03/15  07:15:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 00 00 00 00 00
2017/03/15  07:25:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 03 00 00 00 00
2017/03/15  12:09:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2017/03/15  12:09:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2017/03/15  12:09:58.0  XRT_FOCUS_POSITION_439_OG [0x1b7]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2017/03/15  12:10:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 00 d1 07 2e f9
2017/03/15  12:10:18.0  XRT_FLD_DIS_428_OG [0x1ac]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2017/03/15  12:10:20.0  XRT_FLRCTRL_DIS_443_OG [0x1bb]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2017/03/15  12:12:56.0  XRT_ARS_DIS_445_OG [0x1bd]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2017/03/15  12:12:58.0  XRT_QT_PROG_SET_444_OG [0x1bc]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 13
2017/03/15  12:13:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2017/03/15  12:19:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2017/03/15  12:19:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2017/03/15  12:19:58.0  XRT_FOCUS_POSITION_439_OG [0x1b7]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2017/03/15  12:20:00.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                AOCU_NM              5 02-76 00 2f df d1 07
2017/03/15  12:20:18.0  XRT_FLD_DIS_428_OG [0x1ac]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2017/03/15  12:20:20.0  XRT_FLRCTRL_DIS_443_OG [0x1bb]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2017/03/15  12:22:56.0  XRT_ARS_DIS_445_OG [0x1bd]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2017/03/15  12:22:58.0  XRT_QT_PROG_SET_401_OG [0x191]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2017/03/15  12:23:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2017/03/15  12:29:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2017/03/15  12:29:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2017/03/15  12:29:58.0  XRT_FOCUS_POSITION_439_OG [0x1b7]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2017/03/15  12:30:00.0  AOCS_ORe-point_Start_6_OG [0x09c]
                                AOCU_NM              5 02-76 00 d1 07 d1 07
2017/03/15  12:30:18.0  XRT_FLD_DIS_428_OG [0x1ac]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2017/03/15  12:30:20.0  XRT_FLRCTRL_DIS_443_OG [0x1bb]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2017/03/15  12:32:56.0  XRT_ARS_DIS_445_OG [0x1bd]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2017/03/15  12:32:58.0  XRT_QT_PROG_SET_422_OG [0x1a6]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 02
2017/03/15  12:33:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2017/03/15  12:39:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2017/03/15  12:39:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2017/03/15  12:39:58.0  XRT_FOCUS_POSITION_439_OG [0x1b7]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2017/03/15  12:40:00.0  AOCS_ORe-point_Start_7_OG [0x09d]
                                AOCU_NM              5 02-76 00 2e f9 2e f9
2017/03/15  12:40:18.0  XRT_FLD_DIS_428_OG [0x1ac]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2017/03/15  12:40:20.0  XRT_FLRCTRL_DIS_443_OG [0x1bb]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2017/03/15  12:42:56.0  XRT_ARS_DIS_445_OG [0x1bd]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2017/03/15  12:42:58.0  XRT_QT_PROG_SET_434_OG [0x1b2]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 08
2017/03/15  12:43:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2017/03/15  12:49:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2017/03/15  12:49:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2017/03/15  12:49:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2017/03/15  12:50:00.0  AOCS_ORe-point_Start_8_OG [0x09e]
```

```
                                    AOCU_NM               5 02-76 00 54 b4 01 f3
2017/03/15  12:50:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                    MDP_XRT_FLD_ENA       1 07-F0 d8
2017/03/15  12:50:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                    MDP_XRT_FLRCTRL_ENA   1 07-F0 c8
2017/03/15  12:50:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                    MDP_XRT_AEC_RESET     1 07-F0 d0
2017/03/15  12:50:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                    MDP_XRT_ARS_DIS       1 07-F0 d5
2017/03/15  12:50:26.0  XRT_FLD_RESET_433_OG [0x1b1]
                                    MDP_XRT_FLD_RESET     1 07-F0 da
2017/03/15  12:52:56.0  XRT_QT_PROG_SET_440_OG [0x1b8]
                                    MDP_XRT_QT_PROG_SET   2 07-F0 c4 10
2017/03/15  12:52:58.0  XRT_FL_PROG_SET_436_OG [0x1b4]
                                    MDP_XRT_FL_PROG_SET   2 07-F0 c5 07
2017/03/15  12:53:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO     1 07-F0 c0
2017/03/15  19:09:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU     1 07-F0 c1
2017/03/15  19:09:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU     1 07-F0 c1
2017/03/15  19:09:58.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                    XRT_FOCUS_POSITION    4 07-F8 22 ff aa 00
2017/03/15  19:10:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                    AOCU_NM               5 02-76 00 00 00 00 00
2017/03/15  19:10:18.0  XRT_FLD_DIS_406_OG [0x196]
                                    MDP_XRT_FLD_DIS       1 07-F0 d9
2017/03/15  19:12:54.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                    MDP_XRT_FLRCTRL_DIS   1 07-F0 c9
2017/03/15  19:12:56.0  XRT_ARS_DIS_423_OG [0x1a7]
                                    MDP_XRT_ARS_DIS       1 07-F0 d5
2017/03/15  19:12:58.0  XRT_QT_PROG_SET_417_OG [0x1a1]
                                    MDP_XRT_QT_PROG_SET   2 07-F0 c4 0e
2017/03/15  19:13:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO     1 07-F0 c0
2017/03/15  19:39:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU     1 07-F0 c1
2017/03/15  19:39:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU     1 07-F0 c1
2017/03/15  19:39:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                    XRT_FOCUS_POSITION    4 07-F8 22 fe 97 00
2017/03/15  19:40:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                    AOCU_NM               5 02-76 01 00 00 00 00
2017/03/15  19:40:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                    MDP_XRT_FLD_ENA       1 07-F0 d8
2017/03/15  19:40:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                    MDP_XRT_FLRCTRL_ENA   1 07-F0 c8
2017/03/15  19:40:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                    MDP_XRT_AEC_RESET     1 07-F0 d0
2017/03/15  19:40:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                    MDP_XRT_ARS_DIS       1 07-F0 d5
2017/03/15  19:40:26.0  XRT_FLD_RESET_433_OG [0x1b1]
                                    MDP_XRT_FLD_RESET     1 07-F0 da
2017/03/15  19:42:56.0  XRT_QT_PROG_SET_440_OG [0x1b8]
                                    MDP_XRT_QT_PROG_SET   2 07-F0 c4 10
2017/03/15  19:42:58.0  XRT_FL_PROG_SET_436_OG [0x1b4]
                                    MDP_XRT_FL_PROG_SET   2 07-F0 c5 07
2017/03/15  19:43:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO     1 07-F0 c0
2017/03/16  06:19:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU     1 07-F0 c1
2017/03/16  06:19:26.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU     1 07-F0 c1
2017/03/16  06:19:28.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                    XRT_FOCUS_POSITION    4 07-F8 22 ff aa 00
2017/03/16  06:19:30.0  AOCS_ORe-point_Start_3_OG [0x099]
                                    AOCU_NM               5 02-76 00 00 00 00 00
2017/03/16  06:19:48.0  XRT_FLD_DIS_406_OG [0x196]
                                    MDP_XRT_FLD_DIS       1 07-F0 d9
2017/03/16  06:22:24.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                    MDP_XRT_FLRCTRL_DIS   1 07-F0 c9
2017/03/16  06:22:26.0  XRT_ARS_DIS_423_OG [0x1a7]
                                    MDP_XRT_ARS_DIS       1 07-F0 d5
2017/03/16  06:22:28.0  XRT_QT_PROG_SET_417_OG [0x1a1]
                                    MDP_XRT_QT_PROG_SET   2 07-F0 c4 0e
2017/03/16  06:22:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO     1 07-F0 c0
2017/03/16  06:29:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU     1 07-F0 c1
2017/03/16  06:29:26.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU     1 07-F0 c1
2017/03/16  06:29:28.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                    XRT_FOCUS_POSITION    4 07-F8 22 ff aa 00
2017/03/16  06:29:30.0  AOCS_ORe-point_Start_9_OG [0x09f]
                                    AOCU_NM               5 02-76 00 ee 36 52 a7
2017/03/16  06:29:48.0  XRT_FLD_ENA_411_OG [0x19b]
                                    MDP_XRT_FLD_ENA       1 07-F0 d8
2017/03/16  06:29:50.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                    MDP_XRT_FLRCTRL_ENA   1 07-F0 c8
2017/03/16  06:29:52.0  XRT_AEC_RESET_448_OG [0x1c0]
                                    MDP_XRT_AEC_RESET     1 07-F0 d0
2017/03/16  06:29:54.0  XRT_ARS_DIS_423_OG [0x1a7]
                                    MDP_XRT_ARS_DIS       1 07-F0 d5
```

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2017/03/16  06:29:56.0  XRT_FLD_RESET_433_OG [0x1b1]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2017/03/16  06:32:26.0  XRT_QT_PROG_SET_407_OG [0x197]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 03
2017/03/16  06:32:28.0  XRT_FL_PROG_SET_436_OG [0x1b4]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 07
2017/03/16  06:32:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2017/03/16  10:21:30.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2017/03/16  10:42:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 00 00 00 00 00
```