

XRT Timeline to be uploaded on 2017/04/25

Period: 2017/04/25 10:11:00 - 2017/04/29 10:54:00

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Normal mode

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XOB #1AF4: CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh (512ms), Al/Poly (1443ms) - w leak image-3 ms												
Term	Pointing (x, y)						Comment					
04/26 12:38:00 - 04/26 12:44:54	Fixed (-528.4, 528.4)						XRT quadra pointing (1/4)					
PROG= 09 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─┬─ Seqn= 3 1-time(s) 2.0sec												
└─┬─┬─ Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec												
└─┬─┬─ Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec												
└─┬─┬─ Open/thick-Be Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec												
└─┬─┬─ Open/thick-Be Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec												
└─┬─ Subr= 2 1-time(s) 2.0sec												
└─┬─┬─ Seqn= 55 2-time(s) 2.0sec												
└─┬─┬─┬─ Open/Al-mesh Open/Ti-poly close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─┬─┬─ Al-poly/Open med-Be/Open close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─ Subr= 3 2-time(s) 2.0sec												
└─┬─┬─ Seqn= 54 1-time(s) 2.0sec												
└─┬─┬─┬─ Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
└─┬─┬─┬─ Open/G-band Open/G-band close Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─ Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1AF2: CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh (512ms), Al/Poly (1443ms) - w leak image-3 ms												
Term	Pointing (x, y)						Comment					
04/26 12:48:00 - 04/26 12:54:54	Fixed (528.4, -528.4)						2/4					
PROG= 16 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─┬─ Seqn= 15 1-time(s) 2.0sec												
└─┬─┬─ Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec												
└─┬─┬─ Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec												
└─┬─┬─ Open/thick-Be Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec												
└─┬─┬─ Open/thick-Be Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec												
└─┬─ Subr= 2 1-time(s) 2.0sec												
└─┬─┬─ Seqn= 55 2-time(s) 2.0sec												
└─┬─┬─┬─ Open/Al-mesh Open/Ti-poly close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─┬─┬─ Al-poly/Open med-Be/Open close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─ Subr= 3 2-time(s) 2.0sec												
└─┬─┬─ Seqn= 54 1-time(s) 2.0sec												
└─┬─┬─┬─ Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
└─┬─┬─┬─ Open/G-band Open/G-band close Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─ Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1AF3: CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - Q90 - 3rd Quadrant - Al/mesh (512ms), Al/Poly (1443ms) - w leak image-3 ms												
Term	Pointing (x, y)						Comment					
04/26 12:58:00 - 04/26 13:04:54	Fixed (528.4, 528.4)						3/4					
PROG= 17 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─┬─ Seqn= 35 1-time(s) 2.0sec												
└─┬─┬─ Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec												
└─┬─┬─ Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec												
└─┬─┬─ Open/thick-Be Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec												
└─┬─┬─ Open/thick-Be Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec												
└─┬─ Subr= 2 1-time(s) 2.0sec												
└─┬─┬─ Seqn= 55 2-time(s) 2.0sec												
└─┬─┬─┬─ Open/Al-mesh Open/Ti-poly close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─┬─┬─ Al-poly/Open med-Be/Open close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─ Subr= 3 2-time(s) 2.0sec												
└─┬─┬─ Seqn= 54 1-time(s) 2.0sec												
└─┬─┬─┬─ Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
└─┬─┬─┬─ Open/G-band Open/G-band close Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─ Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1AF1: CCD Monitor During Bakeout - G-band 3ms - 1kx1k - Q90 - 1st Quadrant -Al/mesh(512ms), Al/Poly(1443ms) - w leak image-3ms												
Term	Pointing (x, y)						Comment					
04/26 13:08:00 - 04/26 13:14:54	Fixed (-528.4, -528.4)						4/4					
PROG= 10 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─┬─ Seqn= 86 1-time(s) 2.0sec												
└─┬─┬─ Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec												
└─┬─┬─ Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec												
└─┬─┬─ Open/thick-Be Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec												
└─┬─┬─ Open/thick-Be Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												

Seqn= 55	2-time(s)	2.0sec																	
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec							
Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec							
Subr= 3	2-time(s)	2.0sec																	
Seqn= 54	1-time(s)	2.0sec																	
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec							
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec							
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval								

XOB #1AFF: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
04/26 13:22:00 - 04/26 13:50:00	Track (438.9, 221.5) ^{® 04/26 13:15:00}	AR12651 obs
PROG= 01	Inf.-time(s)	
Subr= 1	1-time(s)	2.0sec
Seqn= 56	1-time(s)	2.0sec
Open/G-band	Open/G-band	open
Open/G-band	Open/G-band	close
Open/Ti-poly	Open/thick-Al	close
Subr= 2	5-time(s)	2.0sec
Seqn= 75	1-time(s)	2.0sec
Al-poly/Open	thin-Be/Open	close
Al-poly/Open	thin-Be/Open	close
thin-Be/Open	med-Be/Open	close
thin-Be/Open	med-Be/Open	close
Seqn= 96	4-time(s)	60.0sec
Al-poly/Open	thin-Be/Open	close
thin-Be/Open	med-Be/Open	close
Al-poly/Open	thin-Be/Open	close
thin-Be/Open	med-Be/Open	close
Al-poly/Open	thin-Be/Open	close
thin-Be/Open	med-Be/Open	close
Default Filter	Thicker Filter	VLS
mode	image	Exp.
CCD	Bin	ROI: size (center)
Comp.	AEC Buffer	Interval

XOB #1B7D: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
04/26 14:12:36 - 04/26 16:29:54	Fixed (912.3, 30.0)	HOP328 at W-limb
04/26 16:33:00 - 04/26 18:00:24	Track (313.4, 255.0) ^{® 04/26 16:30:00}	HOP311
PROG= 03	Inf.-time(s)	
Subr= 1	1-time(s)	2.0sec
Seqn= 56	1-time(s)	2.0sec
Open/G-band	Open/G-band	open
Open/G-band	Open/G-band	close
Open/Ti-poly	Open/thick-Al	close
Subr= 2	5-time(s)	2.0sec
Seqn= 75	1-time(s)	2.0sec
Al-poly/Open	thin-Be/Open	close
Al-poly/Open	thin-Be/Open	close
thin-Be/Open	med-Be/Open	close
thin-Be/Open	med-Be/Open	close
Seqn= 96	4-time(s)	45.0sec
Al-poly/Open	thin-Be/Open	close
thin-Be/Open	med-Be/Open	close
Al-poly/Open	thin-Be/Open	close
thin-Be/Open	med-Be/Open	close
Al-poly/Open	thin-Be/Open	close
thin-Be/Open	med-Be/Open	close
Default Filter	Thicker Filter	VLS
mode	image	Exp.
CCD	Bin	ROI: size (center)
Comp.	AEC Buffer	Interval

XOB #1B64: Synoptic Q95 2x2 - Al/mesh(64/512/2048) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(45/512/4096) + Ti

Term	Pointing (x, y)	Comment
04/26 18:03:30 - 04/26 18:10:24	Fixed (0.0, 0.0)	synoptic, shifted 0.5 min
04/27 05:34:00 - 04/27 05:40:54	Fixed (0.0, 0.0)	synoptic, shifted -29.0 min
PROG= 11	1-time(s)	
Subr= 1	1-time(s)	2.0sec
Seqn= 5	1-time(s)	2.0sec
Open/Ti-poly	Open/thick-Al	close
Open/Ti-poly	Open/thick-Al	close
Open/Ti-poly	Open/thick-Al	close
Open/Ti-poly	Open/thick-Al	close
Open/Ti-poly	Open/thick-Al	close
Seqn= 27	1-time(s)	2.0sec
Open/Al-mesh	Open/Al-mesh	close
Open/Al-mesh	Open/Al-mesh	close
Open/Al-mesh	Open/Al-mesh	close
Seqn= 99	1-time(s)	2.0sec
Al-poly/Open	Al-poly/Open	close
Al-poly/Open	Al-poly/Open	close

Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 85 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	16.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1B46: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, Al/Poly context, with G-band (3ms/3ms)

Term	Pointing (x, y)	Comment
04/26 18:13:30 - 04/27 05:30:54	Track (478.1, 219.5) @ 04/26 18:10:30	AR12651 obs
04/27 05:44:00 - 04/27 10:04:25	Track (565.2, 214.2) @ 04/27 05:41:00	# Cont,

PROG= 13 Inf-time(s)

Subr= 1 1-time(s) 2.0sec												
Seqn= 56 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 42 3-time(s) 2.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 32 40-time(s) 90.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	34.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	34.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512)

Term	Pointing (x, y)	Comment
04/26 13:22:00 - 04/26 13:50:00	Track (438.9, 221.5) @ 04/26 13:15:00	AR12651 obs
04/26 14:12:36 - 04/26 16:29:54	Fixed (912.3, 30.0)	HOP328 at W-limb
04/26 16:33:00 - 04/26 18:00:24	Track (313.4, 255.0) @ 04/26 16:30:00	HOP311
04/26 18:13:30 - 04/27 05:30:54	Track (478.1, 219.5) @ 04/26 18:10:30	AR12651 obs
04/27 05:44:00 - 04/27 10:04:25	Track (565.2, 214.2) @ 04/27 05:41:00	# Cont,

PROG= 07 30-time(s)

Subr= 1 20-time(s) 2.0sec												
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn=100 1-time(s) 10.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 10 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 84 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

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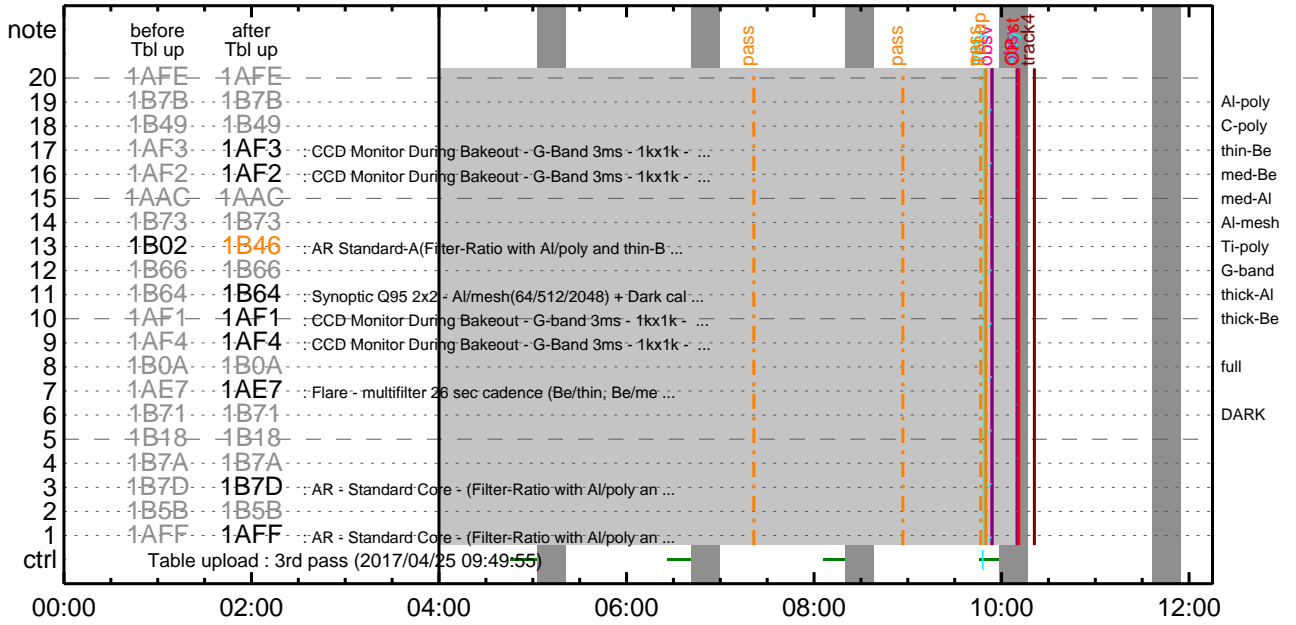
Flare Detection

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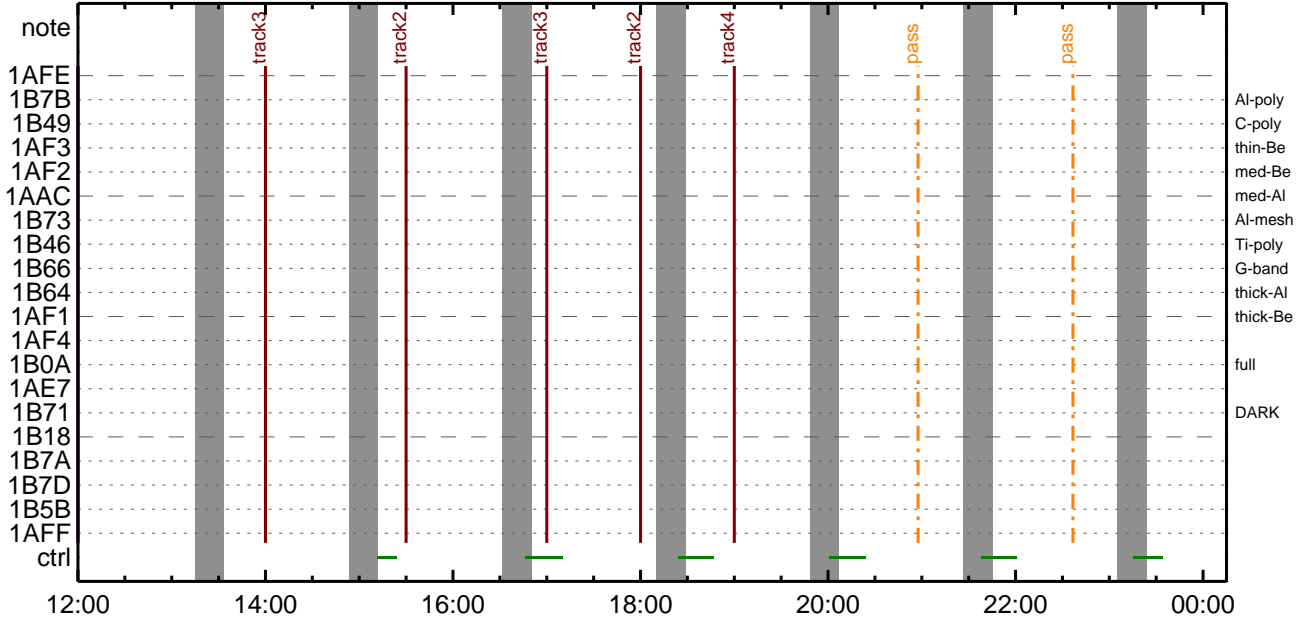
FLD Patrol

Term	Pointing (x, y)	Comment										
04/26 13:19:18 - 04/26 18:00:48	Track (438.9, 221.5) @ 04/26 13:15:00	AR12651 obs										
04/26 18:10:48 - 04/27 05:31:18	Track (478.1, 219.5) @ 04/26 18:10:30	AR12651 obs										
04/27 05:41:18 - 04/29 10:54:00	Track (565.2, 214.2) @ 04/27 05:41:00	# Cont,										
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

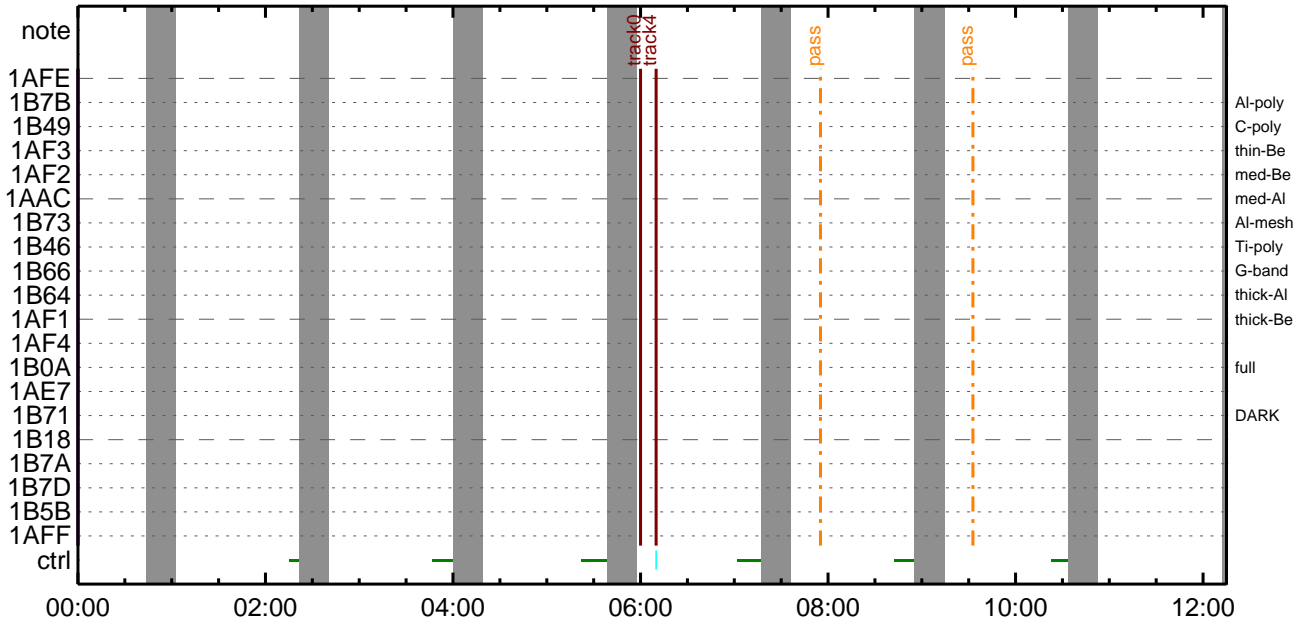
CMDI #0704 2017/04/25



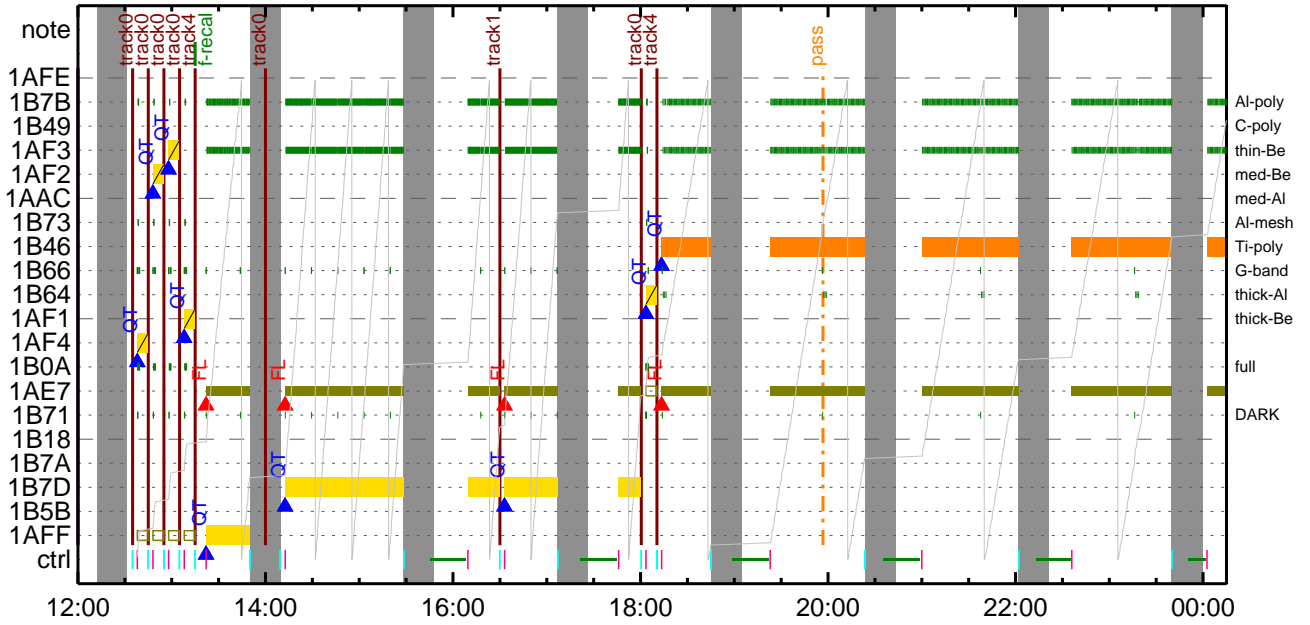
CMDI #0704 2017/04/25



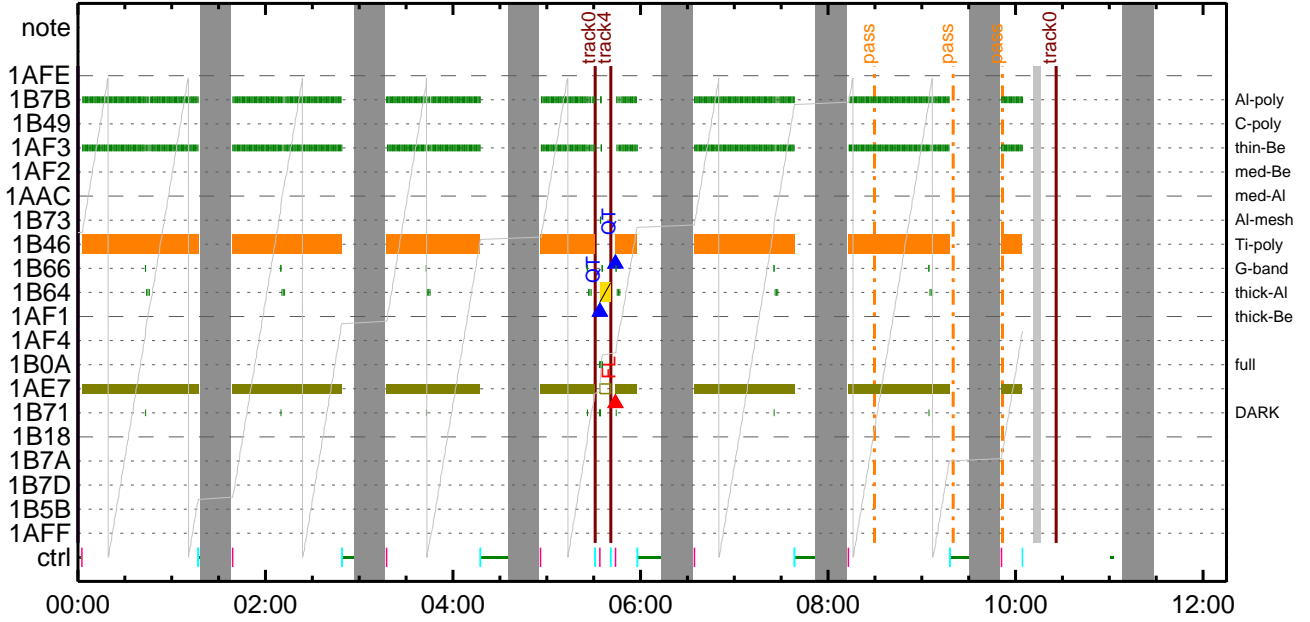
CMDI #0704 2017/04/26



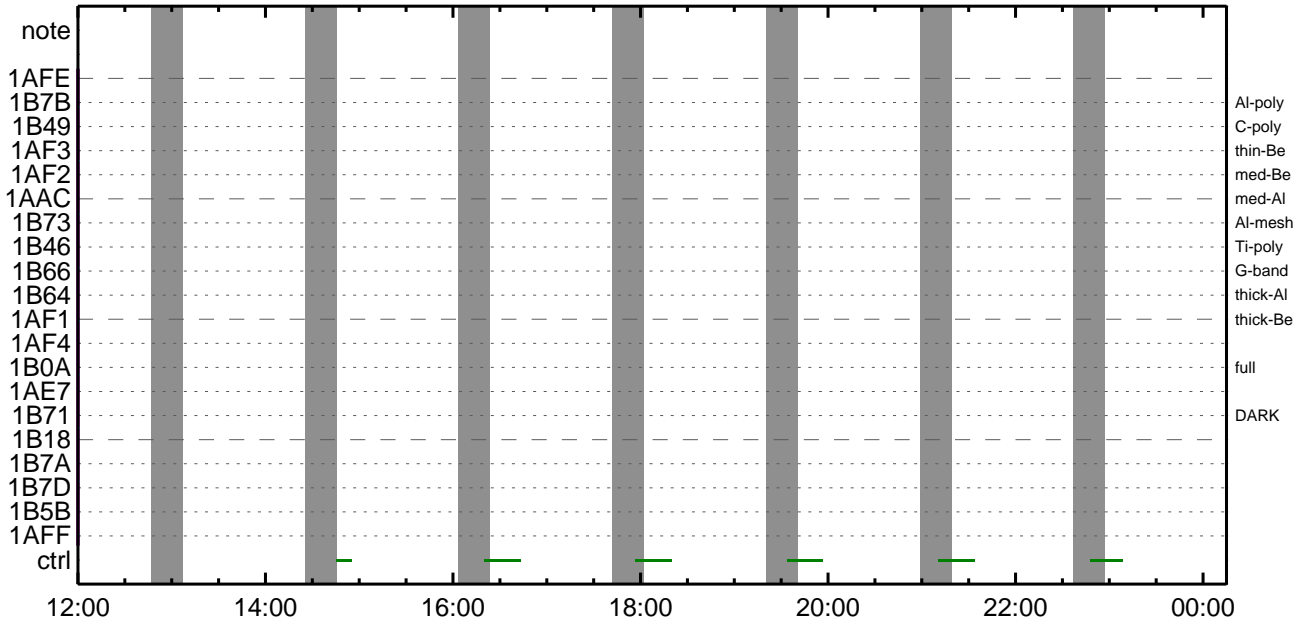
CMDI #0704 2017/04/26



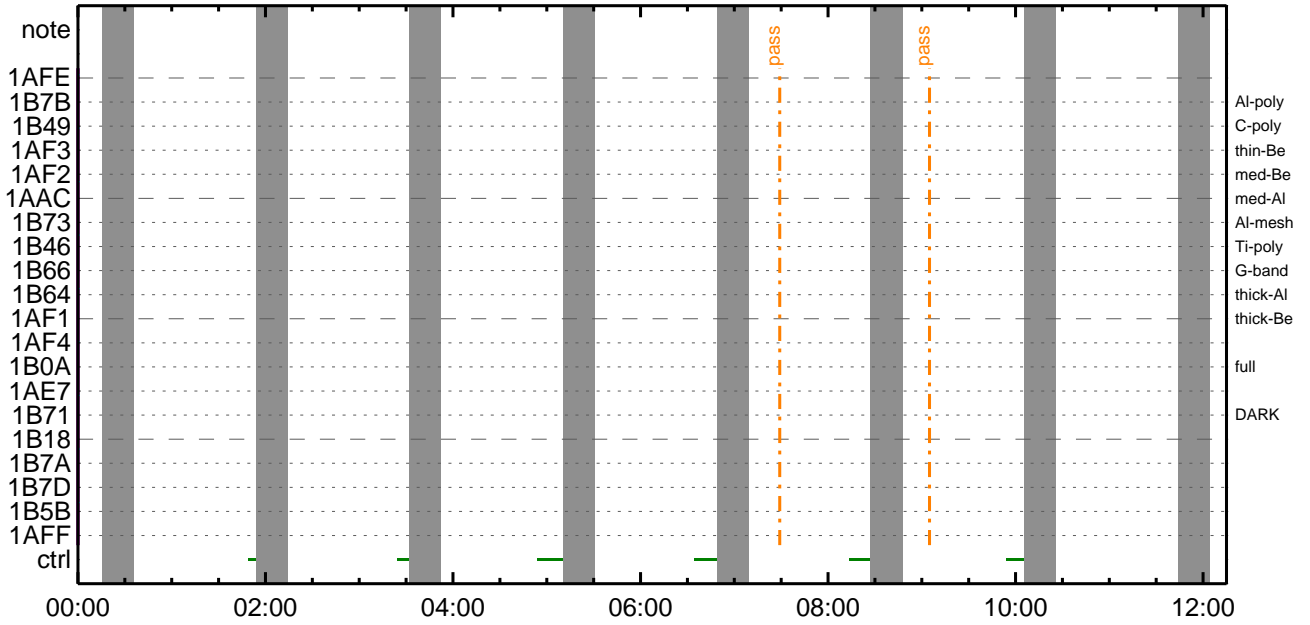
CMDI #0704 2017/04/27



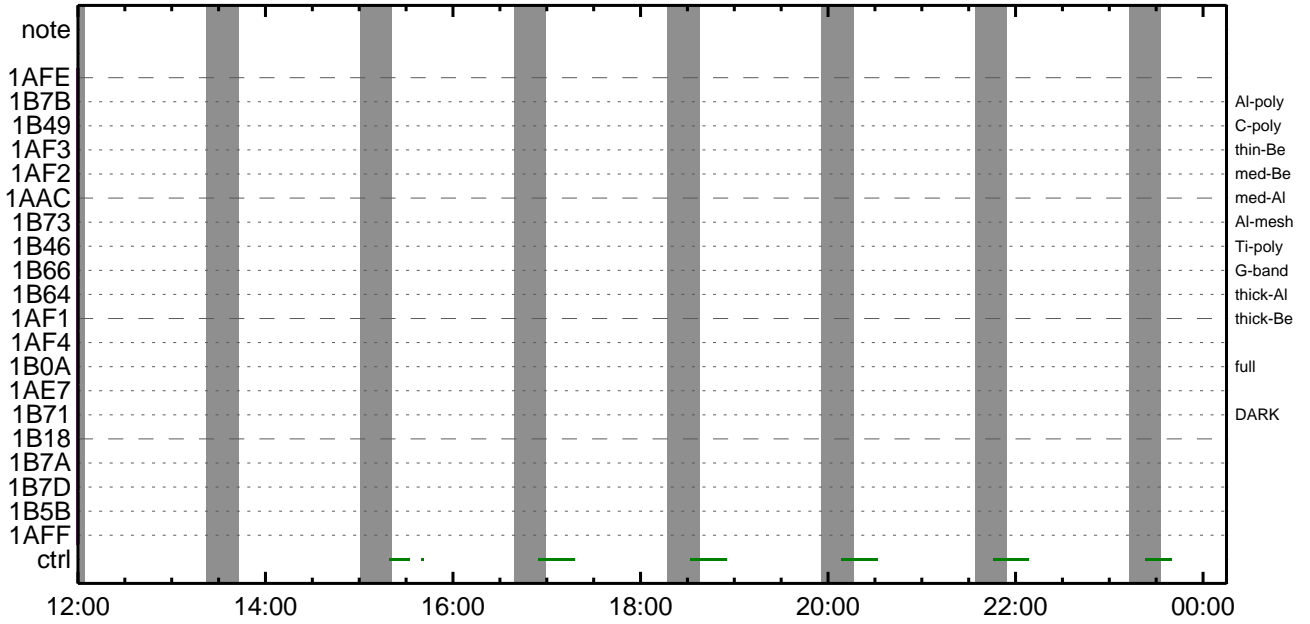
CMDI #0704 2017/04/27



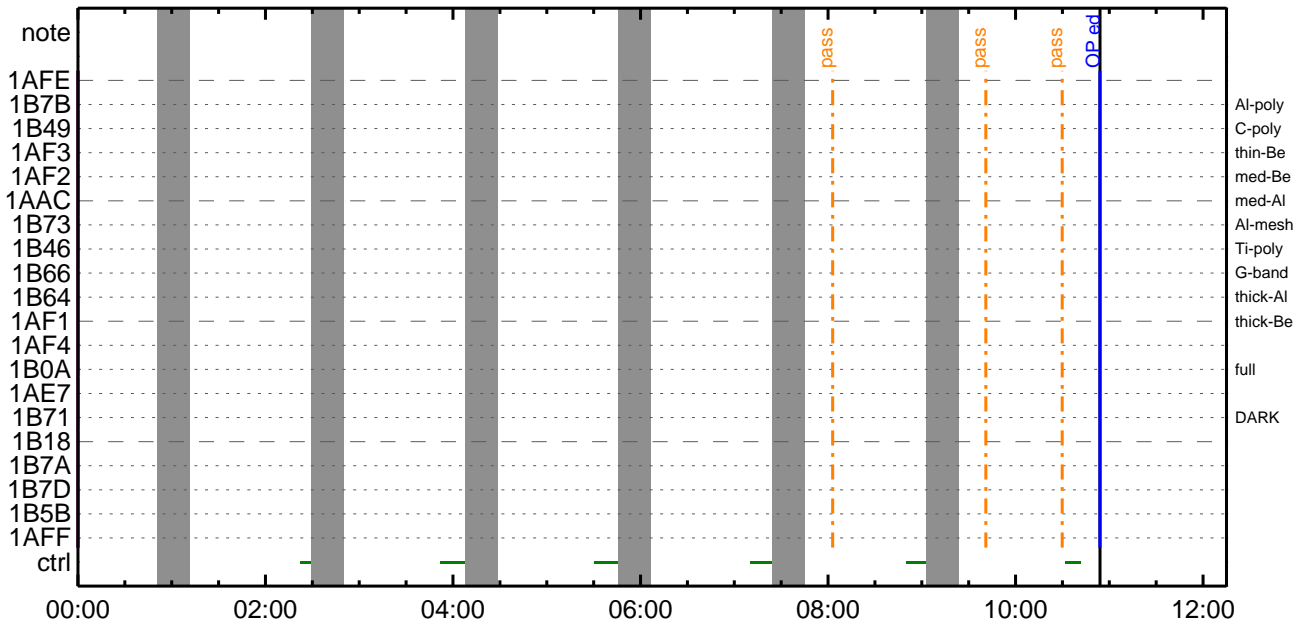
CMDI #0704 2017/04/28

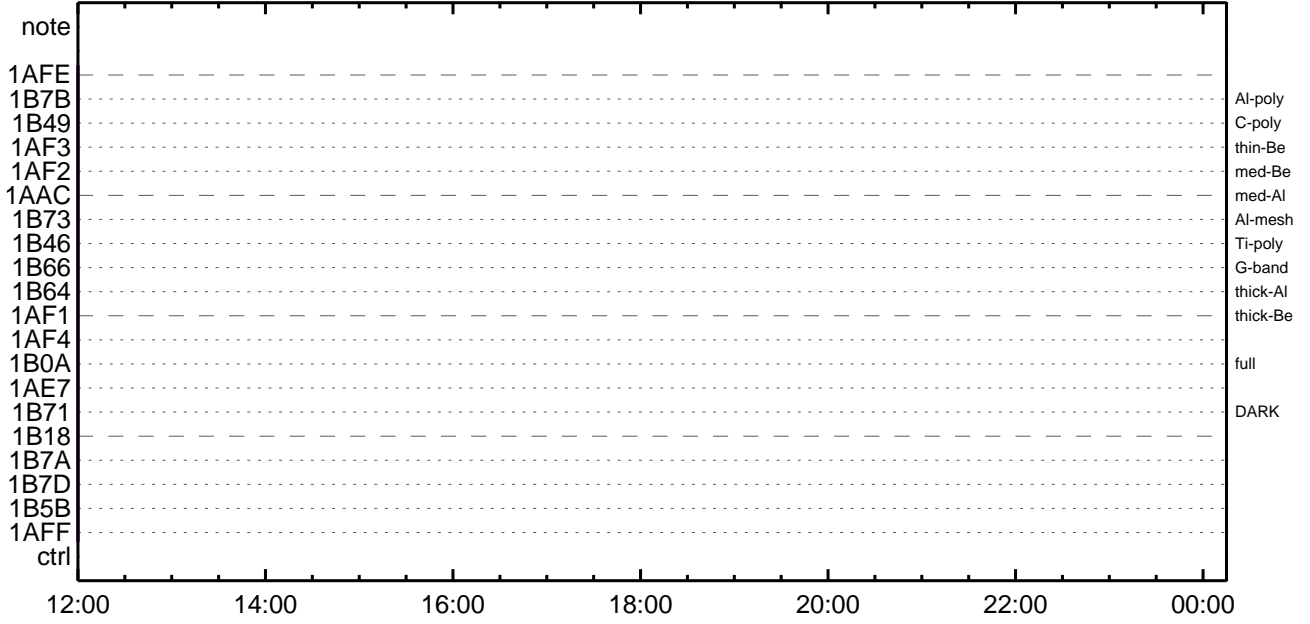


CMDI #0704 2017/04/28



CMDI #0704 2017/04/29






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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-566:OP
0104 ( )
0105 S. OG og-566:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½A´¶Á°òEÉ-ò°Á÷¿@ (¼âµ-YAYOYx½ê½çòðÁÓÆòÇ¼ª°"òè¼i¹çòçòâ) *****
0167 C. DHUYâ;4YE;E½Y½, Y1;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½i¹ç;ç°E²¼òîTI-CMDÁ÷¿@²î½Á¹Ôª°²E²ò²³òE;f
0180 C. ²²ò¿;çSET²E²DUMP²î½±°iYNY¹ç¹Ôª|²³òE;f
0181 C.
0182 C. TIY³Y²YóYÉòðÁDî¿¿(UT)
0183 +. TI 2017-04-25 10:06:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2017-04-25 10:06:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2017-04-25 10:06:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```

```
0194 C.
0195 +. TI 2017-04-25 10:10:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]                EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼õîÄê%îíñòîî¥Ä¥§¥Ä¥~¹àîü
0200 C.          çç[HK1_TI_CMD_ENA/DIS]            EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]                EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]            EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]             EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîî°è¥Ä¥Ö¥×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]          EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]          EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]           EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]         EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]            EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]             EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]            EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]         EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]         EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]            EQ      EXEC
0224 C.
0225 C. ¥Ä¥Ö¥×½ªî»ò³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]            EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLîî½è¹ç•è²ìOKò³îç§
0229 C.
0230 C. DHU¥â;¼¥È;È¼¥¼. ¥ì;¼¥È;Èòðîã²¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]             EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]            EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]         EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]         EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2017-04-25 10:10:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2017-04-25 10:10:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2017-04-25 10:10:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]            EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2017-04-25 10:10:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]            EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP `úÄîãî»ò¼¥òÉÄò¹èèDCBC•x²è *****
0276 C. (¼á°îî¥Ä¥È¥½¥È¥á¥ç¥èè¼ò¼Ä»Üò¹èè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ¥Ð¥¹•î Daily±çîñòè'Øò¹èèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOS¥Ä¥§¥Ä¥~¼Ä»Ü;ã
0287 C.
0288 C. ***** LOS *****
0289 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-567 2017-04-25 12:49:59 178 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁY$;¼Y³YFÝÖYÉÁ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èççãã•μ°È»Í×ÁÇçÍYçYÁY×Yí;¼YÉ;ÈÈÈ¼μ•íÉ;ÈÈÈ¼°ÇÖã•çç¼l¹ççí;çÁ®, ùã¹ãÈãããçÁ+ç®ã•ãÈããããÈ;ç
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+ççμ;ON
0016 C. *****
0017 C. ç" °ÈÀ, í×ÈYããLOSpãççã»p´Ôãð¹íí, ç; çÉÔÍ×ãÈXÁÓONãí¹ÔãÈãíãÈããããÈ;ç
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYÖYÉYíYÁY-¾ÔÁÖã-ãÁÁÈã•çç;ç°È²¼ççí°ÈÀ, ¼È¼ççð¼Á¹Ôã¹çç;ç
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°ÈÀ,
0033 C. *****
0034 C. ç" RESTART;ÈPT1;Èã•çççç¼l¹ççí;ç°È²¼ççí°ÈÀ¹Ôã»ã°;çDCBC-150çççÈçã;ç
0035 C.
0036 . C. ;ãPT1°ÈÀ, ³«»Í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ô, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ô, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ô, ;¼Ú)
0045 C.
0046 . C. ;ãYçYÖYÉYÉÁÙÁÖ;ÈÁ•Á°²óÈð;È, áãí°ÈÀ, °È³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ô, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ô, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ô, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÈÀ, ç-¼«È°Áá»ßã•çç, á;ç°È²¼ççð¼Á¹Ôã¹çç;ç
0055 C. YçYÖYÉYÉÁÙÁÖãÁ•Á°²óÈðã-¾áãç¼l¹ççí°ÈÁ»ã¹ãÈãããçÁÖã;ç
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°ÈÀ,
0059 C. *****
0060 C. ç" RESTART;ÈPT2;Èã•çççç¼l¹ççí;ç°È²¼ççí°ÈÀ¹Ôã»ã°;çDCBC-151çççÈçã;ç
0061 C.
0062 . C. ;ãPT2°ÈÀ, ³«»Í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ô, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ô, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ô, ;¼Ú)
0071 C.
0072 . C. ;ãYçYÖYÉYÉÁÙÁÖ;ÈÁ•Á°²óÈð;È, áãí°ÈÀ, °È³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ô, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ô, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ô, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÈÀ, Áá»ß;çXÁ+ççμ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÈÀ, Áá»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+ççμ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
```


(a) Spacecraft Operation Procedure (real-commands)

```
main-568 2017-04-25 12:49:59 134 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿ãÀ•µ°È»Í×ÁÇ¿ÍY¿Y×YÍ;¼YÉ;ÈÈ¿µ•íÉ;ÈÈ¼°ÇÔã•¿¿¼í¹¿ãÍ;¿À®, ù¿¹ãÈãÈãÇÁ+¿®ã•ÈÈãã³ãÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop SP table >
0018 +. DC 07-F0 MDP_SP_CTRL_MANU
0019 BC (61)
0020 C. -----
0021 C. MDP_SP_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload SP Observation Table>
0025 . S. RAM ram-288:MDP_OBS_S
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_S >
0029 +. DC 07-F0 MDP_DUMP_SPTBL
0030 BC (83 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_S verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2017-04-25 10:10:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 + DC 07-F0 MDP_XRT_MODE_STBY
0052 BC (c3)
0053 . C. ----- Success Verify ? OK / NG_____
0054 C.
0055 C. XRT Obs. Table Upload
0056 . S. RAM ram-291:MDP_OBS_X
0057 ( )
0058 C.
0059 +. DC 07-F0 MDP_DUMP_XRTTBL
0060 BC (84 07 00 00 00 3a d4)
0061 . C. ----- Comparison Check ? OK / ERR _____
0062 C.
0063 C.
0064 +. DC 07-F0 MDP_XRT_ROI_SET
0065 BC (cd 01 b1 b1 04 04)
0066 + DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 02 b1 b1 08 08)
0068 + DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 03 b1 b1 08 08)
0070 + DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 04 b1 b1 06 06)
0072 + DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 05 85 83 06 06)
0074 + DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 06 c0 40 10 10)
0076 + DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 07 80 80 20 20)
0078 + DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 08 40 c0 10 10)
0080 + DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 09 40 40 10 10)
0082 + DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 0a c0 c0 10 10)
0084 + DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0b 85 83 06 06)
0086 + DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 0c 80 80 20 08)
0088 + DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 0d 80 80 08 20)
0090 + DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 0e 85 83 08 08)
0092 + DC 07-F0 MDP_XRT_ROI_SET
0093 BC (cd 0f 80 80 06 06)
0094 + DC 07-F0 MDP_XRT_ROI_SET
0095 BC (cd 10 80 80 08 08)
```

```
0096 + DC 07-F0 MDP_XRT_FLD_ENA
0097 BC (d8)
0098 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0099 BC (c8)
0100 + DC 07-F0 MDP_XRT_ARS_DIS
0101 BC (d5)
0102 + DC 07-F0 MDP_XRT_AEC_RESET
0103 BC (d0)
0104 + DC 07-F0 MDP_XRT_FLD_RESET
0105 BC (da)
0106 . C. ----- Success Verify ? OK / NG ____
0107 C.
0108 C.
0109 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0110 C.
0111 +. DC 07-F0 MDP_XRT_MODE_OBSV
0112 BC (c2)
0113 +. TI 2017-04-25 10:10:02.0
0114 DC 07-F0 MDP_XRT_MODE_OBSV
0115 BC (c2)
0116 . C. ----- Success Verify ? OK / NG ____
0117 C.
0118 C. ***** XRT END *****
0119 C.
0120 . C. ***** MDP `úÃîñî»ò%ÝðÊÂð¹ñèDCBC•x²è *****
0121 C. (%á°îÿÓÿÃÿÈÿËÿËÿáÿçÿèñÊ¼ñ¼Â»Ûñ¹ñè)
0122 . S. DC-BC dcbc-402:DCBC
0123 (MDP_known_event)
0124 C.
0125 C.
0126 . C. ***** ÿDÿ¹•Ï Daily±¿îññè´Øñ¹ñèDCBC•x²è *****
0127 . S. DC-BC dcbc-153:DCBC
0128 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0129 C.
0130 C.
0131 . C. ¡ãLOSÿÁÿ$ÿÃÿ-¼Â»Û;ã
0132 C.
0133 . C. ***** LOS *****
0134 C.
```

*** OP Sequence for XRT ***

2017/04/25	10:21:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04	00	00	00	00
2017/04/25	14:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	03	00	00	00	00
2017/04/25	15:30:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	02	00	00	00	00
2017/04/25	17:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	03	00	00	00	00
2017/04/25	18:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	02	00	00	00	00
2017/04/25	19:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04	00	00	00	00
2017/04/26	06:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	00	00	00	00
2017/04/26	06:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04	00	00	00	00
2017/04/26	06:10:00.5	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/04/26	06:10:02.5	XRT_TCIB_XRT_S_HTR_A_DIS_404_OG [0x194]							
		TCIB_XRT_S_HTR_A_DIS	0	04-C0					
2017/04/26	12:34:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/04/26	12:34:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/04/26	12:34:58.0	XRT_FOCUS_POSITION_439_OG [0x1b7]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2017/04/26	12:35:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	d1	07	2e	f9
2017/04/26	12:35:18.0	XRT_FLD_DIS_428_OG [0x1ac]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2017/04/26	12:35:20.0	XRT_FLRCTRL_DIS_443_OG [0x1bb]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2017/04/26	12:37:56.0	XRT_ARS_DIS_445_OG [0x1bd]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2017/04/26	12:37:58.0	XRT_QT_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	09			
2017/04/26	12:38:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/04/26	12:44:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/04/26	12:44:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/04/26	12:44:58.0	XRT_FOCUS_POSITION_439_OG [0x1b7]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2017/04/26	12:45:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	2e	f9	d1	07
2017/04/26	12:45:18.0	XRT_FLD_DIS_428_OG [0x1ac]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2017/04/26	12:45:20.0	XRT_FLRCTRL_DIS_443_OG [0x1bb]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2017/04/26	12:47:56.0	XRT_ARS_DIS_445_OG [0x1bd]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2017/04/26	12:47:58.0	XRT_QT_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	10			
2017/04/26	12:48:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/04/26	12:54:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/04/26	12:54:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/04/26	12:54:58.0	XRT_FOCUS_POSITION_439_OG [0x1b7]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2017/04/26	12:55:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	d1	07	d1	07
2017/04/26	12:55:18.0	XRT_FLD_DIS_428_OG [0x1ac]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2017/04/26	12:55:20.0	XRT_FLRCTRL_DIS_443_OG [0x1bb]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2017/04/26	12:57:56.0	XRT_ARS_DIS_445_OG [0x1bd]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2017/04/26	12:57:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2017/04/26	12:58:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/04/26	13:04:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/04/26	13:04:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/04/26	13:04:58.0	XRT_FOCUS_POSITION_439_OG [0x1b7]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2017/04/26	13:05:00.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00	2e	f9	2e	f9
2017/04/26	13:05:18.0	XRT_FLD_DIS_428_OG [0x1ac]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2017/04/26	13:05:20.0	XRT_FLRCTRL_DIS_443_OG [0x1bb]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2017/04/26	13:07:56.0	XRT_ARS_DIS_445_OG [0x1bd]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2017/04/26	13:07:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2017/04/26	13:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]							

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2017/04/26	13:14:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/04/26	13:14:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/04/26	13:14:58.0	XRT_FOCUS_RECALIBRATE_416_OG [0x1a0]	XRT_FOCUS_RECAL	2	07-F8	78 00
2017/04/26	13:15:00.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04 00 00 00 00
2017/04/26	13:18:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2017/04/26	13:19:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2017/04/26	13:19:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2017/04/26	13:19:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2017/04/26	13:19:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2017/04/26	13:19:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/04/26	13:21:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2017/04/26	13:21:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2017/04/26	13:22:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/04/26	13:50:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/04/26	13:50:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/04/26	13:50:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/04/26	13:50:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/04/26	13:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/04/26	14:00:00.0	AOCS_OrE-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	00 fd 59 ae ea
2017/04/26	14:09:30.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/04/26	14:09:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/04/26	14:09:34.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2017/04/26	14:09:54.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2017/04/26	14:09:56.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2017/04/26	14:09:58.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2017/04/26	14:10:00.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2017/04/26	14:10:02.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/04/26	14:12:32.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2017/04/26	14:12:34.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2017/04/26	14:12:36.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/04/26	15:28:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/04/26	15:28:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/04/26	15:28:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/04/26	15:28:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/04/26	15:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/04/26	16:08:30.0	XRT_Custom_430_OG [0x1ae]				
2017/04/26	16:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/04/26	16:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/04/26	16:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/04/26	16:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2017/04/26	16:30:00.0	AOCS_OrE-point_Start_10_OG [0x0a0]	AOCU_NM	5	02-76	01 00 00 00 00
2017/04/26	16:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2017/04/26	16:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2017/04/26	16:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2017/04/26	16:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2017/04/26	16:30:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/04/26	16:32:56.0	XRT_QT_PROG_SET_407_OG [0x197]				

2017/04/26	16:32:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2017/04/26	16:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/04/26	17:07:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/04/26	17:07:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/04/26	17:07:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/04/26	17:07:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/04/26	17:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/04/26	17:45:00.0	XRT_Custom_430_OG [0x1ae]					
2017/04/26	17:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/04/26	18:00:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/04/26	18:00:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/04/26	18:00:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2017/04/26	18:00:30.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 00 00 00	00
2017/04/26	18:00:48.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2017/04/26	18:03:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2017/04/26	18:03:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2017/04/26	18:03:28.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b
2017/04/26	18:03:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/04/26	18:10:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/04/26	18:10:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/04/26	18:10:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00
2017/04/26	18:10:30.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04 00 00 00	00
2017/04/26	18:10:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2017/04/26	18:10:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2017/04/26	18:10:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2017/04/26	18:10:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2017/04/26	18:10:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/04/26	18:13:26.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d
2017/04/26	18:13:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2017/04/26	18:13:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/04/26	18:45:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/04/26	18:45:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/04/26	18:45:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/04/26	18:45:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/04/26	18:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/04/26	19:22:00.0	XRT_Custom_430_OG [0x1ae]					
2017/04/26	19:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/04/26	20:23:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/04/26	20:23:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/04/26	20:23:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/04/26	20:23:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/04/26	20:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/04/26	20:59:00.0	XRT_Custom_430_OG [0x1ae]					
2017/04/26	21:00:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/04/26	22:02:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/04/26	22:02:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/04/26	22:02:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/04/26	22:02:06.0	XRT_PREFLR_STRT_414_OG [0x19e]					

2017/04/26	22:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/04/26	22:35:00.0	XRT_Custom_430_OG [0x1ae]					
2017/04/26	22:36:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/04/26	23:40:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/04/26	23:40:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/04/26	23:40:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2017/04/26	23:40:06.0	XRT_PREFLR_STRT_414_OG [0x19e]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/04/26	23:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/04/27	00:01:30.0	XRT_Custom_430_OG [0x1ae]					
2017/04/27	00:02:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/04/27	01:17:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/04/27	01:17:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/04/27	01:17:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2017/04/27	01:17:06.0	XRT_PREFLR_STRT_414_OG [0x19e]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/04/27	01:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/04/27	01:38:00.0	XRT_Custom_430_OG [0x1ae]					
2017/04/27	01:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/04/27	02:49:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/04/27	02:49:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/04/27	02:49:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2017/04/27	02:49:06.0	XRT_PREFLR_STRT_414_OG [0x19e]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/04/27	02:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/04/27	03:16:30.0	XRT_Custom_430_OG [0x1ae]					
2017/04/27	03:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/04/27	04:17:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/04/27	04:17:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/04/27	04:17:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2017/04/27	04:17:36.0	XRT_PREFLR_STRT_414_OG [0x19e]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/04/27	04:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/04/27	04:55:00.0	XRT_Custom_430_OG [0x1ae]					
2017/04/27	04:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/04/27	05:30:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/04/27	05:30:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/04/27	05:30:58.0	XRT_FOCUS_POSITION_403_OG [0x193]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2017/04/27	05:31:00.5	AOCS_OrE-point_Start_4_OG [0x09a]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2017/04/27	05:31:18.0	XRT_FLD_DIS_406_OG [0x196]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2017/04/27	05:33:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2017/04/27	05:33:56.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2017/04/27	05:33:58.0	XRT_QT_PROG_SET_449_OG [0x1c1]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b	
2017/04/27	05:34:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/04/27	05:40:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/04/27	05:40:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/04/27	05:40:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2017/04/27	05:41:00.0	AOCS_OrE-point_Start_1_OG [0x097]					
			AOCU_NM	5	02-76	04 00 00 00 00	
2017/04/27	05:41:18.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2017/04/27	05:41:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2017/04/27	05:41:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2017/04/27	05:41:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2017/04/27	05:41:26.0	XRT_FLD_RESET_433_OG [0x1b1]					

2017/04/27	05:43:56.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/04/27	05:43:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2017/04/27	05:44:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2017/04/27	05:58:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/04/27	05:58:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/04/27	05:58:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/04/27	05:58:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/04/27	06:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/04/27	06:33:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/04/27	06:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG			
2017/04/27	07:38:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/04/27	07:38:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/04/27	07:38:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/04/27	07:38:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/04/27	07:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/04/27	08:12:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/04/27	08:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG			
2017/04/27	09:18:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/04/27	09:18:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/04/27	09:18:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/04/27	09:18:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/04/27	09:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/04/27	09:50:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/04/27	09:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG			
2017/04/27	10:04:25.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/04/27	10:26:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		AOCU_NM		5	02-76	00 00 00 00 00