

XRT Timeline to be uploaded on 2017/05/02

Period: 2017/05/02 10:29:00 - 2017/05/06 10:08:00

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Normal mode

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XOB #1B70: CME watch - 4x4 - AEC 2/3 - 2-filter (Be-thin, Al-poly) - G-band (1x1,512x512,3ms) - Leak (1x1,512x512,3ms) - 180s cad (G-band/Leak first)

Term	Pointing (x, y)	Comment
05/02 11:11:30 - 05/02 13:29:54	Fixed (-923.0, 220.0)	# OP start + 10min. Observe AR at East limb for EIS.
05/03 18:37:00 - 05/04 01:56:54	Track (-479.1, -13.4) @ 05/03 17:57:00	# BP & sensitivity monitoring obs. for EIS.
PROG= 10 Inf.-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 36 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 512x512 (1024, 1024) Q=90 0 0 2.0sec	
└─ Open/G-band	Open/G-band close Safe Norm 3ms Obs 1x1 512x512 (1024, 1024) Q=95 0 0 2.0sec	
└─ Subr= 2 20-time(s) 180.0sec		
└─ Seqn= 8 1-time(s) 2.0sec		
└─ thin-Be/Open	med-Be/Open close Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec	
└─ thin-Be/Open	med-Be/Open close Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec	
└─ Seqn= 6 1-time(s) 2.0sec		
└─ Al-poly/Open	Al-poly/Open close Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec	
└─ Al-poly/Open	Al-poly/Open close Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec	
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

XOB #1B44: AR-(filter ratio Al/poly thin-Be), 512x512 at 1064 1048, with G-band 3ms, PFB, 60s cad

Term	Pointing (x, y)	Comment
05/02 13:33:00 - 05/02 17:21:30	Track (-165.4, 220.1) @ 05/02 13:30:00	# HOP 311 with Sac Peak.
05/02 18:24:00 - 05/03 05:46:54	Track (826.0, -190.1) @ 05/02 18:21:00	# AR 12653 observations.
05/03 06:00:00 - 05/03 10:59:54	Track (867.4, -196.8) @ 05/03 05:57:00	# AR obs.
05/03 11:03:00 - 05/03 17:46:54	Fixed (904.0, -170.0)	# AR with FIXED pointing.
05/04 02:00:00 - 05/04 05:59:54	Fixed (904.0, -170.0)	# AR obs.
05/04 06:13:00 - 05/04 10:00:00	Fixed (904.0, -170.0)	# AR obs.
PROG= 05 Inf.-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 78 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 512x512 (1064, 1048) DPCM 0 0 2.0sec	
└─ Open/G-band	Open/G-band close Safe Norm 3ms Obs 1x1 512x512 (1064, 1048) DPCM 0 0 2.0sec	
└─ Open/Ti-poly	Open/thick-Al close Safe Dark 16.0s Obs 1x1 512x512 (1064, 1048) Q=98 0 0 2.0sec	
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 66 60-time(s) 60.0sec		
└─ thin-Be/Open	med-Be/Open close Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec	
└─ Al-poly/Open	thin-Be/Open close Safe Norm 500ms Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec	
└─ thin-Be/Open	med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec	
└─ Al-poly/Open	thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec	
└─ thin-Be/Open	med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec	
└─ Al-poly/Open	thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec	
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

XOB #1B64: Synoptic Q95 2x2 - Al/mesh(64/512/2048) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(45/512/4096) + Ti

Term	Pointing (x, y)	Comment
05/02 18:04:00 - 05/02 18:20:54	Fixed (0.0, 0.0)	synoptic, shifted 1.0 min, extended for SOT.
05/03 05:50:00 - 05/03 05:56:54	Fixed (0.0, 0.0)	synoptic, shifted -13.0 min
05/03 17:50:00 - 05/03 17:56:54	Fixed (0.0, 0.0)	synoptic, shifted -13.0 min
PROG= 11 1-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 5 1-time(s) 2.0sec		
└─ Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec	
└─ Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec	
└─ Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec	
└─ Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec	
└─ Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec	
└─ Seqn= 27 1-time(s) 2.0sec		
└─ Open/Al-mesh	Open/Al-mesh close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Open/Al-mesh	Open/Al-mesh close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Open/Al-mesh	Open/Al-mesh close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Seqn= 99 1-time(s) 2.0sec		
└─ Al-poly/Open	Al-poly/Open close Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Al-poly/Open	Al-poly/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Al-poly/Open	Al-poly/thick-Al close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Seqn= 85 1-time(s) 2.0sec		
└─ thin-Be/Open	thin-Be/Open close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ thin-Be/Open	thin-Be/Open close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ thin-Be/Open	thin-Be/Open close Safe Norm 16.0s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Seqn= 54 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec	
└─ Open/G-band	Open/G-band close Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

XOB #1B66: Synoptic 7 Filter w/ Al-mesh(64/512/2897), Al-poly(45/512/4096), Thin-Be(512/8192/23142) - Thick-Be(65536), Al-poly+Ti-poly(256/5795), Med-Al

Term	Pointing (x, y)	Comment
05/04 06:03:00 - 05/04 06:09:54	Fixed (0.0, 0.0)	synoptic
PROG= 12 1-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 5 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
Seqn= 1 1-time(s) 2.0sec		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 99 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/Open close	Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 94 1-time(s) 2.0sec		
thin-Be/Open	thin-Be/Open close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 8.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 22.6s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 54 1-time(s) 4.0sec		
Open/G-band	Open/G-band open	Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 46 1-time(s) 2.0sec		
Open/thick-Be	Open/thick-Be close	Safe Norm 64.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Seqn= 20 1-time(s) 2.0sec		
med-Al/Open	med-Al/Open close	Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
med-Al/Open	med-Al/Open close	Safe Norm 64.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Seqn= 40 1-time(s) 2.0sec		
Al-poly/Ti-poly	Al-poly/thick-Al close	Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al close	Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Flare mode

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XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512)

Term	Pointing (x, y)	Comment
05/02 11:11:30 - 05/02 13:29:54	Fixed (-923.0, 220.0)	# OP start + 10min. Observe AR at East limb for EIS.
05/02 13:33:00 - 05/02 17:21:30	Track (-165.4, 220.1) ^{05/02 13:30:00}	# HOP 311 with Sac Peak.
05/02 18:24:00 - 05/03 05:46:54	Track (826.0, -190.1) ^{05/02 18:21:00}	# AR 12653 observations.
05/03 06:00:00 - 05/03 10:59:54	Track (867.4, -196.8) ^{05/03 05:57:00}	# AR obs.
05/03 11:03:00 - 05/03 17:46:54	Fixed (904.0, -170.0)	# AR with FIXED pointing.
05/03 18:37:00 - 05/04 01:56:54	Track (-479.1, -13.4) ^{05/03 17:57:00}	# BP & sensitivity monitoring obs. for EIS.
05/04 02:00:00 - 05/04 05:59:54	Fixed (904.0, -170.0)	# AR obs.
05/04 06:13:00 - 05/04 10:00:00	Fixed (904.0, -170.0)	# AR obs.

PROG= 07 30-time(s)		
Subr= 1 20-time(s) 2.0sec		
Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn=100 1-time(s) 10.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm 125ms Obs 1x1 384x384 (1024, 1024) Q=95 2 0 2.0sec
med-Be/Open	Open/thick-Al close	Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 10 1-time(s) 2.0sec		
med-Al/Open	med-Al/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn= 84 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 3ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 3ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Active Region Search

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NOT USED

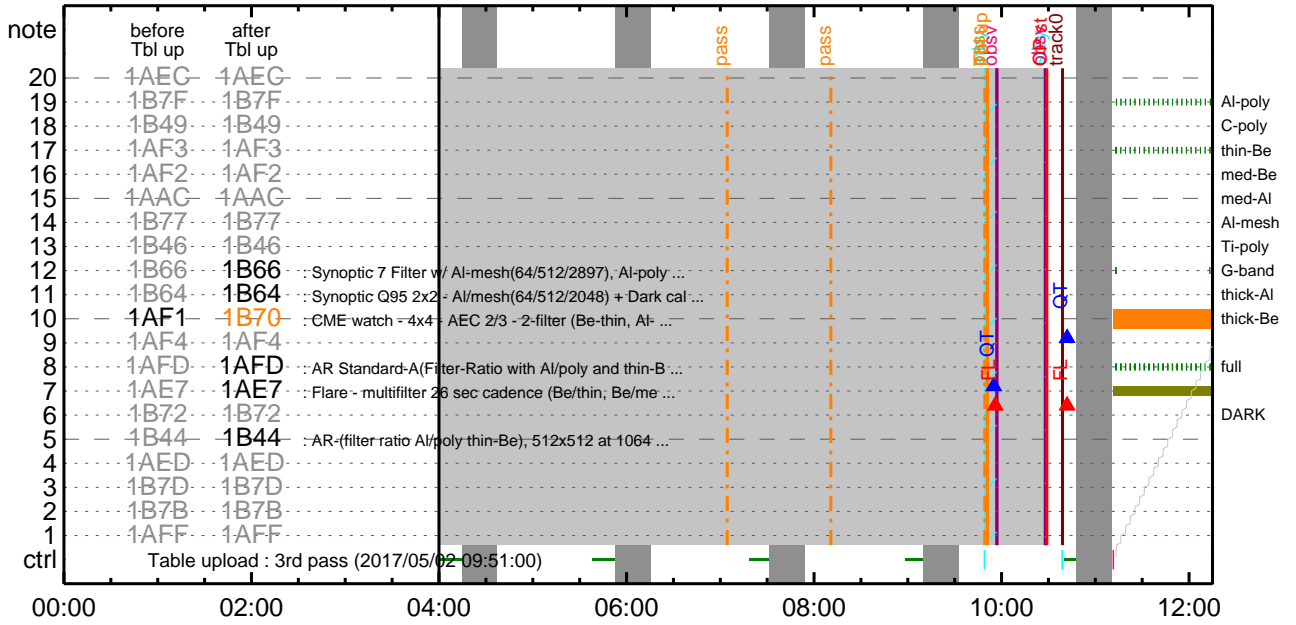
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Flare Detection

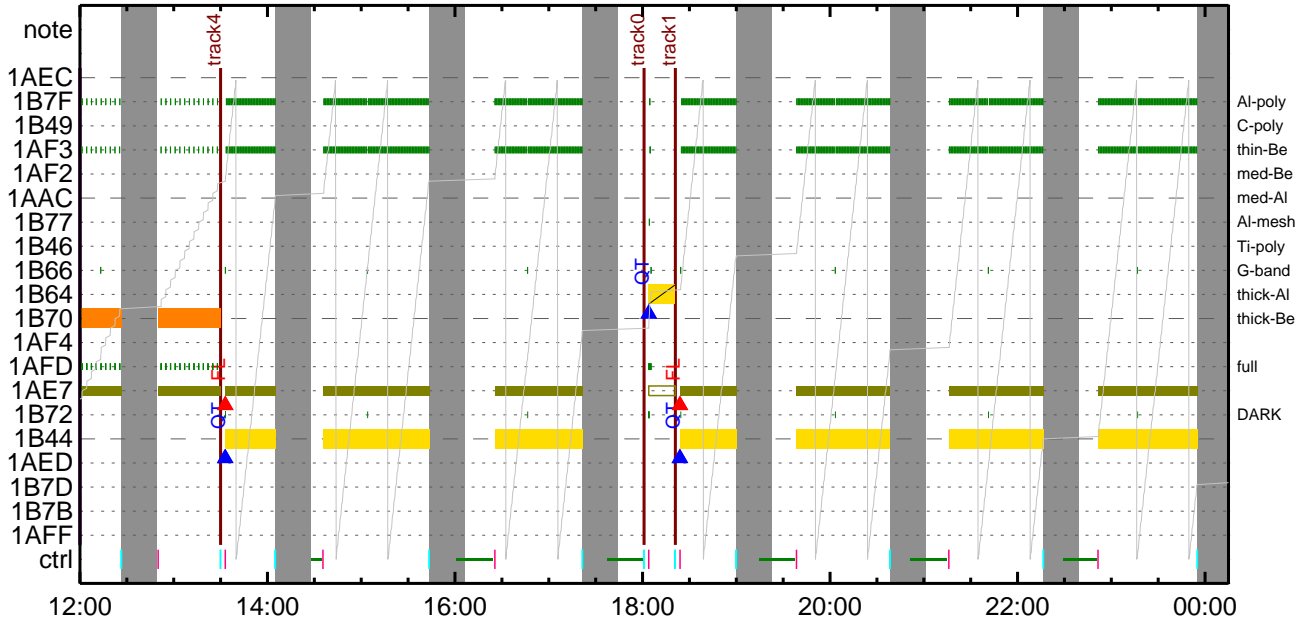
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Term	Pointing (x, y)	Comment
05/02 18:21:18 - 05/03 05:47:18	Track (826.0, -190.1) ^{@ 05/02 18:21:00}	# AR 12653 observations.
05/03 05:57:18 - 05/03 17:47:18	Track (867.4, -196.8) ^{@ 05/03 05:57:00}	# AR obs.
05/03 17:57:18 - 05/04 06:00:18	Track (-479.1, -13.4) ^{@ 05/03 17:57:00}	# BP & sensitivity monitoring obs. for EIS.
05/04 06:10:18 - 05/06 10:08:00	Fixed (904.0, -170.0)	# AR obs.
Open/Ti-poly	Open/thick-Al close Safe Norm 8ms Obs 8x8	Q=50 30sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center)	Comp. AEC Buffer Interval

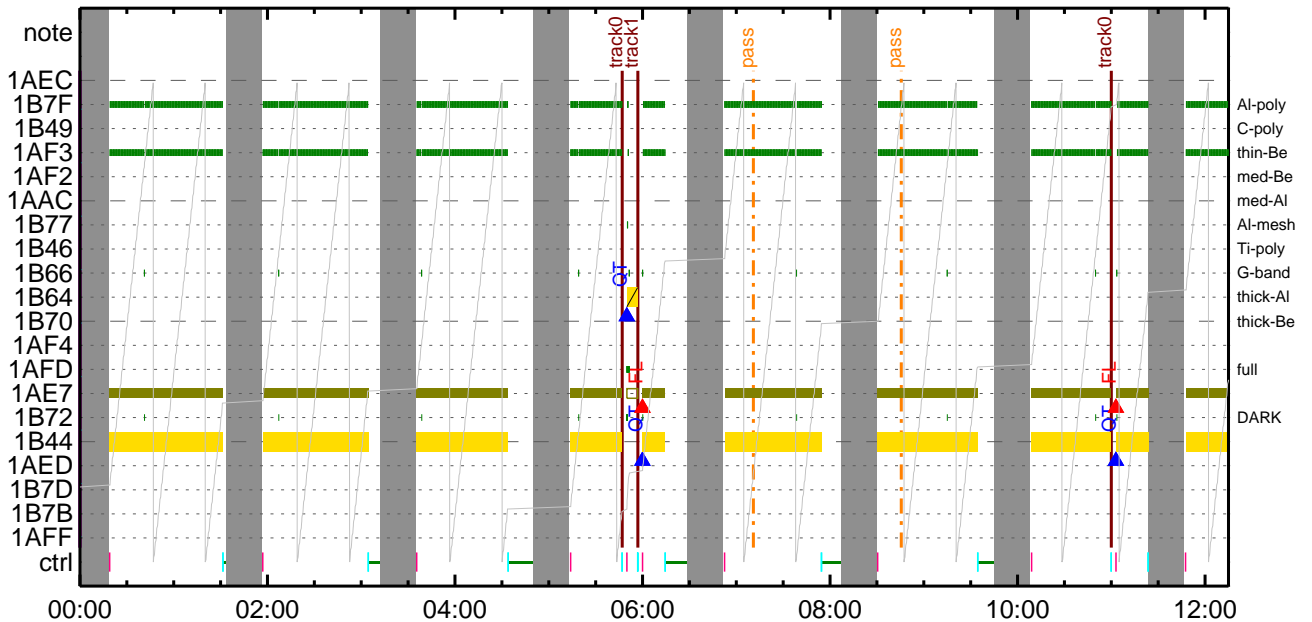
CMDI #0722 2017/05/02



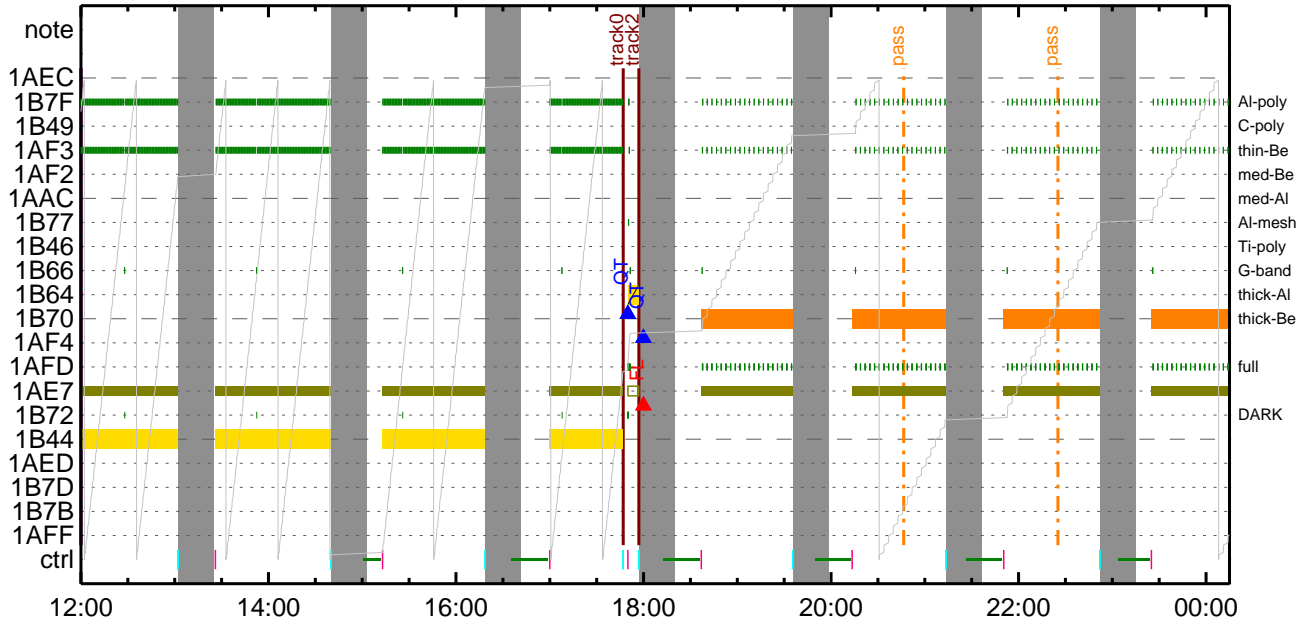
CMDI #0722 2017/05/02



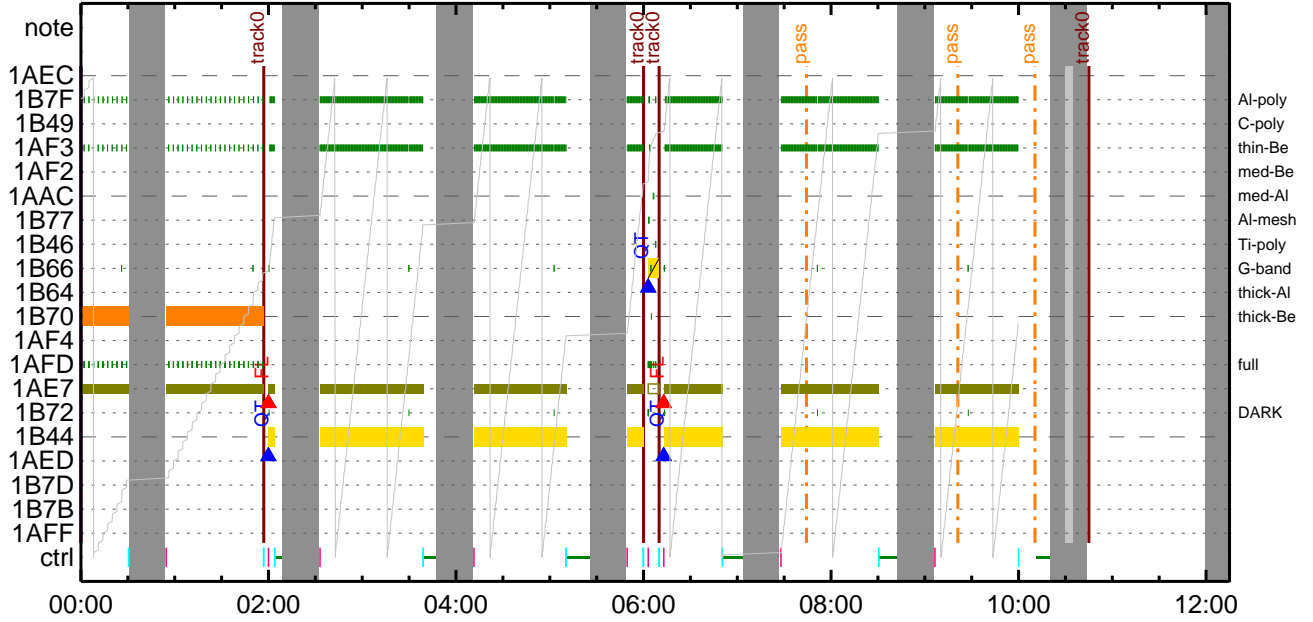
CMDI #0722 2017/05/03



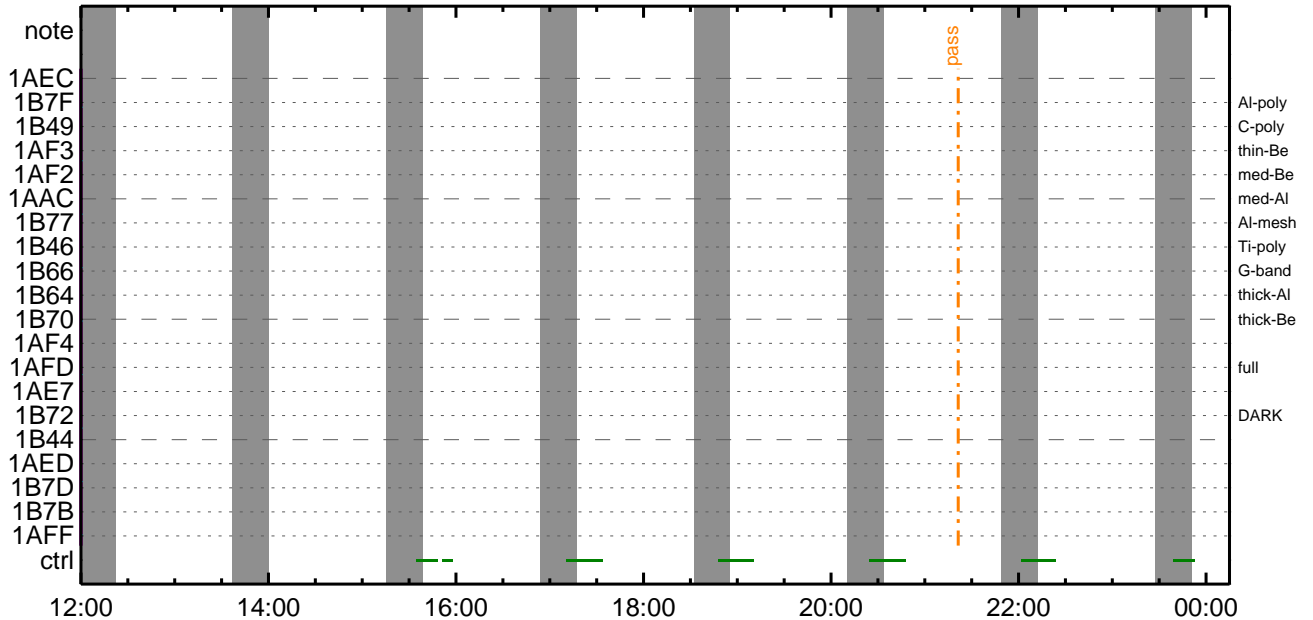
CMDI #0722 2017/05/03



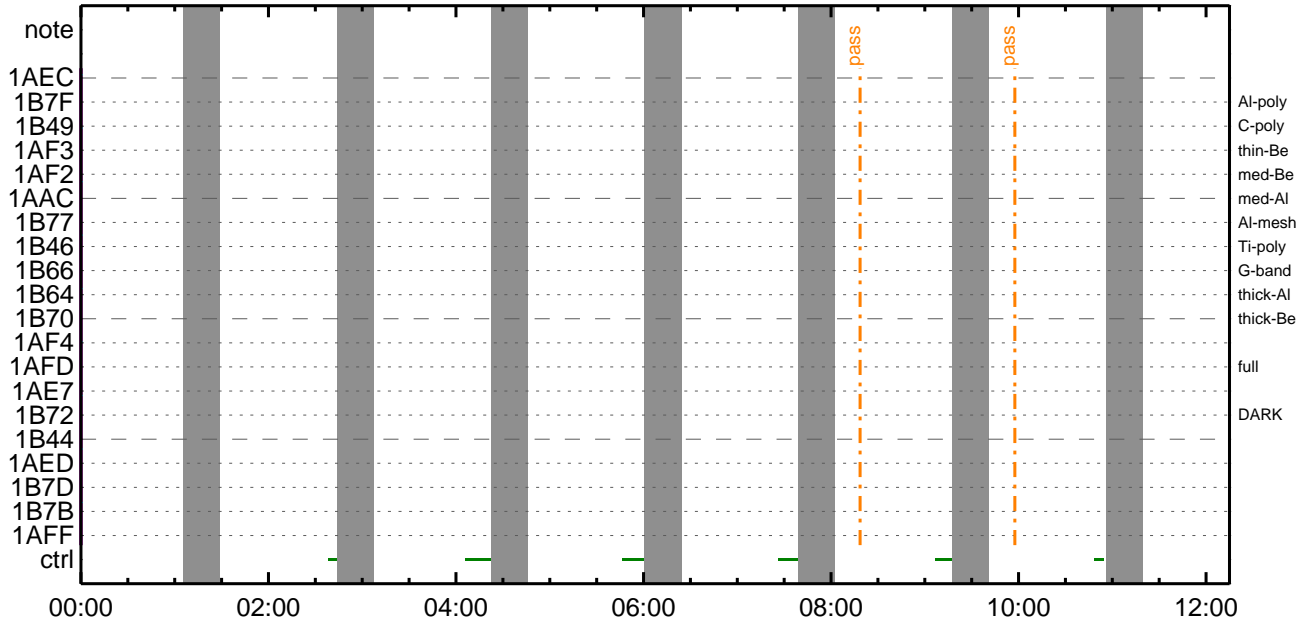
CMDI #0722 2017/05/04



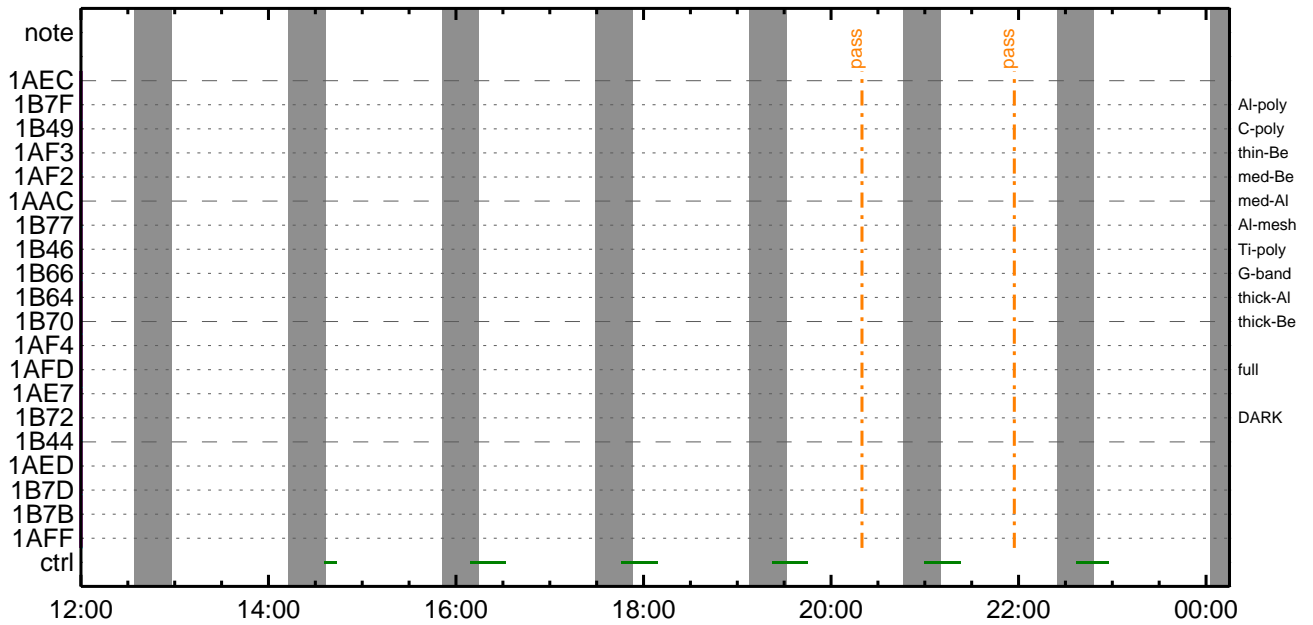
CMDI #0722 2017/05/04



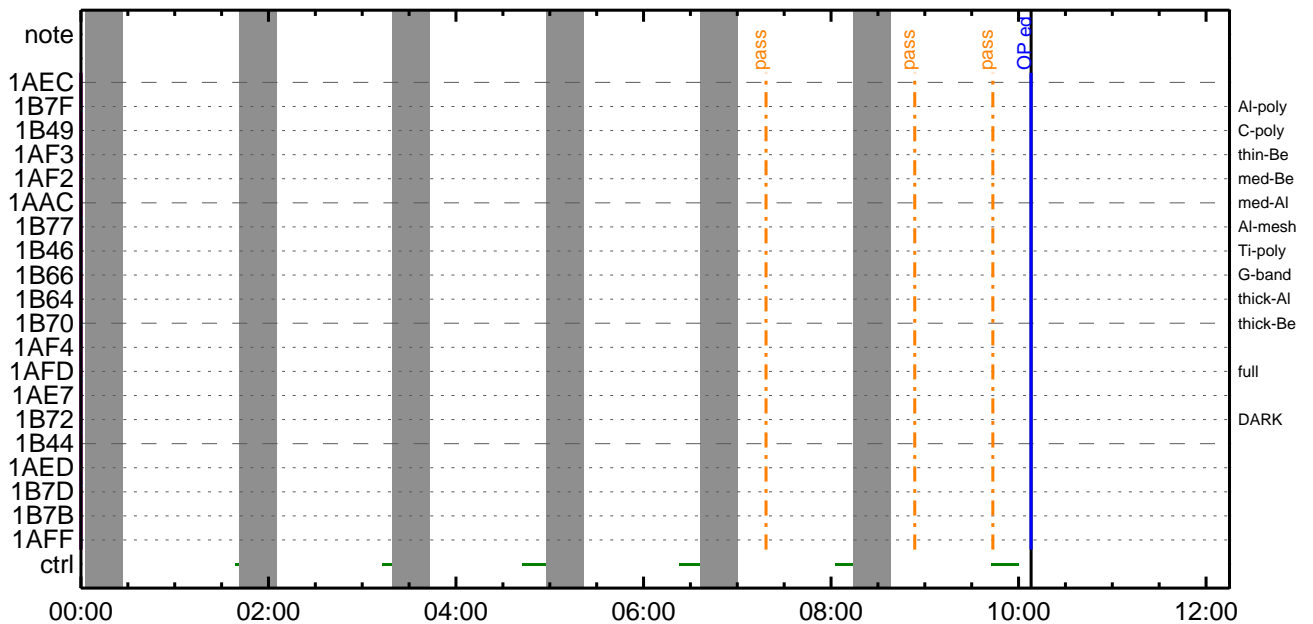
CMDI #0722 2017/05/05



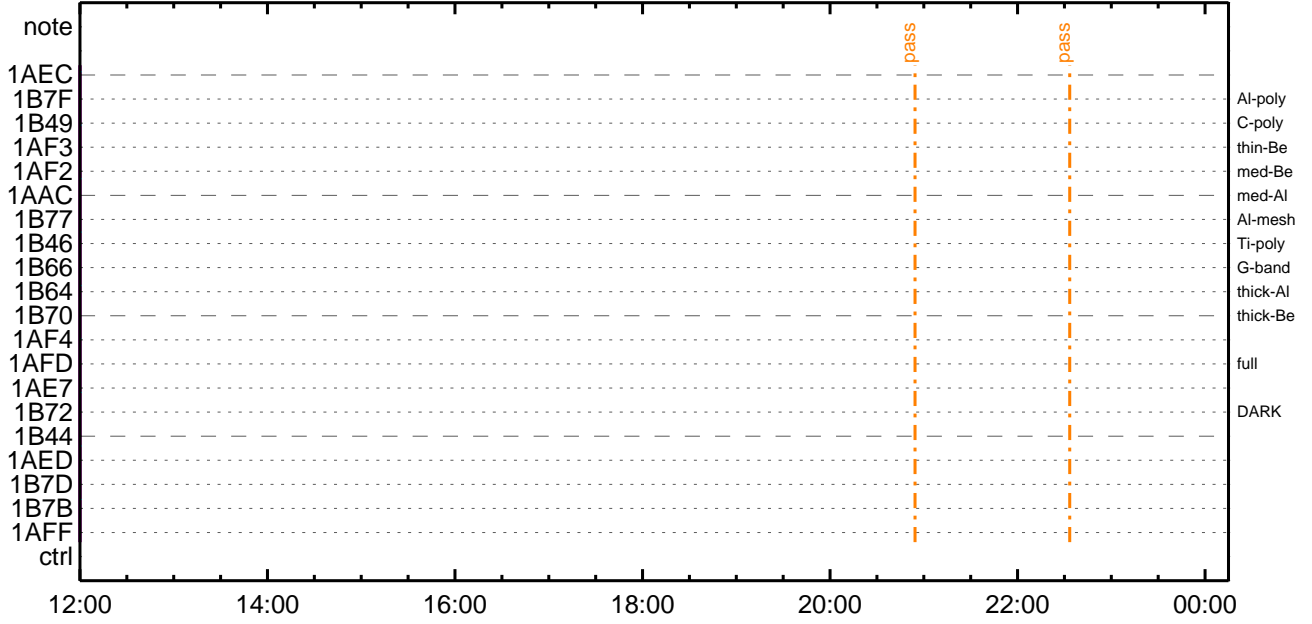
CMDI #0722 2017/05/05



CMDI #0722 2017/05/06



CMDI #0722 2017/05/06



(a) Spacecraft Operation Procedure (real-commands)

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main-581 2017-05-02 11:56:29 205 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÀYB;¼Y³YF¥ÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èò¿òÀò•µ°Ë»Í×ÁÇòÍYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;ÈòÈ¼°ÇÒò•ò¿¼l¹çòí;çÀ®, ùò¹òÈòòòçÁ+¿®ò•òÈòòò³òÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGYí;¼YÉ;|YÁYÖY×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGYí;¼YÉ;ä
0019 . S. OP op-581:OP
0020 ()
0021 . S. OG og-581:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPÍ°èYÁYÖY×;ä
0025 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. YÀYÖY×¼ªª Í»òò³ÍÇ§
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGòí¼È¹ç•è²ÍOKòò³ÍÇ§
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. YÀYÖY×¼ªª Í»òò³ÍÇ§
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGòí¼È¹ç•è²ÍOKòò³ÍÇ§
0062 C.
0063 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. YÀYÖY×¼ªª Í»òò³ÍÇ§
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG, RAM ID=OPòí¼È¹ç•è²ÍOKòò³ÍÇ§
0081 C.
0082 . C. ***** òÈ²¼òí¼Á´¶Á°òÈÈ-òòÁ+¿® (¼áµ-YÀYÖY×¼È¼çòòÁÓÃæòçªªò°ò¼¼í¹çòçòâ) *****
0083 C. DHUYâ;¼YÉ;È¼Y¼;Yí;¼YÉ;Èòòíáò¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE |§ OPOG UPLOADò-Á+¿®NGòí¼í¹ç;ç°È²¼òí¼TI-CMDÁ+¿®òí¼Á¹Òò•òÈòòò³òÈ;f
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0096 C.      01-02; SET 01-02 DUMP 01-02 01-02 01-02 01-02;
0097 C.
0098 C.      TI 2017-05-02 10:24:00.0
0099 +. TI 2017-05-02 10:24:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.      01-02 [HK1_TI_CMD_NUM] EQ 1COUNTUP
0102 C.
0103 +. TI 2017-05-02 10:24:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.      01-02 [HK1_TI_CMD_NUM] EQ 1COUNTUP
0106 C.
0107 +. TI 2017-05-02 10:24:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.      01-02 [HK1_TI_CMD_NUM] EQ 1COUNTUP
0110 C.
0111 +. TI 2017-05-02 10:28:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.      01-02 [HK1_TI_CMD_NUM] EQ 1COUNTUP
0114 C.
0115 C.      01-02 [HK1_TI_CMD_ENA/DIS] EQ ENA
0116 C.      01-02 [HK1_TI_CMD_NUM] EQ 4
0117 C.      01-02 [HK1_NEXT_EXEC_PIM] EQ DHU
0118 C.      01-02 [HK1_NEXT_EXEC_DC] EQ 0xB3
0119 C.
0120 C.
0121 C. *****
0122 C. TI 2017-05-02 10:28:59.5
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF; 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.      01-23 [HK1_DMP_TOP_ADRS_1] EQ 07
0129 C.      01-23 [HK1_DMP_TOP_ADRS_0] EQ 2B
0130 C.      01-23 [HK1_DMP_BLOCK_NUM] EQ 3
0131 C.      01-23 [HK1_DMP_REPEAT_NUM] EQ 0
0132 C.      01-23 [HK1_DMA_DMP_PIM] EQ DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.      01-22 [HK1_PKT_FORM_NO] EQ 7
0136 C.      01-22 [HK1_PKT_GEN_TIME] EQ 0.25 s
0137 C.      01-22 [HK1_S_TLM_BIT_RATE] EQ 32k
0138 C.      01-22 [HK1_X_TLM_BIT_RATE] EQ 4M
0139 C.      01-22 [HK1_DMP_CHK_FLG] EQ EXEC
0140 C.
0141 C.      01-22 [HK1_DMP_CHK_FLG] EQ NON
0142 C.
0143 C.
0144 C. RAM ID=TI_TBL 01-23 01-02 01-02 01-02
0145 C.
0146 C. DHU 01-22 01-02 01-02 01-02 01-02
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.      01-22 [HK1_PKT_FORM_NO] EQ 2
0150 C.      01-22 [HK1_PKT_GEN_TIME] EQ 0.5S
0151 C.      01-22 [HK1_S_TLM_BIT_RATE] EQ 32K
0152 C.      01-22 [HK1_X_TLM_BIT_RATE] EQ 4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2017-05-02 10:28:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC (41)
0161 C. -----
0162 C.      HK1_TI_CMD_NUM = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2017-05-02 10:28:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC (21 02)
0174 +. TI 2017-05-02 10:28:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC (22)
0177 C.      [ ] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2017-05-02 10:28:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC (c3)
0187 C.      [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 C. ***** MDP 01-02 01-02 01-02 01-02 *****
0192 C. (01-02 01-02 01-02 01-02)
0193 S. DC-BC dcbc-402:DCBC

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0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. ***** AOCs Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 +. DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 7186536.9 +- 1.0 (s) [ ]
0136 C.
0137 . C.
0138 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139 +. DC 07-FC EIS_MODE_CHG_ENA
0140 BC (20)
0141 . C. Verify EIS_MODE_CHG_FLG is ENA
0142 +. DC 07-FC EIS_MODE_MANU
0143 BC (21 02)
0144 . C. Verify EIS in MANUAL mode
0145 . C. Estimated OBSTBL upload time is 46s
0146 C. *****
0147 C. EIS START OBSTBL LOAD
0148 C. *****
0149 . S. RAM ram-820:EIS_OBSTBL
0150 ( )
0151 +. DC 07-FC EIS_DUMP_OBSTBL
0152 BC (07 07 07 00 00 70 00)
0153 C.
0154 C. Execute, after the success of OBSTBL upload.
0155 C. Set EIS TI-commands
0156 +. TI 2017-05-02 10:28:50.0
0157 DC 07-FC EIS_MODE_CHG_ENA
0158 BC (20)
0159 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0160 C. *****
0161 C. EIS END OBSTBL LOAD
0162 C. *****
0163 C.
0164 . C. ***** MDP 'úÃîî»ö¼ÝðËÄð¹ñèDCBC•x²è *****
0165 C. (¼á°îÝÖÝÄÝËÝÞÝËÝ¼ÝèñË¼ð¼Ä»Û¹ñè)
0166 . S. DC-BC dcbc-402:DCBC
0167 (MDP_known_event)
0168 C.
0169 C.
0170 . C. ***** ÝÐÝ¹•Ï Daily±¿ÎÑè'Ø¹ñèDCBC•x²è *****
0171 . S. DC-BC dcbc-153:DCBC
0172 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0173 C.
0174 C.
0175 . C. ;ãLOSÝÁÝSÝÄÝ¹¼Ä»Û;ã
0176 C.
0177 . C. ***** LOS *****
0178 C.

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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 85 83 08 08)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 08 08)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 20 20)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 80 80 20 08)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0b 80 80 08 20)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0f 80 80 06 06)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 10 80 80 08 08)
0142 + DC 07-F0 MDP_XRT_FLD_ENA
0143 BC (d8)
0144 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0145 BC (c8)
0146 + DC 07-F0 MDP_XRT_ARS_DIS
0147 BC (d5)
0148 + DC 07-F0 MDP_XRT_AEC_RESET
0149 BC (d0)
0150 + DC 07-F0 MDP_XRT_FLD_RESET
0151 BC (da)
0152 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0153 BC (c4 08)
0154 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0155 BC (c5 07)
0156 . C. ----- Success Verify ? OK / NG ____
0157 C.
0158 C.
0159 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0160 C.
0161 +. DC 07-F0 MDP_XRT_MODE_OBSV
0162 BC (c2)
0163 +. TI 2017-05-02 10:28:02.0
0164 DC 07-F0 MDP_XRT_MODE_OBSV
0165 BC (c2)
0166 . C. ----- Success Verify ? OK / NG ____
0167 C.
0168 C. ***** XRT END *****
0169 . C. *****
0170 C. SOT table upload
0171 C. *****
0172 . C. < Stop SP table >
0173 +. DC 07-F0 MDP_SP_CTRL_MANU
0174 BC (61)
0175 C. -----
0176 C. MDP_SP_CTRL_MODE = MANU [ ]
0177 C. -----
0178 C.
0179 . C. <Upload SP Observation Table>
0180 . S. RAM ram-282:MDP_OBS_S
0181 ( )
0182 C.
0183 . C. < Dump RAMID=MDP_OBS_S >
0184 +. DC 07-F0 MDP_DUMP_SPTBL
0185 BC (83 07 00 00 00 38 b8)
0186 C. -----
0187 C. MDP_OBS_S verify = OK/NG [ ]
0188 C. -----
0189 C.
0190 C. *****
0191 C. SOT TI command set
0192 C. *****
0193 C. Execute, after the success of TBL upload.

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0194 +. TI 2017-05-02 10:28:18.0
0195 DC 07-F0 MDP_SOT_MODE_OBSV
0196 BC (40)
0197 . C. -----
0198 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0199 C. -----
0200 C.
0201 C.
0202 . C. ***** MDP 'úÃîñî»ô¼ÝðËÂðñ¹ñèDCBC•x²è *****
0203 C. (¼ã°îÝÓÝÃÝËÝÞÝËÝáÝçÝèñ¼ñ¼Ã»Ûñ¹ñè)
0204 . S. DC-BC dcbc-402:DCBC
0205 (MDP_known_event)
0206 C.
0207 C.
0208 . C. ***** ÝÐÝ¹•İ Daily±¿İÑñË´Øñ¹ñèDCBC•x²è *****
0209 . S. DC-BC dcbc-153:DCBC
0210 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0211 C.
0212 C.
0213 . C. ;ãLOSÝÁÝ§ÝÃÝ¬¼Ã»Û;ã
0214 C.
0215 . C. ***** LOS *****
0216 C.
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*** OP Sequence for XRT ***

2017/05/02	10:38:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/05/02	10:38:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/05/02	10:38:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2017/05/02	10:39:00.5	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 ec 73 52 0c		
2017/05/02	10:39:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2017/05/02	10:39:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2017/05/02	10:39:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2017/05/02	10:39:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2017/05/02	10:39:26.0	XRT_FLD_RESET_433_OG [0x1b1]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2017/05/02	10:41:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a		
2017/05/02	10:41:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07		
2017/05/02	11:10:30.0	XRT_Custom_430_OG [0x1ae]					
2017/05/02	11:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/05/02	12:26:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/05/02	12:26:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/05/02	12:26:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2017/05/02	12:26:36.0	XRT_PREFLR_STRT_414_OG [0x19e]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2017/05/02	12:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2017/05/02	12:49:00.0	XRT_Custom_430_OG [0x1ae]					
2017/05/02	12:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/05/02	13:29:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/05/02	13:29:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/05/02	13:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2017/05/02	13:30:00.0	AOCS_ORe-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	04 00 00 00 00		
2017/05/02	13:30:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2017/05/02	13:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2017/05/02	13:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2017/05/02	13:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2017/05/02	13:30:26.0	XRT_FLD_RESET_433_OG [0x1b1]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2017/05/02	13:32:56.0	XRT_QT_PROG_SET_413_OG [0x19d]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05		
2017/05/02	13:32:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07		
2017/05/02	13:33:00.5	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/05/02	14:05:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/05/02	14:05:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/05/02	14:05:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2017/05/02	14:05:06.0	XRT_PREFLR_STRT_414_OG [0x19e]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2017/05/02	14:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2017/05/02	14:34:30.0	XRT_Custom_430_OG [0x1ae]					
2017/05/02	14:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/05/02	15:43:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/05/02	15:43:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/05/02	15:43:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2017/05/02	15:43:36.0	XRT_PREFLR_STRT_414_OG [0x19e]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2017/05/02	15:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2017/05/02	16:24:30.0	XRT_Custom_430_OG [0x1ae]					
2017/05/02	16:25:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/05/02	17:21:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/05/02	17:21:32.0	XRT_CTRL_MANU_402_OG [0x192]					

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2017/05/02	17:21:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2017/05/02	17:21:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2017/05/02	17:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2017/05/02	18:00:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/05/02	18:00:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/05/02	18:00:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2017/05/02	18:01:00.0	AOCS_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00		
2017/05/02	18:01:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2017/05/02	18:03:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2017/05/02	18:03:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2017/05/02	18:03:58.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b		
2017/05/02	18:04:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/05/02	18:20:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/05/02	18:20:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/05/02	18:20:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2017/05/02	18:21:00.0	AOCS_OrE-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	01 00 00 00 00		
2017/05/02	18:21:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2017/05/02	18:21:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2017/05/02	18:21:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2017/05/02	18:21:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2017/05/02	18:21:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2017/05/02	18:23:56.0	XRT_QT_PROG_SET_413_OG [0x19d]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05		
2017/05/02	18:23:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07		
2017/05/02	18:24:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/05/02	19:00:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/05/02	19:00:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/05/02	19:00:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2017/05/02	19:00:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2017/05/02	19:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2017/05/02	19:37:30.5	XRT_Custom_430_OG [0x1ae]						
2017/05/02	19:38:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/05/02	20:38:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/05/02	20:38:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/05/02	20:38:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2017/05/02	20:38:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2017/05/02	20:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2017/05/02	21:15:00.0	XRT_Custom_430_OG [0x1ae]						
2017/05/02	21:16:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/05/02	22:16:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/05/02	22:16:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/05/02	22:16:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2017/05/02	22:16:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2017/05/02	22:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2017/05/02	22:50:30.5	XRT_Custom_430_OG [0x1ae]						
2017/05/02	22:51:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/05/02	23:55:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/05/02	23:55:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/05/02	23:55:04.0	XRT_FLD_RESET_415_OG [0x19f]						

2017/05/02	23:55:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da		
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2017/05/02	23:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2017/05/03	00:18:00.0	XRT_Custom_430_OG [0x1ae]						
2017/05/03	00:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/05/03	01:31:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/05/03	01:31:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/05/03	01:31:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2017/05/03	01:31:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2017/05/03	01:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2017/05/03	01:56:00.0	XRT_Custom_430_OG [0x1ae]						
2017/05/03	01:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/05/03	03:04:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/05/03	03:04:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/05/03	03:04:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2017/05/03	03:04:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2017/05/03	03:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2017/05/03	03:34:30.0	XRT_Custom_430_OG [0x1ae]						
2017/05/03	03:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/05/03	04:34:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/05/03	04:34:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/05/03	04:34:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2017/05/03	04:34:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2017/05/03	04:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2017/05/03	05:13:00.0	XRT_Custom_430_OG [0x1ae]						
2017/05/03	05:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/05/03	05:46:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/05/03	05:46:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/05/03	05:46:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2017/05/03	05:47:00.5	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00		
2017/05/03	05:47:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2017/05/03	05:49:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2017/05/03	05:49:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2017/05/03	05:49:58.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b		
2017/05/03	05:50:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/05/03	05:56:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/05/03	05:56:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/05/03	05:56:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2017/05/03	05:57:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	01 00 00 00 00		
2017/05/03	05:57:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2017/05/03	05:57:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2017/05/03	05:57:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2017/05/03	05:57:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2017/05/03	05:57:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2017/05/03	05:59:56.0	XRT_QT_PROG_SET_413_OG [0x19d]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05		
2017/05/03	05:59:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07		
2017/05/03	06:00:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/05/03	06:14:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/05/03	06:14:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		

2017/05/03	06:14:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/05/03	06:14:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/05/03	06:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/05/03	06:51:30.0	XRT_Custom_430_OG [0x1ae]					
2017/05/03	06:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/05/03	07:54:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/05/03	07:54:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/05/03	07:54:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/05/03	07:54:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/05/03	07:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/05/03	08:29:30.0	XRT_Custom_430_OG [0x1ae]					
2017/05/03	08:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/05/03	09:34:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/05/03	09:34:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/05/03	09:34:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/05/03	09:34:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/05/03	09:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/05/03	10:08:00.0	XRT_Custom_430_OG [0x1ae]					
2017/05/03	10:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/05/03	10:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/05/03	10:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/05/03	10:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2017/05/03	11:00:00.0	AOCS_Ore-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00 0f 1a af a6	
2017/05/03	11:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2017/05/03	11:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2017/05/03	11:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2017/05/03	11:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2017/05/03	11:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/05/03	11:02:56.0	XRT_QT_PROG_SET_413_OG [0x19d]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05	
2017/05/03	11:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2017/05/03	11:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/05/03	11:23:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/05/03	11:23:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/05/03	11:23:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/05/03	11:23:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/05/03	11:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/05/03	11:46:30.0	XRT_Custom_430_OG [0x1ae]					
2017/05/03	11:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/05/03	13:02:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/05/03	13:02:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/05/03	13:02:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/05/03	13:02:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/05/03	13:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/05/03	13:25:00.0	XRT_Custom_430_OG [0x1ae]					
2017/05/03	13:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/05/03	14:40:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/05/03	14:40:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/05/03	14:40:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/05/03	14:40:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	

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2017/05/03	14:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/05/03	15:12:00.0	XRT_Custom_430_OG [0x1ae]							
2017/05/03	15:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/05/03	16:18:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/05/03	16:18:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/05/03	16:18:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/05/03	16:18:36.0	XRT_PREFLR_STRT_414_OG [0x19e]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/05/03	16:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/05/03	16:59:00.0	XRT_Custom_430_OG [0x1ae]							
2017/05/03	17:00:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/05/03	17:46:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/05/03	17:46:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/05/03	17:46:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2017/05/03	17:47:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2017/05/03	17:47:18.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2017/05/03	17:49:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2017/05/03	17:49:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2017/05/03	17:49:58.0	XRT_QT_PROG_SET_449_OG [0x1c1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2017/05/03	17:50:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/05/03	17:56:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/05/03	17:56:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/05/03	17:56:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2017/05/03	17:57:00.0	AOCS_Ore-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2017/05/03	17:57:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2017/05/03	17:57:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2017/05/03	17:57:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2017/05/03	17:57:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2017/05/03	17:57:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/05/03	17:59:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2017/05/03	17:59:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2017/05/03	18:36:00.5	XRT_Custom_430_OG [0x1ae]							
2017/05/03	18:37:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/05/03	19:35:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/05/03	19:35:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/05/03	19:35:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/05/03	19:35:36.0	XRT_PREFLR_STRT_414_OG [0x19e]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/05/03	19:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/05/03	20:12:30.0	XRT_Custom_430_OG [0x1ae]							
2017/05/03	20:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/05/03	21:13:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/05/03	21:13:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/05/03	21:13:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/05/03	21:13:36.0	XRT_PREFLR_STRT_414_OG [0x19e]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/05/03	21:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/05/03	21:49:30.0	XRT_Custom_430_OG [0x1ae]							
2017/05/03	21:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/05/03	22:52:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/05/03	22:52:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/05/03	22:52:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				

2017/05/03	22:52:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2017/05/03	22:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2017/05/03	23:24:00.0	XRT_Custom_430_OG [0x1ae]							
2017/05/03	23:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2017/05/04	00:30:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/05/04	00:30:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/05/04	00:30:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2017/05/04	00:30:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2017/05/04	00:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2017/05/04	00:53:30.0	XRT_Custom_430_OG [0x1ae]							
2017/05/04	00:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2017/05/04	01:56:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/05/04	01:56:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/05/04	01:56:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2017/05/04	01:57:00.0	AOCS_Ore-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00 0f 1a af a6			
2017/05/04	01:57:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2017/05/04	01:57:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2017/05/04	01:57:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2017/05/04	01:57:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2017/05/04	01:57:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da			
2017/05/04	01:59:56.0	XRT_QT_PROG_SET_413_OG [0x19d]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05			
2017/05/04	01:59:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07			
2017/05/04	02:00:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2017/05/04	02:04:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/05/04	02:04:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/05/04	02:04:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2017/05/04	02:04:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2017/05/04	02:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2017/05/04	02:32:00.0	XRT_Custom_430_OG [0x1ae]							
2017/05/04	02:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2017/05/04	03:39:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/05/04	03:39:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/05/04	03:39:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2017/05/04	03:39:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2017/05/04	03:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2017/05/04	04:10:30.0	XRT_Custom_430_OG [0x1ae]							
2017/05/04	04:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2017/05/04	05:10:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/05/04	05:10:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/05/04	05:10:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2017/05/04	05:10:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2017/05/04	05:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2017/05/04	05:48:30.0	XRT_Custom_430_OG [0x1ae]							
2017/05/04	05:49:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2017/05/04	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/05/04	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/05/04	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2017/05/04	06:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00			
2017/05/04	06:00:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9			

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2017/05/04	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2017/05/04	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2017/05/04	06:02:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2017/05/04	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/05/04	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/05/04	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/05/04	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2017/05/04	06:10:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	0f	1a	af	a6
2017/05/04	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2017/05/04	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2017/05/04	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2017/05/04	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2017/05/04	06:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/05/04	06:12:56.0	XRT_QT_PROG_SET_413_OG [0x19d]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			
2017/05/04	06:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2017/05/04	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/05/04	06:50:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/05/04	06:50:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/05/04	06:50:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/05/04	06:50:36.0	XRT_PREFLR_STRT_414_OG [0x19e]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/05/04	06:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/05/04	07:27:00.0	XRT_Custom_430_OG [0x1ae]							
2017/05/04	07:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/05/04	08:30:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/05/04	08:30:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/05/04	08:30:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/05/04	08:30:36.0	XRT_PREFLR_STRT_414_OG [0x19e]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/05/04	08:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/05/04	09:05:30.0	XRT_Custom_430_OG [0x1ae]							
2017/05/04	09:06:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/05/04	10:00:00.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/05/04	10:45:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	00	00