

XRT Timeline to be uploaded on 2017/05/16

Period: 2017/05/16 11:06:00 - 2017/05/20 10:12:00

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Normal mode

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XOB #1AF1: CCD Monitor During Bakeout - G-band 3ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh(512ms), Al/Poly(1443ms) - w leak image-3ms

Term	Pointing (x, y)	Comment
05/17 12:03:00 - 05/17 12:09:54	Fixed (-528.4, -528.4)	XRT post bakeout 1/4
PROG= 10 1-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 86 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec	
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec	
└─ Open/thick-Be	Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec	
└─ Open/thick-Be	Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec	
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 55 2-time(s) 2.0sec		
└─ Open/Al-mesh	Open/Ti-poly close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Al-poly/Open	med-Be/Open close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 54 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec	
└─ Open/G-band	Open/G-band close Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

XOB #1AF2: CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh (512ms), Al/Poly (1443ms) - w leak image-3 ms

Term	Pointing (x, y)	Comment
05/17 12:13:00 - 05/17 12:19:54	Fixed (528.4, -528.4)	XRT post bakeout 2/4
PROG= 08 1-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 15 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec	
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec	
└─ Open/thick-Be	Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec	
└─ Open/thick-Be	Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec	
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 55 2-time(s) 2.0sec		
└─ Open/Al-mesh	Open/Ti-poly close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Al-poly/Open	med-Be/Open close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 54 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec	
└─ Open/G-band	Open/G-band close Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

XOB #1AF3: CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - Q90 - 3rd Quadrant - Al/mesh (512ms), Al/Poly (1443ms) - w leak image-3 ms

Term	Pointing (x, y)	Comment
05/17 12:23:00 - 05/17 12:29:54	Fixed (528.4, 528.4)	XRT post bakeout 3/4
PROG= 13 1-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 35 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec	
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec	
└─ Open/thick-Be	Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec	
└─ Open/thick-Be	Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec	
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 55 2-time(s) 2.0sec		
└─ Open/Al-mesh	Open/Ti-poly close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Al-poly/Open	med-Be/Open close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 54 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec	
└─ Open/G-band	Open/G-band close Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

XOB #1AF4: CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh (512ms), Al/Poly (1443ms) - w leak image-3 ms

Term	Pointing (x, y)	Comment
05/17 12:33:00 - 05/17 12:39:54	Fixed (-528.4, 528.4)	XRT post bakeout 4/4
PROG= 16 1-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 3 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec	
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec	
└─ Open/thick-Be	Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec	
└─ Open/thick-Be	Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec	
└─ Subr= 2 1-time(s) 2.0sec		

Seqn= 55 2-time(s) 2.0sec												
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
Subr= 3 2-time(s) 2.0sec												
Seqn= 54 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

XOB #1AEC: G-Band Alignment with North Pole Q90 2x2 (G-band and VLS=CLS) - 1msec (Al/poly) - 4096msec - 5min cadence - Partial Sun-wNGT

Term		Pointing (x, y)		Comment								
05/17 12:55:00 - 05/17 14:39:54		Fixed (0.0, 930.0)		co-alignment program at N-pole (1/2)								
PROG= 20 1-time(s)												
Subr= 1 24-time(s) 300.0sec												
Seqn= 98 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	2x2	2048x1536	(1024, 768)	Q=90	0 0 2.0sec
Seqn= 63 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	2048x1536	(1024, 768)	Q=90	0 0 2.0sec
Seqn= 45 1-time(s) 2.0sec												
	Al-poly/Open	med-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x1536	(1024, 768)	Q=95	0 0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

XOB #1AED: G-Band Alignment with East limb Q90 2x2 (G-band and VLS=CLS) - 1msec - (Al/poly) 1443msec - 8 min cadence-wNGT

Term		Pointing (x, y)		Comment								
05/17 14:55:00 - 05/17 17:04:54		Fixed (-970.0, 0.0)		co-alignment program at E-limb (2/2)								
PROG= 04 1-time(s)												
Subr= 1 15-time(s) 480.0sec												
Seqn= 19 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	2x2	1536x2048	(1280, 1024)	Q=90	0 0 2.0sec
Seqn= 43 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	1536x2048	(1280, 1024)	Q=90	0 0 2.0sec
Seqn= 70 1-time(s) 2.0sec												
	Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	1536x2048	(1280, 1024)	Q=95	0 0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

XOB #1B66: Synoptic 7 Filter w/ Al-mesh(64/512/2897), Al-poly(45/512/4096), Thin-Be(512/8192/23142) - Thick-Be(65536), Al-poly+Ti-poly(256/5795), Med-Al

Term		Pointing (x, y)		Comment								
05/17 17:08:05 - 05/17 17:14:54		Fixed (0.0, 0.0)		synoptic, shifted manually								
05/18 06:03:00 - 05/18 06:09:54		Fixed (0.0, 0.0)		synoptic								
PROG= 12 1-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 5 1-time(s) 2.0sec												
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048	(1024, 1024)	Q=98	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	1024x1024	(512, 512)	DPCM	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	1024x1024	(512, 1536)	DPCM	0 0 2.0sec
Seqn= 1 1-time(s) 2.0sec												
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	24ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
Seqn= 99 1-time(s) 2.0sec												
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
Seqn= 94 1-time(s) 2.0sec												
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	8.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
Seqn= 54 1-time(s) 4.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 46 1-time(s) 2.0sec												
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0 0 2.0sec
Seqn= 20 1-time(s) 2.0sec												
	med-Al/Open	med-Al/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0 0 2.0sec
	med-Al/Open	med-Al/Open	close	Safe	Norm	64.0s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0 0 2.0sec
Seqn= 40 1-time(s) 2.0sec												
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0 0 2.0sec
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	5.66s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0 0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

XOB #1B49: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, Al/Poly context, with G-band (3ms/3ms)

Term		Pointing (x, y)		Comment								
05/17 17:18:00 - 05/18 05:59:54		Track (-159.4, 135.4)		New AR obs at East side								

PROG= 18 Inf.-time(s)

Subr= 1		1-time(s)		2.0sec											
Seqn= 56		1-time(s)		2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec			
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec			
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec			
Seqn= 42		4-time(s)		2.0sec											
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec			
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec			
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec			
Seqn= 28		30-time(s)		60.0sec											
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec			
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec			
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec			
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec			
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec			
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval				

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Flare mode

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XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
05/17 17:18:00 - 05/18 05:59:54	Track (-159.4, 135.4) @ 05/17 17:15:00	New AR obs at East side
05/18 06:13:00 - 05/18 11:20:54	Track (-41.7, 134.9) @ 05/18 06:10:00	New AR obs. at East side

PROG= 07 30-time(s)

Subr= 1		20-time(s)		2.0sec											
Seqn= 11		1-time(s)		2.0sec											
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec			
Seqn=100		1-time(s)		10.0sec											
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec			
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
Subr= 2		1-time(s)		2.0sec											
Seqn= 10		1-time(s)		2.0sec											
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
Seqn= 11		1-time(s)		2.0sec											
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec			
Seqn= 84		1-time(s)		2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec			
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec			
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec			
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval				

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Active Region Search

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NOT USED

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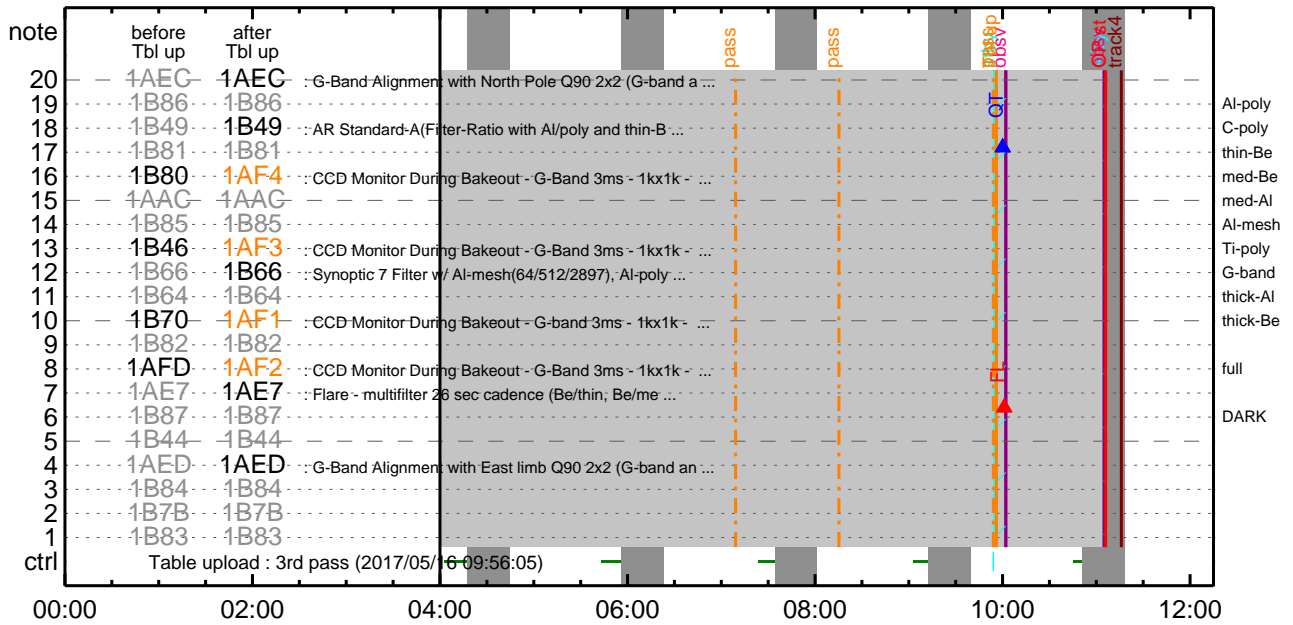
Flare Detection

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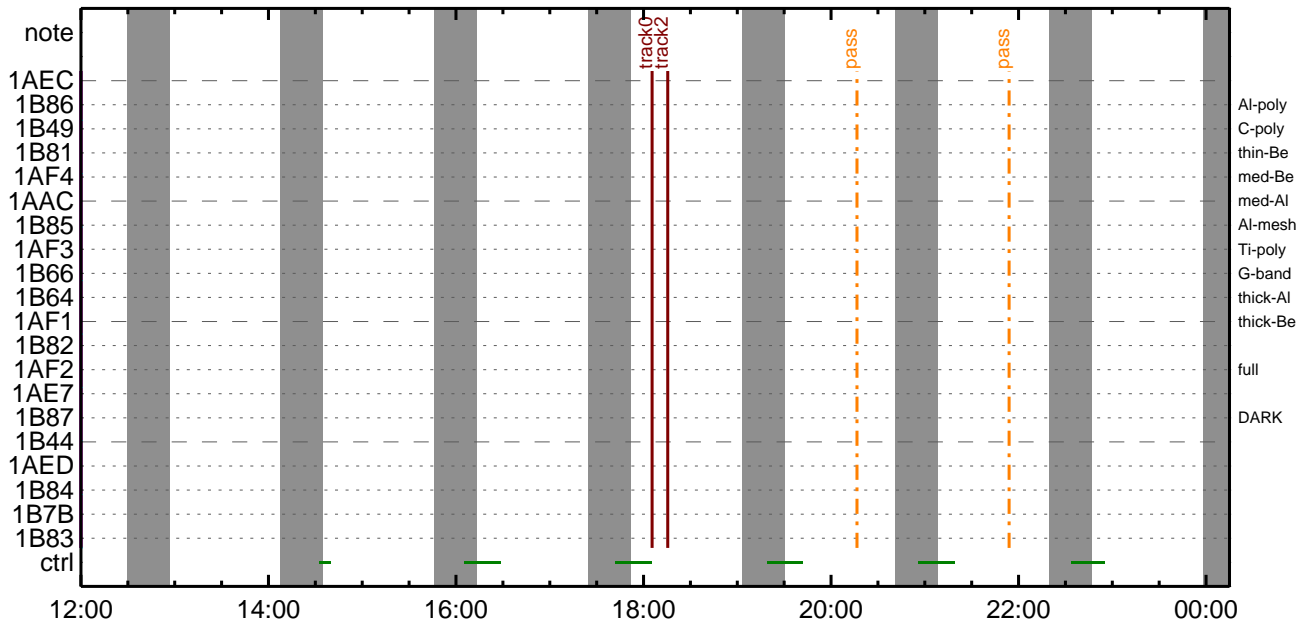
FLD Patrol

Term	Pointing (x, y)	Comment
05/17 17:15:18 - 05/18 06:00:18	Track (-159.4, 135.4) @ 05/17 17:15:00	New AR obs at East side
05/18 06:10:18 - 05/20 10:12:00	Track (-41.7, 134.9) @ 05/18 06:10:00	New AR obs. at East side
Open/Ti-poly	Open/thick-Al	close Safe Norm 8ms Obs 8x8 Q=50 30sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

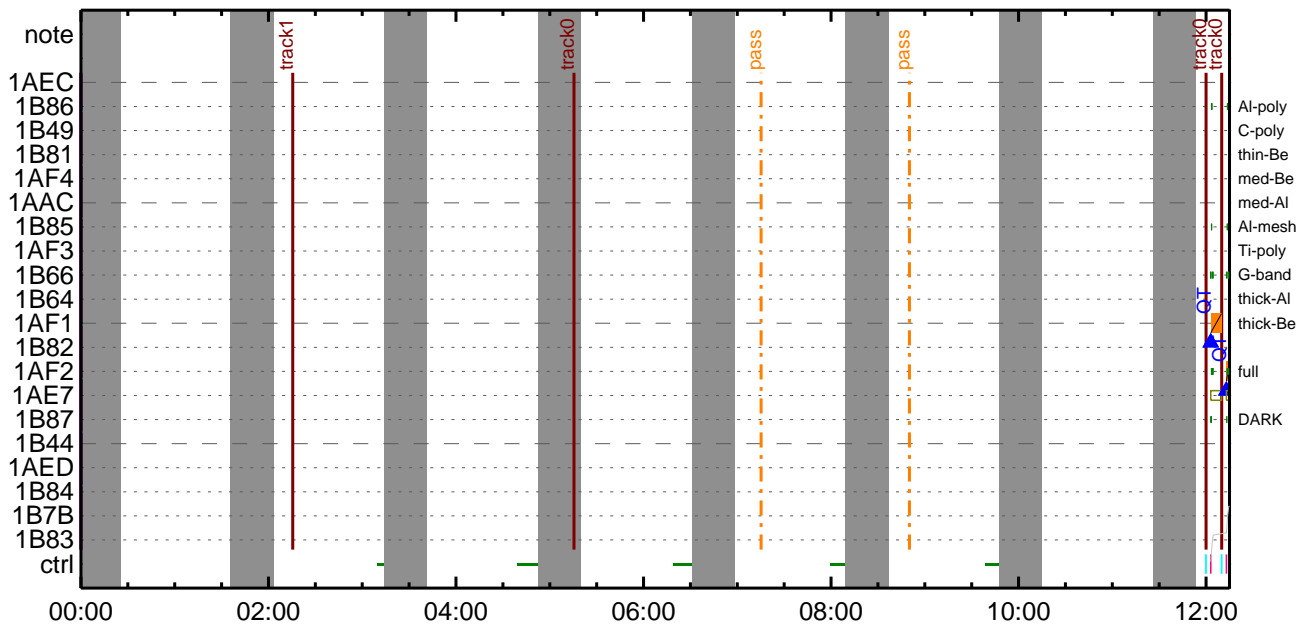
CMDI #0751 2017/05/16



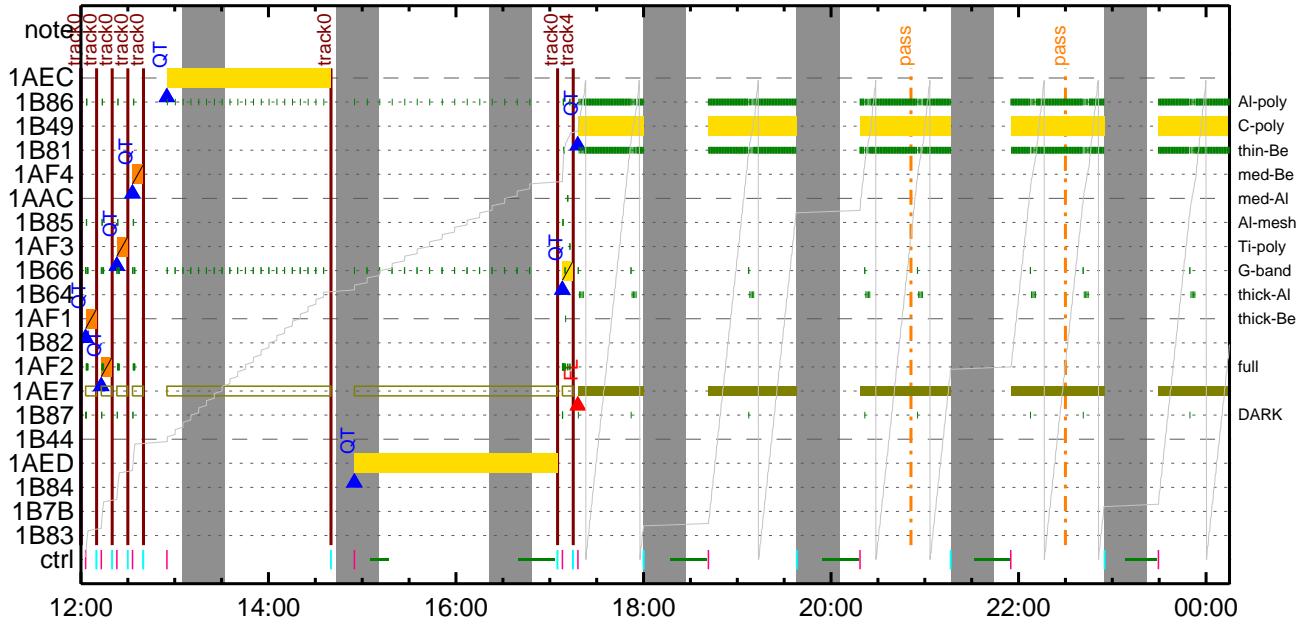
CMDI #0751 2017/05/16



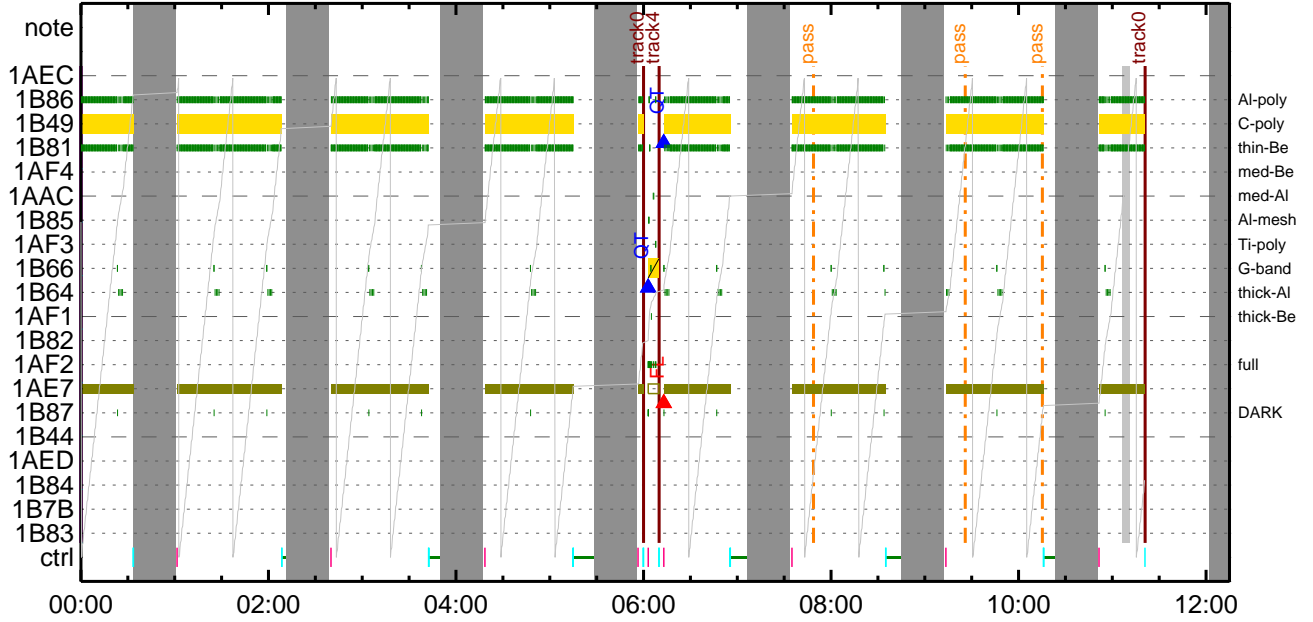
CMDI #0751 2017/05/17



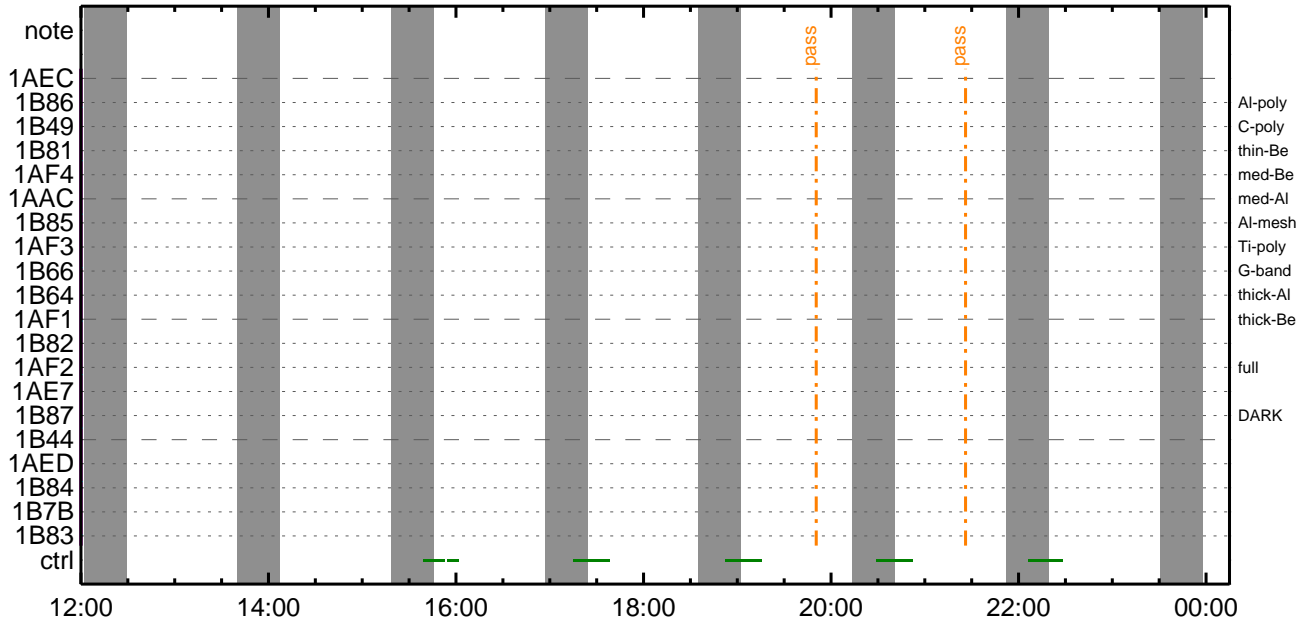
CMDI #0751 2017/05/17



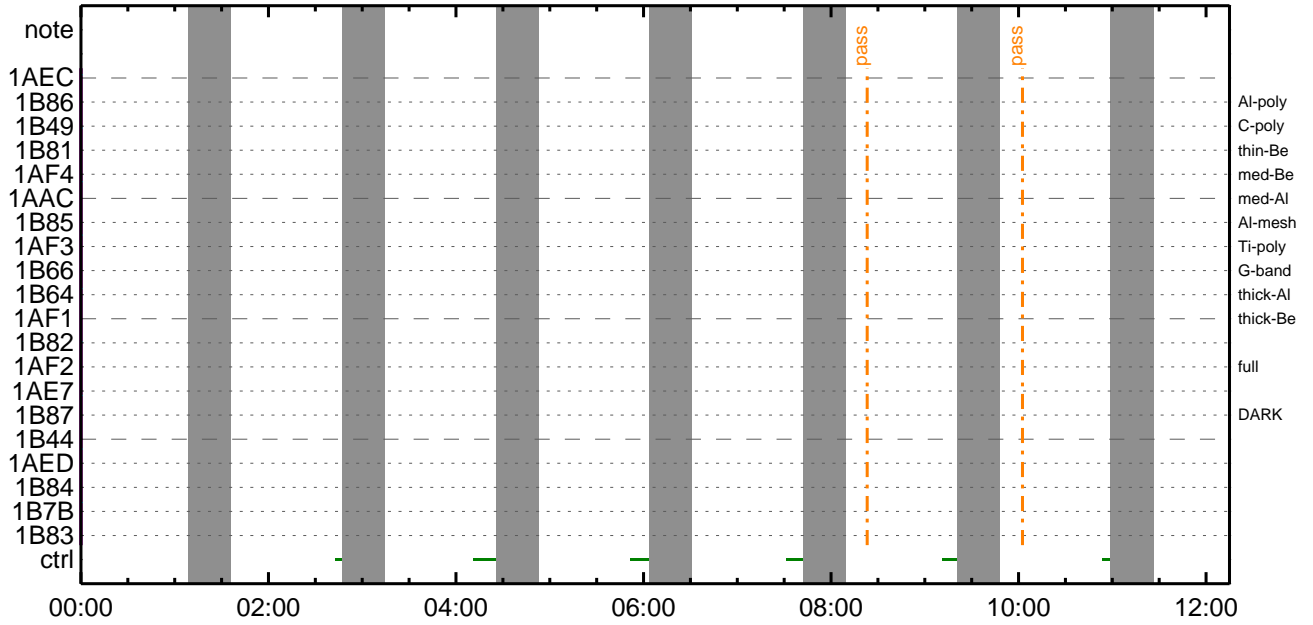
CMDI #0751 2017/05/18



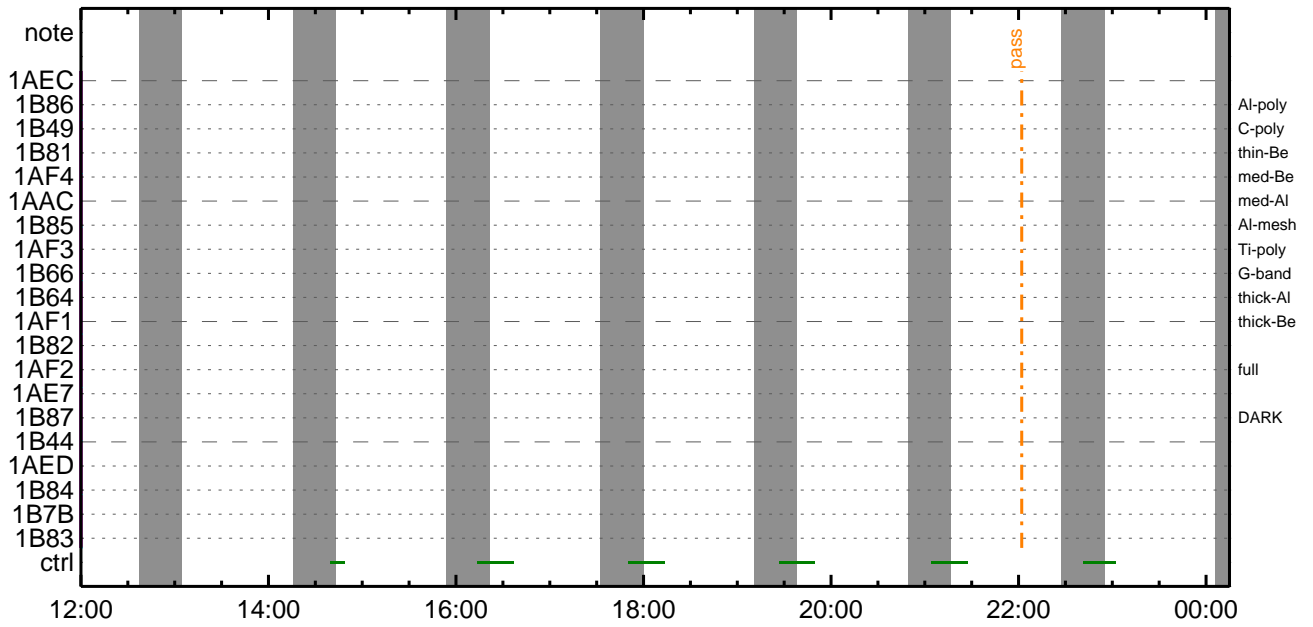
CMDI #0751 2017/05/18



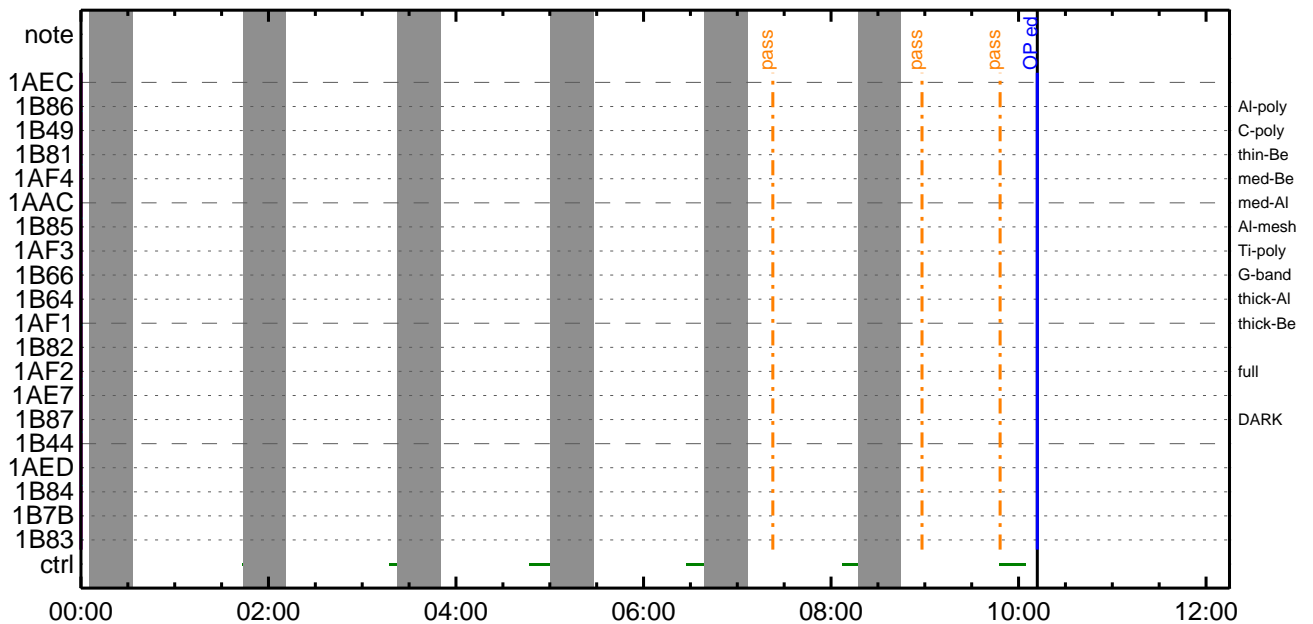
CMDI #0751 2017/05/19



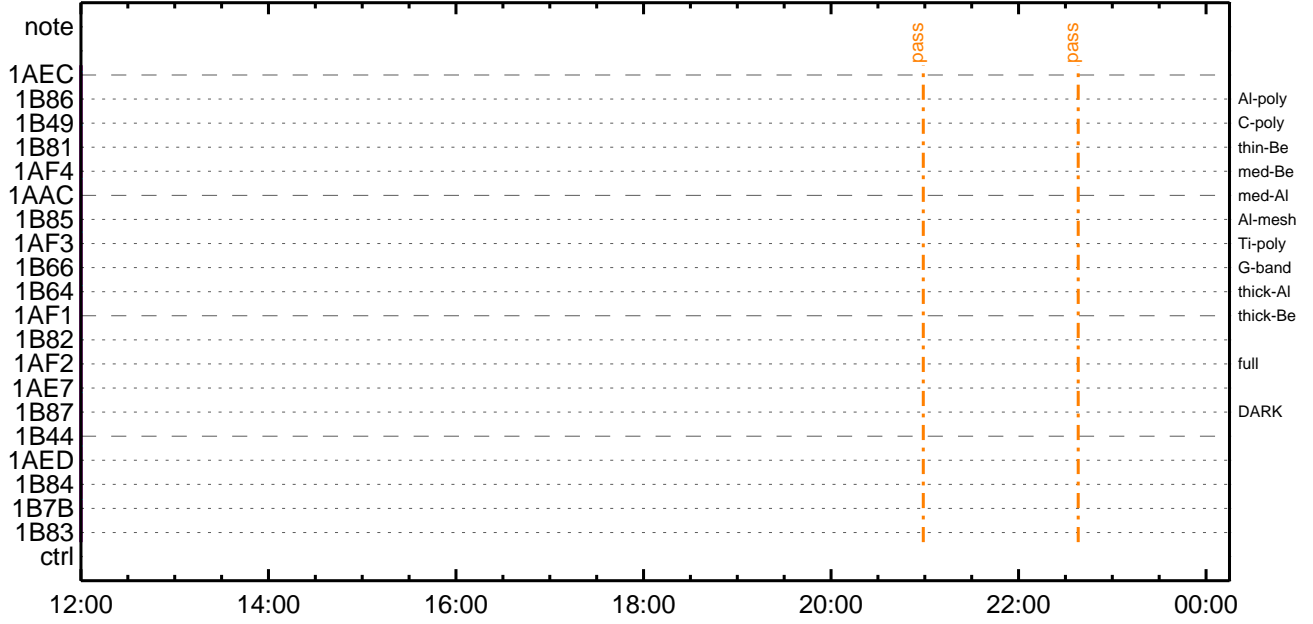
CMDI #0751 2017/05/19



CMDI #0751 2017/05/20



CMDI #0751 2017/05/20




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0096 C.          0100; SET 0EDUMP 0100; 0100; 0100;
0097 C.
0098 C. TI 2017-05-16 11:01:00.0
0099 +. TI 2017-05-16 11:01:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.          0100; SET 0EDUMP 0100; 0100; 0100;
0102 C.
0103 +. TI 2017-05-16 11:01:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.          0100; SET 0EDUMP 0100; 0100; 0100;
0106 C.
0107 +. TI 2017-05-16 11:01:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.          0100; SET 0EDUMP 0100; 0100; 0100;
0110 C.
0111 +. TI 2017-05-16 11:05:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.          0100; SET 0EDUMP 0100; 0100; 0100;
0114 C.
0115 C.          0100; SET 0EDUMP 0100; 0100; 0100;
0116 C.          0100; SET 0EDUMP 0100; 0100; 0100;
0117 C.          0100; SET 0EDUMP 0100; 0100; 0100;
0118 C.          0100; SET 0EDUMP 0100; 0100; 0100;
0119 C.          0100; SET 0EDUMP 0100; 0100; 0100;
0120 C.
0121 C. *****
0122 C. TI 2017-05-16 11:05:59.5
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF; 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC      (03 ab 03 01 02)
0128 C.          0100; SET 0EDUMP 0100; 0100; 0100;
0129 C.          0100; SET 0EDUMP 0100; 0100; 0100;
0130 C.          0100; SET 0EDUMP 0100; 0100; 0100;
0131 C.          0100; SET 0EDUMP 0100; 0100; 0100;
0132 C.          0100; SET 0EDUMP 0100; 0100; 0100;
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC      (07 0b f8)
0135 C.          0100; SET 0EDUMP 0100; 0100; 0100;
0136 C.          0100; SET 0EDUMP 0100; 0100; 0100;
0137 C.          0100; SET 0EDUMP 0100; 0100; 0100;
0138 C.          0100; SET 0EDUMP 0100; 0100; 0100;
0139 C.          0100; SET 0EDUMP 0100; 0100; 0100;
0140 C.
0141 C.          0100; SET 0EDUMP 0100; 0100; 0100;
0142 C.          0100; SET 0EDUMP 0100; 0100; 0100;
0143 C.
0144 C. RAM ID=TI_TBL 0100; SET 0EDUMP 0100; 0100; 0100;
0145 C.
0146 C. DHU 0100; SET 0EDUMP 0100; 0100; 0100;
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC      (02 0a f8)
0149 C.          0100; SET 0EDUMP 0100; 0100; 0100;
0150 C.          0100; SET 0EDUMP 0100; 0100; 0100;
0151 C.          0100; SET 0EDUMP 0100; 0100; 0100;
0152 C.          0100; SET 0EDUMP 0100; 0100; 0100;
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2017-05-16 11:05:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC      (41)
0161 C.
0162 C.          HK1_TI_CMD_NUM = 1 CNTUP [ ]
0163 C.
0164 C. ***** SOT END *****
0165 C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2017-05-16 11:05:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC      (21 02)
0174 +. TI 2017-05-16 11:05:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC      (22)
0177 C.          [ ] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2017-05-16 11:05:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC      (c3)
0187 C.          [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 C. ***** MDP 0100; SET 0EDUMP 0100; 0100; 0100; *****
0192 C. (0100; SET 0EDUMP 0100; 0100; 0100;)
0193 S. DC-BC dcbc-402:DCBC

```

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
```


(a) Spacecraft Operation Procedure (real-commands)

```
main-611 2017-05-16 12:53:25 222 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÄY~¼Ä»Û;ä
0005 C.
0006 C. YÄYß;¼Y³YÞYÓYÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ë¿µÄµ°¸»Í×ÁÇ¿ÍYÇYÄY×YÍ;¼YÉ;ËÈÈ¼µ°ííË;Ë°È¼°ÇÖµ°¿¿¼í¹¿µí;¿Ä®, ù¹µ°Èµ°ÇÄ+¿®µ°¸Èµ°¿µÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+¿µ;ON
0016 C. *****
0017 C. ¸ °¸Ä, í×ÈYµ°äLOSµ°Çµ°í»´Öµ°ð¹íí, µ°; ¿ÉÖÍ×µ°ÈXÄÖONµ°í¹Öµ°Èµ°íµ°Èµ°¿µÈ;f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ¿¿[HK1_XPA_ON/OFF] EQ ON
0025 C. ¿¿[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ¿¿[HK1_XMOD_ON/OFF] EQ ON
0027 C. ¿¿[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYÓYÉYÍYÄY~¾ÖÄÖµ°-°ÄÄÈµ°¿¿µ°é; ¿°È²¼µ°í°¸Ä, ¼È½¿µ°ð¼Ä¹Öµ°íµ°é;f
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°¸Ä,
0033 C. *****
0034 C. ¸" RESTART;ÈPT1;Ëµ°¿¿µ°¿¼í¹¿µ°í; ¿°È²¼µ°í¼Ä¹Öµ°íµ°é; ¿DCBC-150µ°Ø¿Èµ°à;f
0035 C.
0036 . C. ;ãPT1°¸Ä, ³«»Í;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ¿¿[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0043 C. ¿¿[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0044 C. ¿¿[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãY¿YÓYÉYÄYËÄÜÄÖ;ÈÄ•Ä°²óÈð;Ë, áµ°í°¸Ä, °¸³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ¿¿[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0050 C. ¿¿[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0051 C. ¿¿[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°¸Ä, µ°-¼«¸°Ä»ßµ°¿¿µ°á; ¿°È²¼µ°ð¼Ä¹Öµ°íµ°é;f
0055 C. Y¿YÓYÉYÄYËÄÜÄÖµ°Ä•Ä°²óÈðµ°-¶áµ°¿¼í¹¿µ°í°í»µ°íµ°éµ°ÇÄÖµ°Ä;f
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°¸Ä,
0059 C. *****
0060 C. ¸" RESTART;ÈPT2;Ëµ°¿¿µ°¿¼í¹¿µ°í; ¿°È²¼µ°í¼Ä¹Öµ°íµ°é; ¿DCBC-151µ°Ø¿Èµ°à;f
0061 C.
0062 . C. ;ãPT2°¸Ä, ³«»Í;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ¿¿[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0069 C. ¿¿[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0070 C. ¿¿[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãY¿YÓYÉYÄYËÄÜÄÖ;ÈÄ•Ä°²óÈð;Ë, áµ°í°¸Ä, °¸³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ¿¿[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0076 C. ¿¿[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0077 C. ¿¿[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°¸Ä, Ää»ß;¿XÁ+¿µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°¸Ä, Ää»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ¿¿[HK1_REP_STA/STP] EQ STOP
0087 C. ¿¿[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ¿¿[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+¿µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ¿¿[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ¿¿[HK1_XPA_ON/OFF] EQ OFF
```

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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 85 83 08 08)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 c0 c0 10 10)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 20 20)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 40 c0 10 10)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0b 40 40 10 10)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0c c0 40 10 10)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 0d 80 60 20 18)
0142 + DC 07-F0 MDP_XRT_ROI_SET
0143 BC (cd 0e a0 80 18 20)
0144 + DC 07-F0 MDP_XRT_ROI_SET
0145 BC (cd 0f 80 80 06 06)
0146 + DC 07-F0 MDP_XRT_ROI_SET
0147 BC (cd 10 80 80 08 08)
0148 + DC 07-F0 MDP_XRT_FLD_ENA
0149 BC (d8)
0150 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0151 BC (c8)
0152 + DC 07-F0 MDP_XRT_ARS_DIS
0153 BC (d5)
0154 + DC 07-F0 MDP_XRT_AEC_RESET
0155 BC (d0)
0156 + DC 07-F0 MDP_XRT_FLD_RESET
0157 BC (da)
0158 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0159 BC (c4 12)
0160 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0161 BC (c5 07)
0162 . C. ----- Success Verify ? OK / NG ____
0163 C.
0164 C.
0165 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0166 C.
0167 +. DC 07-F0 MDP_XRT_MODE_OBSV
0168 BC (c2)
0169 +. TI 2017-05-16 11:05:02.0
0170 DC 07-F0 MDP_XRT_MODE_OBSV
0171 BC (c2)
0172 . C. ----- Success Verify ? OK / NG ____
0173 C.
0174 C. ***** XRT END *****
0175 . C. *****
0176 C. SOT table upload
0177 C. *****
0178 . C. < Stop SP table >
0179 +. DC 07-F0 MDP_SP_CTRL_MANU
0180 BC (61)
0181 C. -----
0182 C. MDP_SP_CTRL_MODE = MANU [ ]
0183 C. -----
0184 C.
0185 . C. <Upload SP Observation Table>
0186 . S. RAM ram-288:MDP_OBS_S
0187 ( )
0188 C.
0189 . C. < Dump RAMID=MDP_OBS_S >
0190 +. DC 07-F0 MDP_DUMP_SPTBL
0191 BC (83 07 00 00 00 38 b8)
0192 C. -----
0193 C. MDP_OBS_S verify = OK/NG [ ]

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0194 C. -----
0195 C.
0196 C. *****
0197 C. SOT TI command set
0198 C. *****
0199 C. Execute, after the success of TBL upload.
0200 +. TI 2017-05-16 11:05:18.0
0201 DC 07-F0 MDP_SOT_MODE_OBSV
0202 BC (40)
0203 . C. -----
0204 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0205 C. -----
0206 C.
0207 C.
0208 . C. ***** MDP 'úÃîâî»ô¼ÝðÊÂð¹ñëDCBC•x²è *****
0209 C. (¼ã°îÝÓÝÃÝÊÝÞÝËÝáÝçÝèñÊ¼ñ¼Ã»Û¹ñè)
0210 . S. DC-BC dcbc-402:DCBC
0211 (MDP_known_event)
0212 C.
0213 C.
0214 . C. ***** ÝÐÝ¹•Ï Daily±¿ÎÑñÊ´Ø¹ñëDCBC•x²è *****
0215 . S. DC-BC dcbc-153:DCBC
0216 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0217 C.
0218 C.
0219 . C. ;ãLOSÝÁÝ$ÝÃÝ⁻¼Ã»Û;ã
0220 C.
0221 . C. ***** LOS *****
0222 C.
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XRT_OGLIST_0751.chk

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*** OP Sequence for XRT ***

2017/05/16	11:16:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04	00	00	00	00
2017/05/16	18:05:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2017/05/16	18:15:30.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	02	00	00	00	00
2017/05/17	02:15:30.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	01	00	00	00	00
2017/05/17	05:15:30.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	f0	00	52	a7
2017/05/17	05:55:00.0	XRT_TCIB_XRT_S_HTR_A_DIS_404_OG [0x194]							
		TCIB_XRT_S_HTR_A_DIS	0	04-C0					
2017/05/17	11:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/05/17	11:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/05/17	11:59:58.0	XRT_FOCUS_POSITION_439_OG [0x1b7]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2017/05/17	12:00:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	2e	f9	2e	f9
2017/05/17	12:00:18.0	XRT_FLD_DIS_428_OG [0x1ac]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2017/05/17	12:00:20.0	XRT_FLRCTRL_DIS_443_OG [0x1bb]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2017/05/17	12:02:56.0	XRT_ARS_DIS_445_OG [0x1bd]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2017/05/17	12:02:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2017/05/17	12:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/05/17	12:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/05/17	12:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/05/17	12:09:58.0	XRT_FOCUS_POSITION_439_OG [0x1b7]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2017/05/17	12:10:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	2e	f9	d1	07
2017/05/17	12:10:18.0	XRT_FLD_DIS_428_OG [0x1ac]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2017/05/17	12:10:20.0	XRT_FLRCTRL_DIS_443_OG [0x1bb]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2017/05/17	12:12:56.0	XRT_ARS_DIS_445_OG [0x1bd]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2017/05/17	12:12:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	08			
2017/05/17	12:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/05/17	12:19:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/05/17	12:19:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/05/17	12:19:58.0	XRT_FOCUS_POSITION_439_OG [0x1b7]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2017/05/17	12:20:00.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00	d1	07	d1	07
2017/05/17	12:20:18.0	XRT_FLD_DIS_428_OG [0x1ac]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2017/05/17	12:20:20.0	XRT_FLRCTRL_DIS_443_OG [0x1bb]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2017/05/17	12:22:56.0	XRT_ARS_DIS_445_OG [0x1bd]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2017/05/17	12:22:58.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d			
2017/05/17	12:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/05/17	12:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/05/17	12:29:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/05/17	12:29:58.0	XRT_FOCUS_POSITION_439_OG [0x1b7]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2017/05/17	12:30:00.0	AOCS_ORe-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00	d1	07	2e	f9
2017/05/17	12:30:18.0	XRT_FLD_DIS_428_OG [0x1ac]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2017/05/17	12:30:20.0	XRT_FLRCTRL_DIS_443_OG [0x1bb]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2017/05/17	12:32:56.0	XRT_ARS_DIS_445_OG [0x1bd]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2017/05/17	12:32:58.0	XRT_QT_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	10			
2017/05/17	12:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/05/17	12:39:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/05/17	12:39:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/05/17	12:39:58.0	XRT_FOCUS_POSITION_435_OG [0x1b3]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2017/05/17	12:40:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]							

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2017/05/17	12:40:18.0	XRT_FLD_DIS_437_OG [0x1b5]	AOCU_NM	5	02-76	00	ad	59	00	00
			MDP_XRT_FLD_DIS	1	07-F0		d9			
2017/05/17	12:54:54.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]		1	07-F0		c9			
			MDP_XRT_FLRCTRL_DIS	1	07-F0		c9			
2017/05/17	12:54:56.0	XRT_ARS_DIS_445_OG [0x1bd]		1	07-F0		d5			
			MDP_XRT_ARS_DIS	1	07-F0		d5			
2017/05/17	12:54:58.0	XRT_QT_PROG_SET_446_OG [0x1be]		2	07-F0		c4	14		
			MDP_XRT_QT_PROG_SET	2	07-F0		c4	14		
2017/05/17	12:55:00.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0		c0			
			MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2017/05/17	14:39:54.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0		c1			
			MDP_XRT_CTRL_MANU	1	07-F0		c1			
2017/05/17	14:39:56.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0		c1			
			MDP_XRT_CTRL_MANU	1	07-F0		c1			
2017/05/17	14:39:58.0	XRT_FOCUS_POSITION_435_OG [0x1b3]		4	07-F8	22	fe	97	00	
			XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2017/05/17	14:40:00.0	AOCS_Ore-point_Start_11_OG [0x0a1]		5	02-76	00	00	00	56	35
			AOCU_NM	5	02-76	00	00	00	56	35
2017/05/17	14:40:18.0	XRT_FLD_DIS_437_OG [0x1b5]		1	07-F0		d9			
			MDP_XRT_FLD_DIS	1	07-F0		d9			
2017/05/17	14:54:54.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]		1	07-F0		c9			
			MDP_XRT_FLRCTRL_DIS	1	07-F0		c9			
2017/05/17	14:54:56.0	XRT_ARS_DIS_445_OG [0x1bd]		1	07-F0		d5			
			MDP_XRT_ARS_DIS	1	07-F0		d5			
2017/05/17	14:54:58.0	XRT_QT_PROG_SET_418_OG [0x1a2]		2	07-F0		c4	04		
			MDP_XRT_QT_PROG_SET	2	07-F0		c4	04		
2017/05/17	14:55:00.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0		c0			
			MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2017/05/17	17:04:54.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0		c1			
			MDP_XRT_CTRL_MANU	1	07-F0		c1			
2017/05/17	17:04:56.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0		c1			
			MDP_XRT_CTRL_MANU	1	07-F0		c1			
2017/05/17	17:04:58.0	XRT_FOCUS_POSITION_403_OG [0x193]		4	07-F8	22	ff	aa	00	
			XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2017/05/17	17:05:00.0	AOCS_Ore-point_Start_2_OG [0x098]		5	02-76	00	00	00	00	00
			AOCU_NM	5	02-76	00	00	00	00	00
2017/05/17	17:05:18.0	XRT_ROI_A_421_OG [0x1a5]		6	07-F0	cd	05	85	83	06
			MDP_XRT_ROI_SET	6	07-F0	cd	05	85	83	06
			MDP_XRT_ROI_SET	6	07-F0	cd	06	85	83	06
			MDP_XRT_ROI_SET	6	07-F0	cd	07	85	83	08
			MDP_XRT_ROI_SET	6	07-F0	cd	08	80	80	20
			MDP_XRT_ROI_SET	6	07-F0	cd	09	80	80	20
			MDP_XRT_ROI_SET	6	07-F0	cd	0a	80	80	20
			MDP_XRT_ROI_SET	6	07-F0	cd	0f	80	80	06
			MDP_XRT_ROI_SET	6	07-F0	cd	10	80	80	08
2017/05/17	17:05:23.0	XRT_FLD_DIS_406_OG [0x196]		1	07-F0		d9			
			MDP_XRT_FLD_DIS	1	07-F0		d9			
2017/05/17	17:07:59.0	XRT_FLRCTRL_DIS_405_OG [0x195]		1	07-F0		c9			
			MDP_XRT_FLRCTRL_DIS	1	07-F0		c9			
2017/05/17	17:08:01.0	XRT_ARS_DIS_423_OG [0x1a7]		1	07-F0		d5			
			MDP_XRT_ARS_DIS	1	07-F0		d5			
2017/05/17	17:08:03.0	XRT_QT_PROG_SET_429_OG [0x1ad]		2	07-F0		c4	0c		
			MDP_XRT_QT_PROG_SET	2	07-F0		c4	0c		
2017/05/17	17:08:05.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0		c0			
			MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2017/05/17	17:14:54.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0		c1			
			MDP_XRT_CTRL_MANU	1	07-F0		c1			
2017/05/17	17:14:56.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0		c1			
			MDP_XRT_CTRL_MANU	1	07-F0		c1			
2017/05/17	17:14:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]		4	07-F8	22	fe	97	00	
			XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2017/05/17	17:15:00.5	AOCS_Ore-point_Start_1_OG [0x097]		5	02-76	04	00	00	00	00
			AOCU_NM	5	02-76	04	00	00	00	00
2017/05/17	17:15:18.0	XRT_FLD_ENA_411_OG [0x19b]		1	07-F0		d8			
			MDP_XRT_FLD_ENA	1	07-F0		d8			
2017/05/17	17:15:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		1	07-F0		c8			
			MDP_XRT_FLRCTRL_ENA	1	07-F0		c8			
2017/05/17	17:15:22.0	XRT_AEC_RESET_448_OG [0x1c0]		1	07-F0		d0			
			MDP_XRT_AEC_RESET	1	07-F0		d0			
2017/05/17	17:15:24.0	XRT_ARS_DIS_423_OG [0x1a7]		1	07-F0		d5			
			MDP_XRT_ARS_DIS	1	07-F0		d5			
2017/05/17	17:15:26.0	XRT_FLD_RESET_433_OG [0x1b1]		1	07-F0		da			
			MDP_XRT_FLD_RESET	1	07-F0		da			
2017/05/17	17:17:56.0	XRT_QT_PROG_SET_442_OG [0x1ba]		2	07-F0		c4	12		
			MDP_XRT_QT_PROG_SET	2	07-F0		c4	12		
2017/05/17	17:17:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]		2	07-F0		c5	07		
			MDP_XRT_FL_PROG_SET	2	07-F0		c5	07		
2017/05/17	17:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0		c0			
			MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2017/05/17	18:00:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0		c1			
			MDP_XRT_CTRL_MANU	1	07-F0		c1			
2017/05/17	18:00:02.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0		c1			
			MDP_XRT_CTRL_MANU	1	07-F0		c1			
2017/05/17	18:00:04.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0		da			
			MDP_XRT_FLD_RESET	1	07-F0		da			
2017/05/17	18:00:06.0	XRT_PREFLR_STRT_414_OG [0x19e]		1	07-F0		e8			
			MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2017/05/17	18:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0		e9			
			MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2017/05/17	18:40:31.0	XRT_Custom_430_OG [0x1ae]								
2017/05/17	18:41:31.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0		c0			
			MDP_XRT_CTRL_AUTO	1	07-F0		c0			

2017/05/17	19:38:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/05/17	19:38:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/05/17	19:38:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/05/17	19:38:06.0	XRT_PREFLR_STRT_414_OG [0x19e]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/05/17	19:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/05/17	20:17:31.0	XRT_Custom_430_OG [0x1ae]			
2017/05/17	20:18:31.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/05/17	21:16:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/05/17	21:16:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/05/17	21:16:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/05/17	21:16:36.0	XRT_PREFLR_STRT_414_OG [0x19e]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/05/17	21:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/05/17	21:54:01.0	XRT_Custom_430_OG [0x1ae]			
2017/05/17	21:55:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/05/17	22:55:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/05/17	22:55:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/05/17	22:55:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/05/17	22:55:06.0	XRT_PREFLR_STRT_414_OG [0x19e]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/05/17	22:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/05/17	23:28:31.0	XRT_Custom_430_OG [0x1ae]			
2017/05/17	23:29:31.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/05/18	00:33:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/05/18	00:33:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/05/18	00:33:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/05/18	00:33:36.0	XRT_PREFLR_STRT_414_OG [0x19e]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/05/18	00:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/05/18	01:00:30.0	XRT_Custom_430_OG [0x1ae]			
2017/05/18	01:01:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/05/18	02:08:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/05/18	02:08:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/05/18	02:08:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/05/18	02:08:36.0	XRT_PREFLR_STRT_414_OG [0x19e]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/05/18	02:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/05/18	02:39:00.0	XRT_Custom_430_OG [0x1ae]			
2017/05/18	02:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/05/18	03:42:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/05/18	03:42:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/05/18	03:42:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/05/18	03:42:36.0	XRT_PREFLR_STRT_414_OG [0x19e]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/05/18	03:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/05/18	04:17:30.0	XRT_Custom_430_OG [0x1ae]			
2017/05/18	04:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/05/18	05:15:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/05/18	05:15:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/05/18	05:15:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/05/18	05:15:06.0	XRT_PREFLR_STRT_414_OG [0x19e]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/05/18	05:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/05/18	05:55:30.0	XRT_Custom_430_OG [0x1ae]			
2017/05/18	05:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/05/18	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1

2017/05/18	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/05/18	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2017/05/18	06:00:00.0	AOCS_ORe-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00 00 00 00 00
2017/05/18	06:00:18.0	XRT_FLD_DIS_406_OG [0x196] MDP_XRT_FLD_DIS	1	07-F0	d9
2017/05/18	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2017/05/18	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2017/05/18	06:02:58.0	XRT_QT_PROG_SET_429_OG [0x1ad] MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2017/05/18	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/05/18	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/05/18	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/05/18	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2017/05/18	06:10:00.0	AOCS_ORe-point_Start_1_OG [0x097] AOCU_NM	5	02-76	04 00 00 00 00
2017/05/18	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2017/05/18	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2017/05/18	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0
2017/05/18	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2017/05/18	06:10:26.0	XRT_FLD_RESET_433_OG [0x1b1] MDP_XRT_FLD_RESET	1	07-F0	da
2017/05/18	06:12:56.0	XRT_QT_PROG_SET_442_OG [0x1ba] MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2017/05/18	06:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4] MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2017/05/18	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/05/18	06:55:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/05/18	06:55:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/05/18	06:55:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2017/05/18	06:55:36.0	XRT_PREFLR_STRT_414_OG [0x19e] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/05/18	06:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/05/18	07:34:00.0	XRT_Custom_430_OG [0x1ae]			
2017/05/18	07:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/05/18	08:35:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/05/18	08:35:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/05/18	08:35:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2017/05/18	08:35:06.0	XRT_PREFLR_STRT_414_OG [0x19e] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/05/18	08:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/05/18	09:12:30.0	XRT_Custom_430_OG [0x1ae]			
2017/05/18	09:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/05/18	10:16:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/05/18	10:16:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/05/18	10:16:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2017/05/18	10:16:06.0	XRT_PREFLR_STRT_414_OG [0x19e] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/05/18	10:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/05/18	10:50:30.0	XRT_Custom_430_OG [0x1ae]			
2017/05/18	10:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/05/18	11:20:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/05/18	11:21:00.0	AOCS_ORe-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00 00 00 00 00