

XRT Timeline to be uploaded on 2017/08/24

Period: 2017/08/24 11:28:00 - 2017/08/29 11:22:00

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Normal mode

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XOB #1BB9: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
08/24 11:41:00 - 08/24 17:35:00	Track (-463.0, -32.1) <small>@ 08/24 11:38:00</small>	AR 12672
08/24 18:22:30 - 08/25 05:55:24	Track (-408.4, -35.7) <small>@ 08/24 18:19:30</small>	AR 12672
08/25 06:08:30 - 08/25 17:57:54	Track (-308.2, -40.9) <small>@ 08/25 06:05:30</small>	AR 12672
08/25 18:11:00 - 08/26 03:46:00	Track (-201.5, -44.8) <small>@ 08/25 18:08:00</small>	AR 12672

PROG= 20 1-time(s)

Subr= 1 1-time(s) 2.0sec													
Seqn= 92 1-time(s) 2.0sec	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec	
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
Subr= 2 5-time(s) 2.0sec													
Seqn= 75 1-time(s) 2.0sec	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec	
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
Seqn= 96 4-time(s) 90.0sec													
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec	
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec	
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec	
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec	
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec	
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec	

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #1BA7: Synoptic Q95 2x2 - Al/mesh(8/181/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(16/181/4096) + Th

Term	Pointing (x, y)	Comment
08/24 18:12:30 - 08/24 18:19:24	Fixed (0.0, 0.0)	synoptic, shifted 9.5 min
08/25 05:58:30 - 08/25 06:05:24	Fixed (0.0, 0.0)	synoptic, shifted -4.5 min
08/25 18:01:00 - 08/25 18:07:54	Fixed (0.0, 0.0)	synoptic, shifted -2.0 min
08/26 06:03:00 - 08/26 06:59:30	Fixed (0.0, 0.0)	synoptic

PROG= 02 1-time(s)

Subr= 1 1-time(s) 2.0sec													
Seqn= 5 1-time(s) 2.0sec	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec	
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec	
Seqn= 26 1-time(s) 2.0sec													
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 73 1-time(s) 2.0sec													
Al-poly/Open	Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 44 1-time(s) 2.0sec													
thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 23 1-time(s) 2.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec	
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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Flare mode

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XOB #1B8E: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
08/24 11:41:00 - 08/24 17:35:00	Track (-463.0, -32.1) <small>@ 08/24 11:38:00</small>	AR 12672
08/24 18:22:30 - 08/25 05:55:24	Track (-408.4, -35.7) <small>@ 08/24 18:19:30</small>	AR 12672
08/25 06:08:30 - 08/25 17:57:54	Track (-308.2, -40.9) <small>@ 08/25 06:05:30</small>	AR 12672
08/25 18:11:00 - 08/26 03:46:00	Track (-201.5, -44.8) <small>@ 08/25 18:08:00</small>	AR 12672

PROG= 13 30-time(s)

Subr= 1 20-time(s) 2.0sec													
Seqn= 11 1-time(s) 2.0sec	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn=100 1-time(s) 10.0sec	thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec

med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 10 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 87 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

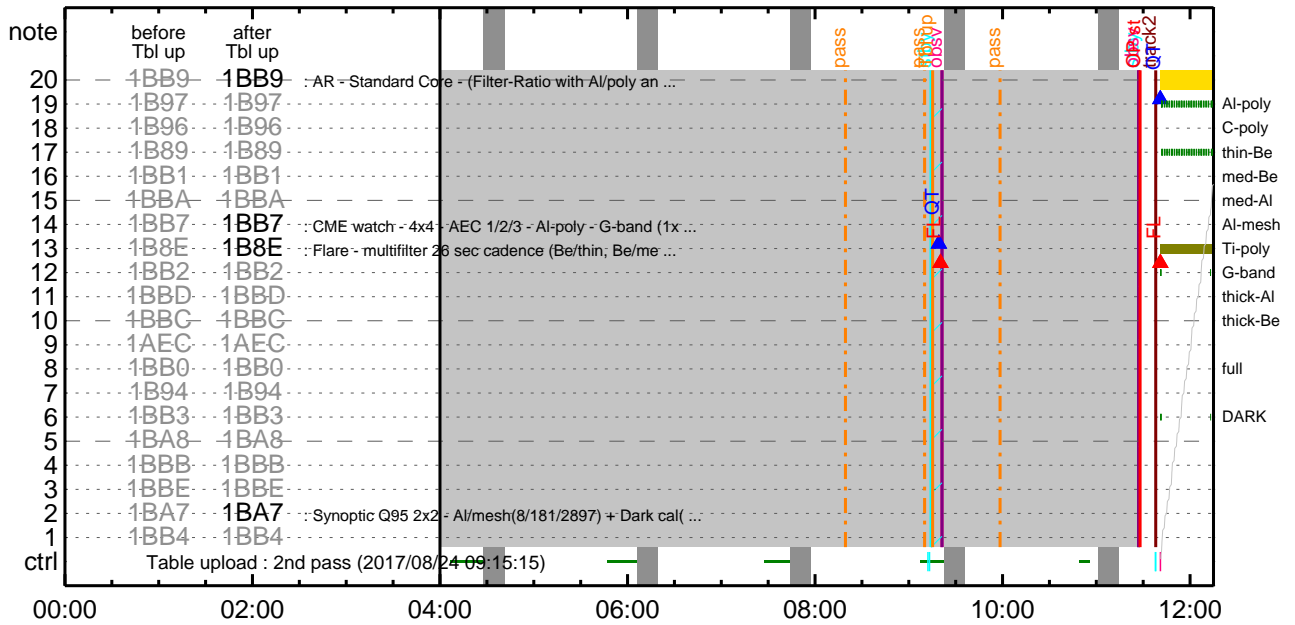
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Flare Detection

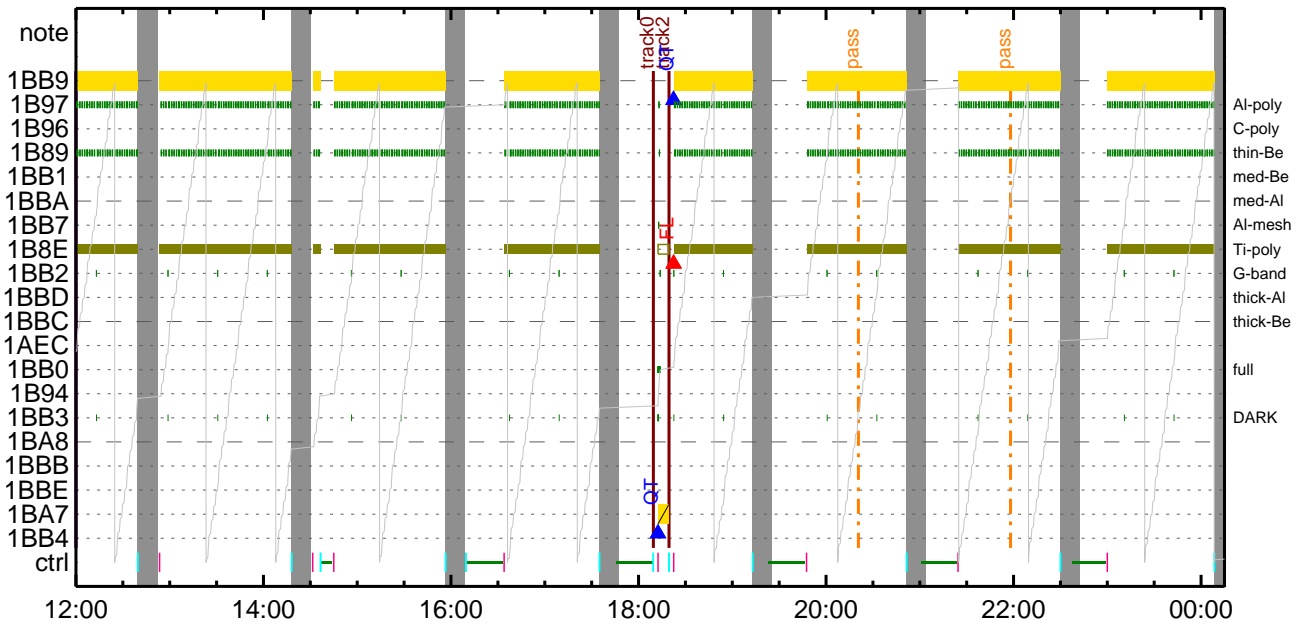
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FLD Patrol												
Term			Pointing (x, y)					Comment				
08/24 18:19:48 - 08/25 05:55:48	Track (-408.4,	-35.7)	@ 08/24 18:19:30	AR	12672							
08/25 06:05:48 - 08/25 17:58:18	Track (-308.2,	-40.9)	@ 08/25 06:05:30	AR	12672							
08/25 18:08:18 - 08/26 06:00:18	Track (-201.5,	-44.8)	@ 08/25 18:08:00	AR	12672							
Al-poly/Open	Al-poly/Open	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

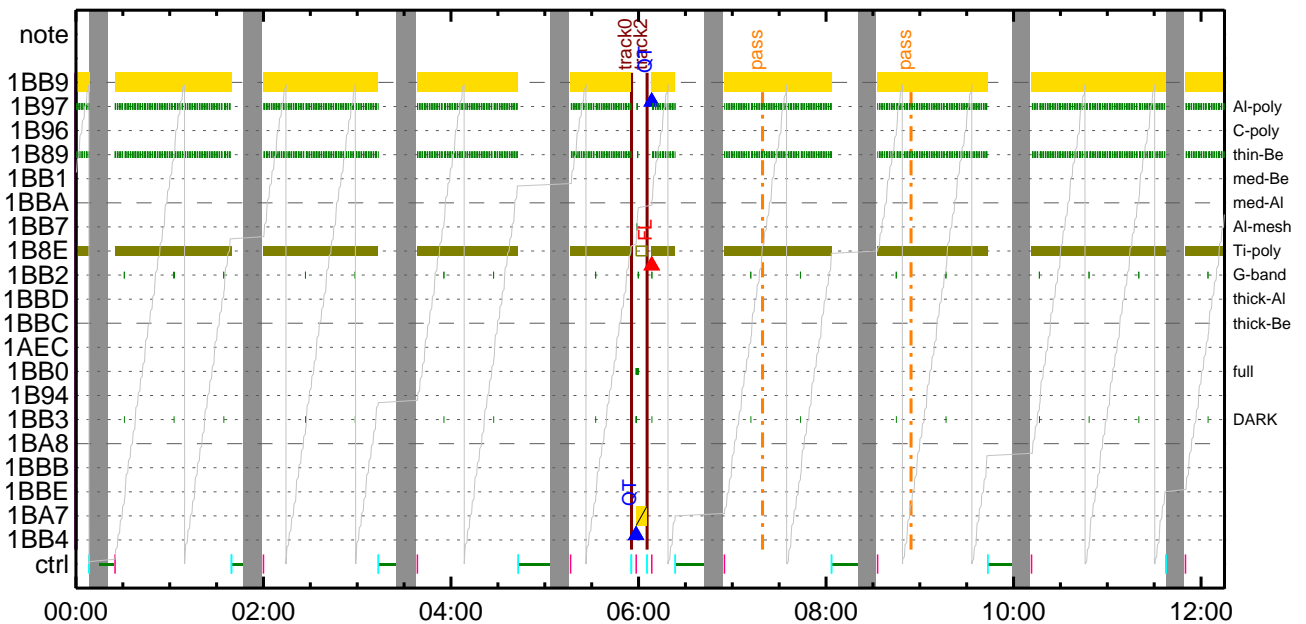
CMDI #0944 2017/08/24



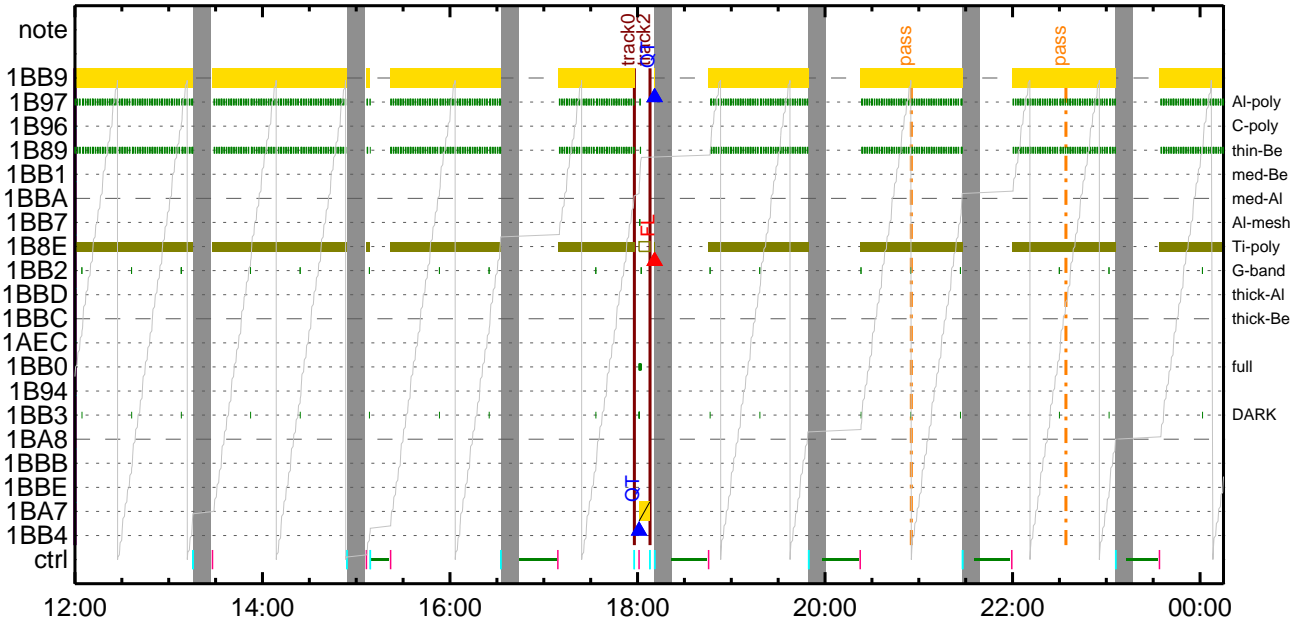
CMDI #0944 2017/08/24



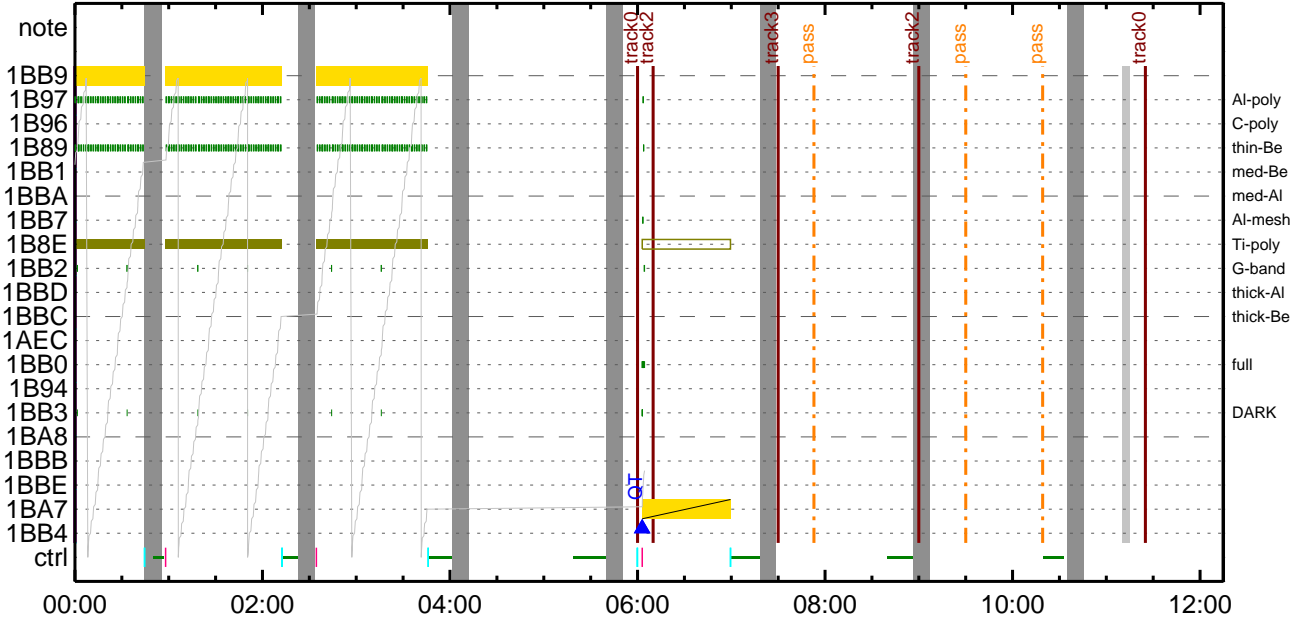
CMDI #0944 2017/08/25



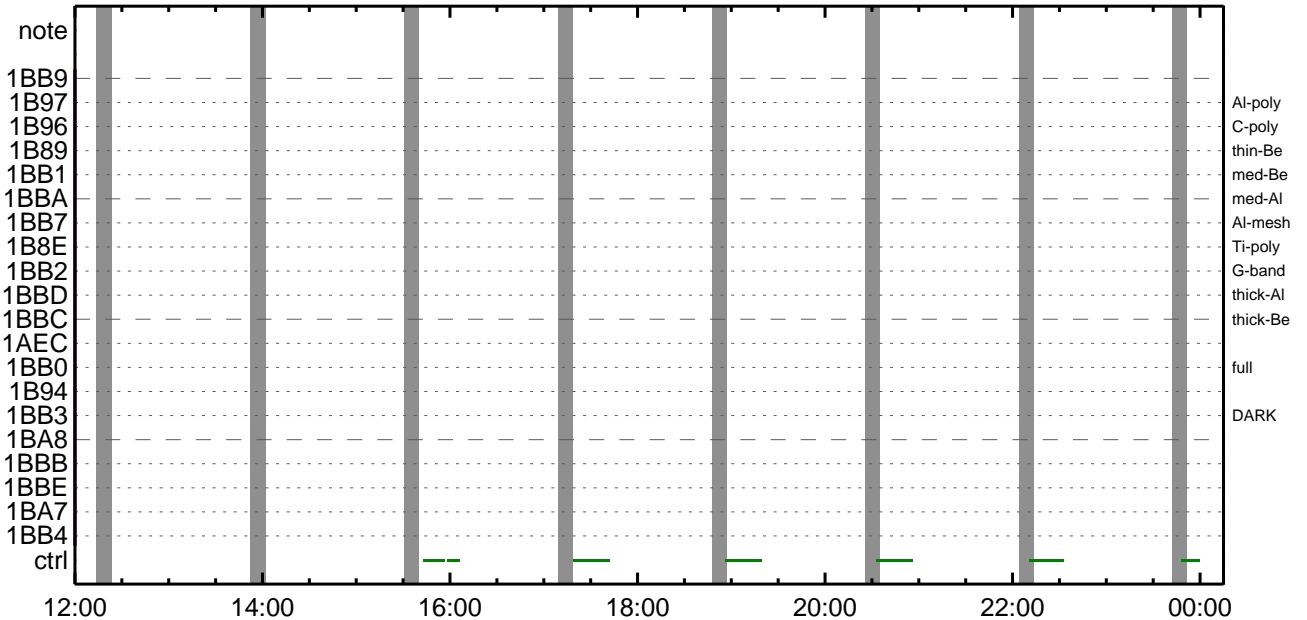
CMDI #0944 2017/08/25



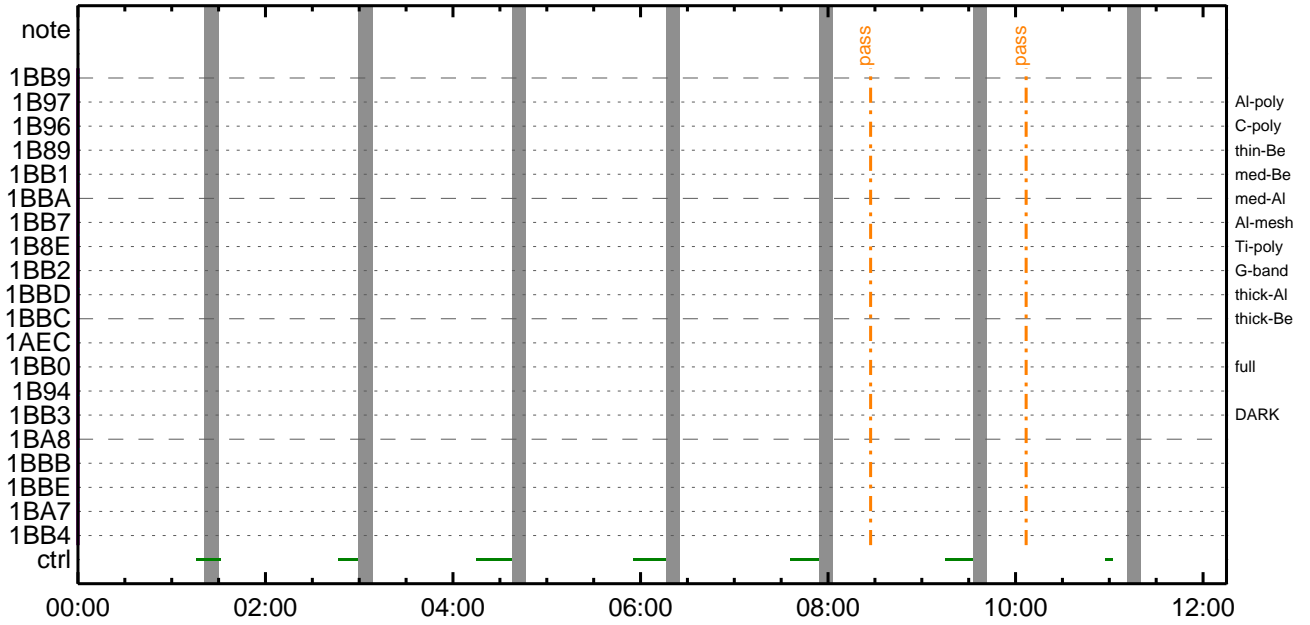
CMDI #0944 2017/08/26



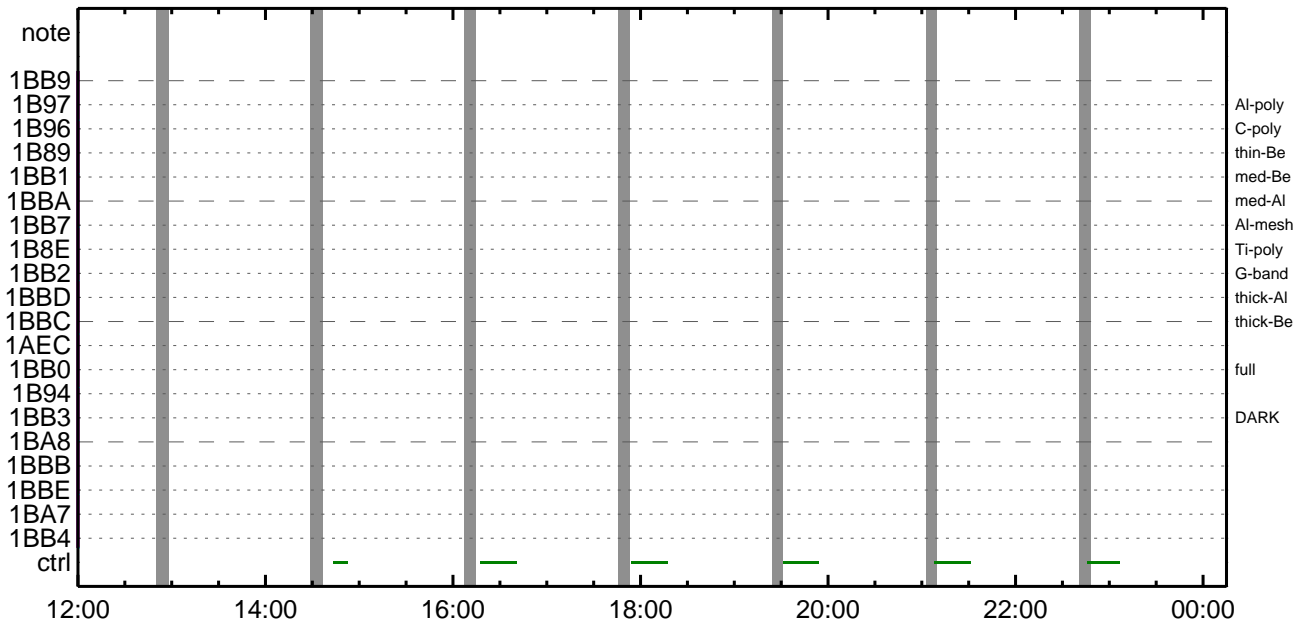
CMDI #0944 2017/08/26



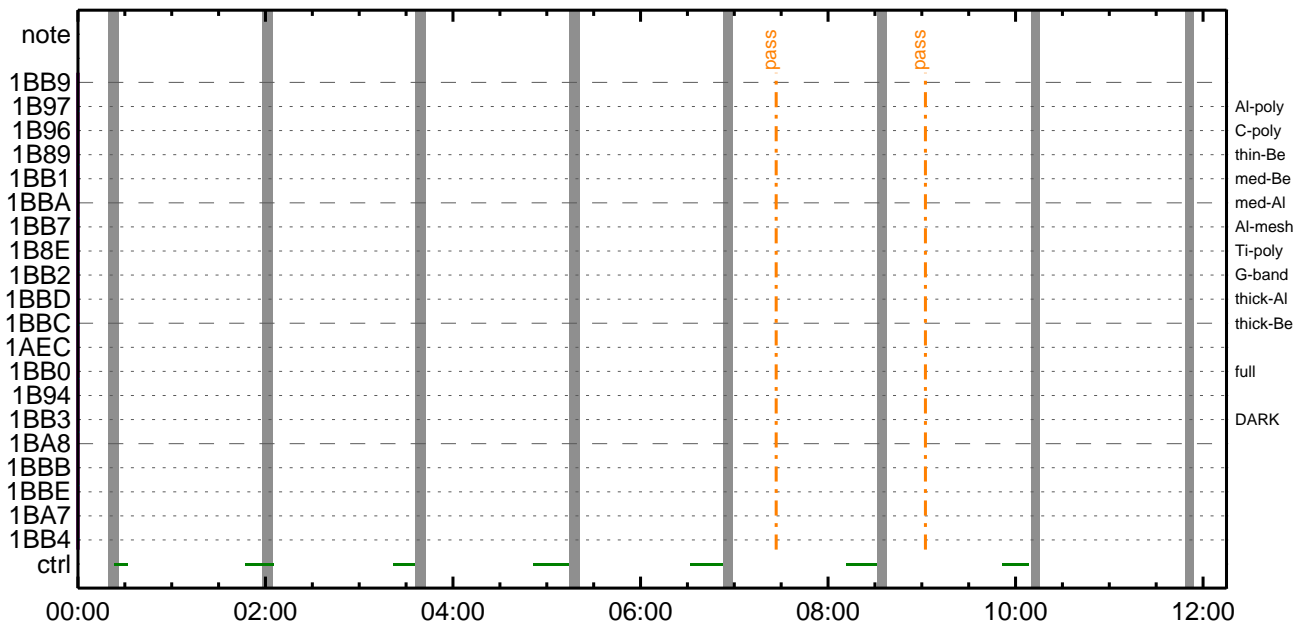
CMDI #0944 2017/08/27



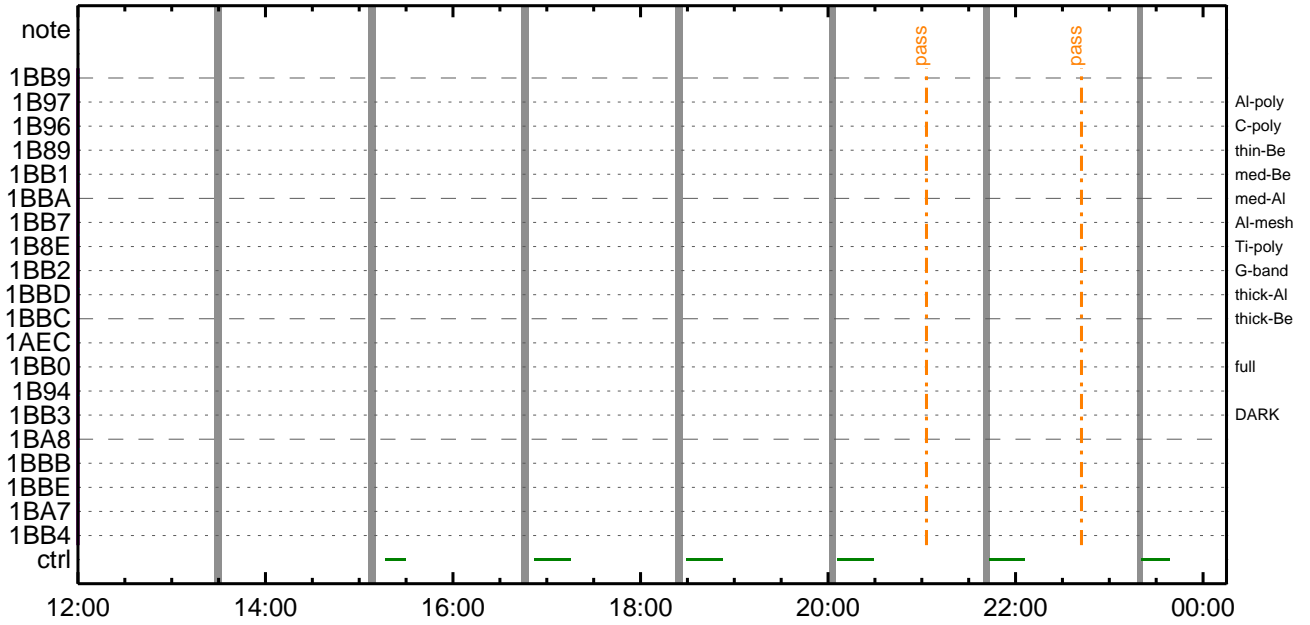
CMDI #0944 2017/08/27



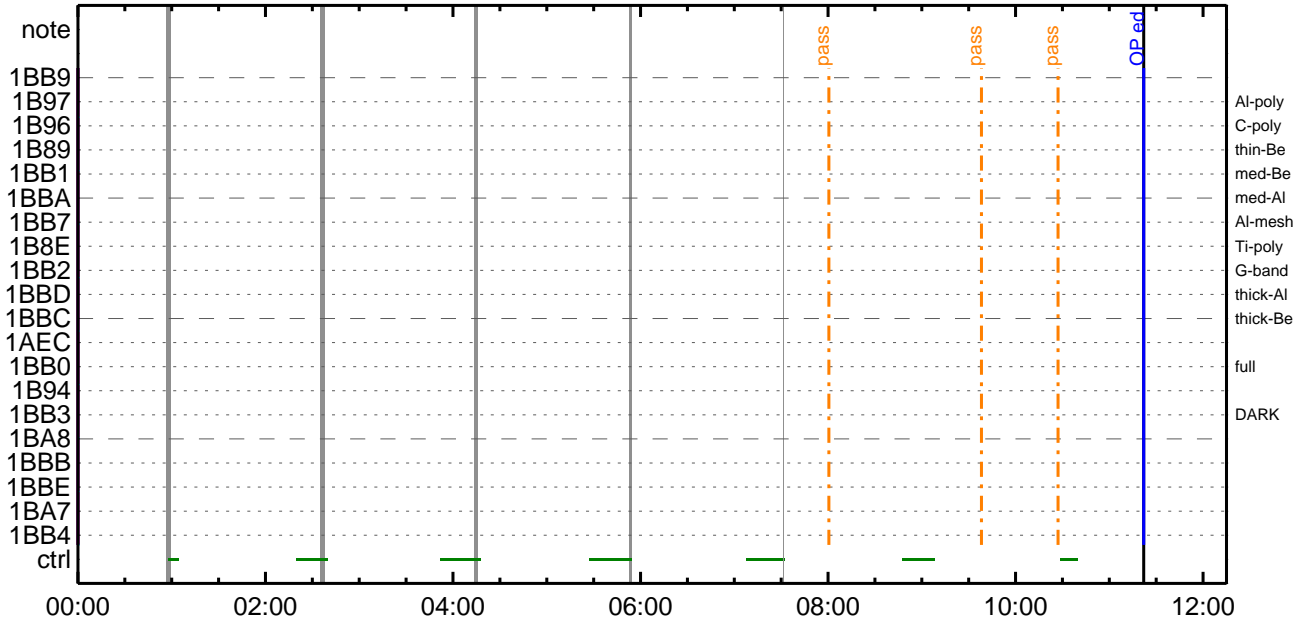
CMDI #0944 2017/08/28



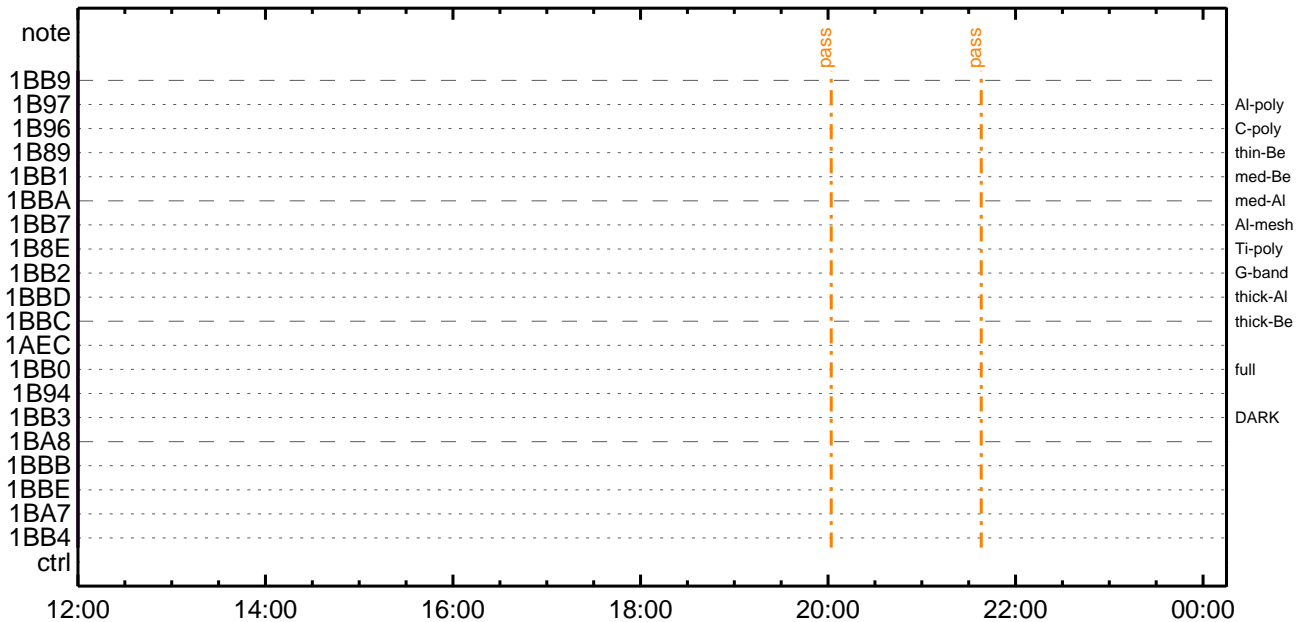
CMDI #0944 2017/08/28



CMDI #0944 2017/08/29



CMDI #0944 2017/08/29




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0194 C.
0195 +. TI 2017-08-24 11:27:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]                EQ      1COUNTUP
0198 C.
0199 C. °È²¼ãïÄë%îíÑõîîŷÄŷ§ŷÄŷ¹àîÜ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]            EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]                EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]            EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]            EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]            EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]            EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]            EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]          EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]            EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]              EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]            EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]          EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]          EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]            EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»õ³îÇ§
0226 C.          çç[HK1_DMP_CHK_FLG]            EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLõîîÈ¹çªè²îOKõ³îÇ§
0229 C.
0230 C. DHUŷã;¼ŷÈ;È¼ŷ¼.ŷî;¼ŷÈ;Èõðîãõ¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]              EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]            EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]          EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]          EQ      4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2017-08-24 11:27:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2017-08-24 11:27:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.          [ ] [HK1_TI_CMD_NUM]            EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.
0255 C. ***** XRT START *****
0256 C. Execute, after the success of OP upload.
0257 +. TI 2017-08-24 11:27:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC      (c3)
0260 C.          [ ] [HK1_TI_CMD_NUM]            EQ      1COUNTUP
0261 C.
0262 C. ***** XRT END *****
0263 C.
0264 C. ***** MDP `ûÄîõî»õ¼ŷõÈÄõ¹õèDCBC•x²è *****
0265 C. (¼ª°îŷÖŷÄŷÈŷŷÈŷáŷçŷèõÈ¼õ¼Ä»Ûõ¹õè)
0266 S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 C. ***** ŷDŷ¹.İ Daily±;îÑõÈ¹õèDCBC•x²è *****
0271 S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 C. îãLOSŷÄŷ§ŷÄŷ¹¼ª»Û;ã
0276 C.
0277 C. ***** LOS *****
0278 C.

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0096 BC (da)
0097 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0098 BC (c4 0e)
0099 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0100 BC (c5 0d)
0101 . C. ----- Success Verify ? OK / NG ____
0102 C.
0103 C.
0104 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0105 C.
0106 +. DC 07-F0 MDP_XRT_MODE_OBSV
0107 BC (c2)
0108 +. TI 2017-08-24 11:27:02.0
0109 DC 07-F0 MDP_XRT_MODE_OBSV
0110 BC (c2)
0111 . C. ----- Success Verify ? OK / NG ____
0112 C.
0113 C. ***** XRT END *****
0114 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0115 +. DC 07-FC EIS_MODE_CHG_ENA
0116 BC (20)
0117 . C. Verify EIS_MODE_CHG_FLG is ENA
0118 +. DC 07-FC EIS_MODE_MANU
0119 BC (21 02)
0120 . C. Verify EIS in MANUAL mode
0121 . C. Estimated OBSTBL upload time is 52s
0122 C. *****
0123 C. EIS START OBSTBL LOAD
0124 C. *****
0125 . S. RAM ram-820:EIS_OBSTBL
0126 ( )
0127 +. DC 07-FC EIS_DUMP_OBSTBL
0128 BC (07 07 07 00 00 70 00)
0129 C.
0130 C. Execute, after the success of OBSTBL upload.
0131 C. Set EIS TI-commands
0132 +. TI 2017-08-24 11:27:50.0
0133 DC 07-FC EIS_MODE_CHG_ENA
0134 BC (20)
0135 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0136 C. *****
0137 C. EIS END OBSTBL LOAD
0138 C. *****
0139 C.
0140 . C. ***** MDP 'uÃîâî»ö¼Ý«ËÄð«¹«èDCBC•×²è *****
0141 C. (%á°îÝÓÝÄÝËÝÞÝËÝáÝçÝè«¼«¼Ä»Û«¹«è)
0142 . S. DC-BC dcbc-402:DCBC
0143 (MDP_known_event)
0144 C.
0145 C.
0146 . C. ***** ÝÐÝ¹•ï Daily±¿îÑ«Ë´Ø«¹«èDCBC•×²è *****
0147 . S. DC-BC dcbc-153:DCBC
0148 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0149 C.
0150 C.
0151 . C. ;ãLOSÝÄÝ§ÝÄÝ¹¼Ä»Û;ä
0152 C.
0153 . C. ***** LOS *****
0154 C.

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0096 C.
0097 C.
0098 C.
0099 . C. ***** MDP ´ûÃîñî»ò¼ÿñÈÄðñ¹ñèDCBC•x²è *****
0100 C. (¼á°ìÿÓÿÃÿÈÿÿÿÿÿáÿçÿèñÈ¼ñ¼¼¼»Ûñ¹ñè)
0101 . S. DC-BC dcbc-402:DCBC
0102 (MDP_known_event)
0103 C.
0104 C.
0105 . C. ***** ÿDÿ¹•ï Daily±¿îññÈ´øñ¹ñèDCBC•x²è *****
0106 . S. DC-BC dcbc-153:DCBC
0107 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0108 C.
0109 C.
0110 . C. ¡ãLOSÿÁÿSÿÿÿÿ¼Ä»Û¿ã
0111 C.
0112 . C. ***** LOS *****
0113 C.

*** OP Sequence for XRT ***

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2017/08/24 11:37:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2017/08/24 11:37:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2017/08/24 11:37:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2017/08/24 11:38:00.0 AOCs_Ore-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 02 00 00 00 00
2017/08/24 11:38:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2017/08/24 11:38:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2017/08/24 11:38:22.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2017/08/24 11:38:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2017/08/24 11:38:26.0 XRT_FLD_RESET_433_OG [0x1b1]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2017/08/24 11:40:56.0 XRT_QT_PROG_SET_446_OG [0x1be]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 14
2017/08/24 11:40:58.0 XRT_FL_PROG_SET_440_OG [0x1b8]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 0d
2017/08/24 11:41:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2017/08/24 12:39:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2017/08/24 12:39:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2017/08/24 12:39:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2017/08/24 12:39:36.0 XRT_PREFLR_STRT_449_OG [0x1c1]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2017/08/24 12:42:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2017/08/24 12:52:30.0 XRT_Custom_430_OG [0x1ae]
2017/08/24 12:53:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2017/08/24 14:18:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2017/08/24 14:18:02.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2017/08/24 14:18:04.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2017/08/24 14:18:06.0 XRT_PREFLR_STRT_449_OG [0x1c1]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2017/08/24 14:21:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2017/08/24 14:30:30.0 XRT_Custom_430_OG [0x1ae]
2017/08/24 14:31:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2017/08/24 14:36:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2017/08/24 14:36:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2017/08/24 14:36:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2017/08/24 14:36:36.0 XRT_PREFLR_STRT_449_OG [0x1c1]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2017/08/24 14:39:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2017/08/24 14:44:00.0 XRT_Custom_430_OG [0x1ae]
2017/08/24 14:45:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2017/08/24 15:56:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2017/08/24 15:56:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2017/08/24 15:56:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2017/08/24 15:56:36.0 XRT_PREFLR_STRT_449_OG [0x1c1]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2017/08/24 15:59:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2017/08/24 16:09:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2017/08/24 16:09:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2017/08/24 16:09:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2017/08/24 16:09:36.0 XRT_PREFLR_STRT_449_OG [0x1c1]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2017/08/24 16:12:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2017/08/24 16:33:00.0 XRT_Custom_430_OG [0x1ae]
2017/08/24 16:34:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2017/08/24 17:35:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2017/08/24 17:35:02.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2017/08/24 17:35:04.0 XRT_FLD_RESET_415_OG [0x19f]

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2017/08/24	17:35:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/08/24	17:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/08/24	18:09:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/08/24	18:09:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/08/24	18:09:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/08/24	18:09:30.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2017/08/24	18:09:48.0	XRT_FLD_DIS_425_OG [0x1a9]	AOCU_NM	5	02-76	00 00 00 00 00
2017/08/24	18:12:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2017/08/24	18:12:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2017/08/24	18:12:28.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5
2017/08/24	18:12:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2017/08/24	18:19:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/08/24	18:19:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/08/24	18:19:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/08/24	18:19:30.0	AOCS_Ore-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2017/08/24	18:19:48.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02 00 00 00 00
2017/08/24	18:19:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2017/08/24	18:19:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2017/08/24	18:19:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2017/08/24	18:19:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2017/08/24	18:22:26.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/08/24	18:22:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2017/08/24	18:22:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2017/08/24	19:13:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/08/24	19:13:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/08/24	19:13:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/08/24	19:13:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/08/24	19:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/08/24	19:46:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/08/24	19:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0
2017/08/24	20:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/08/24	20:51:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/08/24	20:51:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/08/24	20:51:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/08/24	20:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/08/24	21:23:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/08/24	21:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0
2017/08/24	22:30:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/08/24	22:30:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/08/24	22:30:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/08/24	22:30:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/08/24	22:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/08/24	22:59:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/08/24	23:00:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0
2017/08/25	00:08:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/08/25	00:08:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/08/25	00:08:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/08/25	00:08:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da

2017/08/25	00:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/08/25	00:24:00.0	XRT_Custom_430_OG [0x1ae]					
2017/08/25	00:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/08/25	01:39:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/08/25	01:39:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/08/25	01:39:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2017/08/25	01:39:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/08/25	01:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/08/25	01:59:00.0	XRT_Custom_430_OG [0x1ae]					
2017/08/25	02:00:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/08/25	03:13:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/08/25	03:13:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/08/25	03:13:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2017/08/25	03:13:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/08/25	03:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/08/25	03:37:30.0	XRT_Custom_430_OG [0x1ae]					
2017/08/25	03:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/08/25	04:43:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/08/25	04:43:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/08/25	04:43:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2017/08/25	04:43:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/08/25	04:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/08/25	05:15:30.0	XRT_Custom_430_OG [0x1ae]					
2017/08/25	05:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/08/25	05:55:24.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/08/25	05:55:26.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/08/25	05:55:28.0	XRT_FOCUS_POSITION_403_OG [0x193]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2017/08/25	05:55:30.0	AOCS_Ore-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2017/08/25	05:55:48.0	XRT_FLD_DIS_425_OG [0x1a9]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2017/08/25	05:58:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2017/08/25	05:58:26.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2017/08/25	05:58:28.0	XRT_QT_PROG_SET_437_OG [0x1b5]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 02	
2017/08/25	05:58:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/08/25	06:05:24.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/08/25	06:05:26.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/08/25	06:05:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2017/08/25	06:05:30.0	AOCS_Ore-point_Start_1_OG [0x097]					
			AOCU_NM	5	02-76	02 00 00 00 00	
2017/08/25	06:05:48.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2017/08/25	06:05:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2017/08/25	06:05:52.0	XRT_AEC_RESET_448_OG [0x1c0]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2017/08/25	06:05:54.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2017/08/25	06:05:56.0	XRT_FLD_RESET_433_OG [0x1b1]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2017/08/25	06:08:26.0	XRT_QT_PROG_SET_446_OG [0x1be]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 14	
2017/08/25	06:08:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2017/08/25	06:08:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/08/25	06:23:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/08/25	06:23:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/08/25	06:23:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	

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2017/08/25	06:23:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/08/25	06:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/08/25	06:54:00.0	XRT_Custom_430_OG [0x1ae]			
2017/08/25	06:55:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/08/25	08:03:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/08/25	08:03:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/08/25	08:03:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/08/25	08:03:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/08/25	08:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/08/25	08:32:00.0	XRT_Custom_430_OG [0x1ae]			
2017/08/25	08:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/08/25	09:43:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/08/25	09:43:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/08/25	09:43:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/08/25	09:43:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/08/25	09:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/08/25	10:10:30.0	XRT_Custom_430_OG [0x1ae]			
2017/08/25	10:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/08/25	11:37:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/08/25	11:37:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/08/25	11:37:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/08/25	11:37:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/08/25	11:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/08/25	11:49:00.0	XRT_Custom_430_OG [0x1ae]			
2017/08/25	11:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/08/25	13:15:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/08/25	13:15:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/08/25	13:15:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/08/25	13:15:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/08/25	13:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/08/25	13:27:00.0	XRT_Custom_430_OG [0x1ae]			
2017/08/25	13:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/08/25	14:54:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/08/25	14:54:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/08/25	14:54:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/08/25	14:54:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/08/25	14:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/08/25	15:05:30.0	XRT_Custom_430_OG [0x1ae]			
2017/08/25	15:06:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/08/25	15:09:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/08/25	15:09:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/08/25	15:09:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/08/25	15:09:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/08/25	15:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/08/25	15:21:00.0	XRT_Custom_430_OG [0x1ae]			
2017/08/25	15:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/08/25	16:32:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/08/25	16:32:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/08/25	16:32:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/08/25	16:32:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8

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2017/08/25	16:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/08/25	17:08:00.0	XRT_Custom_430_OG [0x1ae]							
2017/08/25	17:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/08/25	17:57:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/08/25	17:57:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/08/25	17:57:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2017/08/25	17:58:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2017/08/25	17:58:18.0	XRT_FLD_DIS_425_OG [0x1a9]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2017/08/25	18:00:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2017/08/25	18:00:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2017/08/25	18:00:58.0	XRT_QT_PROG_SET_437_OG [0x1b5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02				
2017/08/25	18:01:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/08/25	18:07:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/08/25	18:07:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/08/25	18:07:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2017/08/25	18:08:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2017/08/25	18:08:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2017/08/25	18:08:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2017/08/25	18:08:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2017/08/25	18:08:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2017/08/25	18:08:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/08/25	18:10:56.0	XRT_QT_PROG_SET_446_OG [0x1be]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2017/08/25	18:10:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2017/08/25	18:11:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/08/25	18:11:00.5	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/08/25	18:11:02.5	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/08/25	18:11:04.5	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/08/25	18:11:06.5	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/08/25	18:14:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/08/25	18:44:30.0	XRT_Custom_430_OG [0x1ae]							
2017/08/25	18:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/08/25	19:49:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/08/25	19:49:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/08/25	19:49:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/08/25	19:49:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/08/25	19:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/08/25	20:21:30.0	XRT_Custom_430_OG [0x1ae]							
2017/08/25	20:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/08/25	21:28:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/08/25	21:28:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/08/25	21:28:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/08/25	21:28:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/08/25	21:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/08/25	21:58:30.0	XRT_Custom_430_OG [0x1ae]							
2017/08/25	21:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/08/25	23:06:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/08/25	23:06:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/08/25	23:06:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/08/25	23:06:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]							

2017/08/25	23:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/08/25	23:33:00.0	XRT_Custom_430_OG [0x1ae]					
2017/08/25	23:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/08/26	00:44:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/08/26	00:44:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/08/26	00:44:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2017/08/26	00:44:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/08/26	00:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/08/26	00:57:00.0	XRT_Custom_430_OG [0x1ae]					
2017/08/26	00:58:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/08/26	02:12:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/08/26	02:12:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/08/26	02:12:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2017/08/26	02:12:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/08/26	02:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/08/26	02:33:30.0	XRT_Custom_430_OG [0x1ae]					
2017/08/26	02:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/08/26	03:46:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/08/26	03:46:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/08/26	03:46:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2017/08/26	03:46:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/08/26	03:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/08/26	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/08/26	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/08/26	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2017/08/26	06:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2017/08/26	06:00:18.0	XRT_FLD_DIS_425_OG [0x1a9]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2017/08/26	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2017/08/26	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2017/08/26	06:02:58.0	XRT_QT_PROG_SET_437_OG [0x1b5]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 02	
2017/08/26	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/08/26	06:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
			AOCU_NM	5	02-76	02 00 00 00 00	
2017/08/26	06:59:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/08/26	06:59:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/08/26	06:59:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2017/08/26	06:59:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/08/26	07:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/08/26	07:30:00.0	AOCS_Ore-point_Start_3_OG [0x099]					
			AOCU_NM	5	02-76	03 00 00 00 00	
2017/08/26	09:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
			AOCU_NM	5	02-76	02 00 00 00 00	
2017/08/26	11:25:00.0	AOCS_Ore-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	00 00 00 00 00	