

XRT Timeline to be uploaded on 2017/09/23

Period: 2017/09/23 10:52:00 - 2017/09/28 11:04:00

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Normal mode

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XOB #1BC6: HOP338 Al/mesh (8092ms) Al/poly (8092ms) 384FOV 60s cad with Gband/lightleak (1ms)													
Term			Pointing (x, y)					Comment					
09/23 11:05:00 - 09/23 12:59:54			Track (94.1, -61.5) @ 09/23 11:02:00					# OP start + 10min HOP 338 w/ GREGOR					
09/24 09:03:00 - 09/24 12:59:54			Track (291.9, -55.5) @ 09/24 09:00:00					# HOP 338 w/ GREGOR					
09/25 09:03:00 - 09/25 12:59:54			Track (491.5, -43.3) @ 09/25 09:00:00					# HOP 338 w/ GREGOR					
09/26 09:19:37 - 09/26 10:37:30			Track (684.3, 10.1) @ 09/26 09:00:00					# HOP 338					
PROG= 09 Inf.-time(s)													
Subr= 1		1-time(s)		2.0sec									
Seqn= 92		1-time(s)		2.0sec									
Open/G-band		Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band		Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2		2-time(s)		2.0sec									
Seqn= 35		30-time(s)		60.0sec									
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	8.00s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Al-poly/Open		Al-poly/thick-Al	close	Safe	Norm	8.00s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 15		1-time(s)		2.0sec									
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	8.00s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1BC4: AR (Filter-Ratio with Al/poly and thin-Be) with PFB, 512x512 at 1064 1048, thick-Al context, with G-band (1ms/1ms leak), 90s cad

Term		Pointing (x, y)							Comment					
09/23 13:03:00 - 09/23 18:06:24		Track (-649.3, -342.4) @ 09/23 13:00:00							# AR 12681 tracking					
09/23 18:19:30 - 09/23 19:04:30		Track (-613.7, -346.2) @ 09/23 18:16:30							# AR 12681 tracking					
09/23 20:40:00 - 09/24 05:36:54		Track (-597.4, -347.8) @ 09/23 20:37:00							# AR 12681 tracking					
09/24 05:50:00 - 09/24 08:59:54		Track (-530.8, -353.5) @ 09/24 05:47:00							# AR 12681 tracking					
PROG= 06 Inf.-time(s)														
Subr= 1		1-time(s)			2.0sec									
Seqn= 92		1-time(s)			2.0sec									
Open/G-band		Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec	
Open/G-band		Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec	
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
Seqn= 71		3-time(s)			2.0sec									
Open/thick-Al		Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	3	0	2.0sec	
Subr= 2		30-time(s)			90.0sec									
Seqn= 89		1-time(s)			24.0sec									
thin-Be/Open		med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0	2.0sec	
Al-poly/Open		thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0	14.0sec	
Seqn= 58		1-time(s)			24.0sec									
Al-poly/Open		thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec	
thin-Be/Open		med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec	
Seqn= 48		1-time(s)			2.0sec									
Al-poly/Open		thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec	
thin-Be/Open		med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec	
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

XOB #1BA7: Synoptic Q95 2x2 - Al/mesh(8/181/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(16/181/4096) + Th

Term		Pointing (x, y)				Comment								
09/23 18:09:30 - 09/23 18:16:24		Fixed (0.0, 0.0)				synoptic, shifted 6.5 min								
09/24 05:40:00 - 09/24 05:46:54		Fixed (0.0, 0.0)				synoptic, shifted -23.0 min								
09/24 13:03:00 - 09/24 13:09:54		Fixed (0.0, 0.0)				synoptic, shifted -21.0 min								
09/25 18:07:00 - 09/25 18:09:54		Fixed (0.0, 0.0)				synoptic								
09/26 06:15:30 - 09/26 06:22:24		Fixed (0.0, 0.0)				synoptic, shifted 12.5 min								
PROG= 02 1-time(s)														
Subr= 1		1-time(s)		2.0sec										
Seqn= 5		1-time(s)		2.0sec										
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)		DPCM	0	0	2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)		DPCM	0	0	2.0sec
Seqn= 26		1-time(s)		2.0sec										
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Seqn= 73		1-time(s)		2.0sec										
Al-poly/Open		Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Al-poly/Open		Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Al-poly/Open		Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Seqn= 44		1-time(s)		2.0sec										
thin-Be/Open		thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
thin-Be/Open		thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec

XOB #1BA8: Synoptic 7 Filter w/ Al-mesh(24/256/2897), Al-poly(45/512/4096), Thin-Be(181/2048/11571) - Thick-Be(65536), Al-poly+Ti-															
Term		Pointing (x, y)			Comment										
09/25 05:59:30 - 09/25 06:06:24		Fixed (0.0, 0.0)			synoptic, shifted -3.5 min										
PROG= 05		1-time(s)													
└ Subr= 1		1-time(s)		2.0sec											
└└ Seqn= 5		1-time(s)		2.0sec											
└└└		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└└└		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└└└		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└└└		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)		DPCM	0	0	2.0sec
└└└		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)		DPCM	0	0	2.0sec
└└└ Seqn= 1		1-time(s)		2.0sec											

Term	Pointing (x, y)	Comment
09/23 11:05:00 - 09/23 12:59:54	Track (94.1, -61.5) @ 09/23 11:02:00	# OP start + 10min HOP 338 w/ GREGOR
09/23 13:03:00 - 09/23 18:06:24	Track (-649.3, -342.4) @ 09/23 13:00:00	# AR 12681 tracking
09/23 18:19:30 - 09/23 19:04:30	Track (-613.7, -346.2) @ 09/23 18:16:30	# AR 12681 tracking
09/23 19:40:00 - 09/23 20:36:54	Track (865.1, 272.8) @ 09/23 19:37:00	# HOP 341 w/ IRIS
09/23 20:40:00 - 09/24 05:36:54	Track (-597.4, -347.8) @ 09/23 20:37:00	# AR 12681 tracking
09/24 05:50:00 - 09/24 08:59:54	Track (-530.8, -353.5) @ 09/24 05:47:00	# AR 12681 tracking
09/24 09:03:00 - 09/24 12:59:54	Track (291.9, -55.5) @ 09/24 09:00:00	# HOP 338 w/ GREGOR
09/24 13:13:00 - 09/24 15:59:54	Track (-453.2, -321.6) @ 09/24 13:10:00	# AR 12681 tracking
09/24 16:03:00 - 09/24 19:29:54	Track (0.0, 559.9) @ 09/24 16:00:00	# HOP 336 /w IRIS
09/24 19:33:00 - 09/25 05:56:24	Track (-423.8, -360.7) @ 09/24 19:30:00	# AR 12681 tracking
09/25 06:09:30 - 09/25 08:59:54	Track (-336.1, -365.1) @ 09/25 06:06:30	# AR 12681 tracking
09/25 09:03:00 - 09/25 12:59:54	Track (491.5, -43.3) @ 09/25 09:00:00	# HOP 338 w/ GREGOR
09/25 13:03:00 - 09/25 17:59:54	Track (-277.3, -367.3) @ 09/25 13:00:00	# AR 12681 tracking
09/25 18:13:00 - 09/26 06:12:24	Track (-232.4, -368.6) @ 09/25 18:10:00	# AR 12681 tracking
09/26 06:25:30 - 09/26 08:56:00	Track (-124.5, -370.6) @ 09/26 06:22:30	# AR 12681 tracking
09/26 09:19:37 - 09/26 10:37:30	Track (684.3, 10.1) @ 09/26 09:00:00	# HOP 338

┌ ├ ├ ├ ├ ├ ├ ├ ├ └	Seqn= 10		1-time(s)		2.0sec									
	┌	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	└	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Seqn= 11		1-time(s)		2.0sec									
	┌	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Seqn= 87		1-time(s)		2.0sec									
	┌	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	┌	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	┌	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	┌	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

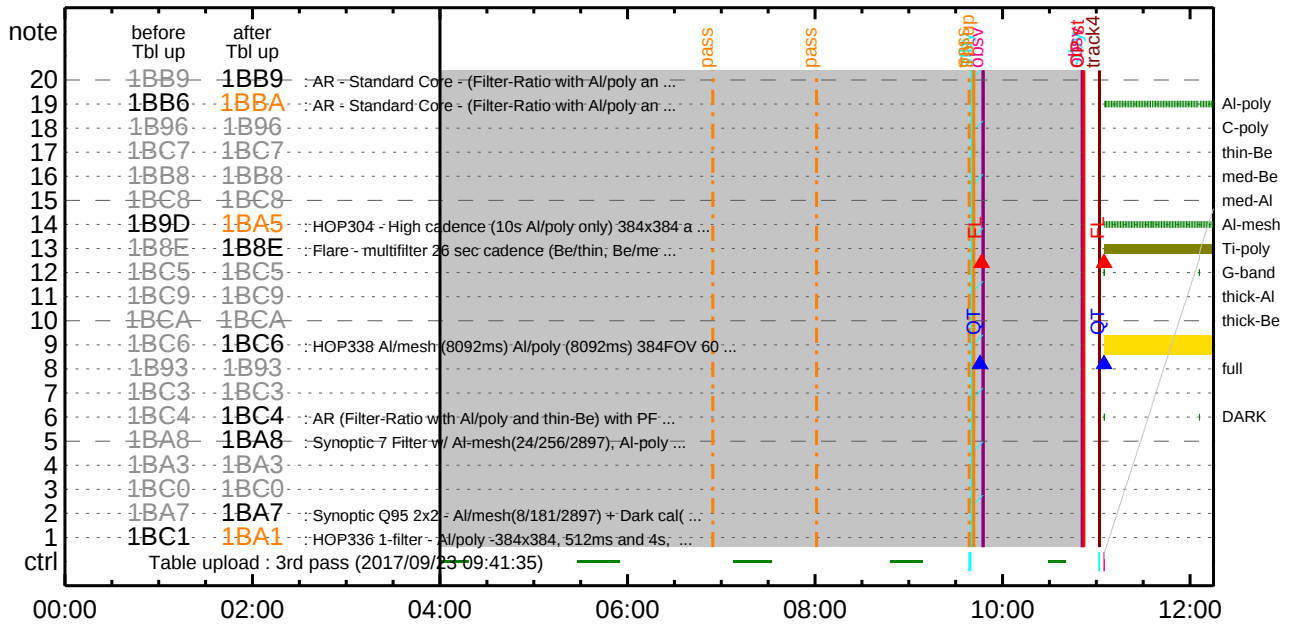
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Flare Detection

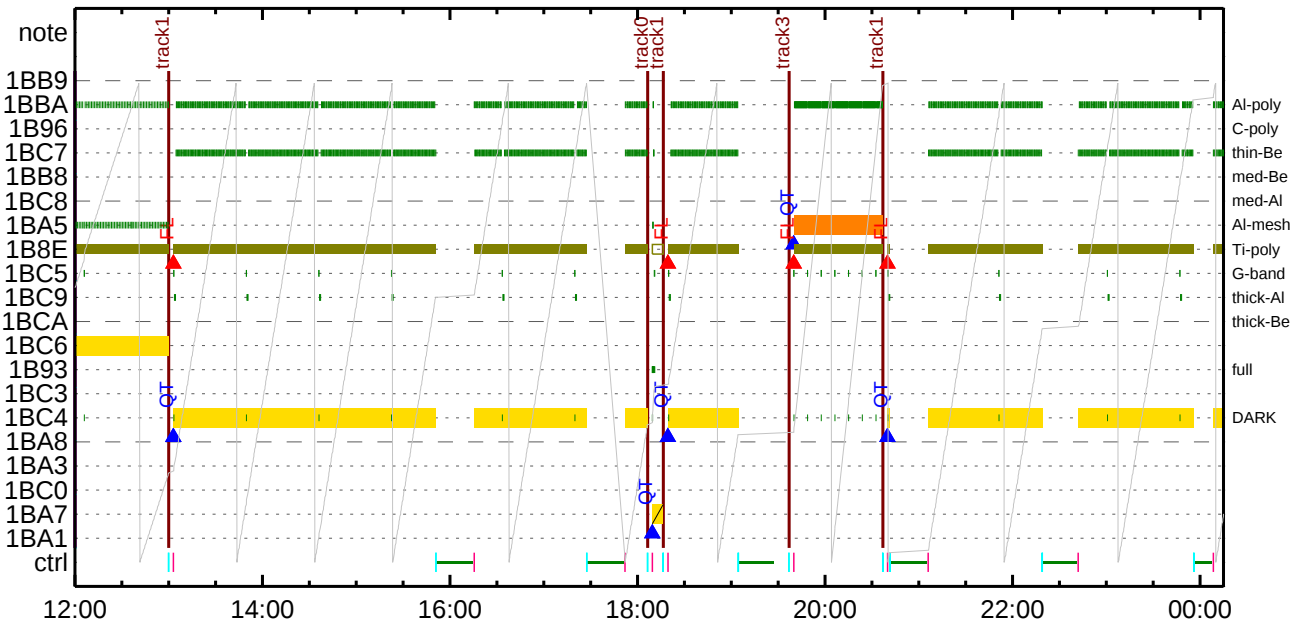
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FLD Patrol													
Term			Pointing (x, y)					Comment					
09/23 18:16:48 - 09/24 05:37:18			Track (-613.7, -346.2) @ 09/23 18:16:30					# AR 12681 tracking					
09/24 05:47:18 - 09/24 13:00:18			Track (-530.8, -353.5) @ 09/24 05:47:00					# AR 12681 tracking					
09/24 13:10:18 - 09/25 05:56:48			Track (-453.2, -321.6) @ 09/24 13:10:00					# AR 12681 tracking					
09/25 06:06:48 - 09/25 18:04:18			Track (-336.1, -365.1) @ 09/25 06:06:30					# AR 12681 tracking					
09/25 18:10:18 - 09/26 06:12:48			Track (-232.4, -368.6) @ 09/25 18:10:00					# AR 12681 tracking					
09/26 06:22:48 - 09/28 11:04:00			Track (-124.5, -370.6) @ 09/26 06:22:30					# AR 12681 tracking					
Al-poly/Open		Al-poly/Open	close	Safe	Norm	8ms	Obs	8x8	Q=50			80sec	
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

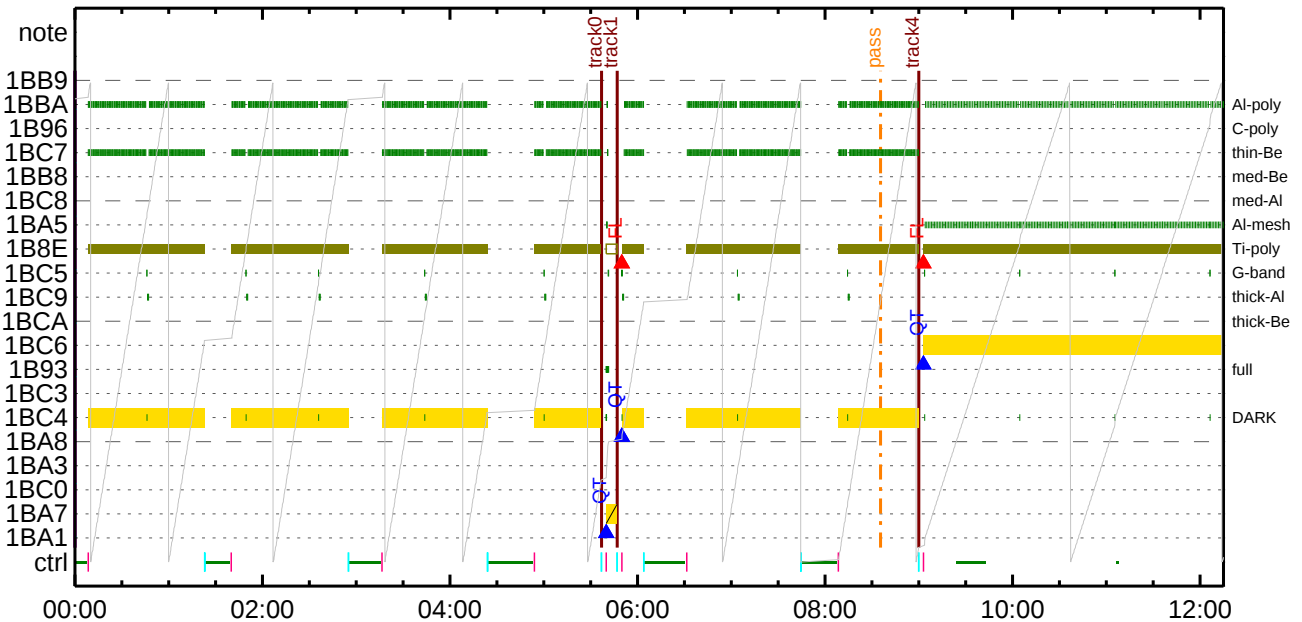
CMDI #0015 2017/09/23



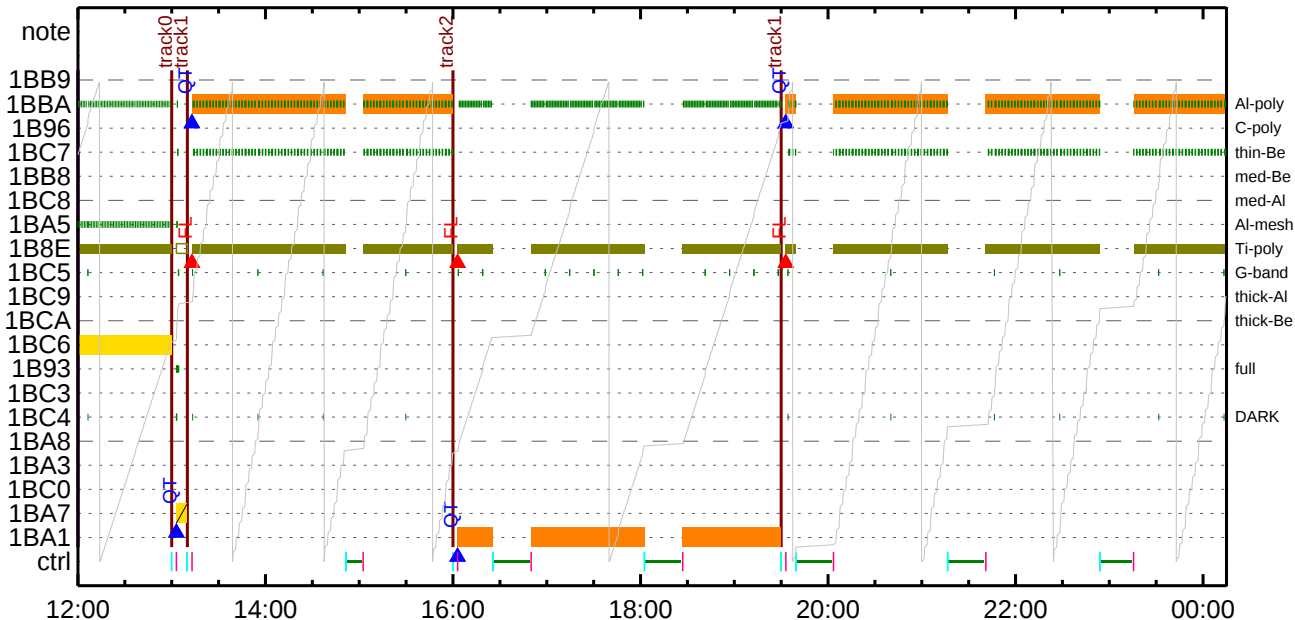
CMDI #0015 2017/09/23



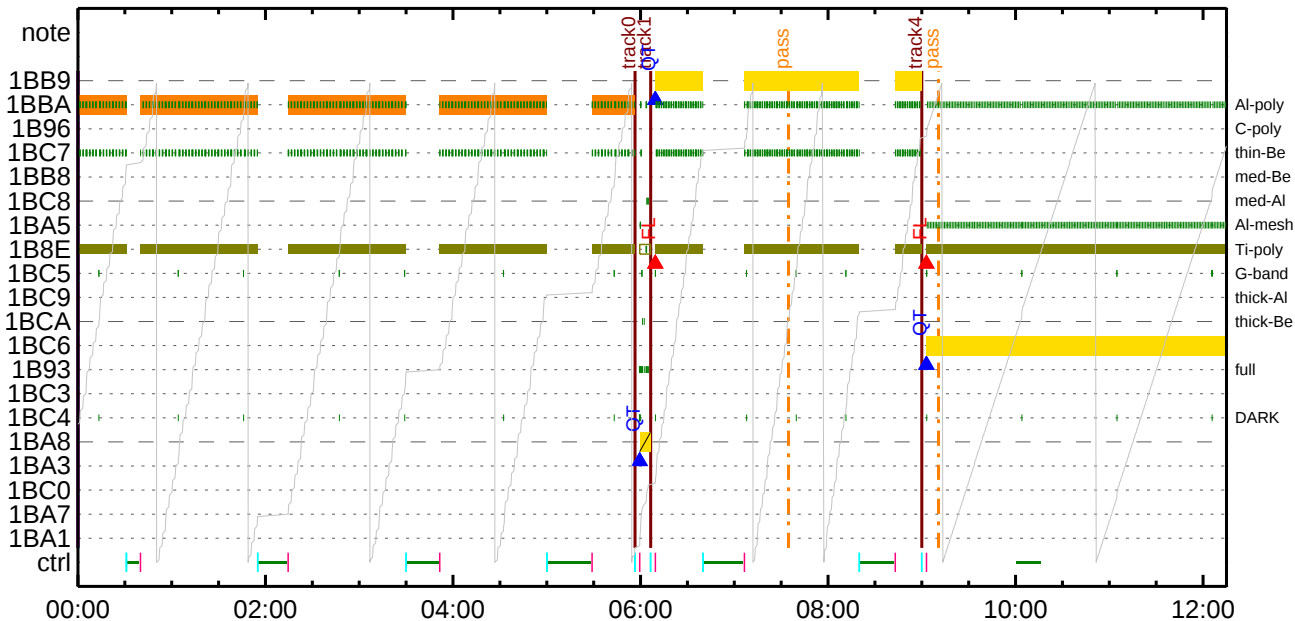
CMDI #0015 2017/09/24



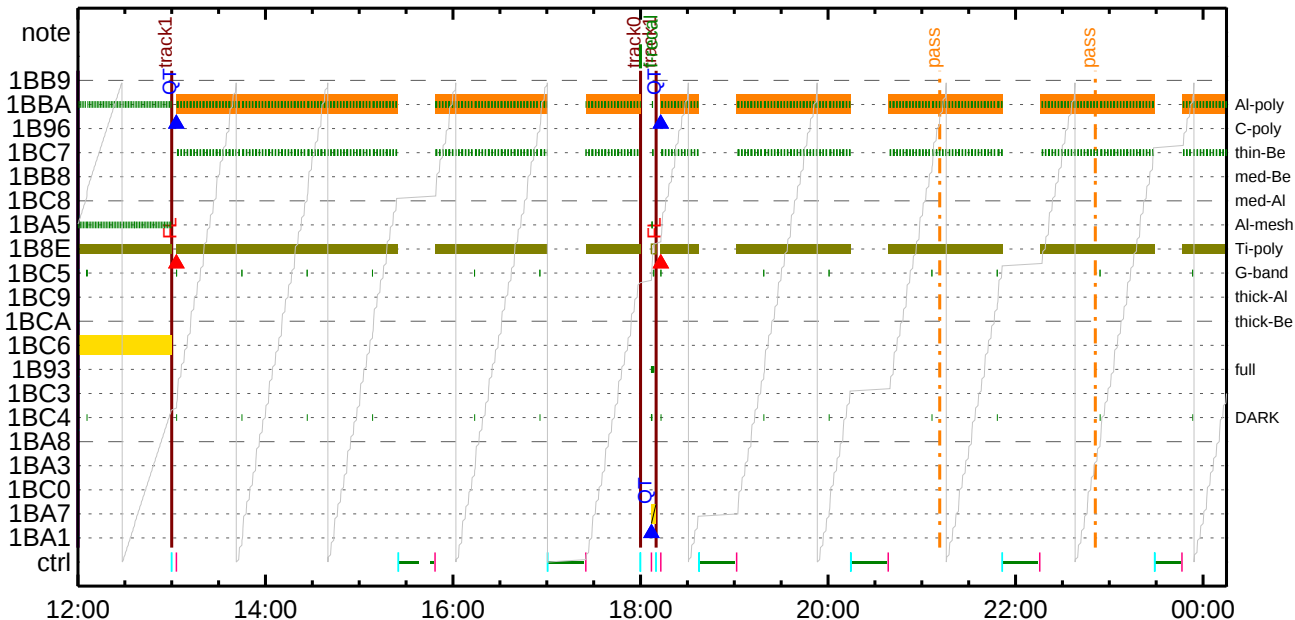
CMDI #0015 2017/09/24



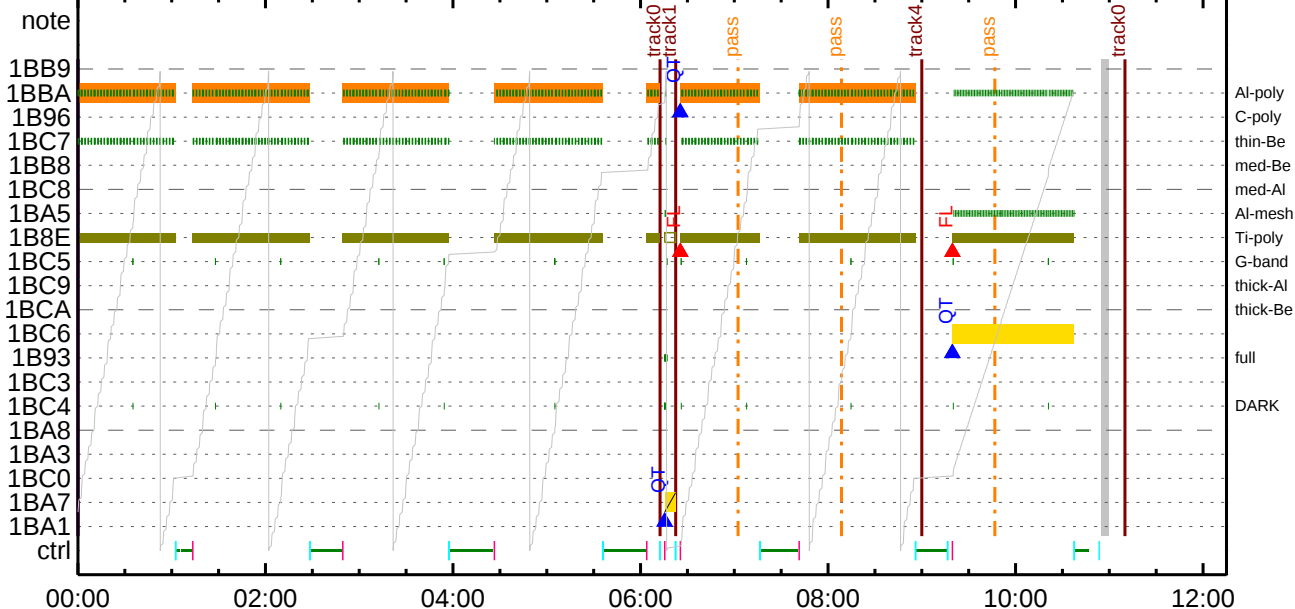
CMDI #0015 2017/09/25



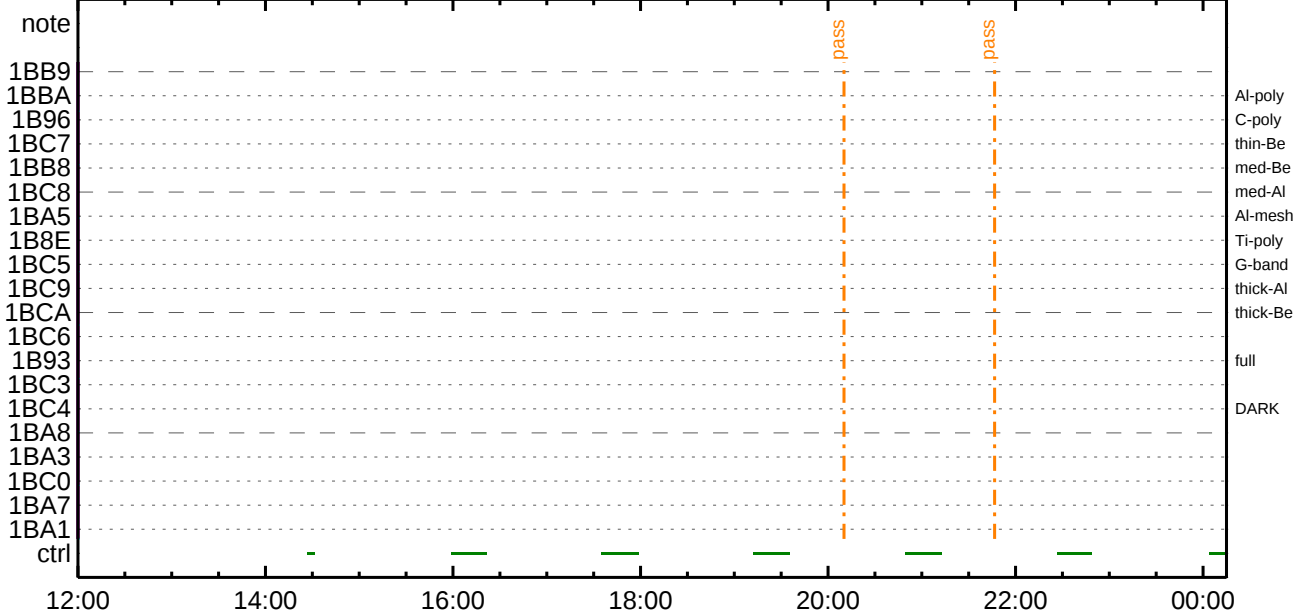
CMDI #0015 2017/09/25



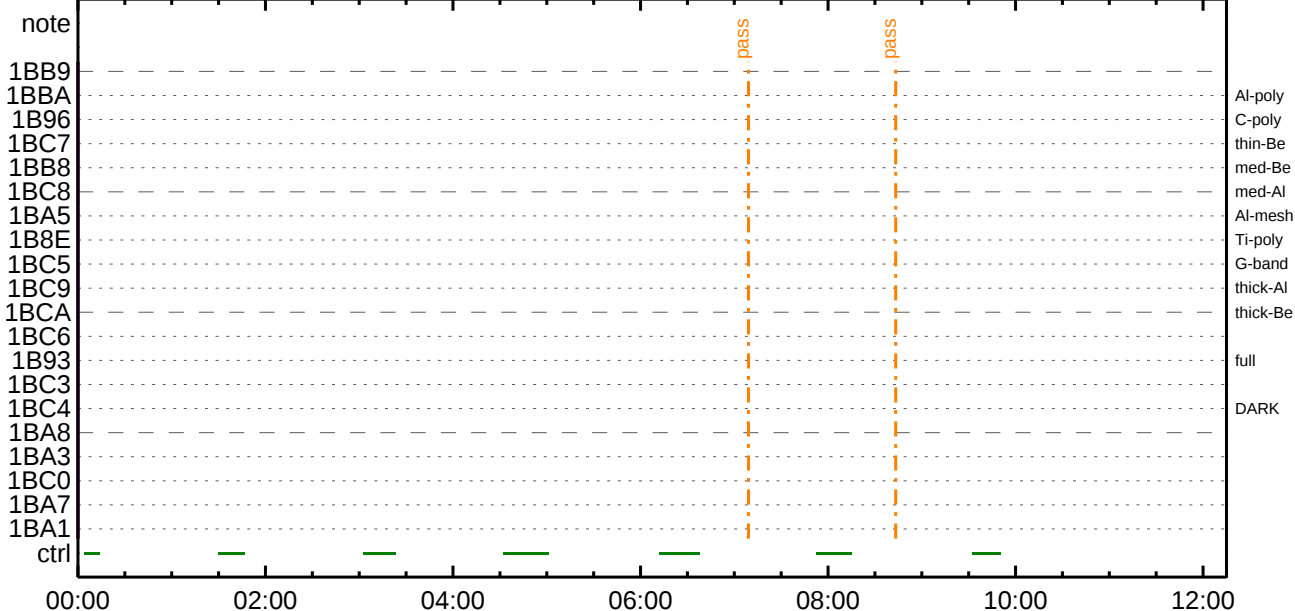
CMDI #0015 2017/09/26



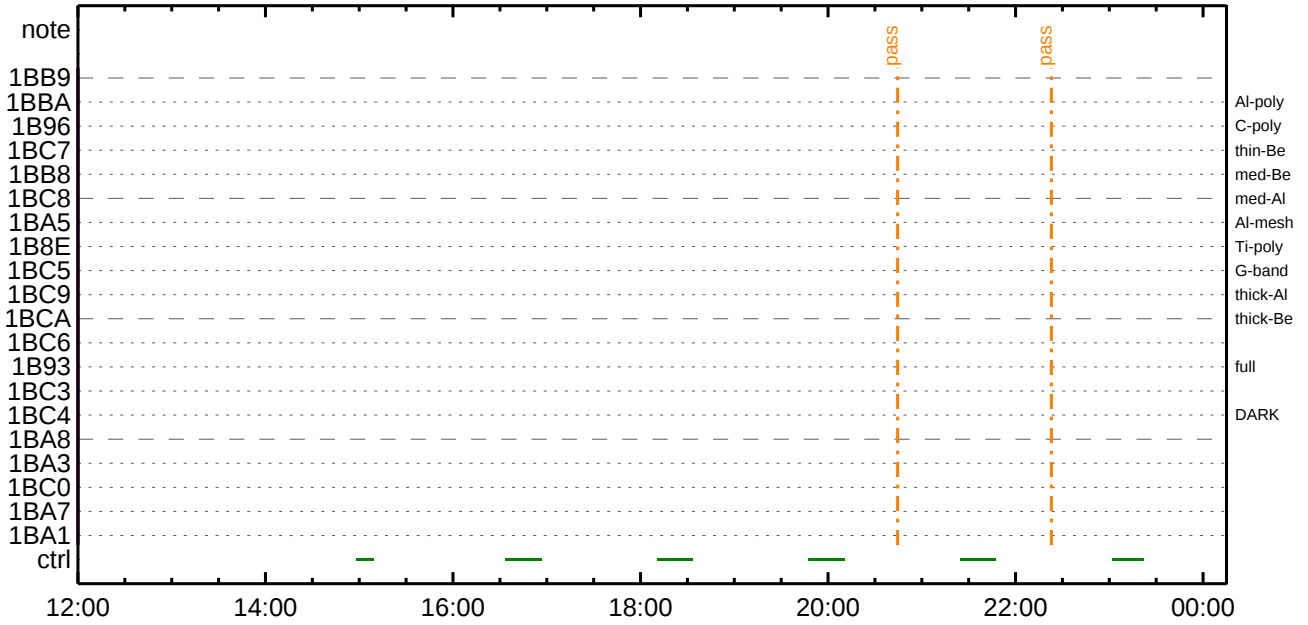
CMDI #0015 2017/09/26



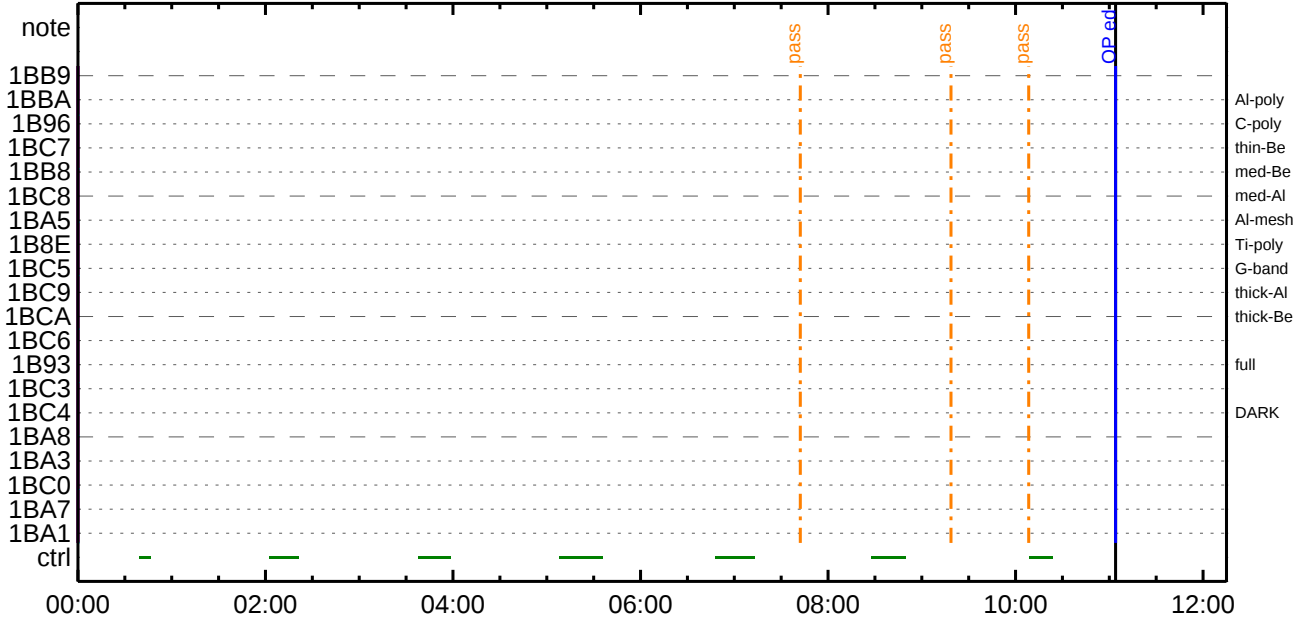
CMDI #0015 2017/09/27



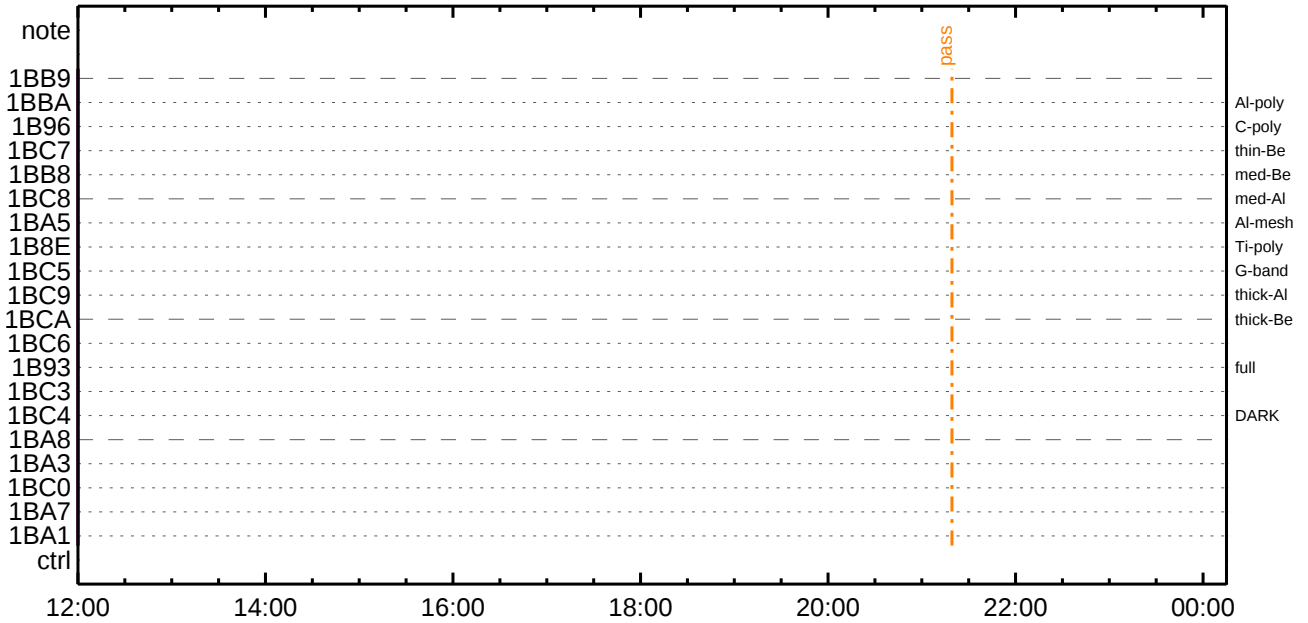
CMDI #0015 2017/09/27



CMDI #0015 2017/09/28



CMDI #0015 2017/09/28



(a) Spacecraft Operation Procedure (real-commands)

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main-876 2017-09-23 14:50:39 205 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥$¥Ã¥¼Ä»Üjã
0005 C.
0006 C. ¥À¥ßj¼¥³¥D¥ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Äi;Ê¿¿A¤•µ°£»I×AÇ¤I¥C¥A¥×¥i;¼¥ÉjÊÊ¿µ•iîÊjÊ¤¼°Ç¿¤•¤¿¼i¹Ç¤IjÇÀ®, Ü¤¹¤Ê¤¤¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤Èj£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OG¥i;¼¥Éj;¥À¥ó¥×
0016 C. *****
0017 C.
0018 . C. iãOP/OG¥i;¼¥Éjã
0019 . S. OP op-876:OP
0020 ()
0021 . S. OG og-876:OG
0022 ()
0023 C.
0024 . C. iãNMOG&OPîî°è¥À¥ó¥×jã
0025 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0036 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. ¥À¥ó¥×½ªi»¤ò³iÇ$
0041 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOG¤i¼£¹Ç•è²iOK¤ò³iÇ$
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0055 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. ¥À¥ó¥×½ªi»¤ò³iÇ$
0060 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOG¤i¼£¹Ç•è²iOK¤ò³iÇ$
0062 C.
0063 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0074 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. ¥À¥ó¥×½ªi»¤ò³iÇ$
0079 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG, RAM ID=OP¤i¼£¹Ç•è²iOK¤ò³iÇ$
0081 C.
0082 . C. ***** °Ê¼¤i¼Ä~¶i°¤ÊÊ¬¤°Á÷¿® (¼¤µ¬¥À¥ó¥×½ªÊ¿¤òÄÓÃ¤¤Ç½¤¤¬¤Ê¼i¹Ç¤Ç¤â) *****
0083 C. DHU¥âj¼¥ÉjÊ¼¥½ ¥i;¼¥ÉjÊ¤òiã¤¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. ¢¢[HK1_PKT_FORM_NO] EQ 2
0087 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE j$ OPOG UPLOAD¤¬Á÷¿®NG¤i¼¹ÇjÇ°Ê¼¤i¼TI-CMDÁ÷¿®¤i¼¹Ô¤¤•¤Ê¤¤¤³¤Èj£
```

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0096      C.          0B0CjCSET0EDUMP0IÆ±°i¥Ñ¥1QÇ10□|□3□È;j£
0097      C.
0098      . C. TI¥³¥D¥Ó¥É¤ðòÀÐİ¿(UT)
0099      +. TI 2017-09-23 10:47:00.0
0100      DC 01-B3 DHU_OP_STOP
0101      C.                      ÇÇ[HK1_TI_CMD_NUM]                      EQ          1COUNTUP
0102      C.
0103      +. TI 2017-09-23 10:47:01.0
0104      DC 01-B4 DHU_OP_COPY
0105      C.                      ÇÇ[HK1_TI_CMD_NUM]                      EQ          1COUNTUP
0106      C.
0107      +. TI 2017-09-23 10:47:01.0
0108      DC 01-B5 DHU_OPOG_COPY
0109      C.                      ÇÇ[HK1_TI_CMD_NUM]                      EQ          1COUNTUP
0110      C.
0111      +. TI 2017-09-23 10:51:59.5
0112      DC 01-B2 DHU_OP_START
0113      C.                      ÇÇ[HK1_TI_CMD_NUM]                      EQ          1COUNTUP
0114      C.
0115      C. °Ê²¼□İÄê%İİŃ□İ¥Å$¥Ã¥~¹àİÜ
0116      C.                      ÇÇ[HK1_TI_CMD_ENA/DIS]                  EQ          ENA
0117      C.                      ÇÇ[HK1_TI_CMD_NUM]                    EQ          4
0118      C.                      ÇÇ[HK1_NEXT_EXEC_PIM]                  EQ          DHU
0119      C.                      ÇÇ[HK1_NEXT_EXEC_DC]                   EQ          0xB3
0120      C.
0121      . C. *****
0122      C. TIIÎ°è¥À¥Ó¥×
0123      C. *****
0124      C.
0125      C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126      +. DC 01-23 DHU_DMA_DUMP_PRM_SET
0127      BC (03 ab 03 01 02)
0128      C.                      ÇÇ[HK1_DMP_TOP_ADRS_1]                  EQ          07
0129      C.                      ÇÇ[HK1_DMP_TOP_ADRS_0]                  EQ          2B
0130      C.                      ÇÇ[HK1_DMP_BLOCK_NUM]                  EQ          3
0131      C.                      ÇÇ[HK1_DMP_REPEAT_NUM]                 EQ          0
0132      C.                      ÇÇ[HK1_DMA_DMP_PIM]                    EQ          DHU
0133      +. DC 01-22 DHU_MODE_CHNG
0134      BC (07 0b f8)
0135      C.                      ÇÇ[HK1_PKT_FORM_NO]                     EQ          7
0136      C.                      ÇÇ[HK1_PKT_GEN_TIME]                    EQ          0.25 s
0137      C.                      ÇÇ[HK1_S_TLM_BIT_RATE]                  EQ          32k
0138      C.                      ÇÇ[HK1_X_TLM_BIT_RATE]                 EQ          4M
0139      C.                      ÇÇ[HK1_DMP_CHK_FLG]                     EQ          EXEC
0140      C.
0141      . C. ¥À¥Ó¥×½ªİ»¤ð³İÇ$
0142      C.                      ÇÇ[HK1_DMP_CHK_FLG]                      EQ          NON
0143      C.
0144      . C. RAM ID=TI_TBL¤İ%Ê¹Ç•ë²İOK¤ð³İÇ$
0145      C.
0146      . C. DHU¥â;j¼¥ÉjÊ¼ý½. ¥İj¼¥ËjË¤ðİá¤¹
0147      +. DC 01-22 DHU_MODE_CHNG
0148      BC (02 0a f8)
0149      C.                      ÇÇ[HK1_PKT_FORM_NO]                     EQ          2
0150      C.                      ÇÇ[HK1_PKT_GEN_TIME]                    EQ          0.5S
0151      C.                      ÇÇ[HK1_S_TLM_BIT_RATE]                  EQ          32K
0152      C.                      ÇÇ[HK1_X_TLM_BIT_RATE]                 EQ          4M
0153      C.
0154      C. *****
0155      C. SOT TI command set
0156      C. *****
0157      C. Execute, after the success of OP upload.
0158      +. TI 2017-09-23 10:51:16.0
0159      DC 07-F0 MDP_SOT_MODE_STBY
0160      BC (41)
0161      . C. -----
0162      C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0163      C. -----
0164      C. ***** SOT END *****
0165      . C. Stop EIS observation and temporarily disable EIS mode changes
0166      C.
0167      C.
0168      C. ***** Start EIS operation (TI set) *****
0169      C. Execute, after the success of OP upload.
0170      C. Set EIS TI-commands
0171      +. TI 2017-09-23 10:51:30.0
0172      DC 07-FC EIS_MODE_MANU
0173      BC (21 02)
0174      +. TI 2017-09-23 10:51:40.0
0175      DC 07-FC EIS_MODE_CHG_DIS
0176      BC (22)
0177      . C. [ ] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0178      C. ***** End EIS operation (TI set) *****
0179      C.
0180      C.
0181      C.
0182      C. ***** XRT START *****
0183      C. Execute, after the success of OP upload.
0184      +. TI 2017-09-23 10:51:00.0
0185      DC 07-F0 MDP_XRT_MODE_STBY
0186      BC (c3)
0187      . C. [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0188      C.
0189      C. ***** XRT END *****
0190      C.
0191      . C. ***** MDP `0Āİİ»Ö%Y¤ÊÄ¤¹¤èDCBC•×²è *****
0192      C. (%ā°İ¥Ô¥Ā¥Ë¥P¥Ë¥Ä¥Ç¥ë¤Ê¼¤¤¼¥Ā»¤¹¤è)
0193      . S. DC-BC dcbc-402:DCBC
```

0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥Ð¥¹•İ Daily±¿İÑ¤Ė´0¤¹¤ĖDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ĩăLOS¥Á¥S¥Ă¥¬¼Â»Üĭă
0203 C.
0204 . C. ***** LOS *****
0205 C.

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main-877 2017-09-23 14:20:39 169 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. jãAOS¥Á¥$¥Ã¥¬¼Ä»Üjã
0005 C.
0006 C. ¥Ä¥ßj¼¥³¥Ð¥Ó¥ÉÄ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. ÄjijË□¿□Ä□•µ°Æ»I×AÇ□I¥¢¥Ä¥×¥ij¼¥ÉjËËè½µ•iîÉjËÈ¼°Ç□□•□¿½¹Ç□IjÇÄ®, ù□¹□è□□ÇÄ÷¿®□•□Ê□□□³□Èj£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÄ÷¿®µjON
0016 C. *****
0017 C. ¢´ °ÆÄ, I×ÉÝ□äLOS□□Ç□I»þ´Ö□ò¹íî, □•j¢ÉÔI×□EXÄÓON□I¹Ö□È□i□Ê□□□³□Èj£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ¢¢[HK1_XPA_ON/OFF] EQ ON
0025 C. ¢¢[HK1_XPA_PWR_HI/L0] EQ HI
0026 C. ¢¢[HK1_XMOD_ON/OFF] EQ ON
0027 C. ¢¢[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. X¥Ð¥Ó¥É¥i¥Ä¥¬¼ÖÄÖ□¬¼ÄÄê□•□¿□éj¢°Ê²¼□I°ÆÄ, ¼ê½Ç□ò¼Ä¹Ö□¹□èj£
0030 C.
0031 . C. *****
0032 C. DR PT1 ÄI¾i°ÆÄ
0033 C. *****
0034 C. ¢´ RESTARTjÊPT1jË□•□¿□□½¹Ç□Ij¢°Ê²¼□I¼Ä¹Ö□»□°j¢DCBC-150□Ø¿Ê□àj£
0035 C.
0036 . C. jãPT1°ÆÄ, ³«»Ijã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ¢¢[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, j¾Ú)
0043 C. ¢¢[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, j¾Ú)
0044 C. ¢¢[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, j¾Ú)
0045 C.
0046 . C. jã¥¢¥ó¥Æ¥ÉÄÜÄØjÊÄ•Äº²ÖÈðjË, ä□I°ÆÄ, °Æ³«jã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ¢¢[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, j¾Ú)
0050 C. ¢¢[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, j¾Ú)
0051 C. ¢¢[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, j¾Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÄ, □¬¼«Æ°Ää»ß□•□¿, äj¢°Ê²¼□ò¼Ä¹Ö□¹□èj£
0055 C. ¥¢¥ó¥Æ¥ÉÄÜÄØ□äÄ•Äº²ÖÈð¬¼ä□□½¹Ç□I´°Î»□¹□è□□ÇÄÖ□Äj£
0056 C.
0057 . C. *****
0058 C. DR PT2 ÄI¾i°ÆÄ
0059 C. *****
0060 C. ¢´ RESTARTjÊPT2jË□•□¿□□½¹Ç□Ij¢°Ê²¼□I¼Ä¹Ö□»□°j¢DCBC-151□Ø¿Ê□àj£
0061 C.
0062 . C. jãPT2°ÆÄ, ³«»Ijã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ¢¢[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, j¾Ú)
0069 C. ¢¢[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, j¾Ú)
0070 C. ¢¢[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, j¾Ú)
0071 C.
0072 . C. jã¥¢¥ó¥Æ¥ÉÄÜÄØjÊÄ•Äº²ÖÈðjË, ä□I°ÆÄ, °Æ³«jã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ¢¢[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, j¾Ú)
0076 C. ¢¢[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, j¾Ú)
0077 C. ¢¢[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, j¾Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÄ, Ää»ßj¢XÄ÷¿®µjOFF
0081 C. *****
0082 C.
0083 . C. jãDR°ÆÄ, Ää»ßjã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ¢¢[HK1_REP_STA/STP] EQ STOP
0087 C. ¢¢[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ¢¢[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. jãXÄ÷¿®µjOFFjã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ¢¢[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ¢¢[HK1_XPA_ON/OFF] EQ OFF
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0096 C.
0097 C.
0098 C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 S. RAM ram-150:TARGET_ATT
0102 C.
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 C.
0129 C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_CHG_ENA
0131 BC (20)
0132 C. Verify EIS_MODE_CHG_FLG is ENA
0133 +. DC 07-FC EIS_MODE_MANU
0134 BC (21 02)
0135 C. Verify EIS in MANUAL mode
0136 C. Estimated OBSTBL upload time is 29s
0137 C. *****
0138 C. EIS START OBSTBL LOAD
0139 C. *****
0140 S. RAM ram-820:EIS_OBSTBL
0141 C.
0142 +. DC 07-FC EIS_DUMP_OBSTBL
0143 BC (07 07 07 00 00 70 00)
0144 C.
0145 C. Execute, after the success of OBSTBL upload.
0146 C. Set EIS TI-commands
0147 +. TI 2017-09-23 10:51:50.0
0148 DC 07-FC EIS_MODE_CHG_ENA
0149 BC (20)
0150 C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0151 C. *****
0152 C. EIS END OBSTBL LOAD
0153 C. *****
0154 C.
0155 C. ***** MDP 'ûÃîî»ö%ýðĒĀð□□ēDCBC•x²è *****
0156 C. (%ā°î¥Ō¥Ā¥Ē¥ð¥Ē¥ā¥Ĉ¥ē□Ē%□□Ā»Ū□¹□ē)
0157 S. DC-BC dcbc-402:DCBC
0158 (MDP_known_event)
0159 C.
0160 C.
0161 C. ***** ¥Đ¥¹•İ Daily±¿İŃ□Ē'Đ□¹ēDCBC•x²è *****
0162 S. DC-BC dcbc-153:DCBC
0163 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164 C.
0165 C.
0166 C. ¡āLOS¥Ā¥Œ¥Ā¥Œ¥Ā»Ū¡ā
0167 C.
0168 C. ***** LOS *****
0169 C.

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main-878      0001 C.  
              0002 . C. ***** AOS *****  
              0003 C.  
              0004 . C. ;ãAOS¥Á¥$¥Ã¥¬¼Ä»Ü;ä  
              0005 C.  
              0006 C. ¥Å¥ßj;¼¥³¥P¥Ó¥ÉÄ÷¿®  
              0007 +. DC 00-00 NULL_DUMMY_CMD  
              0008 C.  
              0009 . C. ***** AOCS : Reload orbital element (send every contact) *****  
              0010 C. Äí;jÊø¿Àð•µ°Æ»Î×ÁCòÍ¥¢¥Á¥×¥ì;j¼¥Ë;ËËè½µ•îË;ËÈ¾°CÒð•²¿%¹¹ÇòÌ;j ÇÄ®, ù±¹æöþðÇÄ÷¿®ð•²Êöö³æ;j£  
              0011 +. DC 02-8E AOCU_ORB_UPD  
              0012 C.  
              0013 C.  
              0014 . C. *****  
              0015 C. XÄ÷¿@ujON  
              0016 C. *****  
              0017 C. ¢¨ °ÆÄ, Î×ËÝàâLOSððÇçôI»Þ´Öðõ¹îî, ð•;¡ÉÔï×óEXÄÓONòT¹ÖðéijÊöö³æ;j£  
              0018 C.  
              0019 +. DC 03-B4 TCIA_XPA_ON/HI  
              0020 M. WAIT_SEC 1  
              0021 +. DC 03-84 TCIA_XMOD_ON  
              0022 M. WAIT_SEC 1  
              0023 +. DC 03-95 TCIA_XMOD_QPSK  
              0024 C.          ¤¤[HK1_XPA_ON/OFF] EQ ON  
              0025 C.          ¤¤[HK1_XPA_PWR_HI/LØ] EQ HI  
              0026 C.          ¤¤[HK1_XMOD_ON/OFF] EQ ON  
              0027 C.          ¤¤[HK1_XMOD_QPSK/PM] EQ QPSK  
              0028 C.  
              0029 C. X¥Ð¥Ó¥É¥ì¥Ã¥¬¼ÖÄÕð¬¼ÄÄêð•²¿ðé;j ¤°Ê²¼øI°ÆÄ, ¼ê½Çðò¼Ä¹Öð¹æ;j£  
              0030 C.  
              0031 . C. *****  
              0032 C. DR PT1 Äî¼¹ºÆÄ,  
              0033 C. *****  
              0034 C. ¢¨ RESTART;j ÊPT1;j Êð•²¿ðø%¹¹ÇòÌ;j ¤°Ê²¼øI¼Ä¹Öð»ð°;¡DCBC-150øØ¿Êðà;j£  
              0035 C.  
              0036 . C. jãPT1°ÆÄ,³«»î;j ä  
              0037 +. DC 01-29 DHU_S/X_VC4_OFF  
              0038 +. DC 06-C8 DR_PT1_REP_SEL  
              0039 BC (01 00)  
              0040 +. DC 06-B3 DR_REP_START  
              0041 +. DC 01-32 DHU_X_VC4_ON  
              0042 C.          ¤¤[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö,j¼Ú)  
              0043 C.          ¤¤[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,j¼Ú)  
              0044 C.          ¤¤[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,j¼Ú)  
              0045 C.  
              0046 C. jã¥¢¥Ó¥Æ¥ÉÄÙÄØ;j ÊÄ•Ä²ºÊÖ;j Ê, ä±I°ÆÄ, °Æ³«;jä  
              0047 +. DC 06-B3 DR_REP_START  
              0048 +. DC 01-32 DHU_X_VC4_ON  
              0049 C.          ¤¤[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö,j¼Ú)  
              0050 C.          ¤¤[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,j¼Ú)  
              0051 C.          ¤¤[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,j¼Ú)  
              0052 C.  
              0053 C.  
              0054 . C. PT1°ÆÄ,ð¬¼«Æ°Ää»ßð•²¿, ä;j ¤°Ê²¼øðò¼Ä¹Öð¹æ;j£  
              0055 C. ¥¢¥Ó¥Æ¥ÉÄÙÄØðäÄ•Ä²ºÊÖð¬¼áðø%¹¹ÇòÌ;j T»ð¹æöþðÇÄÔðÄ;j£  
              0056 C.  
              0057 . C. *****  
              0058 C. DR PT2 Äî¼¹ºÆÄ,  
              0059 C. *****  
              0060 C. ¢¨ RESTART;j ÊPT2;j Êð•²¿ðø%¹¹ÇòÌ;j ¤°Ê²¼øI¼Ä¹Öð»ð°;¡DCBC-151øØ¿Êðà;j£  
              0061 C.  
              0062 . C. jãPT2°ÆÄ,³«»î;j ä  
              0063 +. DC 01-29 DHU_S/X_VC4_OFF  
              0064 +. DC 06-C8 DR_PT2_REP_SEL  
              0065 BC (02 00)  
              0066 +. DC 06-B3 DR_REP_START  
              0067 +. DC 01-32 DHU_X_VC4_ON  
              0068 C.          ¤¤[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö,j¼Ú)  
              0069 C.          ¤¤[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,j¼Ú)  
              0070 C.          ¤¤[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,j¼Ú)  
              0071 C.  
              0072 . C. jã¥¢¥Ó¥Æ¥ÉÄÙÄØ;j ÊÄ•Ä²ºÊÖ;j Ê, ä±I°ÆÄ, °Æ³«;jä  
              0073 +. DC 06-B3 DR_REP_START  
              0074 +. DC 01-32 DHU_X_VC4_ON  
              0075 C.          ¤¤[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö,j¼Ú)  
              0076 C.          ¤¤[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,j¼Ú)  
              0077 C.          ¤¤[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,j¼Ú)  
              0078 C.  
              0079 . C. *****  
              0080 C. DR°ÆÄ, Ää»ß;j ¤XÄ÷¿@ujOFF  
              0081 C. *****  
              0082 C.  
              0083 . C. jãDR°ÆÄ, Ää»ß;j ä  
              0084 +. DC 06-B4 DR_REP_STOP  
              0085 +. DC 01-29 DHU_S/X_VC4_OFF  
              0086 C.          ¤¤[HK1_REP_STA/STP] EQ STOP  
              0087 C.          ¤¤[HK1_S_VC4_ON/OFF] EQ OFF  
              0088 C.          ¤¤[HK1_X_VC4_ON/OFF] EQ OFF  
              0089 C.  
              0090 . C. jãXÄ÷¿@ujOFF;j ä  
              0091 +. DC 03-85 TCIA_XMOD_OFF  
              0092 M. WAIT_SEC 1  
              0093 +. DC 03-B5TCIA_XPA_OFF  
              0094 C.          ¤¤[HK1_XMOD_ON/OFF] EQ OFF  
              0095 C.          ¤¤[HK1_XPA_ON/OFF] EQ OFF
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0096 C.
0097 C.
0098 C. *****
0099 C. SOT table upload
0100 C. *****
0101 C. < Stop SP table >
0102 +. DC 07-F0 MDP_SP_CTRL_MANU
0103 BC (61)
0104 C. -----
0105 C. MDP_SP_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 C. <Upload SP Observation Table>
0109 S. RAM ram-284:MDP_OBS_S
0110 ( )
0111 C.
0112 C. < Dump RAMID=MDP_OBS_S >
0113 +. DC 07-F0 MDP_DUMP_SPTBL
0114 BC (83 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_S verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 C. *****
0120 C. SOT TI command set
0121 C. *****
0122 C. Execute, after the success of TBL upload.
0123 +. TI 2017-09-23 10:51:18.0
0124 DC 07-F0 MDP_SOT_MODE_OBSV
0125 BC (40)
0126 C. -----
0127 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0128 C. -----
0129 C.
0130 C.
0131 C. ***** XRT START *****
0132 C.
0133 +. DC 07-F0 MDP_XRT_CTRL_MANU
0134 BC (c1)
0135 +. DC 07-F0 MDP_XRT_CTRL_MANU
0136 BC (c1)
0137 +. DC 07-F0 MDP_XRT_MODE_STBY
0138 BC (c3)
0139 C. ----- Success Verify ? OK / NG____
0140 C.
0141 C. XRT Obs. Table Upload
0142 S. RAM ram-291:MDP_OBS_X
0143 ( )
0144 C.
0145 +. DC 07-F0 MDP_DUMP_XRTTBL
0146 BC (84 07 00 00 00 3a d4)
0147 C. ----- Comparison Check ? OK / ERR ____
0148 C.
0149 C.
0150 +. DC 07-F0 MDP_XRT_ROI_SET
0151 BC (cd 01 b1 b1 04 04)
0152 +. DC 07-F0 MDP_XRT_ROI_SET
0153 BC (cd 02 b1 b1 08 08)
0154 +. DC 07-F0 MDP_XRT_ROI_SET
0155 BC (cd 03 b1 b1 08 08)
0156 +. DC 07-F0 MDP_XRT_ROI_SET
0157 BC (cd 04 b1 b1 06 06)
0158 +. DC 07-F0 MDP_XRT_ROI_SET
0159 BC (cd 05 85 83 06 06)
0160 +. DC 07-F0 MDP_XRT_ROI_SET
0161 BC (cd 06 85 83 06 06)
0162 +. DC 07-F0 MDP_XRT_ROI_SET
0163 BC (cd 07 85 83 08 08)
0164 +. DC 07-F0 MDP_XRT_ROI_SET
0165 BC (cd 08 80 80 20 20)
0166 +. DC 07-F0 MDP_XRT_ROI_SET
0167 BC (cd 09 80 80 20 08)
0168 +. DC 07-F0 MDP_XRT_ROI_SET
0169 BC (cd 0a 80 80 08 20)
0170 +. DC 07-F0 MDP_XRT_ROI_SET
0171 BC (cd 0f 80 80 06 06)
0172 +. DC 07-F0 MDP_XRT_ROI_SET
0173 BC (cd 10 80 80 08 08)
0174 +. DC 07-F0 MDP_XRT_FLD_ENA
0175 BC (d8)
0176 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0177 BC (c8)
0178 +. DC 07-F0 MDP_XRT_ARS_DIS
0179 BC (d5)
0180 +. DC 07-F0 MDP_XRT_AEC_RESET
0181 BC (d0)
0182 +. DC 07-F0 MDP_XRT_FLD_RESET
0183 BC (da)
0184 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0185 BC (c4 09)
0186 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0187 BC (c5 0d)
0188 C. ----- Success Verify ? OK / NG ____
0189 C.
0190 C.
0191 C. All OK? Yes--> Please Proceed. / No --> Stop here.
0192 C.
0193 +. DC 07-F0 MDP_XRT_MODE_OBSV

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0194 BC (c2)
0195 +. TI 2017-09-23 10:51:02.0
0196 DC 07-F0 MDP_XRT_MODE_OBSV
0197 BC (c2)
0198 . C. ----- Success Verify ? OK / NG ____
0199 C.
0200 C. ***** XRT END *****
0201 C.
0202 . C. ***** MDP 'ÜÃÎ±Î»Ö¼Ý±ÊÂÐ±¹±èDCBC•×²è *****
0203 C. (%ã°Î±Ö¼Ã±Ê±Ð±ÝÊ±ã±Ç±è±½¼±±¼Ã»Ü±¹±è)
0204 . S. DC-BC dcbc-402:DCBC
0205 (MDP_known_event)
0206 C.
0207 C.
0208 . C. ***** ±Ð±¹•Î Daily±¿ÎÑ±Ê´Ø±¹±èDCBC•×²è *****
0209 . S. DC-BC dcbc-153:DCBC
0210 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0211 C.
0212 C.
0213 . C. ¡ãLOS±Ã±S±Ã±´¼Ã»Ü¡ã
0214 C.
0215 . C. ***** LOS *****
0216 C.

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*** OP Sequence for XRT ***

2017/09/23	11:01:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/23	11:01:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/23	11:01:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2017/09/23	11:02:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	04 03 33 01 db
2017/09/23	11:02:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2017/09/23	11:02:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2017/09/23	11:02:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2017/09/23	11:02:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2017/09/23	11:02:26.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/09/23	11:04:56.0	XRT_QT_PROG_SET_438_OG [0x1b6]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2017/09/23	11:04:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2017/09/23	11:05:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/09/23	12:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/23	12:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/23	12:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2017/09/23	13:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	01 03 33 01 db
2017/09/23	13:00:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2017/09/23	13:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2017/09/23	13:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2017/09/23	13:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2017/09/23	13:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/09/23	13:02:56.0	XRT_QT_PROG_SET_404_OG [0x194]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2017/09/23	13:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2017/09/23	13:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/09/23	15:51:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/23	15:51:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/23	15:51:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/09/23	15:51:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/09/23	15:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/09/23	16:14:30.0	XRT_Custom_430_OG [0x1ae]			
2017/09/23	16:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/09/23	17:27:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/23	17:27:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/23	17:27:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/09/23	17:27:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/09/23	17:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/09/23	17:51:00.0	XRT_Custom_430_OG [0x1ae]			
2017/09/23	17:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/09/23	18:06:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/23	18:06:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/23	18:06:28.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2017/09/23	18:06:30.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2017/09/23	18:06:48.0	XRT_FLD_DIS_425_OG [0x1a9]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2017/09/23	18:09:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2017/09/23	18:09:26.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2017/09/23	18:09:28.0	XRT_QT_PROG_SET_437_OG [0x1b5]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2017/09/23	18:09:30.0	XRT_CTRL_AUTO_408_OG [0x198]			

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2017/09/23	18:16:24.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/09/23	18:16:26.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/23	18:16:28.0	XRT_FOCUS_POSITION_410_0G [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/23	18:16:30.0	AOCS_ORe-point_Start_2_0G [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2017/09/23	18:16:48.0	XRT_FLD_ENA_411_0G [0x19b]	AOCU_NM	5	02-76	01 03 33 01 db
2017/09/23	18:16:50.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2017/09/23	18:16:52.0	XRT_AEC_RESET_448_0G [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2017/09/23	18:16:54.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2017/09/23	18:16:56.0	XRT_FLD_RESET_433_0G [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2017/09/23	18:19:26.0	XRT_QT_PROG_SET_404_0G [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/09/23	18:19:28.0	XRT_FL_PROG_SET_440_0G [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2017/09/23	18:19:30.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2017/09/23	19:04:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/09/23	19:04:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/23	19:04:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/23	19:04:36.0	XRT_PREFLR_STRT_449_0G [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/09/23	19:07:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/09/23	19:36:54.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/09/23	19:36:56.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/23	19:36:58.0	XRT_FOCUS_POSITION_410_0G [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/23	19:37:00.0	AOCS_ORe-point_Start_4_0G [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2017/09/23	19:37:18.0	XRT_FLD_ENA_411_0G [0x19b]	AOCU_NM	5	02-76	03 03 33 01 db
2017/09/23	19:37:20.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2017/09/23	19:37:22.0	XRT_AEC_RESET_448_0G [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2017/09/23	19:37:24.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2017/09/23	19:37:26.0	XRT_FLD_RESET_433_0G [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2017/09/23	19:39:56.0	XRT_QT_PROG_SET_407_0G [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/09/23	19:39:58.0	XRT_FL_PROG_SET_440_0G [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2017/09/23	19:40:00.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2017/09/23	20:36:54.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/09/23	20:36:56.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/23	20:36:58.0	XRT_FOCUS_POSITION_410_0G [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/23	20:37:00.0	AOCS_ORe-point_Start_2_0G [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2017/09/23	20:37:18.0	XRT_FLD_ENA_411_0G [0x19b]	AOCU_NM	5	02-76	01 03 33 01 db
2017/09/23	20:37:20.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2017/09/23	20:37:22.0	XRT_AEC_RESET_448_0G [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2017/09/23	20:37:24.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2017/09/23	20:37:26.0	XRT_FLD_RESET_433_0G [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2017/09/23	20:39:56.0	XRT_QT_PROG_SET_404_0G [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/09/23	20:39:58.0	XRT_FL_PROG_SET_440_0G [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2017/09/23	20:40:00.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2017/09/23	20:41:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/09/23	20:41:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/23	20:41:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/23	20:41:36.0	XRT_PREFLR_STRT_449_0G [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/09/23	20:44:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
			MDP_XRT_PREFLR_STOP	1	07-F0	e9

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2017/09/23	21:05:00.0	XRT_Custom_430_OG [0x1ae]		
2017/09/23	21:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2017/09/23	22:19:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/09/23	22:19:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/09/23	22:19:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2017/09/23	22:19:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2017/09/23	22:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2017/09/23	22:41:00.0	XRT_Custom_430_OG [0x1ae]		
2017/09/23	22:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2017/09/23	23:56:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/09/23	23:56:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/09/23	23:56:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2017/09/23	23:56:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2017/09/23	23:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2017/09/24	00:07:30.0	XRT_Custom_430_OG [0x1ae]		
2017/09/24	00:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2017/09/24	01:23:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/09/24	01:23:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/09/24	01:23:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2017/09/24	01:23:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2017/09/24	01:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2017/09/24	01:39:00.0	XRT_Custom_430_OG [0x1ae]		
2017/09/24	01:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2017/09/24	02:55:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/09/24	02:55:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/09/24	02:55:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2017/09/24	02:55:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2017/09/24	02:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2017/09/24	03:15:30.0	XRT_Custom_430_OG [0x1ae]		
2017/09/24	03:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2017/09/24	04:24:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/09/24	04:24:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/09/24	04:24:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2017/09/24	04:24:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2017/09/24	04:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2017/09/24	04:53:00.0	XRT_Custom_430_OG [0x1ae]		
2017/09/24	04:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2017/09/24	05:36:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/09/24	05:36:56.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/09/24	05:36:58.0	XRT_FOCUS_POSITION_403_OG [0x193]		
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2017/09/24	05:37:00.0	AOCS_Ore-point_Start_3_OG [0x099]		
		AOCU_NM	5	02-76 00 00 00 00 00
2017/09/24	05:37:18.0	XRT_FLD_DIS_425_OG [0x1a9]		
		MDP_XRT_FLD_DIS	1	07-F0 d9
2017/09/24	05:39:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]		
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9
2017/09/24	05:39:56.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2017/09/24	05:39:58.0	XRT_QT_PROG_SET_437_OG [0x1b5]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 02
2017/09/24	05:40:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2017/09/24	05:46:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/09/24	05:46:56.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/09/24	05:46:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]		
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2017/09/24	05:47:00.0	AOCS_Ore-point_Start_2_OG [0x098]		

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2017/09/24	05:47:18.0	XRT_FLD_ENA_411_OG [0x19b]	5	02-76 01 03 33 01 db
		MDP_XRT_FLD_ENA	1	07-F0 d8
2017/09/24	05:47:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	1	07-F0 c8
		MDP_XRT_FLRCTRL_ENA	1	07-F0 d0
2017/09/24	05:47:22.0	XRT_AEC_RESET_448_OG [0x1c0]	1	07-F0 d0
		MDP_XRT_AEC_RESET	1	07-F0 d5
2017/09/24	05:47:24.0	XRT_ARS_DIS_423_OG [0x1a7]	1	07-F0 d5
		MDP_XRT_ARS_DIS	1	07-F0 da
2017/09/24	05:47:26.0	XRT_FLD_RESET_433_OG [0x1b1]	1	07-F0 da
		MDP_XRT_FLD_RESET	2	07-F0 c4 06
2017/09/24	05:49:56.0	XRT_QT_PROG_SET_404_OG [0x194]	2	07-F0 c5 0d
		MDP_XRT_QT_PROG_SET	2	07-F0 c0
2017/09/24	05:49:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	2	07-F0 c1
		MDP_XRT_FL_PROG_SET	1	07-F0 c1
2017/09/24	05:50:00.0	XRT_CTRL_AUTO_408_OG [0x198]	1	07-F0 da
		MDP_XRT_CTRL_AUTO	1	07-F0 e8
2017/09/24	06:04:00.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0 e9
		MDP_XRT_CTRL_MANU	1	07-F0 c0
2017/09/24	06:04:02.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/09/24	06:04:04.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0 da
		MDP_XRT_FLD_RESET	1	07-F0 e8
2017/09/24	06:04:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]	1	07-F0 e9
		MDP_XRT_PREFLR_STRT	1	07-F0 c0
2017/09/24	06:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0 c1
		MDP_XRT_PREFLR_STOP	1	07-F0 da
2017/09/24	06:30:30.0	XRT_Custom_430_OG [0x1ae]	4	07-F8 22 fe 97 00
2017/09/24	06:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0 e9
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2017/09/24	07:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/09/24	07:44:32.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0 da
		MDP_XRT_CTRL_MANU	1	07-F0 e8
2017/09/24	07:44:34.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0 e9
		MDP_XRT_FLD_RESET	1	07-F0 c0
2017/09/24	07:44:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	1	07-F0 c1
		MDP_XRT_PREFLR_STRT	1	07-F0 c1
2017/09/24	07:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0 c1
		MDP_XRT_PREFLR_STOP	4	07-F8 22 ff aa 00
2017/09/24	08:07:30.0	XRT_Custom_430_OG [0x1ae]	5	02-76 04 03 33 01 db
2017/09/24	08:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0 d8
		MDP_XRT_CTRL_AUTO	1	07-F0 c8
2017/09/24	08:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0 d0
		MDP_XRT_CTRL_MANU	1	07-F0 d5
2017/09/24	08:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0 d5
		MDP_XRT_CTRL_MANU	1	07-F0 da
2017/09/24	08:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	2	07-F0 da
		XRT_FOCUS_POSITION	2	07-F0 c4 09
2017/09/24	09:00:00.5	AOCS_0Re-point_Start_1_OG [0x097]	2	07-F0 c5 0d
		AOCU_NM	2	07-F0 c0
2017/09/24	09:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	1	07-F0 c0
		MDP_XRT_FLD_ENA	1	07-F0 c1
2017/09/24	09:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	1	07-F0 c1
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c1
2017/09/24	09:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	1	07-F0 c1
		MDP_XRT_AEC_RESET	1	07-F0 c1
2017/09/24	09:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	1	07-F0 c1
		MDP_XRT_ARS_DIS	1	07-F0 c1
2017/09/24	09:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	1	07-F0 c1
		MDP_XRT_FLD_RESET	1	07-F0 c1
2017/09/24	09:02:56.0	XRT_QT_PROG_SET_438_OG [0x1b6]	2	07-F0 c1
		MDP_XRT_QT_PROG_SET	2	07-F0 c1
2017/09/24	09:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	2	07-F0 c1
		MDP_XRT_FL_PROG_SET	2	07-F0 c1
2017/09/24	09:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	1	07-F0 c1
		MDP_XRT_CTRL_AUTO	1	07-F0 c1
2017/09/24	12:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/09/24	12:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/09/24	12:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	4	07-F8 22 ff aa 00
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2017/09/24	13:00:00.0	AOCS_0Re-point_Start_3_OG [0x099]	5	02-76 00 00 00 00 00
		AOCU_NM	5	02-76 00 00 00 00 00
2017/09/24	13:00:18.0	XRT_FLD_DIS_425_OG [0x1a9]	1	07-F0 d9
		MDP_XRT_FLD_DIS	1	07-F0 c9
2017/09/24	13:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	1	07-F0 d5
		MDP_XRT_FLRCTRL_DIS	1	07-F0 d5
2017/09/24	13:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	1	07-F0 d5
		MDP_XRT_ARS_DIS	2	07-F0 c4 02
2017/09/24	13:02:58.0	XRT_QT_PROG_SET_437_OG [0x1b5]	2	07-F0 c4 02
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 02
2017/09/24	13:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	1	07-F0 c0
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2017/09/24	13:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/09/24	13:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/09/24	13:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	4	07-F8 22 fe 97 00
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2017/09/24	13:10:00.0	AOCS_0Re-point_Start_5_OG [0x09b]	5	02-76 01 00 00 00 00
		AOCU_NM	5	02-76 01 00 00 00 00

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2017/09/24	13:10:18.0	XRT_FLD_ENA_411_0G [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2017/09/24	13:10:20.0	XRT_FLRCTRL_ENA_412_0G [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2017/09/24	13:10:22.0	XRT_AEC_RESET_448_0G [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2017/09/24	13:10:24.0	XRT_ARS_DIS_423_0G [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2017/09/24	13:10:26.0	XRT_FLD_RESET_433_0G [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/09/24	13:12:56.0	XRT_QT_PROG_SET_444_0G [0x1bc]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2017/09/24	13:12:58.0	XRT_FL_PROG_SET_440_0G [0x1b8]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2017/09/24	13:13:00.0	XRT_CTRL_AUTO_408_0G [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/09/24	14:51:30.0	XRT_CTRL_MANU_400_0G [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/24	14:51:32.0	XRT_CTRL_MANU_402_0G [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/24	14:51:34.0	XRT_FLD_RESET_415_0G [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/09/24	14:51:36.0	XRT_PREFLR_STRT_449_0G [0x1c1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/09/24	14:54:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/09/24	15:01:30.0	XRT_Custom_430_0G [0x1ae]			
2017/09/24	15:02:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/09/24	15:59:54.0	XRT_CTRL_MANU_402_0G [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/24	15:59:56.0	XRT_CTRL_MANU_402_0G [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/24	15:59:58.0	XRT_FOCUS_POSITION_410_0G [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2017/09/24	16:00:00.0	AOCS_ORe-point_Start_6_0G [0x09c]			
		AOCU_NM	5	02-76	02 00 00 00 00
2017/09/24	16:00:18.0	XRT_FLD_ENA_411_0G [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2017/09/24	16:00:20.0	XRT_FLRCTRL_ENA_412_0G [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2017/09/24	16:00:22.0	XRT_AEC_RESET_448_0G [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2017/09/24	16:00:24.0	XRT_ARS_DIS_423_0G [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2017/09/24	16:00:26.0	XRT_FLD_RESET_433_0G [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/09/24	16:02:56.0	XRT_QT_PROG_SET_414_0G [0x19e]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2017/09/24	16:02:58.0	XRT_FL_PROG_SET_440_0G [0x1b8]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2017/09/24	16:03:00.0	XRT_CTRL_AUTO_408_0G [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/09/24	16:25:30.0	XRT_CTRL_MANU_400_0G [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/24	16:25:32.0	XRT_CTRL_MANU_402_0G [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/24	16:25:34.0	XRT_FLD_RESET_415_0G [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/09/24	16:25:36.0	XRT_PREFLR_STRT_449_0G [0x1c1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/09/24	16:28:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/09/24	16:49:00.0	XRT_Custom_430_0G [0x1ae]			
2017/09/24	16:50:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/09/24	18:02:30.0	XRT_CTRL_MANU_400_0G [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/24	18:02:32.0	XRT_CTRL_MANU_402_0G [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/24	18:02:34.0	XRT_FLD_RESET_415_0G [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/09/24	18:02:36.0	XRT_PREFLR_STRT_449_0G [0x1c1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/09/24	18:05:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/09/24	18:26:00.0	XRT_Custom_430_0G [0x1ae]			
2017/09/24	18:27:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/09/24	19:29:54.0	XRT_CTRL_MANU_402_0G [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/24	19:29:56.0	XRT_CTRL_MANU_402_0G [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/24	19:29:58.0	XRT_FOCUS_POSITION_410_0G [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2017/09/24	19:30:00.0	AOCS_ORe-point_Start_2_0G [0x098]			
		AOCU_NM	5	02-76	01 03 33 01 db
2017/09/24	19:30:18.0	XRT_FLD_ENA_411_0G [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2017/09/24	19:30:20.0	XRT_FLRCTRL_ENA_412_0G [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2017/09/24	19:30:22.0	XRT_AEC_RESET_448_0G [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0

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2017/09/24	19:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2017/09/24	19:30:26.0	XRT_FLD_RESET_433_OG [0x1b1]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2017/09/24	19:32:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 13
2017/09/24	19:32:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 0d
2017/09/24	19:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2017/09/24	19:39:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/09/24	19:39:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/09/24	19:39:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2017/09/24	19:39:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2017/09/24	19:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2017/09/24	20:02:30.0	XRT_Custom_430_OG [0x1ae]		
2017/09/24	20:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2017/09/24	21:16:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/09/24	21:16:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/09/24	21:16:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2017/09/24	21:16:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2017/09/24	21:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2017/09/24	21:40:00.0	XRT_Custom_430_OG [0x1ae]		
2017/09/24	21:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2017/09/24	22:54:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/09/24	22:54:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/09/24	22:54:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2017/09/24	22:54:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2017/09/24	22:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2017/09/24	23:14:30.0	XRT_Custom_430_OG [0x1ae]		
2017/09/24	23:15:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2017/09/25	00:31:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/09/25	00:31:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/09/25	00:31:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2017/09/25	00:31:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2017/09/25	00:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2017/09/25	00:39:00.0	XRT_Custom_430_OG [0x1ae]		
2017/09/25	00:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2017/09/25	01:55:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/09/25	01:55:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/09/25	01:55:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2017/09/25	01:55:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2017/09/25	01:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2017/09/25	02:13:30.0	XRT_Custom_430_OG [0x1ae]		
2017/09/25	02:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2017/09/25	03:30:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/09/25	03:30:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/09/25	03:30:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2017/09/25	03:30:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2017/09/25	03:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2017/09/25	03:50:30.0	XRT_Custom_430_OG [0x1ae]		
2017/09/25	03:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2017/09/25	05:00:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/09/25	05:00:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/09/25	05:00:04.0	XRT_FLD_RESET_415_OG [0x19f]		

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2017/09/25	05:00:06.0	XRT_PREFLR_STRT_449_0G [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/09/25	05:03:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/09/25	05:28:00.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/09/25	05:29:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/09/25	05:56:24.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/25	05:56:26.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/25	05:56:28.0	XRT_FOCUS_POSITION_403_0G [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2017/09/25	05:56:30.0	AOCS_Ore-point_Start_3_0G [0x099]	AOCU_NM	5	02-76	00 00 00 00 00
2017/09/25	05:56:48.0	XRT_FLD_DIS_425_0G [0x1a9]	MDP_XRT_FLD_DIS	1	07-F0	d9
2017/09/25	05:59:24.0	XRT_FLRCTRL_DIS_405_0G [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2017/09/25	05:59:26.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2017/09/25	05:59:28.0	XRT_QT_PROG_SET_426_0G [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2017/09/25	05:59:30.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/09/25	06:06:24.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/25	06:06:26.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/25	06:06:28.0	XRT_FOCUS_POSITION_410_0G [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2017/09/25	06:06:30.0	AOCS_Ore-point_Start_2_0G [0x098]	AOCU_NM	5	02-76	01 03 33 01 db
2017/09/25	06:06:48.0	XRT_FLD_ENA_411_0G [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2017/09/25	06:06:50.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2017/09/25	06:06:52.0	XRT_AEC_RESET_448_0G [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2017/09/25	06:06:54.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2017/09/25	06:06:56.0	XRT_FLD_RESET_433_0G [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/09/25	06:09:26.0	XRT_QT_PROG_SET_417_0G [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2017/09/25	06:09:28.0	XRT_FL_PROG_SET_440_0G [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2017/09/25	06:09:30.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/09/25	06:40:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/25	06:40:02.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/25	06:40:04.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/09/25	06:40:06.0	XRT_PREFLR_STRT_449_0G [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/09/25	06:43:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/09/25	07:05:30.0	XRT_Custom_430_0G [0x1ae]				
2017/09/25	07:06:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/09/25	08:20:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/25	08:20:02.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/25	08:20:04.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/09/25	08:20:06.0	XRT_PREFLR_STRT_449_0G [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/09/25	08:23:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/09/25	08:42:00.0	XRT_Custom_430_0G [0x1ae]				
2017/09/25	08:43:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/09/25	08:59:54.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/25	08:59:56.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/25	08:59:58.0	XRT_FOCUS_POSITION_410_0G [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2017/09/25	09:00:00.0	AOCS_Ore-point_Start_1_0G [0x097]	AOCU_NM	5	02-76	04 03 33 01 db
2017/09/25	09:00:18.0	XRT_FLD_ENA_411_0G [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2017/09/25	09:00:20.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2017/09/25	09:00:22.0	XRT_AEC_RESET_448_0G [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2017/09/25	09:00:24.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2017/09/25	09:00:26.0	XRT_FLD_RESET_433_0G [0x1b1]				

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2017/09/25	09:02:56.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/09/25	09:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2017/09/25	09:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2017/09/25	12:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/09/25	12:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/25	12:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/25	13:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2017/09/25	13:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01 03 33 01 db
2017/09/25	13:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2017/09/25	13:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2017/09/25	13:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2017/09/25	13:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2017/09/25	13:02:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/09/25	13:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2017/09/25	13:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2017/09/25	15:25:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/09/25	15:25:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/25	15:25:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/25	15:25:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/09/25	15:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/09/25	15:47:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/09/25	15:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2017/09/25	17:00:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/09/25	17:00:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/25	17:00:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/25	17:00:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/09/25	17:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/09/25	17:24:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/09/25	17:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2017/09/25	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/09/25	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/25	17:59:58.0	XRT_FOCUS_RECALIBRATE_416_OG [0x1a0]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/25	18:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]	XRT_FOCUS_RECAL	2	07-F8	78 00
2017/09/25	18:03:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	AOCU_NM	5	02-76	00 00 00 00 00
2017/09/25	18:04:18.0	XRT_FLD_DIS_425_OG [0x1a9]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2017/09/25	18:06:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2017/09/25	18:06:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2017/09/25	18:06:58.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5
2017/09/25	18:07:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2017/09/25	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/09/25	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/25	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/25	18:10:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2017/09/25	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01 03 33 01 db
2017/09/25	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2017/09/25	18:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2017/09/25	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
			MDP_XRT_ARS_DIS	1	07-F0	d5

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2017/09/25	18:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/09/25	18:12:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2017/09/25	18:12:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2017/09/25	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/09/25	18:37:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/25	18:37:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/25	18:37:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/09/25	18:37:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/09/25	18:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/09/25	19:00:30.0	XRT_Custom_430_OG [0x1ae]				
2017/09/25	19:01:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/09/25	20:14:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/25	20:14:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/25	20:14:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/09/25	20:14:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/09/25	20:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/09/25	20:37:30.0	XRT_Custom_430_OG [0x1ae]				
2017/09/25	20:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/09/25	21:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/25	21:51:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/25	21:51:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/09/25	21:51:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/09/25	21:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/09/25	22:14:30.0	XRT_Custom_430_OG [0x1ae]				
2017/09/25	22:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/09/25	23:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/25	23:29:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/25	23:29:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/09/25	23:29:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/09/25	23:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/09/25	23:45:30.0	XRT_Custom_430_OG [0x1ae]				
2017/09/25	23:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/09/26	01:02:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/26	01:02:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/26	01:02:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/09/26	01:02:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/09/26	01:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/09/26	01:12:30.5	XRT_Custom_430_OG [0x1ae]				
2017/09/26	01:13:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/09/26	02:28:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/26	02:28:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/26	02:28:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/09/26	02:28:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/09/26	02:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/09/26	02:48:30.0	XRT_Custom_430_OG [0x1ae]				
2017/09/26	02:49:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/09/26	03:57:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/26	03:57:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/26	03:57:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/09/26	03:57:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]				

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2017/09/26	04:00:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/09/26	04:25:30.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/09/26	04:26:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/09/26	05:36:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/26	05:36:02.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/26	05:36:04.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/09/26	05:36:06.0	XRT_PREFLR_STRT_449_0G [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/09/26	05:39:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/09/26	06:03:00.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/09/26	06:04:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/26	06:12:24.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/26	06:12:26.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/26	06:12:28.0	XRT_FOCUS_POSITION_403_0G [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2017/09/26	06:12:30.0	AOCS_ORe-point_Start_3_0G [0x099]	AOCU_NM	5	02-76	00 00 00 00 00
2017/09/26	06:12:48.0	XRT_FLD_DIS_425_0G [0x1a9]	MDP_XRT_FLD_DIS	1	07-F0	d9
2017/09/26	06:15:24.0	XRT_FLRCTRL_DIS_405_0G [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2017/09/26	06:15:26.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2017/09/26	06:15:28.0	XRT_QT_PROG_SET_437_0G [0x1b5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2017/09/26	06:15:30.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/09/26	06:22:24.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/26	06:22:26.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/26	06:22:28.0	XRT_FOCUS_POSITION_410_0G [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2017/09/26	06:22:30.0	AOCS_ORe-point_Start_2_0G [0x098]	AOCU_NM	5	02-76	01 03 33 01 db
2017/09/26	06:22:48.0	XRT_FLD_ENA_411_0G [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2017/09/26	06:22:50.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2017/09/26	06:22:52.0	XRT_AEC_RESET_448_0G [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2017/09/26	06:22:54.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2017/09/26	06:22:56.0	XRT_FLD_RESET_433_0G [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/09/26	06:25:26.0	XRT_QT_PROG_SET_444_0G [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2017/09/26	06:25:28.0	XRT_FL_PROG_SET_440_0G [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2017/09/26	06:25:30.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/09/26	07:16:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/26	07:16:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/26	07:16:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/09/26	07:16:36.0	XRT_PREFLR_STRT_449_0G [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/09/26	07:19:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/09/26	07:40:30.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/09/26	07:41:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/26	08:56:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/26	08:56:02.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/26	08:56:04.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/09/26	08:56:06.0	XRT_PREFLR_STRT_449_0G [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/09/26	08:59:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/09/26	09:00:00.0	AOCS_ORe-point_Start_7_0G [0x09d]	AOCU_NM	5	02-76	04 00 00 00 00
2017/09/26	09:16:31.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/26	09:16:33.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/26	09:16:35.0	XRT_FOCUS_POSITION_410_0G [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2017/09/26	09:16:55.0	XRT_FLD_ENA_411_0G [0x19b]				

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2017/09/26	09:16:57.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2017/09/26	09:16:59.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2017/09/26	09:17:01.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2017/09/26	09:17:03.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2017/09/26	09:19:33.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/09/26	09:19:35.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2017/09/26	09:19:37.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2017/09/26	10:37:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/09/26	10:37:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/26	10:37:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/09/26	10:37:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/09/26	10:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/09/26	10:53:30.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/09/26	11:10:00.0	AOCS_ORe-point_Start_3_OG [0x099]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		AOCU_NM		5	02-76	00 00 00 00 00