

Period: 2017/10/10 10:31:00 - 2017/10/14 10:09:00

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XOB #1BCA: CCD Monitor During Bakeout - G-Band 1ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh (2048ms), Al/Poly (4096ms) - w leak image-1 ms															
Term		Pointing (x, y)						Comment							
10/11 12:33:00 - 10/11 12:39:54		Fixed (-528.4, 528.4)						4/4							
PROG= 19		1-time(s)													
Subr= 1		1-time(s)			2.0sec										
└─ Seqn= 14		1-time(s)			2.0sec										
└─┬─		Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024	(1536, 512)	Q=90	0	0	2.0sec
└─┬─		Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024	(1536, 512)	Q=90	0	0	2.0sec
└─┬─		Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024	(1536, 512)	Q=98	0	0	2.0sec
└─┬─		Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024	(1536, 512)	Q=98	0	0	2.0sec
Subr= 2		1-time(s)			2.0sec										

└	Seqn= 3	2-time(s)	2.0sec										
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└	Al-poly/Open	Al-poly/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└	Subr= 3	2-time(s)	2.0sec										
	Seqn= 34	1-time(s)	2.0sec										
└	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
				Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp. AEC Buffer Interval

XOB #1B94: CME watch - 4x4 - AEC 2/3 - 2-filter (Be-thin, Al-poly) - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 180s cad (G-band/Leak first)

Term	Pointing (x, y)	Comment
10/11 12:47:05 - 10/11 16:37:30	Track (-356.2, 20.4) @ 10/11 12:40:00	HOP 323 plage
10/11 21:52:30 - 10/12 00:40:54	Track (102.6, 461.7) @ 10/11 21:41:00	EIS north large CH
10/12 06:13:00 - 10/12 10:10:00	Track (-201.4, 16.5) @ 10/12 06:10:00	HOP 323 plage

PROG= 08 Inf.-time(s)													
└	Subr= 1	1-time(s)	2.0sec										
	Seqn= 30	1-time(s)	2.0sec										
└	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
└	Subr= 2	20-time(s)	180.0sec										
	Seqn= 8	1-time(s)	2.0sec										
└	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
└	Seqn= 6	1-time(s)	2.0sec										
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
				Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp. AEC Buffer Interval

XOB #1BA8: Synoptic 7 Filter w/ Al-mesh(24/256/2897), Al-poly(45/512/4096), Thin-Be(181/2048/11571) - Thick-Be(65536), Al-poly+Ti-poly(256/5795), Med-Al

Term	Pointing (x, y)	Comment
10/11 17:04:00 - 10/11 17:10:54	Fixed (0.0, 0.0)	synoptic, shifted -50.0 min

PROG= 05 1-time(s)													
└	Subr= 1	1-time(s)	2.0sec										
	Seqn= 5	1-time(s)	2.0sec										
└	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
└	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	1024x1024 (1536, 1536)	DPCM	0	0	2.0sec
└	Seqn= 1	1-time(s)	2.0sec										
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└	Seqn= 99	1-time(s)	2.0sec										
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└	Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└	Seqn= 67	1-time(s)	2.0sec										
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└	thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└	Seqn= 23	1-time(s)	4.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└	Subr= 2	1-time(s)	2.0sec										
	Seqn= 46	2-time(s)	2.0sec										
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└	Seqn= 40	2-time(s)	2.0sec										
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└	Seqn= 65	2-time(s)	2.0sec										
	med-Al/Open	med-Al/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
				Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp. AEC Buffer Interval

XOB #1B96: CME watch - 4x4 - AEC 2/3 - 2-filter (Be-thin, Al-poly) - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 360s cad (G-band/Leak first)

Term	Pointing (x, y)	Comment
10/11 17:14:00 - 10/11 21:28:30	Track (527.8, 27.8) @ 10/11 17:11:00	QS network and coronal BP
10/12 00:51:30 - 10/12 05:59:54	Track (129.0, -721.0) @ 10/12 00:41:00	HOP 336 southwest

PROG= 18 Inf.-time(s)													
└	Subr= 1	1-time(s)	2.0sec										
	Seqn= 30	1-time(s)	2.0sec										
└	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
└	Subr= 2	10-time(s)	360.0sec										
	Seqn= 8	1-time(s)	2.0sec										
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec

thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 6 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1BC0: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(12/181/1443) + Th

Term			Pointing (x, y)					Comment							
10/12 06:03:00 - 10/12 06:09:54			Fixed (0.0, 0.0)					synoptic							
PROG= 03 1-time(s)															
Subr= 1		1-time(s)		2.0sec											
Seqn= 5		1-time(s)		2.0sec											
├ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)			Q=98	0	0	2.0sec
├ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)			Q=98	0	0	2.0sec
├ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)			Q=98	0	0	2.0sec
├ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)			DPCM	0	0	2.0sec
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	1024x1024 (1536, 1536)			DPCM	0	0	2.0sec
Seqn= 80		1-time(s)		2.0sec											
├ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
├ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
└ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
Seqn= 13		1-time(s)		2.0sec											
├ Al-poly/Open		Al-poly/Open	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
├ Al-poly/Open		Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
└ Al-poly/Open		Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
Seqn= 37		1-time(s)		2.0sec											
├ thin-Be/Open		thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
├ thin-Be/Open		thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
└ thin-Be/Open		thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
Seqn= 23		1-time(s)		2.0sec											
├ Open/G-band		Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)			Q=90	0	0	2.0sec
└ Open/G-band		Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1B8E: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)				Comment									
10/11 12:47:05 - 10/11 16:37:30	Track (-356.2, 20.4) @ 10/11 12:40:00				HOP 323 plage									
10/11 17:14:00 - 10/11 21:28:30	Track (527.8, 27.8) @ 10/11 17:11:00				QS network and coronal BP									
10/11 21:52:30 - 10/12 00:40:54	Track (102.6, 461.7) @ 10/11 21:41:00				EIS north large CH									
10/12 00:51:30 - 10/12 05:59:54	Track (129.0, -721.0) @ 10/12 00:41:00				HOP 336 southwest									
10/12 06:13:00 - 10/12 10:10:00	Track (-201.4, 16.5) @ 10/12 06:10:00				HOP 323 plage									
PROG= 13 30-time(s)														
Subr= 1		20-time(s)		2.0sec										
Seqn= 11		1-time(s)		2.0sec										
Al-poly/Open		Al-poly/thick-Al		close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn=100		1-time(s)		10.0sec										
thin-Be/Open		med-Be/Open		close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
med-Be/Open		Open/thick-Al		close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al		Open/thick-Be		close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2		1-time(s)		2.0sec										
Seqn= 10		1-time(s)		2.0sec										
med-Al/Open		med-Al/thick-Al		close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be		Open/thick-Be		close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11		1-time(s)		2.0sec										
Al-poly/Open		Al-poly/thick-Al		close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 87		1-time(s)		2.0sec										
Open/G-band		Open/G-band		open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band		Open/G-band		close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al		Open/thick-Al		close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al		Open/thick-Al		close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

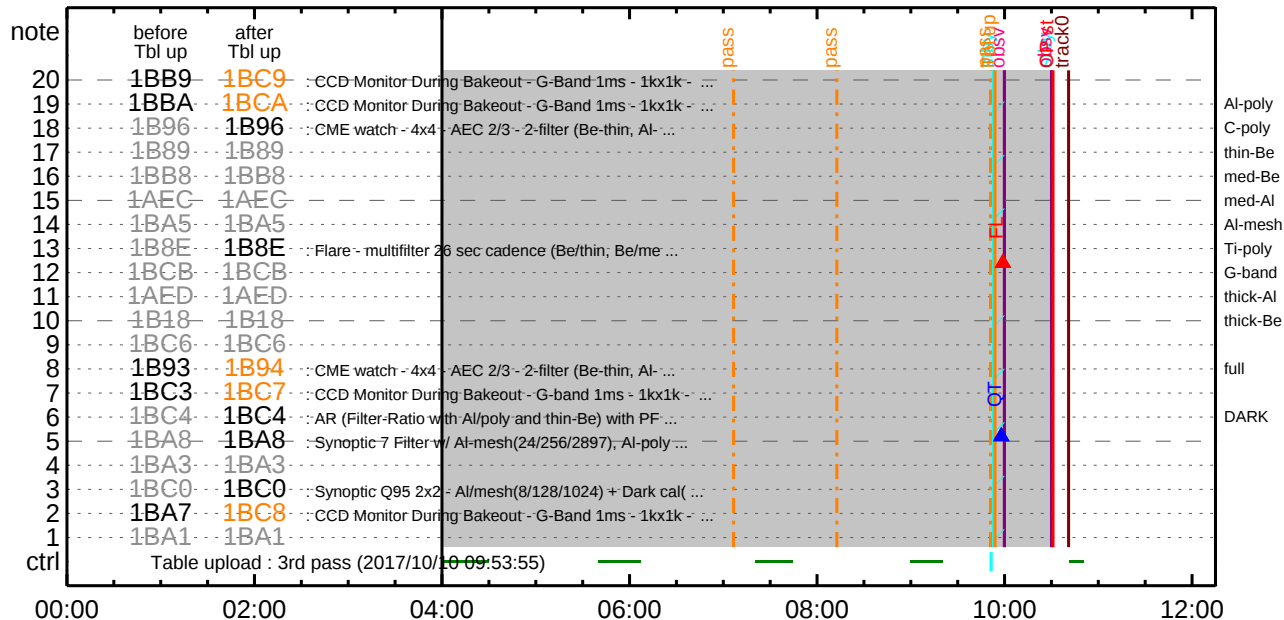
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Flare Detection

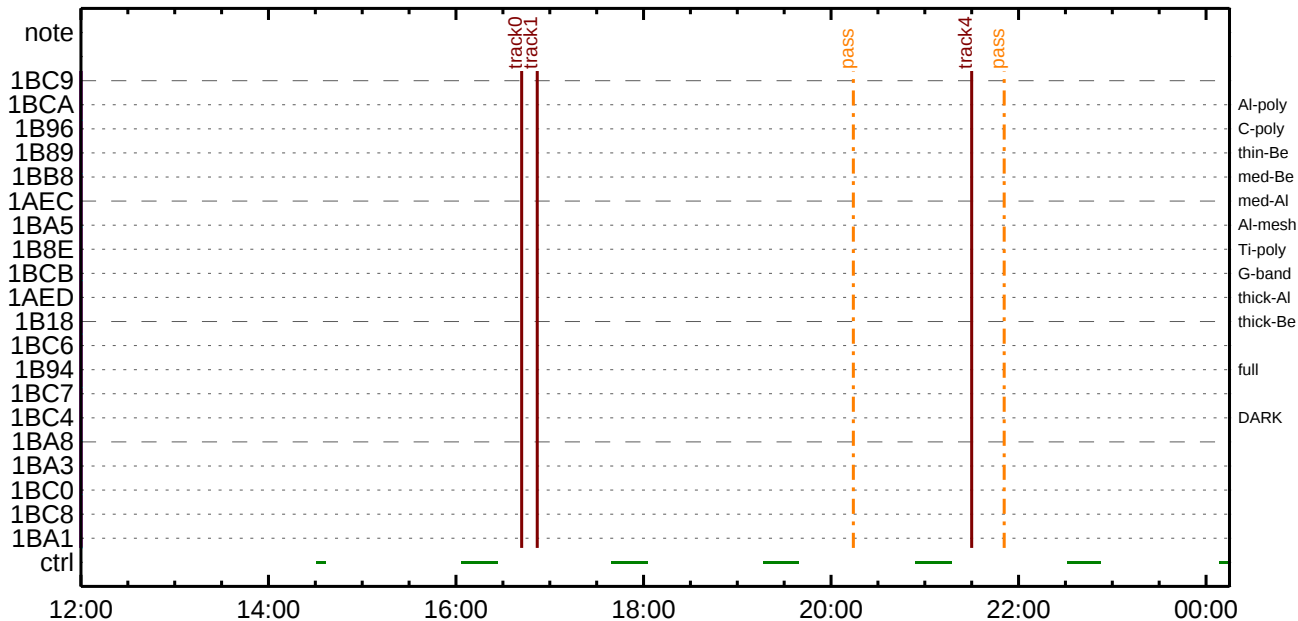
* * * * *

FLD Patrol												
Term	Pointing (x, y)		Comment									
10/11 12:44:18 - 10/11 17:01:18	Track (-356.2, 20.4) @ 10/11 12:40:00		HOP 323 plage									
10/11 17:11:18 - 10/12 06:00:18	Track (527.8, 27.8) @ 10/11 17:11:00		QS network and coronal BP									
10/12 06:10:18 - 10/14 10:09:00	Track (-201.4, 16.5) @ 10/12 06:10:00		HOP 323 plage									
Al-poly/Open	Al-poly/Open	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

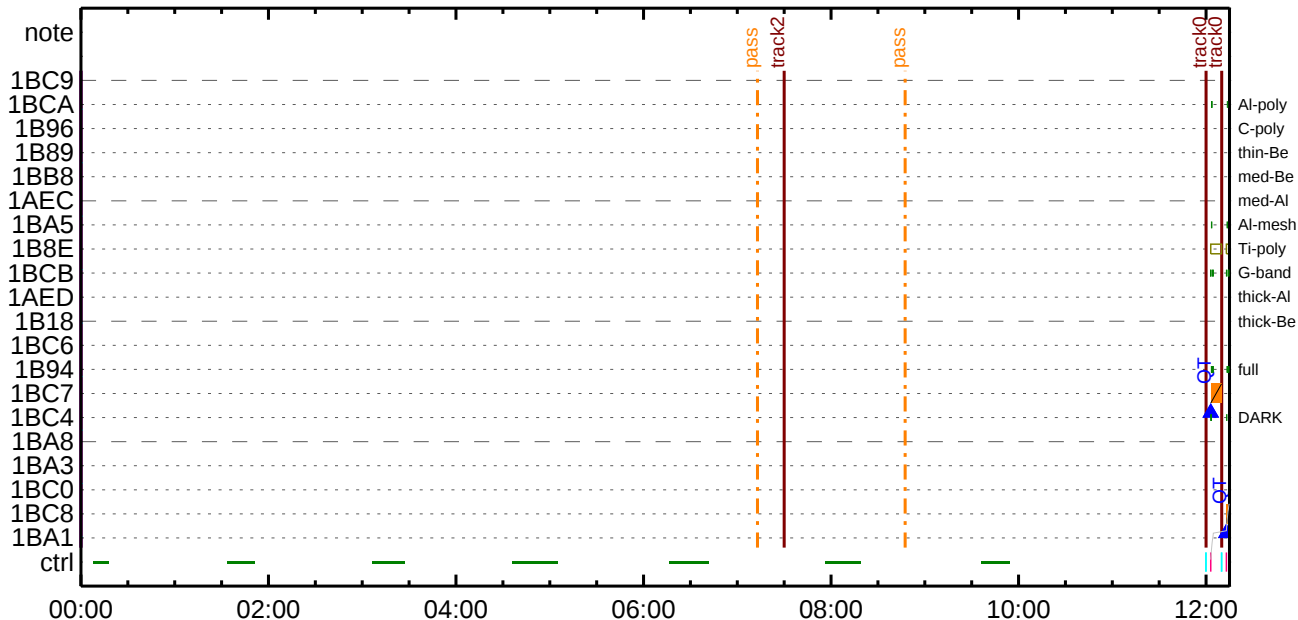
CMDI #0051 2017/10/10



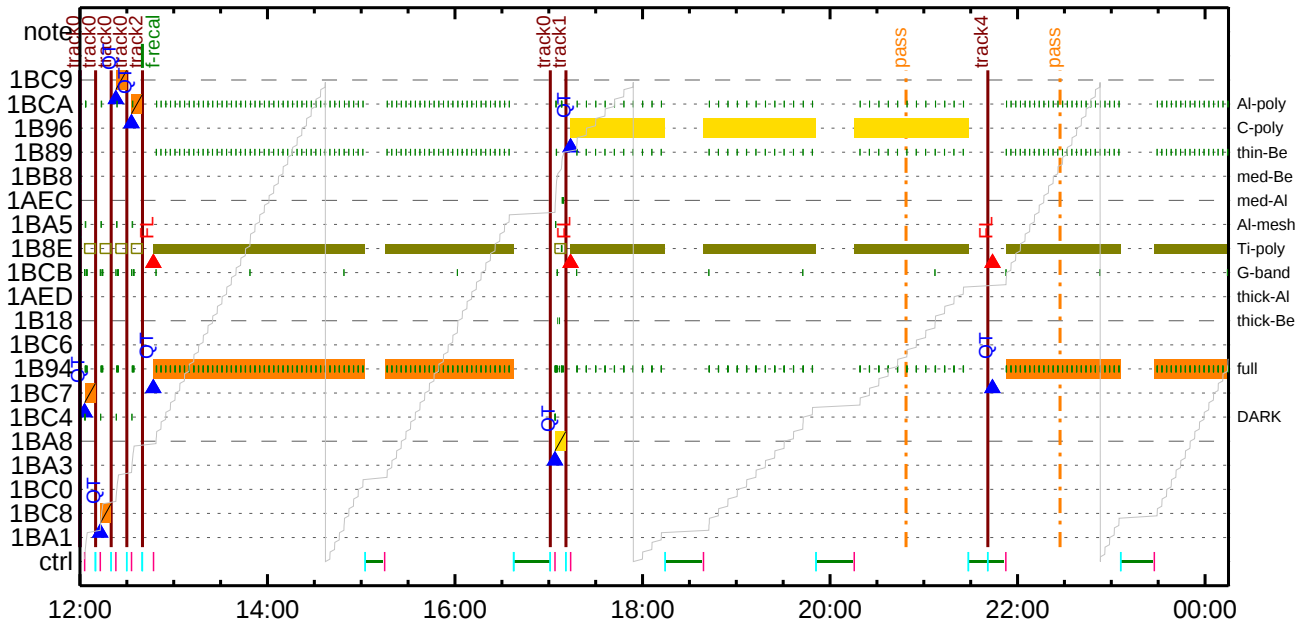
CMDI #0051 2017/10/10



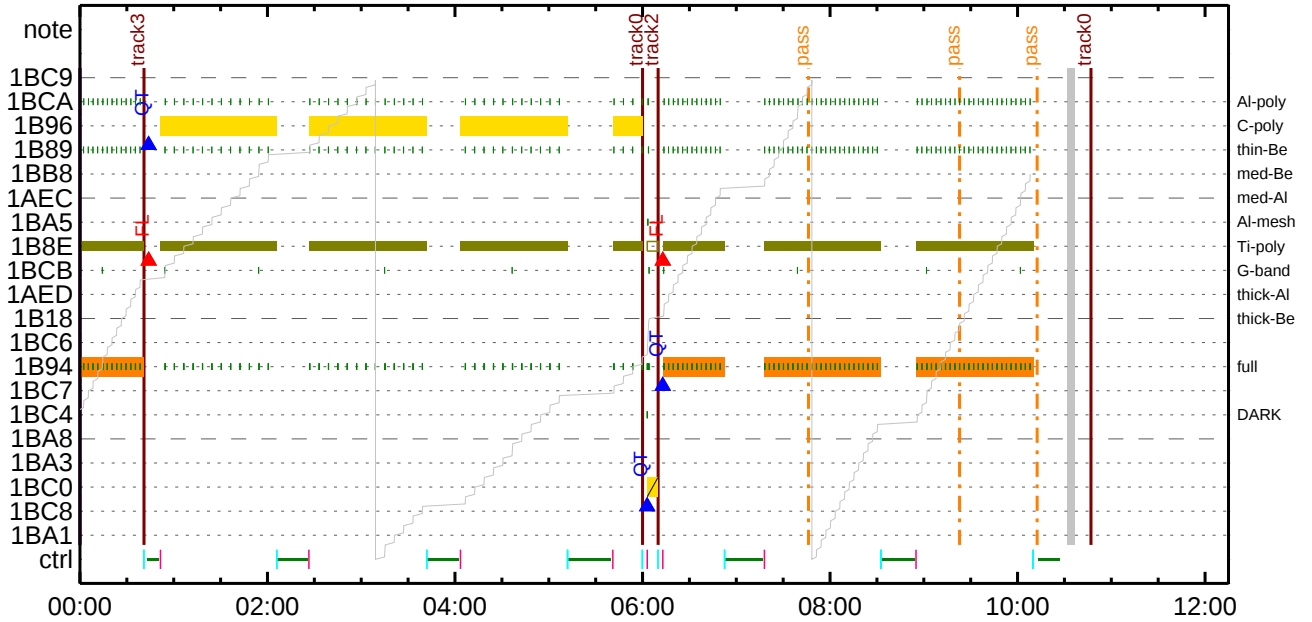
CMDI #0051 2017/10/11



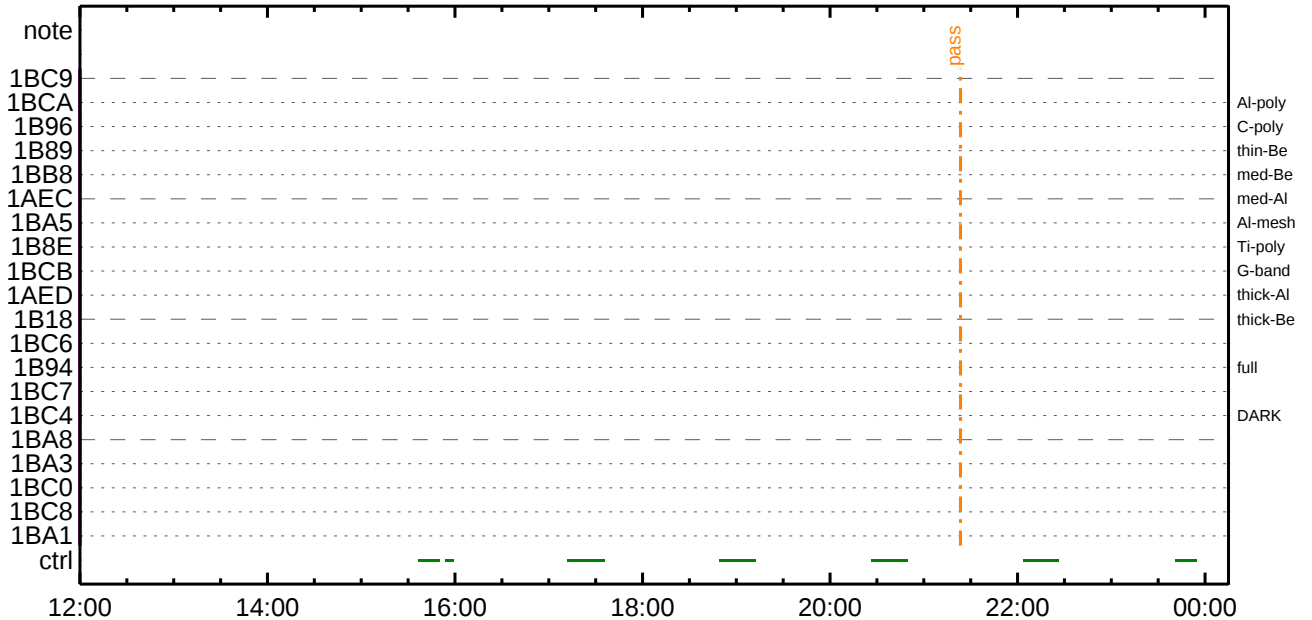
CMDI #0051 2017/10/11



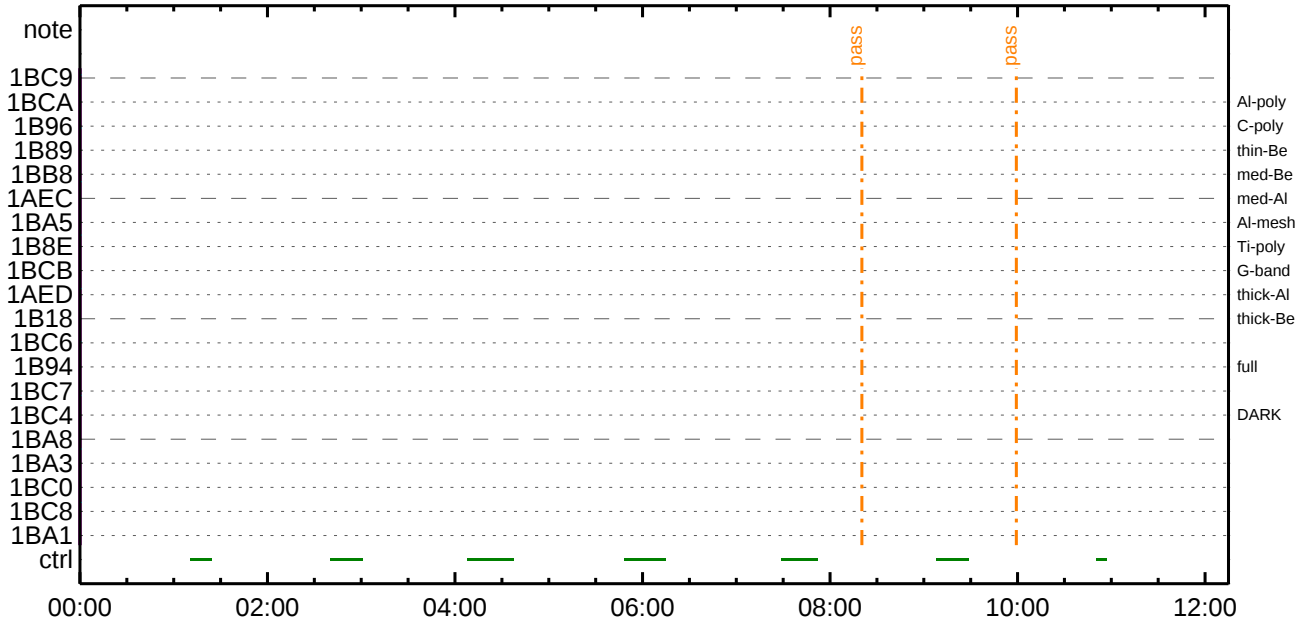
CMDI #0051 2017/10/12



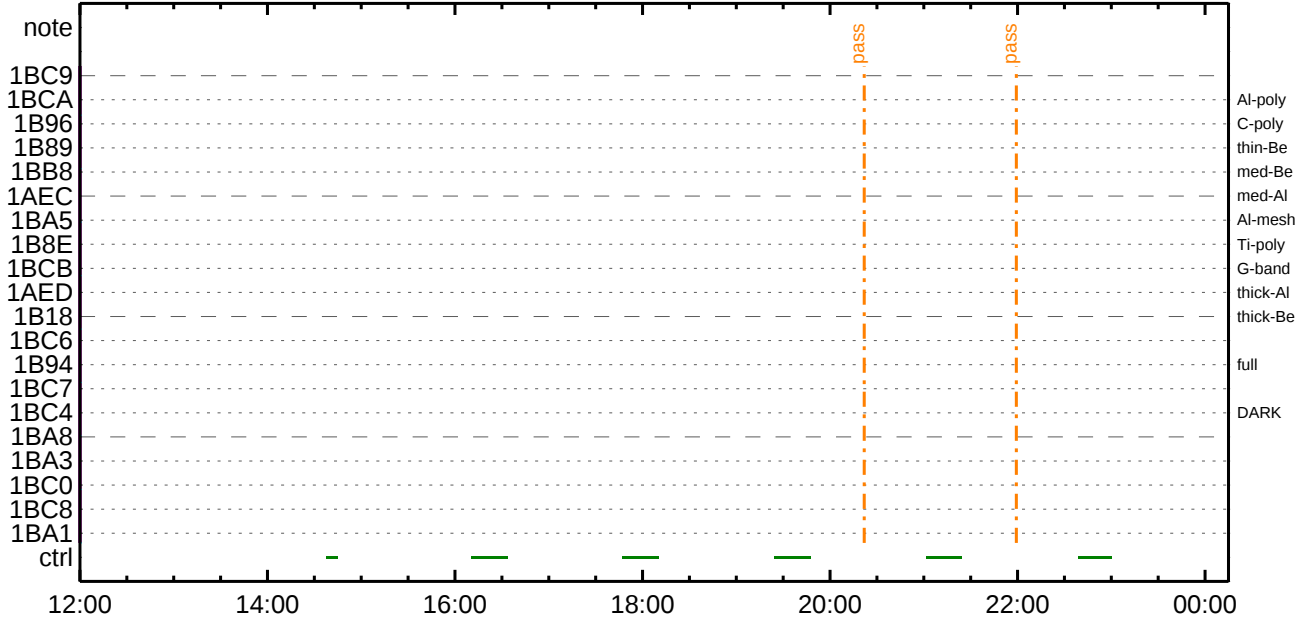
CMDI #0051 2017/10/12



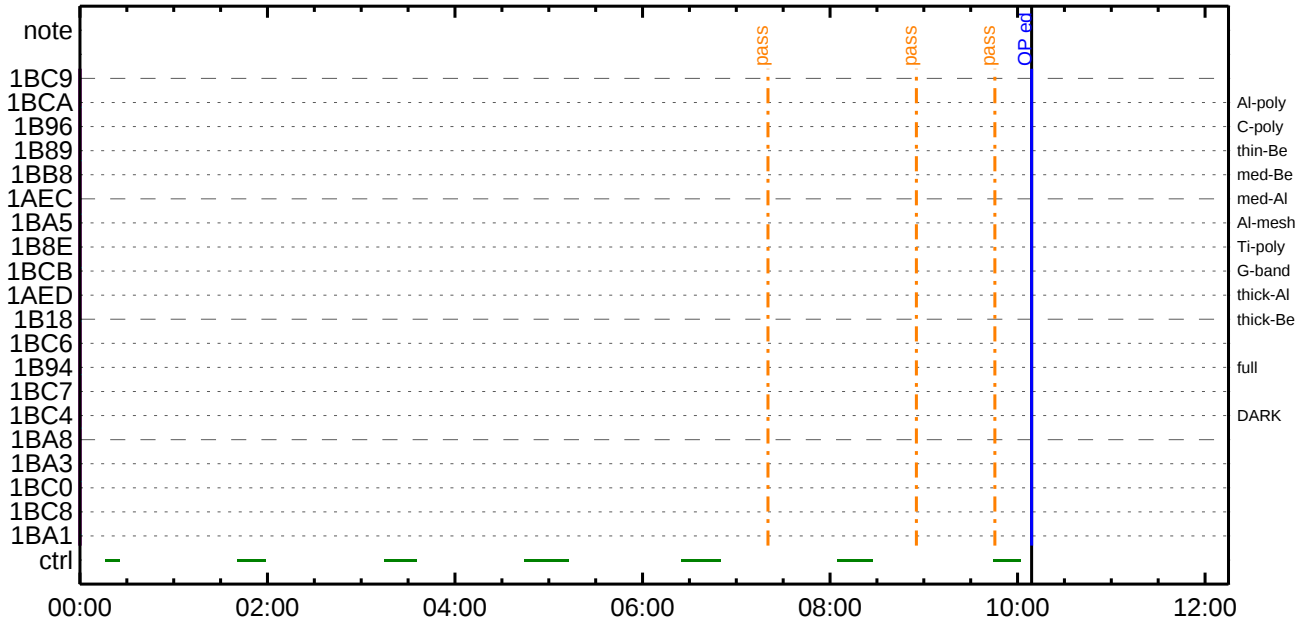
CMDI #0051 2017/10/13

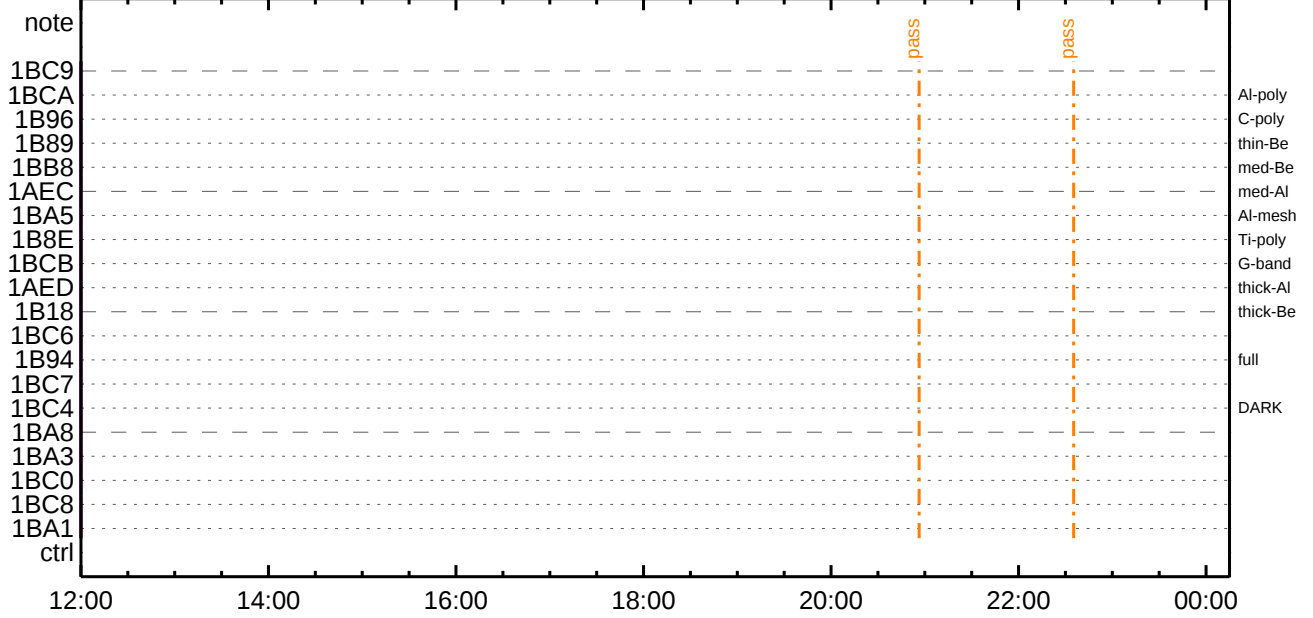


CMDI #0051 2017/10/13



CMDI #0051 2017/10/14





Line	Command	Parameter	Condition	Countup
0096	C.	0B0CjCSET0EDUMP0iE±0iYñY10Ç100 030EjE		
0097	C.			
0098	C.	TIY3YpYóYÉ00ÀDİç(UT)		
0099	+	TI 2017-10-10 10:26:00.0		
0100	DC	01-B3 DHU_OP_STOP		
0101	C.	ÇÇ[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0102	C.			
0103	+	TI 2017-10-10 10:26:01.0		
0104	DC	01-B4 DHU_OP_COPY		
0105	C.	ÇÇ[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0106	C.			
0107	+	TI 2017-10-10 10:26:01.0		
0108	DC	01-B5 DHU_OPOG_COPY		
0109	C.	ÇÇ[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0110	C.			
0111	+	TI 2017-10-10 10:30:59.5		
0112	DC	01-B2 DHU_OP_START		
0113	C.	ÇÇ[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0114	C.			
0115	C.	°Ê²¼0İÄê%İİŃ0İ¥Ä\$¥\$Ä¥~¹àİÜ		
0116	C.	ÇÇ[HK1_TI_CMD_ENA/DIS]	EQ	ENA
0117	C.	ÇÇ[HK1_TI_CMD_NUM]	EQ	4
0118	C.	ÇÇ[HK1_NEXT_EXEC_PIM]	EQ	DHU
0119	C.	ÇÇ[HK1_NEXT_EXEC_DC]	EQ	0xB3
0120	C.			
0121	C.	*****		
0122	C.	TIİİ°è¥ÄYó¥x		
0123	C.	*****		
0124	C.			
0125	C.	TI_TBL(0x03AB00-0x03AEFFj\$ 1024byte)		
0126	+	DC 01-23 DHU_DMP_PRM_SET		
0127	BC	(03 ab 03 01 02)		
0128	C.	ÇÇ[HK1_DMP_TOP_ADRS_1]	EQ	07
0129	C.	ÇÇ[HK1_DMP_TOP_ADRS_0]	EQ	2B
0130	C.	ÇÇ[HK1_DMP_BLOCK_NUM]	EQ	3
0131	C.	ÇÇ[HK1_DMP_REPEAT_NUM]	EQ	0
0132	C.	ÇÇ[HK1_DMA_DMP_PIM]	EQ	DHU
0133	+	DC 01-22 DHU_MODE_CHNG		
0134	BC	(07 0b f8)		
0135	C.	ÇÇ[HK1_PKT_FORM_NO]	EQ	7
0136	C.	ÇÇ[HK1_PKT_GEN_TIME]	EQ	0.25 s
0137	C.	ÇÇ[HK1_S_TLM_BIT_RATE]	EQ	32k
0138	C.	ÇÇ[HK1_X_TLM_BIT_RATE]	EQ	4M
0139	C.	ÇÇ[HK1_DMP_CHK_FLG]	EQ	EXEC
0140	C.			
0141	C.	¥ÄYó¥x½ªİ»00³İÇ\$		
0142	C.	ÇÇ[HK1_DMP_CHK_FLG]	EQ	NON
0143	C.			
0144	C.	RAM ID=TI_TBL0İ%Ê¹Ç•è²İOK00³İÇ\$		
0145	C.			
0146	C.	DHU¥âj¼¥ÉjÊ¼ý½ ¥İj¼¥ÉjÊ00İâ0¹		
0147	+	DC 01-22 DHU_MODE_CHNG		
0148	BC	(02 0a f8)		
0149	C.	ÇÇ[HK1_PKT_FORM_NO]	EQ	2
0150	C.	ÇÇ[HK1_PKT_GEN_TIME]	EQ	0.5S
0151	C.	ÇÇ[HK1_S_TLM_BIT_RATE]	EQ	32K
0152	C.	ÇÇ[HK1_X_TLM_BIT_RATE]	EQ	4M
0153	C.			
0154	C.	Stop EIS observation and temporarily disable EIS mode changes		
0155	C.			
0156	C.			
0157	C.	***** Start EIS operation (TI set) *****		
0158	C.	Execute, after the success of OP upload.		
0159	C.	Set EIS TI-commands		
0160	+	TI 2017-10-10 10:30:30.0		
0161	DC	07-FC EIS_MODE_MANU		
0162	BC	(21 02)		
0163	+	TI 2017-10-10 10:30:40.0		
0164	DC	07-FC EIS_MODE_CHG_DIS		
0165	BC	(22)		
0166	C.	[] [HK1_TI_CMD_NUM]	EQ	2 COUNTUP
0167	C.	***** End EIS operation (TI set) *****		
0168	C.			
0169	C.			
0170	C.			
0171	C.	***** XRT START *****		
0172	C.	Execute, after the success of OP upload.		
0173	+	TI 2017-10-10 10:30:00.0		
0174	DC	07-F0 MDP_XRT_MODE_STBY		
0175	BC	(c3)		
0176	C.	[] [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0177	C.			
0178	C.	***** XRT END *****		
0179	C.			
0180	C.	***** MDP 'üÄİ0İ»0¼Y0EÄ00¹0èDCBC•x²è *****		
0181	C.	(%â°İ0¥Ä¥É¥p¥É¥â¥Ç¥é0½¼0¼¼A»Ü0¹0è)		
0182	S.	DC-BC dcbc-402:DCBC		
0183		(MDP_known_event)		
0184	C.			
0185	C.			
0186	C.	***** ¥D¥1•İ Daily±çİŃ0E'00¹0èDCBC•x²è *****		
0187	S.	DC-BC dcbc-153:DCBC		
0188		(SPECIAL-CMD_DAILY_OPERATIN_DCB)		
0189	C.			
0190	C.	</		


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0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCS DUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 C.
0129 . C. ***** AOCS Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 +. DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 4363022.5 +- 1.0 (s) [ ]
0136 C.
0137 C.
0138 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139 +. DC 07-FC EIS_MODE_CHG_ENA
0140 BC (20)
0141 . C. Verify EIS_MODE_CHG_FLG is ENA
0142 +. DC 07-FC EIS_MODE_MANU
0143 BC (21 02)
0144 . C. Verify EIS in MANUAL mode
0145 . C. Estimated OBSTBL upload time is 29s
0146 C. *****
0147 C. EIS START OBSTBL LOAD
0148 C. *****
0149 . S. RAM ram-820:EIS_OBSTBL
0150 ( )
0151 +. DC 07-FC EIS_DUMP_OBSTBL
0152 BC (07 07 07 00 00 70 00)
0153 C.
0154 C. Execute, after the success of OBSTBL upload.
0155 C. Set EIS TI-commands
0156 +. TI 2017-10-10 10:30:50.0
0157 DC 07-FC EIS_MODE_CHG_ENA
0158 BC (20)
0159 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0160 C. *****
0161 C. EIS END OBSTBL LOAD
0162 C. *****
0163 C.
0164 . C. ***** MDP '0ÃÎï»0%ÝðËÄð¹ëDCBC•x²è *****
0165 C. (%ã°Î¥0¥Ã¥Ë¥ð¥Ë¥ã¥Ç¥èðË½¼¼Å»Û¹¼é)
0166 . S. DC-BC dcbc-402:DCBC
0167 (MDP_known_event)
0168 C.
0169 C.
0170 . C. ***** ¥Ð¥¹•Ï Daily±¿ÎÑðË´0¹ëDCBC•x²è *****
0171 . S. DC-BC dcbc-153:DCBC
0172 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0173 C.
0174 C.
0175 . C. ¡ãLOS¥Á¥S¥Ã¥´¼Å»Û¡ã
0176 C.
0177 . C. ***** LOS *****
0178 C.

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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 +. DC 07-F0 MDP_XRT_CTRL_MANU
0104 BC (c1)
0105 + DC 07-F0 MDP_XRT_MODE_STBY
0106 BC (c3)
0107 . C. ----- Success Verify ? OK / NG ____
0108 C.
0109 C. XRT Obs. Table Upload
0110 . S. RAM ram-291:MDP_OBS_X
0111 ( )
0112 C.
0113 +. DC 07-F0 MDP_DUMP_XRTTBL
0114 BC (84 07 00 00 00 3a d4)
0115 . C. ----- Comparison Check ? OK / ERR ____
0116 C.
0117 C.
0118 +. DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 01 b1 b1 04 04)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 02 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 03 b1 b1 08 08)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 04 b1 b1 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 05 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 06 85 83 06 06)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 07 85 83 08 08)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 08 c0 c0 10 10)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 09 80 80 20 20)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0a 40 c0 10 10)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0b 40 40 10 10)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 0c c0 40 10 10)
0142 + DC 07-F0 MDP_XRT_ROI_SET
0143 BC (cd 0d 80 80 08 08)
0144 + DC 07-F0 MDP_XRT_ROI_SET
0145 BC (cd 0e 80 80 20 08)
0146 + DC 07-F0 MDP_XRT_ROI_SET
0147 BC (cd 0f 80 80 06 06)
0148 + DC 07-F0 MDP_XRT_ROI_SET
0149 BC (cd 10 80 80 08 08)
0150 + DC 07-F0 MDP_XRT_FLD_ENA
0151 BC (d8)
0152 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0153 BC (c8)
0154 + DC 07-F0 MDP_XRT_ARS_DIS
0155 BC (d5)
0156 +. DC 07-F0 MDP_XRT_AEC_RESET
0157 BC (d0)
0158 +. DC 07-F0 MDP_XRT_FLD_RESET
0159 BC (da)
0160 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0161 BC (c4 06)
0162 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0163 BC (c5 0d)
0164 . C. ----- Success Verify ? OK / NG ____
0165 C.
0166 C.
0167 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0168 C.
0169 +. DC 07-F0 MDP_XRT_MODE_OBSV
0170 BC (c2)
0171 +. TI 2017-10-10 10:30:02.0
0172 DC 07-F0 MDP_XRT_MODE_OBSV
0173 BC (c2)
0174 . C. ----- Success Verify ? OK / NG ____
0175 C.
0176 C. ***** XRT END *****
0177 C.
0178 . C. ***** MDP ´ÛÃÏ±Ï»Ö%Y±ÊÂÐ±¹±èDCBC•x²è *****
0179 C. (%â°i¥Ö¥Ã¥Ê¥Ð¥Ê¥â¥Ç¥è±Ê½%±±¼Ã»Û±¹±è)
0180 . S. DC-BC dcbc-402:DCBC
0181 (MDP_known_event)
0182 C.
0183 C.
0184 . C. ***** ¥Ð¥¹•Ï Daily±¿ÏÑ±Ê´Ø±¹±èDCBC•x²è *****
0185 . S. DC-BC dcbc-153:DCBC
0186 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0187 C.
0188 C.
0189 . C. ¡ãLOS¥Ã¥S¥Ã¥¬¼Ã»Û¡ä
0190 C.
0191 . C. ***** LOS *****
0192 C.

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*** OP Sequence for XRT ***

2017/10/10	10:41:00.0	AOCS_ORe-point_Start_1_OG [0x097]		
		AOCU_NM	5	02-76 00 b3 34 01 db
2017/10/10	16:42:00.0	AOCS_ORe-point_Start_2_OG [0x098]		
		AOCU_NM	5	02-76 00 00 00 00 00
2017/10/10	16:52:00.0	AOCS_ORe-point_Start_3_OG [0x099]		
		AOCU_NM	5	02-76 01 03 33 01 db
2017/10/10	21:30:00.0	AOCS_ORe-point_Start_4_OG [0x09a]		
		AOCU_NM	5	02-76 04 03 33 01 db
2017/10/11	06:00:00.0	XRT_TCIB_XRT_S_HTR_A_DIS_441_OG [0x1b9]		
		TCIB_XRT_S_HTR_A_DIS	0	04-C0
2017/10/11	07:30:00.0	AOCS_ORe-point_Start_5_OG [0x09b]		
		AOCU_NM	5	02-76 02 03 33 01 db
2017/10/11	11:59:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/10/11	11:59:56.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/10/11	11:59:58.0	XRT_FOCUS_POSITION_447_OG [0x1bf]		
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2017/10/11	12:00:00.0	AOCS_ORe-point_Start_6_OG [0x09c]		
		AOCU_NM	5	02-76 00 2e f9 2e f9
2017/10/11	12:00:18.0	XRT_FLD_DIS_409_OG [0x199]		
		MDP_XRT_FLD_DIS	1	07-F0 d9
2017/10/11	12:00:20.0	XRT_FLRCTRL_DIS_418_OG [0x1a2]		
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9
2017/10/11	12:02:56.0	XRT_ARS_DIS_445_OG [0x1bd]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2017/10/11	12:02:58.0	XRT_QT_PROG_SET_442_OG [0x1ba]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 07
2017/10/11	12:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2017/10/11	12:09:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/10/11	12:09:56.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/10/11	12:09:58.0	XRT_FOCUS_POSITION_447_OG [0x1bf]		
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2017/10/11	12:10:00.0	AOCS_ORe-point_Start_7_OG [0x09d]		
		AOCU_NM	5	02-76 00 2e f9 d1 07
2017/10/11	12:10:18.0	XRT_FLD_DIS_409_OG [0x199]		
		MDP_XRT_FLD_DIS	1	07-F0 d9
2017/10/11	12:10:20.0	XRT_FLRCTRL_DIS_418_OG [0x1a2]		
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9
2017/10/11	12:12:56.0	XRT_ARS_DIS_445_OG [0x1bd]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2017/10/11	12:12:58.0	XRT_QT_PROG_SET_437_OG [0x1b5]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 02
2017/10/11	12:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2017/10/11	12:19:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/10/11	12:19:56.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/10/11	12:19:58.0	XRT_FOCUS_POSITION_447_OG [0x1bf]		
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2017/10/11	12:20:00.0	AOCS_ORe-point_Start_8_OG [0x09e]		
		AOCU_NM	5	02-76 00 d1 07 d1 07
2017/10/11	12:20:18.0	XRT_FLD_DIS_409_OG [0x199]		
		MDP_XRT_FLD_DIS	1	07-F0 d9
2017/10/11	12:20:20.0	XRT_FLRCTRL_DIS_418_OG [0x1a2]		
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9
2017/10/11	12:22:56.0	XRT_ARS_DIS_445_OG [0x1bd]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2017/10/11	12:22:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 14
2017/10/11	12:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2017/10/11	12:29:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/10/11	12:29:56.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/10/11	12:29:58.0	XRT_FOCUS_POSITION_447_OG [0x1bf]		
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2017/10/11	12:30:00.5	AOCS_ORe-point_Start_9_OG [0x09f]		
		AOCU_NM	5	02-76 00 d1 07 2e f9
2017/10/11	12:30:18.0	XRT_FLD_DIS_409_OG [0x199]		
		MDP_XRT_FLD_DIS	1	07-F0 d9
2017/10/11	12:30:20.0	XRT_FLRCTRL_DIS_418_OG [0x1a2]		
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9
2017/10/11	12:32:56.0	XRT_ARS_DIS_445_OG [0x1bd]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2017/10/11	12:32:58.0	XRT_QT_PROG_SET_444_OG [0x1bc]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 13
2017/10/11	12:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2017/10/11	12:39:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/10/11	12:39:56.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2017/10/11	12:39:58.0	XRT_FOCUS_RECALIBRATE_416_OG [0x1a0]		
		XRT_FOCUS_RECAL	2	07-F8 78 00
2017/10/11	12:40:00.0	AOCS_ORe-point_Start_5_OG [0x09b]		

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2017/10/11	12:43:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	5	02-76	02	03	33	01	db
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2017/10/11	12:44:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2017/10/11	12:44:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2017/10/11	12:44:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2017/10/11	12:44:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2017/10/11	12:44:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/10/11	12:46:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	08			
2017/10/11	12:46:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2017/10/11	12:47:00.0	XRT_ROI_A_406_OG [0x196]							
		MDP_XRT_ROI_SET	6	07-F0	cd	05	85	83	06 06
		MDP_XRT_ROI_SET	6	07-F0	cd	06	85	83	06 06
		MDP_XRT_ROI_SET	6	07-F0	cd	07	85	83	08 08
		MDP_XRT_ROI_SET	6	07-F0	cd	08	80	80	08 20
		MDP_XRT_ROI_SET	6	07-F0	cd	09	80	80	20 20
		MDP_XRT_ROI_SET	6	07-F0	cd	0d	80	80	08 08
		MDP_XRT_ROI_SET	6	07-F0	cd	0e	80	80	20 08
		MDP_XRT_ROI_SET	6	07-F0	cd	0f	80	80	06 06
2017/10/11	12:47:00.5	XRT_ROI_B_414_OG [0x19e]							
		MDP_XRT_ROI_SET	6	07-F0	cd	0f	80	80	06 06
		MDP_XRT_ROI_SET	6	07-F0	cd	10	80	80	08 08
2017/10/11	12:47:05.5	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/10/11	15:02:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/10/11	15:02:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/10/11	15:02:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/10/11	15:02:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/10/11	15:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/10/11	15:14:00.0	XRT_Custom_430_OG [0x1ae]							
2017/10/11	15:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/10/11	16:37:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/10/11	16:37:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/10/11	16:37:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/10/11	16:37:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/10/11	16:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/10/11	17:00:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/10/11	17:00:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/10/11	17:00:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2017/10/11	17:01:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2017/10/11	17:01:18.0	XRT_FLD_DIS_425_OG [0x1a9]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2017/10/11	17:03:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2017/10/11	17:03:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2017/10/11	17:03:58.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			
2017/10/11	17:04:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/10/11	17:10:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/10/11	17:10:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/10/11	17:10:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2017/10/11	17:11:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	01	03	33	01	db
2017/10/11	17:11:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2017/10/11	17:11:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2017/10/11	17:11:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2017/10/11	17:11:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2017/10/11	17:11:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/10/11	17:13:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2017/10/11	17:13:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							

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2017/10/11	17:14:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2017/10/11	18:14:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/10/11	18:14:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/10/11	18:14:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/10/11	18:14:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/10/11	18:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/10/11	18:38:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/10/11	18:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2017/10/11	19:51:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/10/11	19:51:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/10/11	19:51:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/10/11	19:51:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/10/11	19:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/10/11	20:14:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/10/11	20:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2017/10/11	21:28:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/10/11	21:28:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/10/11	21:28:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/10/11	21:28:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/10/11	21:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/10/11	21:40:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/10/11	21:40:56.0	XRT_CTRL_MANU_402_OG [0x192]					
2017/10/11	21:40:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/10/11	21:41:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2017/10/11	21:41:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	04	03 33 01 db
2017/10/11	21:41:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2017/10/11	21:41:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2017/10/11	21:41:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2017/10/11	21:41:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2017/10/11	21:43:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/10/11	21:43:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	08
2017/10/11	21:51:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2017/10/11	21:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2017/10/11	23:06:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/10/11	23:06:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/10/11	23:06:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/10/11	23:06:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/10/11	23:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/10/11	23:26:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/10/11	23:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2017/10/12	00:40:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/10/12	00:40:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/10/12	00:40:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/10/12	00:41:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2017/10/12	00:41:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03	03 33 01 db
2017/10/12	00:41:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2017/10/12	00:41:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2017/10/12	00:41:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2017/10/12	00:41:24.0		MDP_XRT_ARS_DIS	1	07-F0	d5	

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2017/10/12	00:41:26.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/10/12	00:43:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2017/10/12	00:43:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2017/10/12	00:50:30.0	XRT_Custom_430_OG [0x1ae]			
2017/10/12	00:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/10/12	02:06:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/12	02:06:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/12	02:06:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/10/12	02:06:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/10/12	02:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/10/12	02:25:30.0	XRT_Custom_430_OG [0x1ae]			
2017/10/12	02:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/10/12	03:42:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/12	03:42:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/12	03:42:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/10/12	03:42:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/10/12	03:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/10/12	04:02:30.0	XRT_Custom_430_OG [0x1ae]			
2017/10/12	04:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/10/12	05:12:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/12	05:12:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/12	05:12:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/10/12	05:12:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/10/12	05:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/10/12	05:40:00.0	XRT_Custom_430_OG [0x1ae]			
2017/10/12	05:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/10/12	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/12	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/12	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2017/10/12	06:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2017/10/12	06:00:18.0	XRT_FLD_DIS_425_OG [0x1a9]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2017/10/12	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2017/10/12	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2017/10/12	06:02:58.0	XRT_QT_PROG_SET_413_OG [0x19d]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2017/10/12	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/10/12	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/12	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/12	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2017/10/12	06:10:00.0	AOCS_ORe-point_Start_5_OG [0x09b]			
		AOCU_NM	5	02-76	02 03 33 01 db
2017/10/12	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2017/10/12	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2017/10/12	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2017/10/12	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2017/10/12	06:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/10/12	06:12:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2017/10/12	06:12:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2017/10/12	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/10/12	06:52:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/12	06:52:32.0	XRT_CTRL_MANU_402_OG [0x192]			

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2017/10/12	06:52:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/12	06:52:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/10/12	06:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/10/12	07:17:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/10/12	07:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2017/10/12	08:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/10/12	08:32:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/12	08:32:34.0	XRT_FLD_RESET_415_OG [0x19f]				
2017/10/12	08:32:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/10/12	08:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/10/12	08:54:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/10/12	08:55:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2017/10/12	10:10:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/10/12	10:10:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/12	10:47:00.0	AOCS_ORe-point_Start_2_OG [0x098]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		AOCU_NM		5	02-76	00 00 00 00 00