

XRT Timeline to be uploaded on 2017/10/24

Period: 2017/10/24 11:08:00 - 2017/10/28 10:13:00

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Normal mode

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XOB #1BBA: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
10/24 11:21:00 - 10/24 17:43:30	Track (-625.5, -281.2) ^{Ⓜ 10/24 11:18:00}	# OP start + 10min AR 12685
10/24 18:20:00 - 10/25 05:52:54	Track (-576.9, -284.5) ^{Ⓜ 10/24 18:17:00}	# AR 12865
10/25 06:06:00 - 10/25 17:54:54	Track (-489.3, -289.4) ^{Ⓜ 10/25 06:03:00}	# AR 12865
10/25 18:08:00 - 10/26 05:59:54	Track (-393.1, -293.3) ^{Ⓜ 10/25 18:05:00}	# AR 12685
10/26 06:13:00 - 10/26 10:17:00	Track (-291.1, -296.1) ^{Ⓜ 10/26 06:10:00}	# AR 12685

PROG= 06 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec										
Seqn= 92	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Subr= 2 5-time(s) 2.0sec

Seqn= 75	1-time(s)	2.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Seqn= 96 4-time(s) 120.0sec

Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1BB4: Synoptic Q95 2x2 - Al/mesh(24/256/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(45/512/4096) + T

Term	Pointing (x, y)	Comment
10/24 18:10:00 - 10/24 18:16:54	Fixed (0.0, 0.0)	synoptic, shifted 7.0 min
10/25 05:56:00 - 10/25 06:02:54	Fixed (0.0, 0.0)	synoptic, shifted -7.0 min
10/25 17:58:00 - 10/25 18:04:54	Fixed (0.0, 0.0)	synoptic, shifted -5.0 min
10/26 06:03:00 - 10/26 06:09:54	Fixed (0.0, 0.0)	synoptic

PROG= 10 1-time(s)

Subr= 1	1-time(s)	2.0sec										
Seqn= 5	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 1 1-time(s) 2.0sec

Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 99 1-time(s) 2.0sec

Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 33 1-time(s) 2.0sec

thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 23 1-time(s) 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Flare mode

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XOB #1B8E: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
10/24 11:21:00 - 10/24 17:43:30	Track (-625.5, -281.2) ^{Ⓜ 10/24 11:18:00}	# OP start + 10min AR 12685
10/24 18:20:00 - 10/25 05:52:54	Track (-576.9, -284.5) ^{Ⓜ 10/24 18:17:00}	# AR 12865
10/25 06:06:00 - 10/25 17:54:54	Track (-489.3, -289.4) ^{Ⓜ 10/25 06:03:00}	# AR 12865
10/25 18:08:00 - 10/26 05:59:54	Track (-393.1, -293.3) ^{Ⓜ 10/25 18:05:00}	# AR 12685
10/26 06:13:00 - 10/26 10:17:00	Track (-291.1, -296.1) ^{Ⓜ 10/26 06:10:00}	# AR 12685

PROG= 13 30-time(s)

Subr= 1	20-time(s)	2.0sec										
Seqn= 11	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec

Seqn=100		1-time(s)		10.0sec											
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec			
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
Subr= 2		1-time(s)		2.0sec											
Seqn= 10		1-time(s)		2.0sec											
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
Seqn= 11		1-time(s)		2.0sec											
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec			
Seqn= 87		1-time(s)		2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec			
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec			
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec			
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval				

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Active Region Search

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NOT USED

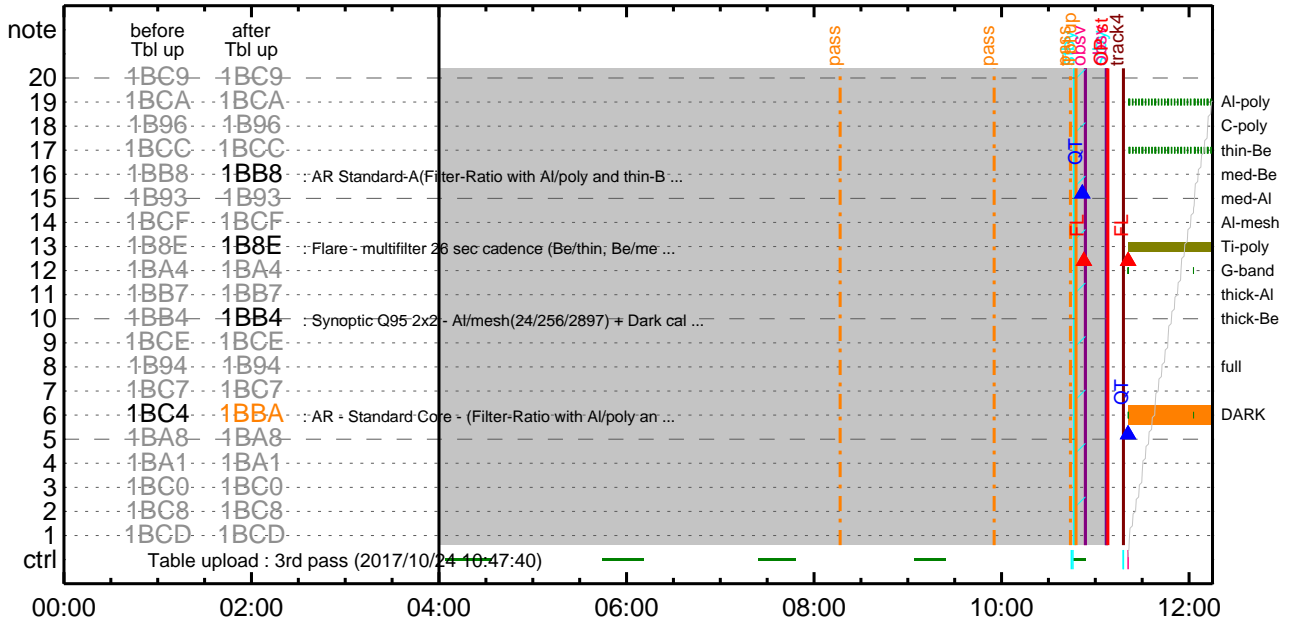
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Flare Detection

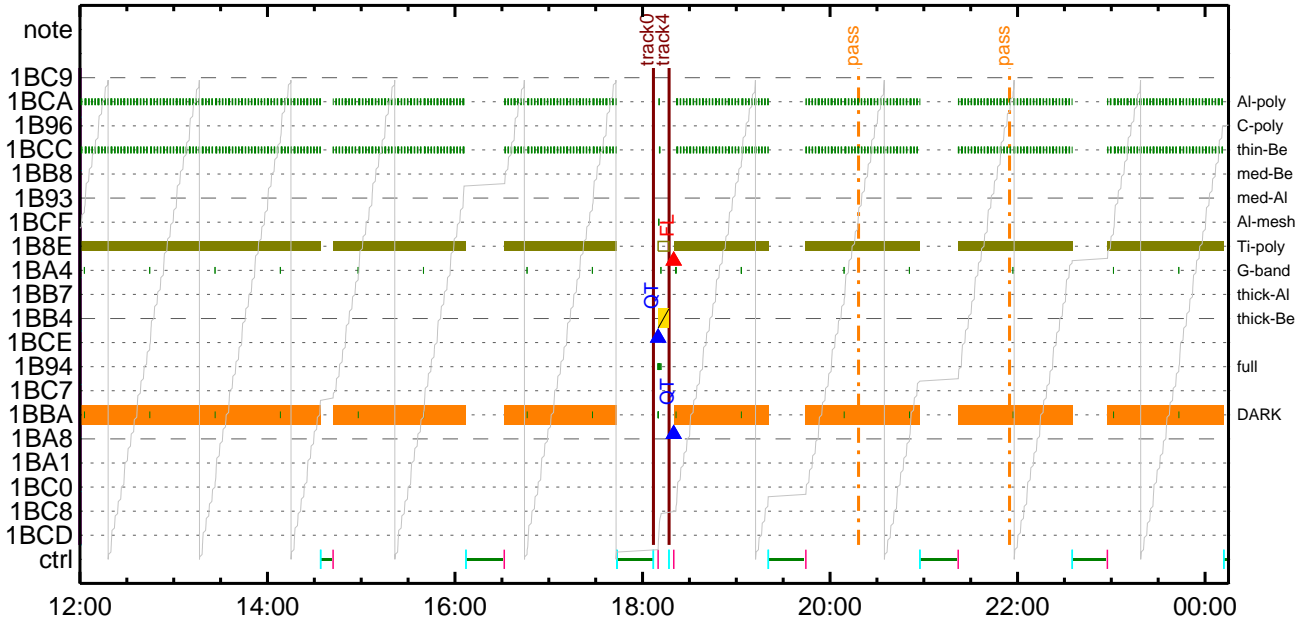
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FLD Patrol												
Term	Pointing (x, y)				Comment							
10/24 18:17:18 - 10/25 05:53:18	Track (-576.9, -284.5)	[Ⓜ] 10/24 18:17:00	# AR 12865									
10/25 06:03:18 - 10/25 17:55:18	Track (-489.3, -289.4)	[Ⓜ] 10/25 06:03:00	# AR 12865									
10/25 18:05:18 - 10/26 06:00:18	Track (-393.1, -293.3)	[Ⓜ] 10/25 18:05:00	# AR 12685									
10/26 06:10:18 - 10/28 10:13:00	Track (-291.1, -296.1)	[Ⓜ] 10/26 06:10:00	# AR 12685									
Al-poly/Open	Al-poly/Open	close	Safe	Norm	8ms	Obs	8x8	Q=50	30sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

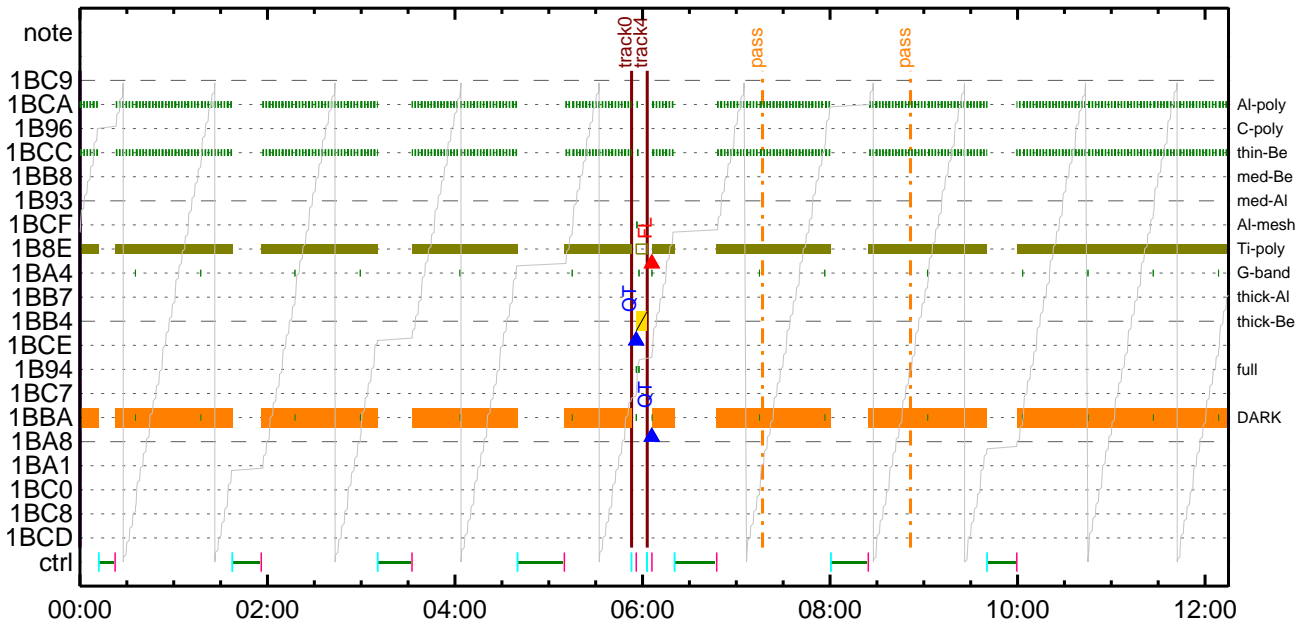
CMDI #0083 2017/10/24



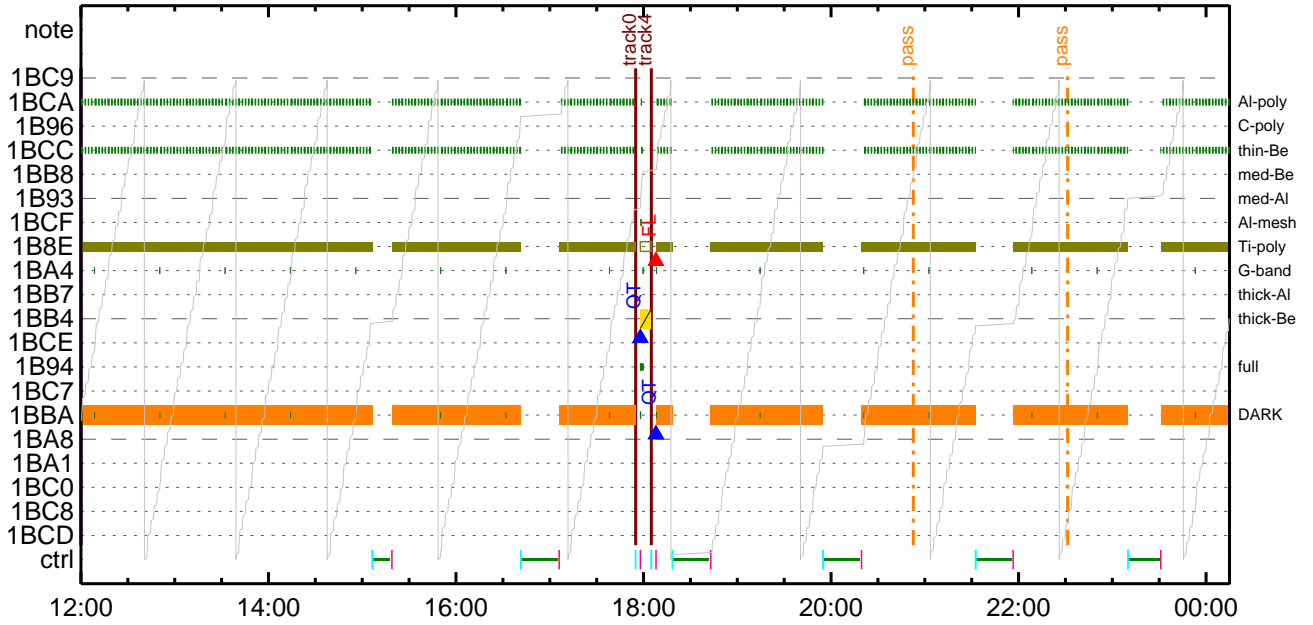
CMDI #0083 2017/10/24



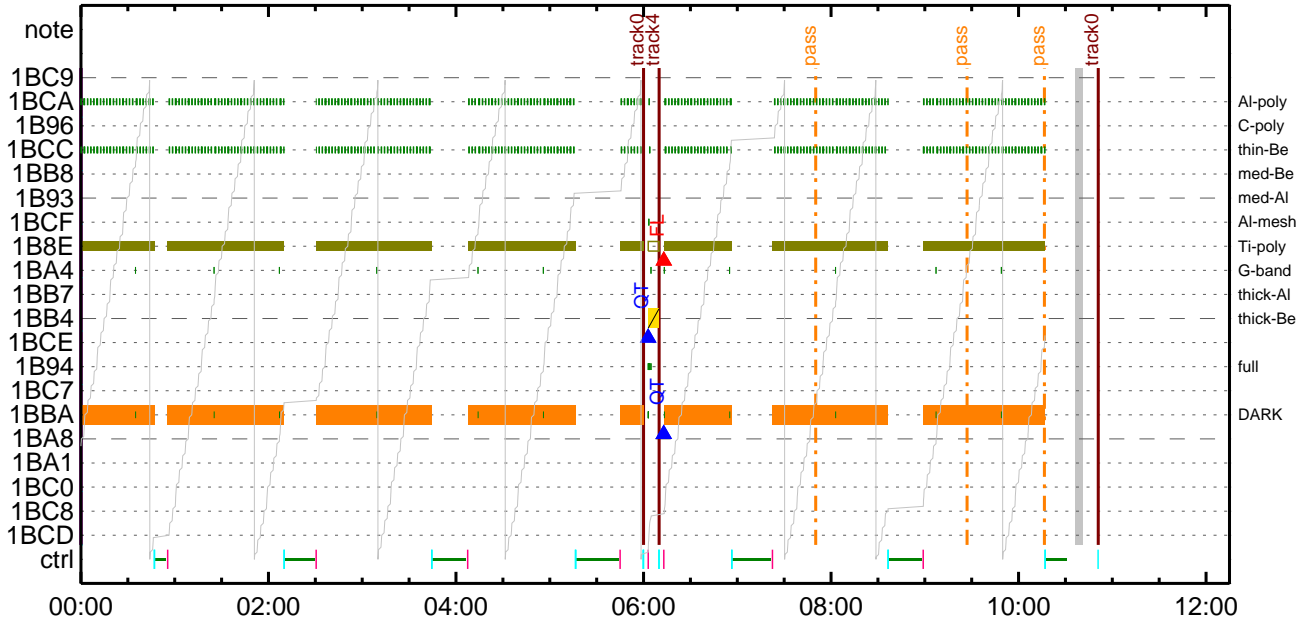
CMDI #0083 2017/10/25



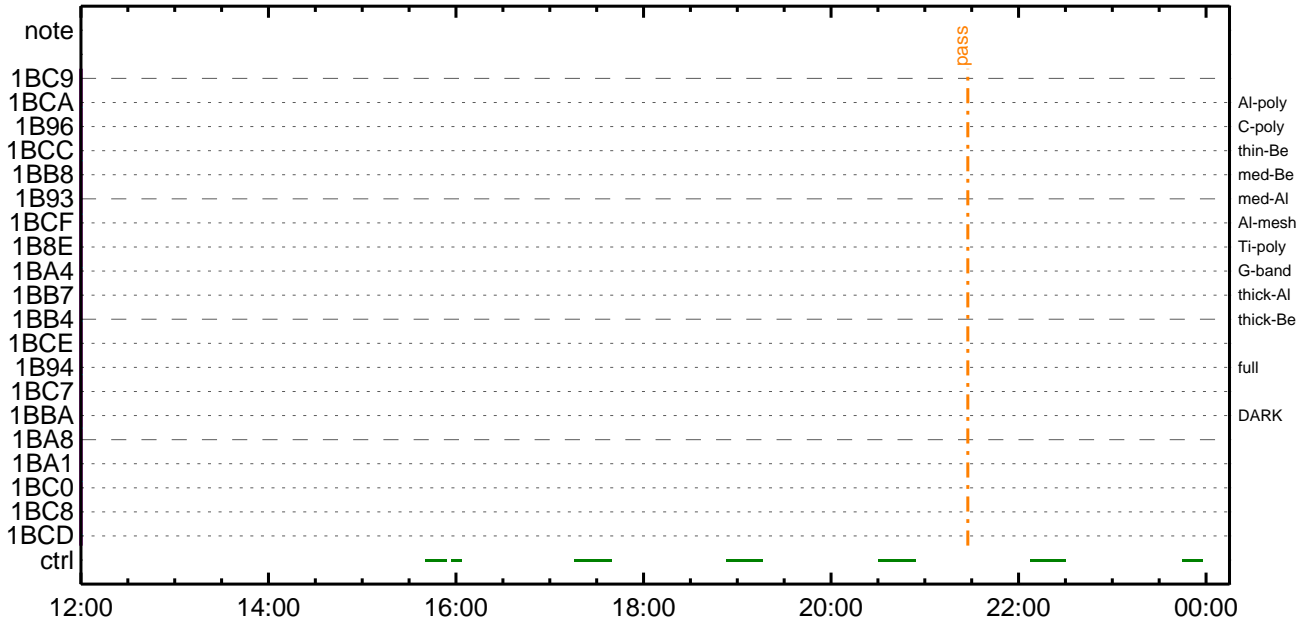
CMDI #0083 2017/10/25



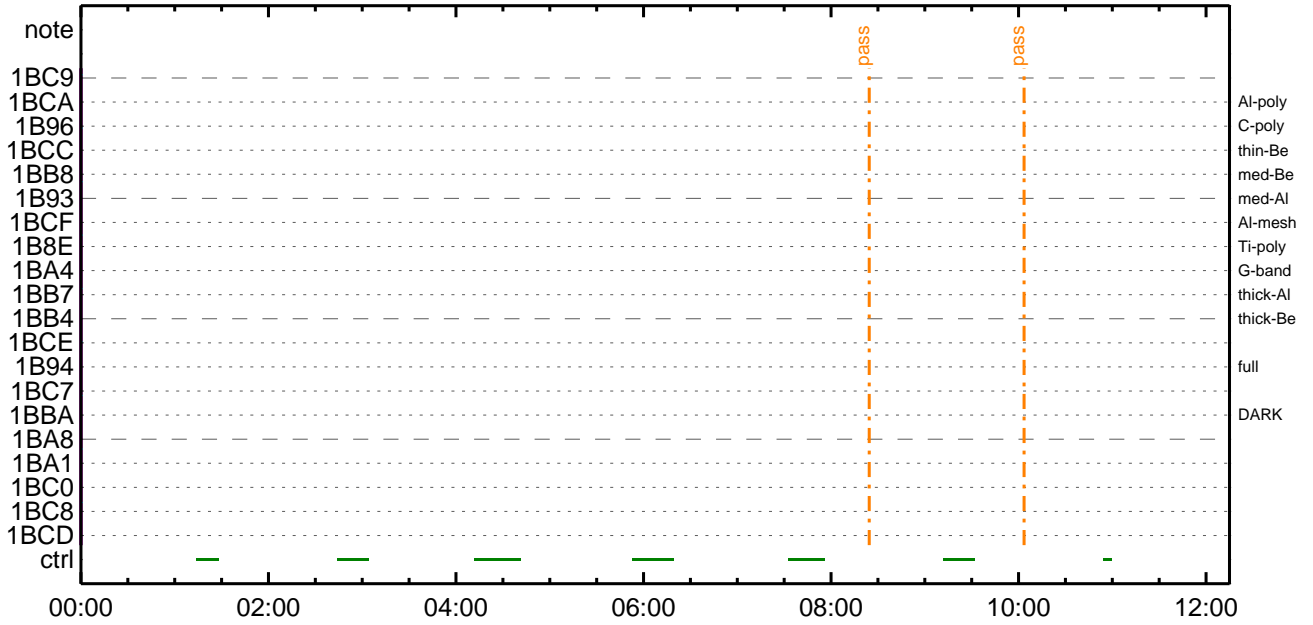
CMDI #0083 2017/10/26



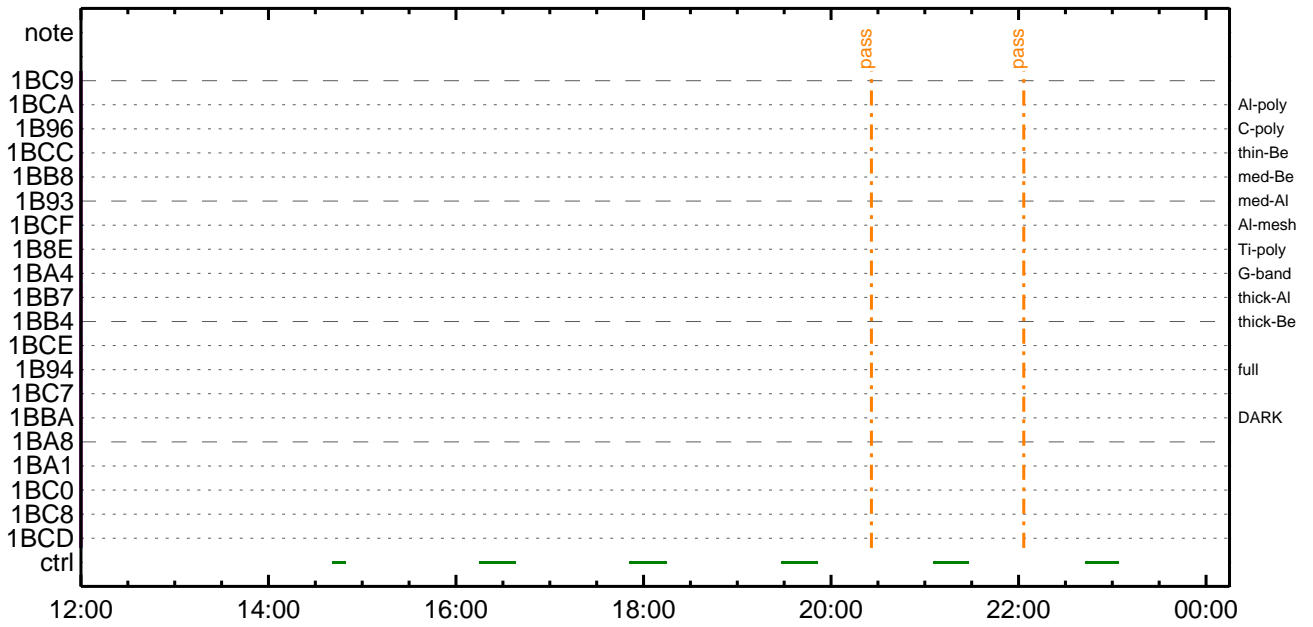
CMDI #0083 2017/10/26



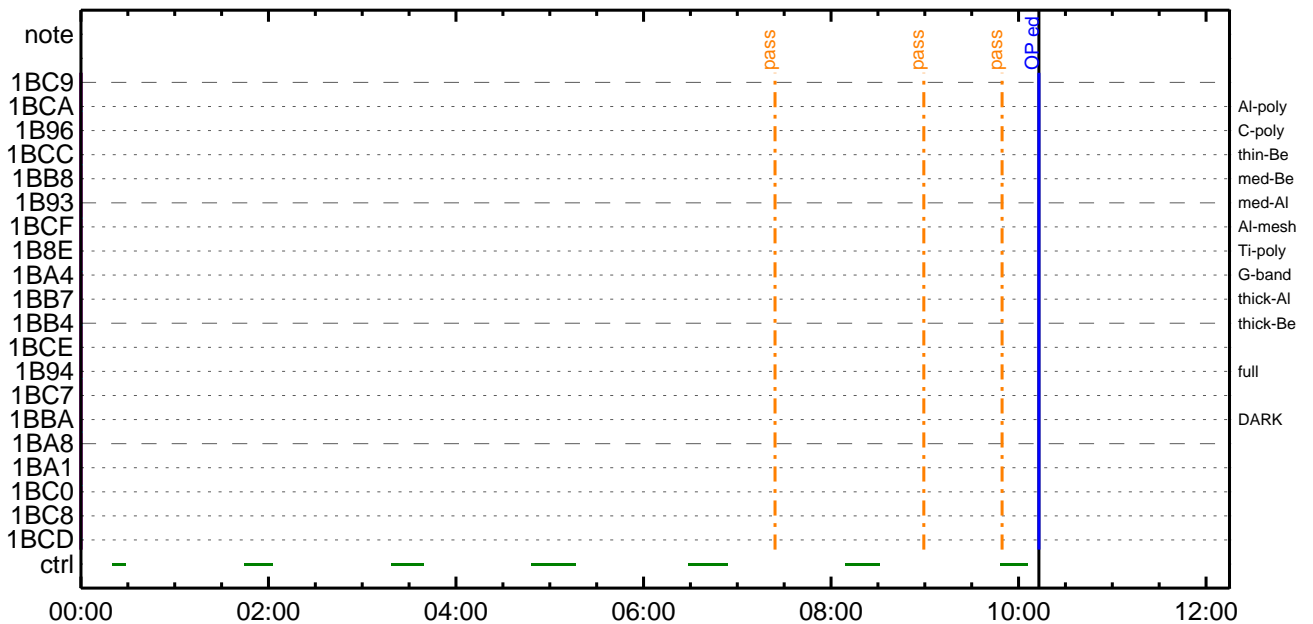
CMDI #0083 2017/10/27

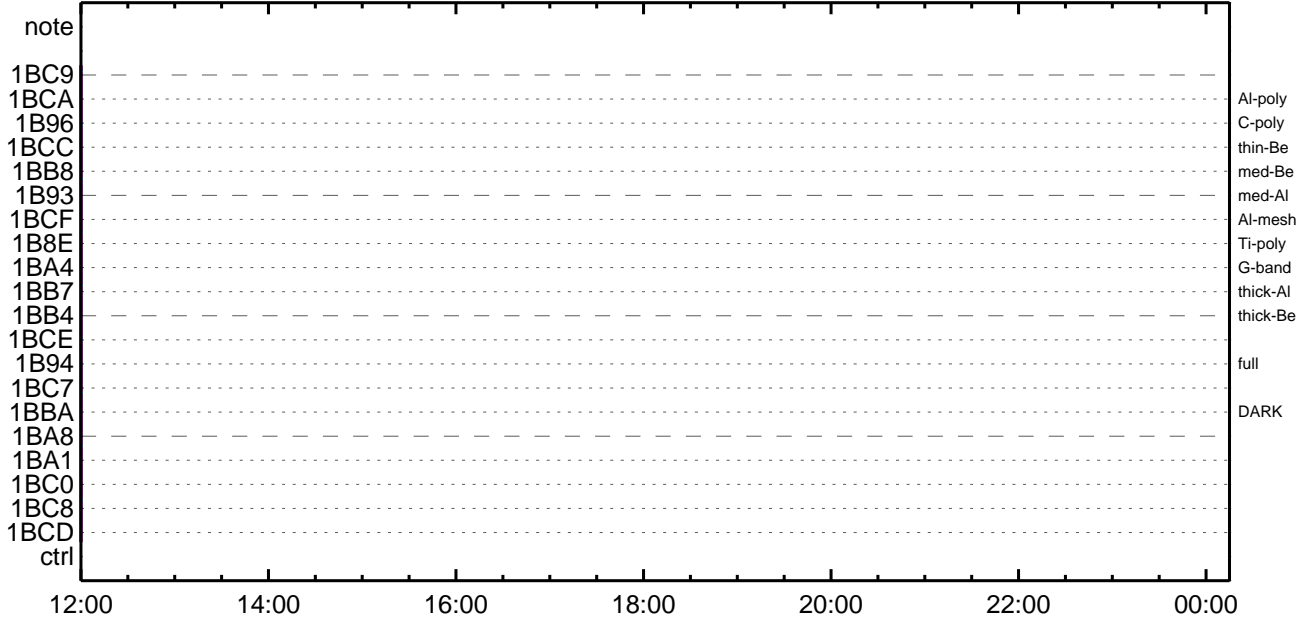


CMDI #0083 2017/10/27



CMDI #0083 2017/10/28






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0096 C.
0097 C.
0098 C.
0099 . C. *****
0099 C. OP/OGY1;4YE;|YA6Yx
0100 C. *****
0101 C.
0102 . C. ;ãOP/OGY1;4YE;ã
0103 . S. OP op-938:OP
0104 ( )
0105 . S. OG og-938:OG
0106 ( )
0107 C.
0108 . C. ;ãNMOG&OPíî°èYAY6Yx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 . C. YA6Yx½ª î»ò³ îçš
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 . C. RAM ID=NMOGñî¼E¹ç•è² îOKò³ îçš
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 . C. YA6Yx½ª î»ò³ îçš
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 . C. RAM ID=NMOGñî¼E¹ç•è² îOKò³ îçš
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 . C. YA6Yx½ª î»ò³ îçš
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 . C. RAM ID=NMOG, RAM ID=OPñî¼E¹ç•è² îOKò³ îçš
0165 C.
0166 . C. ***** òE²¼ò î¼Ã´ ¶Á°òEÈ-ò°Á÷¿@ (¼âµ-YA6Yx½ª ê¼çòðÁÓÀæç¼ª°"òE¼i¹çççà) *****
0167 C. DHUYâ;4YE;È¼Y½;Yi;4YE;Èòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 . C. NOTICE ;§ OPOG UPLOADñ-Á÷¿@NGñî¼i¹ç;ç°E²¼ò îTI-CMDÁ÷¿@ñî¼Á¹Ôñ•ñEñññ³ ñE;f
0180 C. ñEñ¿;çSETòEDUMPAîE±°iYñY¹ ñç¹Ôñ|ñ³ ñE;f
0181 C.
0182 . C. TIY³YpY6YÈòðÁDî¿ (UT)
0183 +. TI 2017-10-24 11:03:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2017-10-24 11:03:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2017-10-24 11:03:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
```



```
0194 C.
0195 +. TI 2017-10-24 11:07:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.      ÷÷[HK1_TI_CMD_NUM]      EQ      1COUNTUP
0198 C.
0199 C.  °Ê²¼ºíÄè%ííñºîŷÄŷ§ŷÄŷ-¹àîŮ
0200 C.      ÷÷[HK1_TI_CMD_ENA/DIS]    EQ      ENA
0201 C.      ÷÷[HK1_TI_CMD_NUM]      EQ      4
0202 C.      ÷÷[HK1_NEXT_EXEC_PIM]    EQ      DHU
0203 C.      ÷÷[HK1_NEXT_EXEC_DC]    EQ      0xB3
0204 C.
0205 . C.  *****
0206 C.  TIîî°èŷÄŷÖŷ×
0207 C.  *****
0208 C.
0209 C.  TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.      ÷÷[HK1_DMP_TOP_ADRS_1]    EQ      07
0213 C.      ÷÷[HK1_DMP_TOP_ADRS_0]    EQ      2B
0214 C.      ÷÷[HK1_DMP_BLOCK_NUM]     EQ      3
0215 C.      ÷÷[HK1_DMP_REPEAT_NUM]   EQ      0
0216 C.      ÷÷[HK1_DMA_DMP_PIM]      EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.      ÷÷[HK1_PKT_FORM_NO]      EQ      7
0220 C.      ÷÷[HK1_PKT_GEN_TIME]      EQ      0.25 s
0221 C.      ÷÷[HK1_S_TLM_BIT_RATE]   EQ      32k
0222 C.      ÷÷[HK1_X_TLM_BIT_RATE]   EQ      4M
0223 C.      ÷÷[HK1_DMP_CHK_FLG]      EQ      EXEC
0224 C.
0225 . C.  ŷÄŷÖŷ×½ªî»º³îÇ§
0226 C.      ÷÷[HK1_DMP_CHK_FLG]      EQ      NON
0227 C.
0228 . C.  RAM ID=TI_TBLºîŷÈ¹ç•è²îOKº³îÇ§
0229 C.
0230 . C.  DHUŷâ;¼ŷÈ;È¼ŷ¼.ŷî;¼ŷÈ;Èº³îº¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.      ÷÷[HK1_PKT_FORM_NO]      EQ      2
0234 C.      ÷÷[HK1_PKT_GEN_TIME]      EQ      0.5S
0235 C.      ÷÷[HK1_S_TLM_BIT_RATE]   EQ      32K
0236 C.      ÷÷[HK1_X_TLM_BIT_RATE]   EQ      4M
0237 C.
0238 . C.  Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C.  ***** Start EIS operation (TI set) *****
0242 C.  Execute, after the success of OP upload.
0243 C.  Set EIS TI-commands
0244 +. TI 2017-10-24 11:07:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2017-10-24 11:07:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 . C.      [ ] [HK1_TI_CMD_NUM]    EQ      2 COUNTUP
0251 C.  ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.
0255 C.  ***** XRT START *****
0256 C.  Execute, after the success of OP upload.
0257 +. TI 2017-10-24 11:07:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC      (c3)
0260 . C.      [ ] [HK1_TI_CMD_NUM]    EQ      1COUNTUP
0261 C.
0262 C.  ***** XRT END *****
0263 C.
0264 . C.  ***** MDP `ûÄîºî»º¼ŷºèÄº¹ºèDCBC•×²è *****
0265 C.  (¼ªºîŷÖŷÄŷÈŷŷŷÈŷâŷçŷèºè¼º¼ª»Ůº¹ºè)
0266 . S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 . C.  ***** ŷDŷ¹.İ Daily±;îñºè´º¹ºèDCBC•×²è *****
0271 . S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 . C.  ÿäLOSŷÄŷŷŷÄŷ-¼ª»Ů;ä
0276 C.
0277 . C.  ***** LOS *****
0278 C.
```


(a) Spacecraft Operation Procedure (real-commands)

```
main-940 2017-10-24 13:07:54 100 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY~¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;È¿ðÁð•µ°È»Í×ÁÇóÍYÇYÁY×Yí;¼YÉ;ÈÈèµ•íÉ;ÈèÈ¼°ÇÒá•¿¼l¹çáÍ;çÁ®, ùá¹áèáðáçÁ+¿®á•ðÈááá³áÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 +. DC 07-F0 MDP_XRT_CTRL_MANU
0020 BC (c1)
0021 + DC 07-F0 MDP_XRT_MODE_STBY
0022 BC (c3)
0023 . C. ----- Success Verify ? OK / NG____
0024 C.
0025 C. XRT Obs. Table Upload
0026 . S. RAM ram-291:MDP_OBS_X
0027 ( )
0028 C.
0029 +. DC 07-F0 MDP_DUMP_XRTTBL
0030 BC (84 07 00 00 00 3a d4)
0031 . C. ----- Comparison Check ? OK / ERR ____
0032 C.
0033 C.
0034 +. DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 01 b1 b1 04 04)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 02 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 03 b1 b1 08 08)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 04 b1 b1 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 05 85 83 06 06)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 06 85 83 06 06)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 07 85 83 08 08)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 08 80 80 20 20)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 09 80 80 20 08)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0a 80 80 08 20)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0f 80 80 06 06)
0056 + DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 10 80 80 08 08)
0058 + DC 07-F0 MDP_XRT_FLD_ENA
0059 BC (d8)
0060 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0061 BC (c8)
0062 + DC 07-F0 MDP_XRT_ARS_DIS
0063 BC (d5)
0064 +. DC 07-F0 MDP_XRT_AEC_RESET
0065 BC (d0)
0066 +. DC 07-F0 MDP_XRT_FLD_RESET
0067 BC (da)
0068 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0069 BC (c4 10)
0070 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0071 BC (c5 0d)
0072 . C. ----- Success Verify ? OK / NG ____
0073 C.
0074 C.
0075 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0076 C.
0077 +. DC 07-F0 MDP_XRT_MODE_OBSV
0078 BC (c2)
0079 +. TI 2017-10-24 11:07:02.0
0080 DC 07-F0 MDP_XRT_MODE_OBSV
0081 BC (c2)
0082 . C. ----- Success Verify ? OK / NG ____
0083 C.
0084 C. ***** XRT END *****
0085 C.
0086 . C. ***** MDP `ûÃíðí»ò¼YðÈÁðá¹áèDCBC•×²è *****
0087 C. (¼á°íYÓYÁYÈY¥YÉYáYçYèáE¼¼á¼Á»Ûá¹áè)
0088 . S. DC-BC dcbc-402:DCBC
0089 (MDP_known_event)
0090 C.
0091 C.
0092 . C. ***** YÐY¹•İ Daily±¿İÑèÈ´Øá¹áèDCBC•×²è *****
0093 . S. DC-BC dcbc-153:DCBC
0094 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0095 C.
```

0096 C.
0097 . C. ;ãLOS¥Á¥§¥Ã¥-¼Á»Û;ã
0098 C.
0099 . C. ***** LOS *****
0100 C.

Oct 24, 17 13:07

XRT_OGLIST_0083.chk

Page 1/6

*** OP Sequence for XRT ***

2017/10/24	11:17:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/10/24	11:17:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/10/24	11:17:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2017/10/24	11:18:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	04 00 00 00 00		
2017/10/24	11:18:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2017/10/24	11:18:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2017/10/24	11:18:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2017/10/24	11:18:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2017/10/24	11:18:26.0	XRT_FLD_RESET_433_OG [0x1b1]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2017/10/24	11:20:56.0	XRT_QT_PROG_SET_404_OG [0x194]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06		
2017/10/24	11:20:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2017/10/24	11:21:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/10/24	14:34:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/10/24	14:34:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/10/24	14:34:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2017/10/24	14:34:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2017/10/24	14:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2017/10/24	14:41:00.0	XRT_Custom_430_OG [0x1ae]					
2017/10/24	14:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/10/24	16:07:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/10/24	16:07:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/10/24	16:07:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2017/10/24	16:07:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2017/10/24	16:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2017/10/24	16:30:30.0	XRT_Custom_430_OG [0x1ae]					
2017/10/24	16:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/10/24	17:43:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/10/24	17:43:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/10/24	17:43:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2017/10/24	17:43:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2017/10/24	17:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2017/10/24	18:06:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/10/24	18:06:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/10/24	18:06:58.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2017/10/24	18:07:00.0	AOCS_Ore-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2017/10/24	18:07:18.0	XRT_FLD_DIS_425_OG [0x1a9]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2017/10/24	18:09:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2017/10/24	18:09:56.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2017/10/24	18:09:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a		
2017/10/24	18:10:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/10/24	18:16:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/10/24	18:16:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/10/24	18:16:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2017/10/24	18:17:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	04 00 00 00 00		
2017/10/24	18:17:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2017/10/24	18:17:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2017/10/24	18:17:22.0	XRT_AEC_RESET_448_OG [0x1c0]					

Oct 24, 17 13:07

XRT_OGLIST_0083.chk

Page 2/6

2017/10/24	18:17:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
			MDP_XRT_ARS_DIS	1	07-F0	d5
2017/10/24	18:17:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/10/24	18:19:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2017/10/24	18:19:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2017/10/24	18:20:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/10/24	19:20:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/24	19:20:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/24	19:20:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/10/24	19:20:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/10/24	19:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/10/24	19:43:30.5	XRT_Custom_430_OG [0x1ae]				
2017/10/24	19:44:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/10/24	20:57:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/24	20:57:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/24	20:57:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/10/24	20:57:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/10/24	21:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/10/24	21:21:00.0	XRT_Custom_430_OG [0x1ae]				
2017/10/24	21:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/10/24	22:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/24	22:35:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/24	22:35:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/10/24	22:35:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/10/24	22:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/10/24	22:56:30.0	XRT_Custom_430_OG [0x1ae]				
2017/10/24	22:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/10/25	00:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/25	00:12:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/25	00:12:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/10/25	00:12:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/10/25	00:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/10/25	00:21:30.0	XRT_Custom_430_OG [0x1ae]				
2017/10/25	00:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/10/25	01:37:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/25	01:37:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/25	01:37:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/10/25	01:37:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/10/25	01:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/10/25	01:55:00.0	XRT_Custom_430_OG [0x1ae]				
2017/10/25	01:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/10/25	03:10:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/25	03:10:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/25	03:10:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/10/25	03:10:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/10/25	03:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/10/25	03:31:30.0	XRT_Custom_430_OG [0x1ae]				
2017/10/25	03:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/10/25	04:40:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/25	04:40:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1

Oct 24, 17 13:07

XRT_OGLIST_0083.chk

Page 3/6

2017/10/25	04:40:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/10/25	04:40:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/10/25	04:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/10/25	05:09:00.0	XRT_Custom_430_OG [0x1ae]				
2017/10/25	05:10:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/10/25	05:52:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/25	05:52:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/25	05:52:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2017/10/25	05:53:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2017/10/25	05:53:18.0	XRT_FLD_DIS_425_OG [0x1a9]	MDP_XRT_FLD_DIS	1	07-F0	d9
2017/10/25	05:55:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2017/10/25	05:55:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2017/10/25	05:55:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2017/10/25	05:56:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/10/25	06:02:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/25	06:02:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/25	06:02:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2017/10/25	06:03:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04 00 00 00 00
2017/10/25	06:03:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2017/10/25	06:03:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2017/10/25	06:03:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2017/10/25	06:03:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2017/10/25	06:03:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/10/25	06:05:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2017/10/25	06:05:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2017/10/25	06:06:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/10/25	06:20:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/25	06:20:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/25	06:20:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/10/25	06:20:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/10/25	06:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/10/25	06:46:30.0	XRT_Custom_430_OG [0x1ae]				
2017/10/25	06:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/10/25	08:00:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/25	08:00:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/25	08:00:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/10/25	08:00:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/10/25	08:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/10/25	08:23:30.0	XRT_Custom_430_OG [0x1ae]				
2017/10/25	08:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/10/25	09:40:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/25	09:40:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/25	09:40:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/10/25	09:40:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/10/25	09:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/10/25	09:58:30.0	XRT_Custom_430_OG [0x1ae]				
2017/10/25	09:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/10/25	15:06:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/25	15:06:32.0	XRT_CTRL_MANU_402_OG [0x192]				

2017/10/25	15:06:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/10/25	15:06:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2017/10/25	15:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2017/10/25	15:18:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2017/10/25	15:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/10/25	16:41:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/10/25	16:41:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/10/25	16:41:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/10/25	16:41:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2017/10/25	16:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2017/10/25	17:05:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2017/10/25	17:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/10/25	17:54:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/10/25	17:54:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/10/25	17:54:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/10/25	17:55:00.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2017/10/25	17:55:18.0	XRT_FLD_DIS_425_OG [0x1a9]	AOCU_NM	5	02-76	00 00 00 00 00		
2017/10/25	17:57:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2017/10/25	17:57:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2017/10/25	17:57:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2017/10/25	17:58:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a		
2017/10/25	18:04:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/10/25	18:04:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/10/25	18:04:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/10/25	18:05:00.0	AOCS_Ore-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2017/10/25	18:05:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	04 00 00 00 00		
2017/10/25	18:05:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2017/10/25	18:05:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2017/10/25	18:05:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2017/10/25	18:05:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2017/10/25	18:07:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da		
2017/10/25	18:07:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06		
2017/10/25	18:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2017/10/25	18:18:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/10/25	18:18:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/10/25	18:18:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/10/25	18:18:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2017/10/25	18:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2017/10/25	18:42:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2017/10/25	18:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/10/25	19:55:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/10/25	19:55:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/10/25	19:55:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/10/25	19:55:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2017/10/25	19:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2017/10/25	20:18:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2017/10/25	20:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/10/25	21:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		

2017/10/25	21:32:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/10/25	21:32:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/10/25	21:32:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/10/25	21:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/10/25	21:55:30.0	XRT_Custom_430_OG [0x1ae]							
2017/10/25	21:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/10/25	23:10:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/10/25	23:10:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/10/25	23:10:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/10/25	23:10:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/10/25	23:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/10/25	23:30:00.0	XRT_Custom_430_OG [0x1ae]							
2017/10/25	23:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/10/26	00:47:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/10/26	00:47:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/10/26	00:47:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/10/26	00:47:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/10/26	00:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/10/26	00:54:30.0	XRT_Custom_430_OG [0x1ae]							
2017/10/26	00:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/10/26	02:10:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/10/26	02:10:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/10/26	02:10:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/10/26	02:10:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/10/26	02:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/10/26	02:29:30.0	XRT_Custom_430_OG [0x1ae]							
2017/10/26	02:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/10/26	03:44:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/10/26	03:44:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/10/26	03:44:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/10/26	03:44:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/10/26	03:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/10/26	04:06:30.5	XRT_Custom_430_OG [0x1ae]							
2017/10/26	04:07:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/10/26	05:16:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/10/26	05:16:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/10/26	05:16:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/10/26	05:16:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/10/26	05:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/10/26	05:44:00.0	XRT_Custom_430_OG [0x1ae]							
2017/10/26	05:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/10/26	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/10/26	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/10/26	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2017/10/26	06:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2017/10/26	06:00:18.0	XRT_FLD_DIS_425_OG [0x1a9]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2017/10/26	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2017/10/26	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2017/10/26	06:02:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2017/10/26	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							

2017/10/26	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/10/26	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/26	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/26	06:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2017/10/26	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	04 00 00 00 00
2017/10/26	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2017/10/26	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2017/10/26	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2017/10/26	06:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2017/10/26	06:12:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/10/26	06:12:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2017/10/26	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2017/10/26	06:56:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/10/26	06:56:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/26	06:56:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/26	06:56:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/10/26	06:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/10/26	07:21:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/10/26	07:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2017/10/26	08:36:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/10/26	08:36:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/26	08:36:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/26	08:36:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/10/26	08:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/10/26	08:58:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/10/26	08:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2017/10/26	10:17:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/10/26	10:17:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/26	10:17:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/10/26	10:17:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/10/26	10:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/10/26	10:50:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/10/26	10:51:00.0	AOCS_ORe-point_Start_2_OG [0x098]	MDP_XRT_CTRL_MANU	1	07-F0	c1
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