

XRT Timeline to be uploaded on 2017/10/31

Period: 2017/10/31 10:21:00 - 2017/11/04 11:04:00

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Normal mode

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XOB #1B8F: CCD Monitor During Bakeout - G-band 1ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh(512ms), Al/Poly(1443ms) - w leak image-1msCCD												
Term	Pointing (x, y)						Comment					
11/01 12:18:00 - 11/01 12:24:54	Fixed (-528.4, -528.4)						XRT quadrant pointing obs (1/4)					
PROG= 19 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 51 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 55 2-time(s) 2.0sec												
└─ Open/Al-mesh Open/Ti-poly close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Al-poly/Open med-Be/Open close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Subr= 3 2-time(s) 2.0sec												
└─ Seqn= 34 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1B90: CCD Monitor During Bakeout - G-Band 1ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh (512ms), Al/Poly (1443ms) - w leak image-1 ms												
Term	Pointing (x, y)						Comment					
11/01 12:28:00 - 11/01 12:34:54	Fixed (528.4, -528.4)						(2/4)					
PROG= 11 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 38 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 55 2-time(s) 2.0sec												
└─ Open/Al-mesh Open/Ti-poly close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Al-poly/Open med-Be/Open close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Subr= 3 2-time(s) 2.0sec												
└─ Seqn= 34 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1B91: CCD Monitor During Bakeout - G-Band 1ms - 1kx1k - Q90 - 3rd Quadrant - Al/mesh (512ms), Al/Poly (1443ms) - w leak image-1 ms												
Term	Pointing (x, y)						Comment					
11/01 12:38:00 - 11/01 12:44:54	Fixed (528.4, 528.4)						(3/4)					
PROG= 15 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 21 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 55 2-time(s) 2.0sec												
└─ Open/Al-mesh Open/Ti-poly close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Al-poly/Open med-Be/Open close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Subr= 3 2-time(s) 2.0sec												
└─ Seqn= 34 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1B92: CCD Monitor During Bakeout - G-Band 1ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh (512ms), Al/Poly (1443ms) - w leak image-1 ms												
Term	Pointing (x, y)						Comment					
11/01 12:48:00 - 11/01 12:54:54	Fixed (-528.4, 528.4)						(4/4)					
PROG= 03 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 14 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												

XOB #1BA8: Synoptic 7 Filter w/ Al-mesh(24/256/2897), Al-poly(45/512/4096), Thin-Be(181/2048/11571) - Thick-Be(65536), Al-poly+Ti-poly(256/5795), Med-Al

Term	Pointing (x, y)	Comment
11/02 05:44:00 - 11/02 05:50:54	Fixed (0.0, 0.0)	synoptic, shifted -19.0 min
PROG= 05 1-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 5 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
Seqn= 1 1-time(s) 2.0sec		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 99 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/Open close	Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 67 1-time(s) 2.0sec		
thin-Be/Open	thin-Be/Open close	Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 23 1-time(s) 4.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 46 2-time(s) 2.0sec		
Open/thick-Be	Open/thick-Be close	Safe Norm 64.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Seqn= 40 2-time(s) 2.0sec		
Al-poly/Ti-poly	Al-poly/thick-Al close	Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al close	Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Seqn= 65 2-time(s) 2.0sec		
med-Al/Open	med-Al/Open close	Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
med-Al/Open	med-Al/Open close	Safe Norm 32.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Flare mode

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XOB #1B8E: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512)

Term	Pointing (x, y)	Comment
11/01 13:02:00 - 11/01 17:31:30	Track (88.3, -464.7) @ 11/01 12:55:00	EIS QS network and BP obs
11/01 18:10:05 - 11/01 23:59:54	Track (889.0, -211.5) @ 11/01 18:07:00	AR12685 obs
11/02 00:12:30 - 11/02 05:40:54	Fixed (0.0, 880.0)	North pole obs
11/02 05:54:00 - 11/02 09:28:30	Fixed (914.0, -205.0)	AR12685 obs with fixed pointing
PROG= 13 30-time(s)		
Subr= 1 20-time(s) 2.0sec		
Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn=100 1-time(s) 10.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm 125ms Obs 1x1 384x384 (1024, 1024) Q=95 2 0 2.0sec
med-Be/Open	Open/thick-Al close	Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 10 1-time(s) 2.0sec		
med-Al/Open	med-Al/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn= 87 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Active Region Search

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NOT USED

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Flare Detection

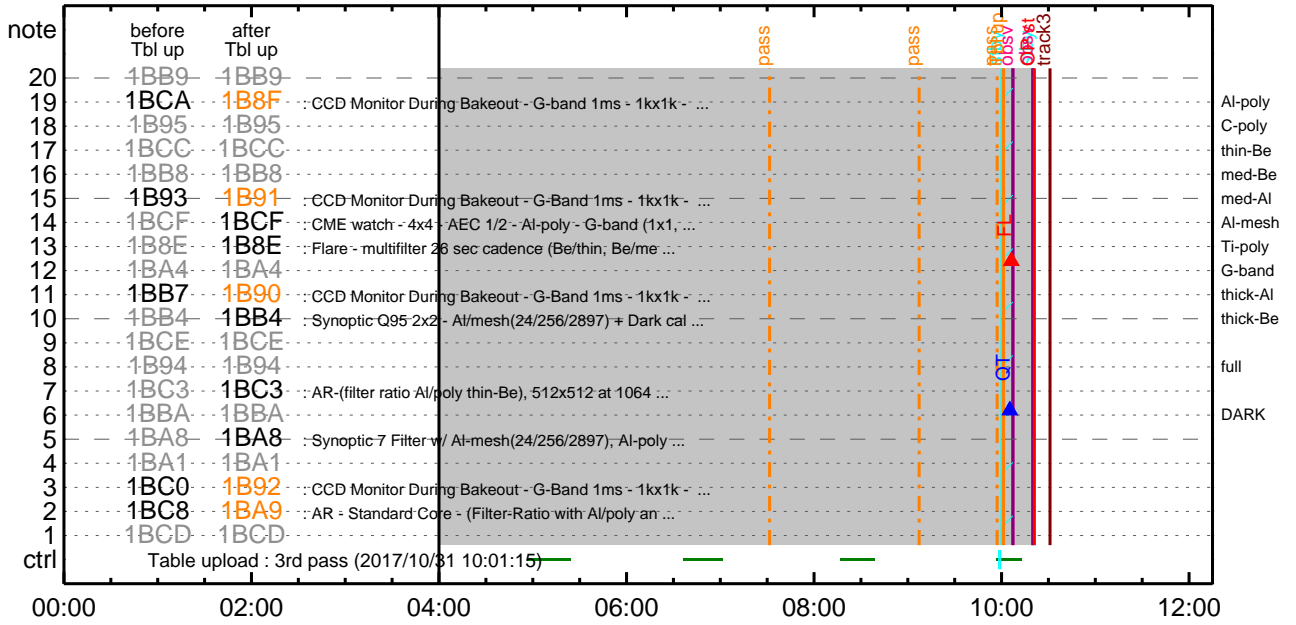
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FLD Patrol

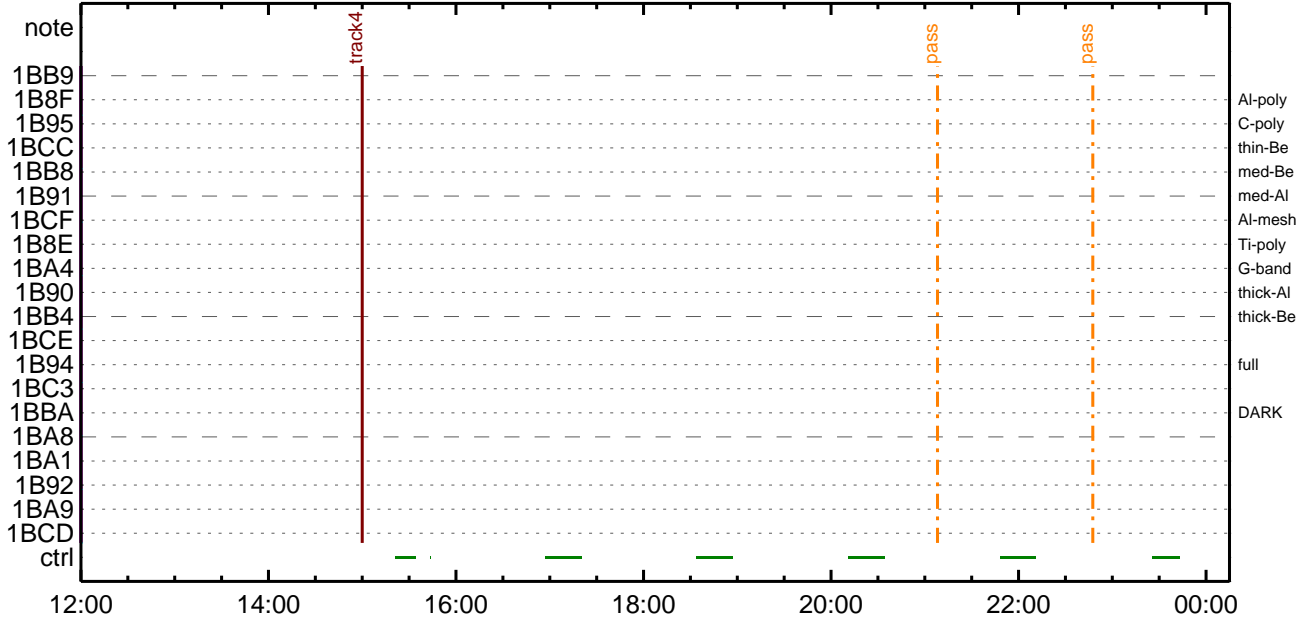
Term	Pointing (x, y)	Comment
11/01 12:59:18 - 11/01 17:57:18	Track (88.3, -464.7) @ 11/01 12:55:00	EIS QS network and BP obs
11/01 18:07:23 - 11/02 05:41:18	Track (889.0, -211.5) @ 11/01 18:07:00	AR12685 obs
11/02 05:51:18 - 11/04 11:04:00	Fixed (914.0, -205.0)	AR12685 obs with fixed pointing

Al-poly/Open	Al-poly/Open	close	Safe	Norm	8ms	Obs	8x8		Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

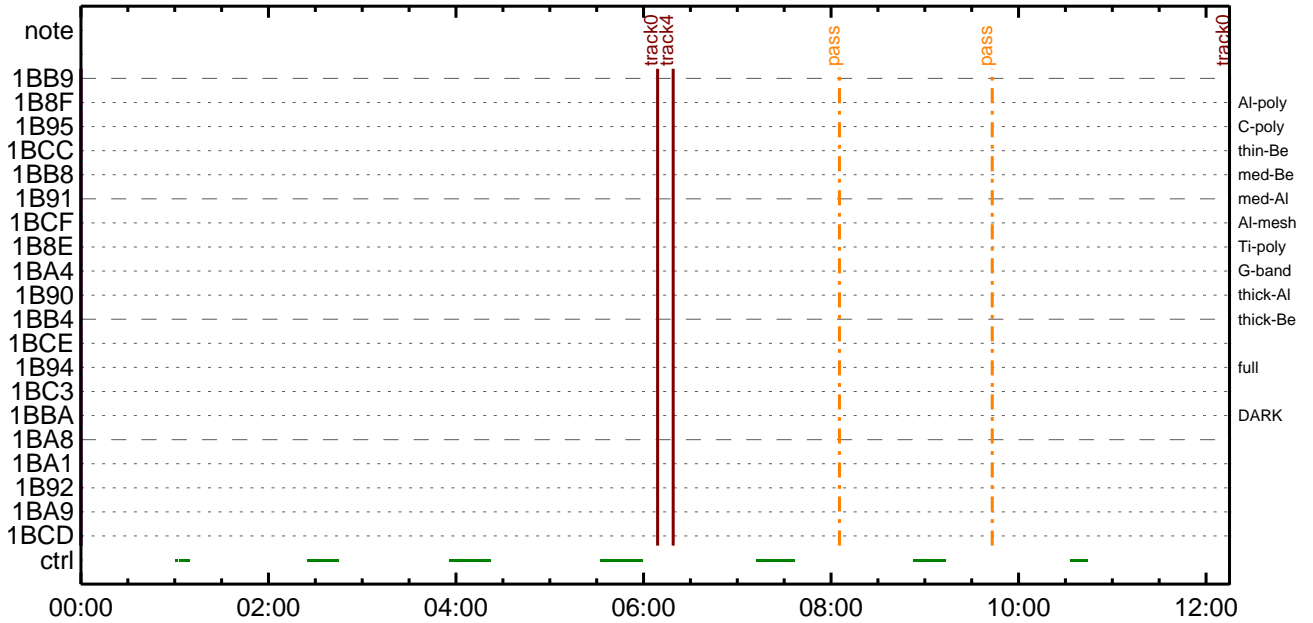
CMDI #0100 2017/10/31



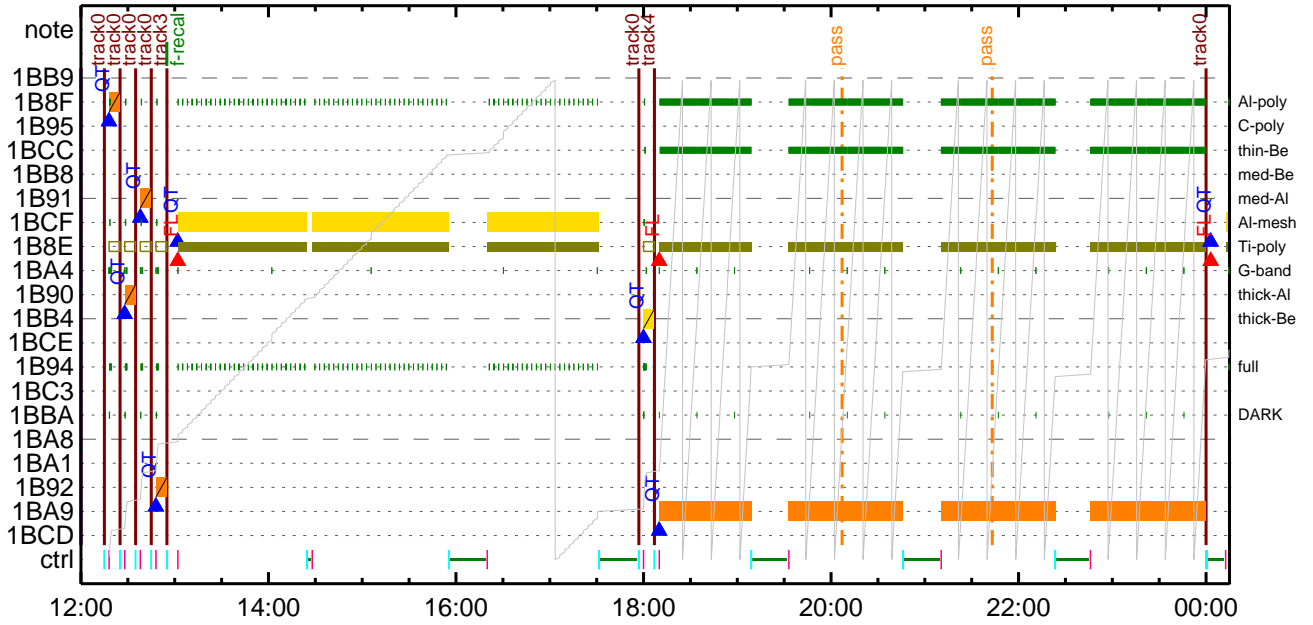
CMDI #0100 2017/10/31



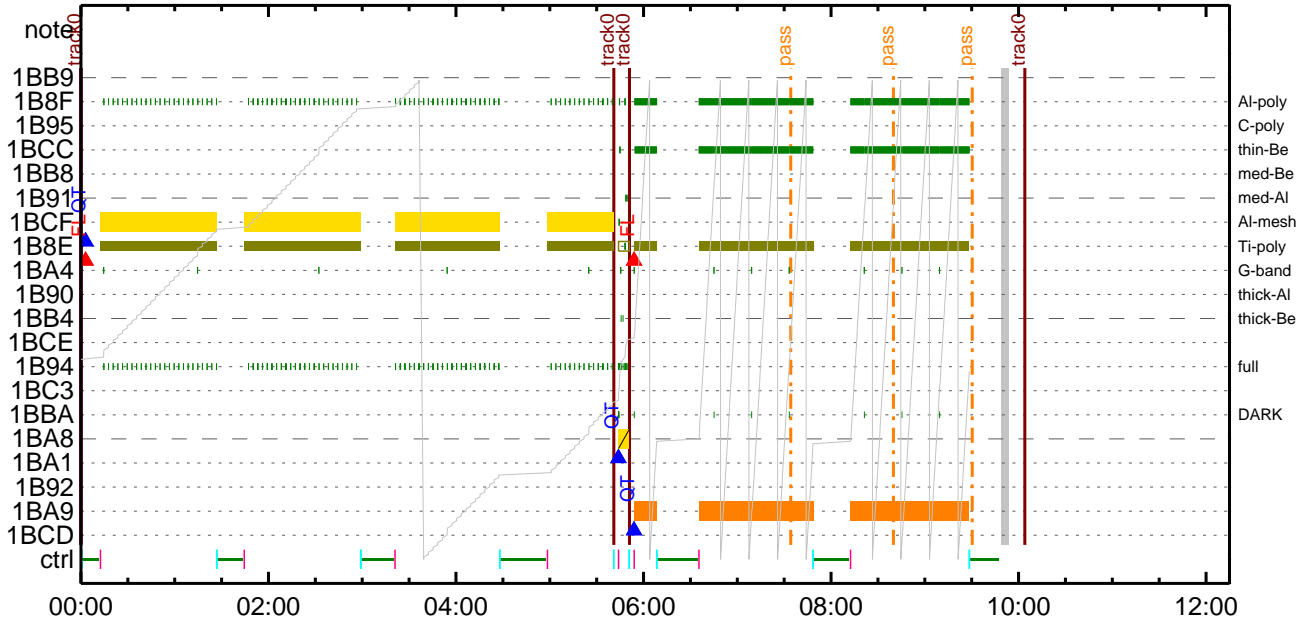
CMDI #0100 2017/11/01



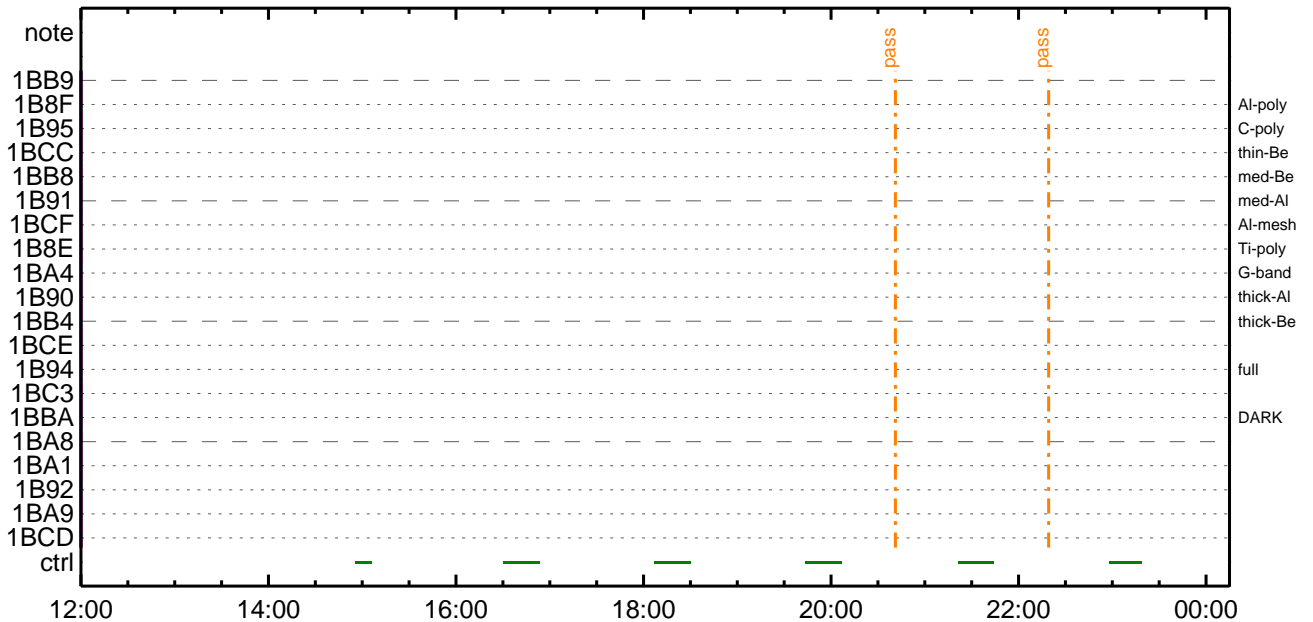
CMDI #0100 2017/11/01



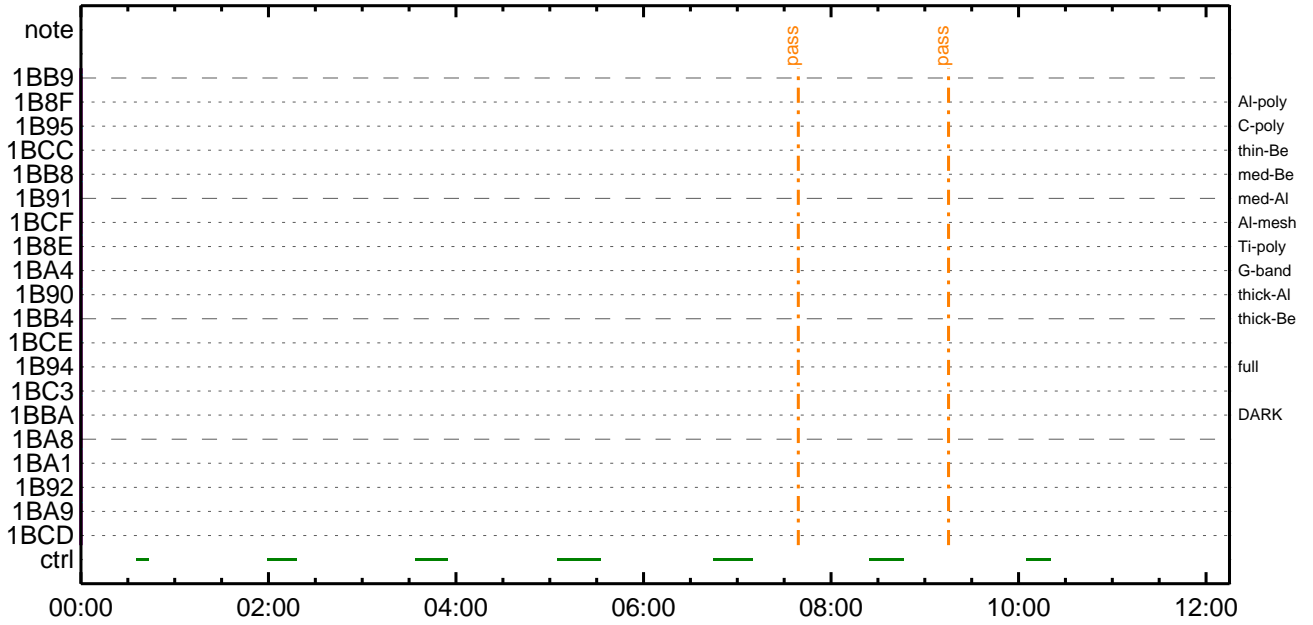
CMDI #0100 2017/11/02



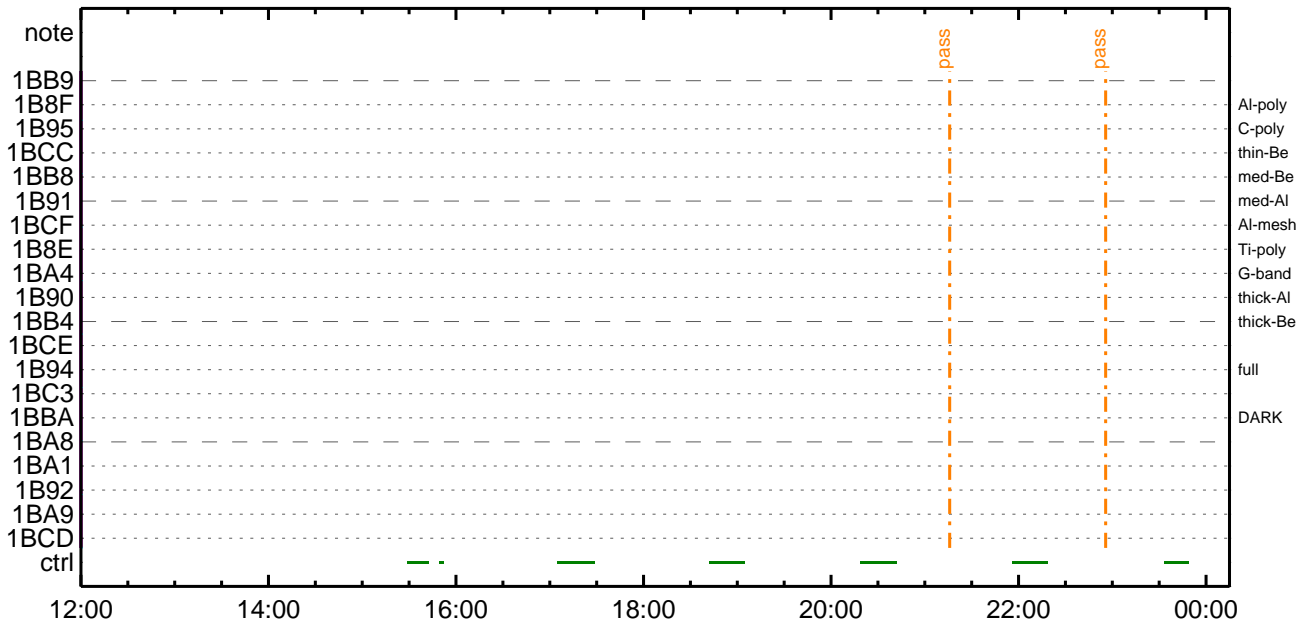
CMDI #0100 2017/11/02



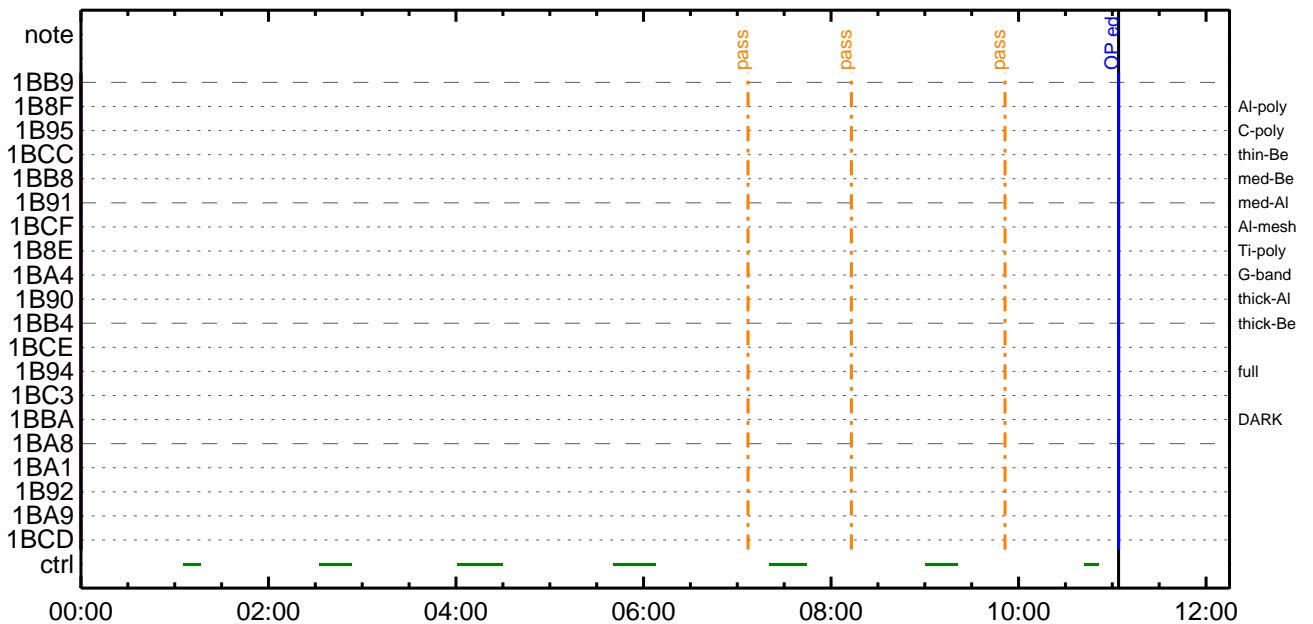
CMDI #0100 2017/11/03



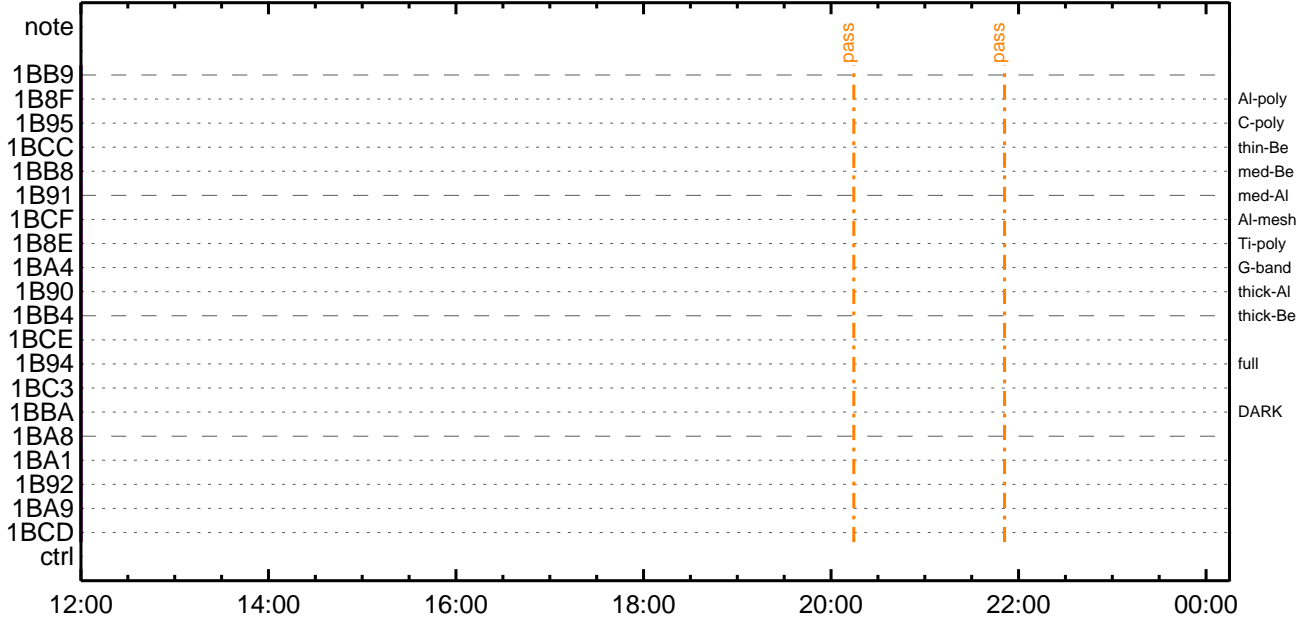
CMDI #0100 2017/11/03



CMDI #0100 2017/11/04



CMDI #0100 2017/11/04




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ã
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-953:OP
0104 ( )
0105 S. OG og-953:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPîî°èYAYôYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYôYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î¼E¹ç•è²îOK²³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYôYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î¼E¹ç•è²îOK²³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYôYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î¼E¹ç•è²îOK²³îÇ§
0165 C.
0166 C. ***** °E²¼²î¼E¹ç•è²îOK²³îÇ§ *****
0167 C. DHUYâ;4YE;E½Y½, Y1;4YE;Eòîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷çîNG²î¼E¹ç•è²¼²îTI-CMDÁ÷çî¼E¹Ô²•²E²³²E;f
0180 C. ²²²ç;çSET²E²DUMP²îE±²îY²¹²ç¹Ô²|²³²E;f
0181 C.
0182 C. TIY³Y²YôYE²òðÁDîç(UT)
0183 +. TI 2017-10-31 10:16:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2017-10-31 10:16:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2017-10-31 10:16:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2017-10-31 10:20:59.5
0196 DC 01-B2 DHU_OP_START
0197 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0198 C.
0199 C. °Ê²¼αîÄê%îíñαîŷÄŷ§ŷÄŷ-¹àîŷ
0200 C. çç[HK1_TI_CMD_ENA/DIS] EQ ENA
0201 C. çç[HK1_TI_CMD_NUM] EQ 4
0202 C. çç[HK1_NEXT_EXEC_PIM] EQ DHU
0203 C. çç[HK1_NEXT_EXEC_DC] EQ 0xB3
0204 C.
0205 C. *****
0206 C. TIîŷ°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC (03 ab 03 01 02)
0212 C. çç[HK1_DMP_TOP_ADRS_1] EQ 07
0213 C. çç[HK1_DMP_TOP_ADRS_0] EQ 2B
0214 C. çç[HK1_DMP_BLOCK_NUM] EQ 3
0215 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0216 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC (07 0b f8)
0219 C. çç[HK1_PKT_FORM_NO] EQ 7
0220 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0221 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0222 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0223 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»αð³îç§
0226 C. çç[HK1_DMP_CHK_FLG] EQ NON
0227 C.
0228 C. RAM ID=TI_TBLαîŷ°è¹ç.ª²îOKαð³îç§
0229 C.
0230 C. DHUŷª;¼ŷÊ;Ê¼ŷ¼.ŷî;¼ŷÊ;Êαðîãα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC (02 0a f8)
0233 C. çç[HK1_PKT_FORM_NO] EQ 2
0234 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0235 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0236 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2017-10-31 10:20:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM = 1 CNTUP []
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2017-10-31 10:20:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC (21 02)
0258 +. TI 2017-10-31 10:20:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC (22)
0261 C. [] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2017-10-31 10:20:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC (c3)
0271 C. [] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃîαî»ö¼ŷ°èÄð¹αèDCBC.×²è *****
0276 C. (¼ª°îŷÖŷÄŷÊŷŷŷÄŷçŷèè¼αα¼Ä»ŷ¹αè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷÐŷ¹.î Daily±çîñαè´Øα¹αèDCBC.×²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼Ä»ŷ;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 10 80 80 08 08)
0098 + DC 07-F0 MDP_XRT_FLD_ENA
0099 BC (d8)
0100 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0101 BC (c8)
0102 + DC 07-F0 MDP_XRT_ARS_DIS
0103 BC (d5)
0104 + DC 07-F0 MDP_XRT_AEC_RESET
0105 BC (d0)
0106 + DC 07-F0 MDP_XRT_FLD_RESET
0107 BC (da)
0108 + DC 07-F0 MDP_XRT_QT_PROG_SET
0109 BC (c4 07)
0110 + DC 07-F0 MDP_XRT_FL_PROG_SET
0111 BC (c5 0d)
0112 . C. ----- Success Verify ? OK / NG ____
0113 C.
0114 C.
0115 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0116 C.
0117 + DC 07-F0 MDP_XRT_MODE_OBSV
0118 BC (c2)
0119 + TI 2017-10-31 10:20:02.0
0120 DC 07-F0 MDP_XRT_MODE_OBSV
0121 BC (c2)
0122 . C. ----- Success Verify ? OK / NG ____
0123 C.
0124 C. ***** XRT END *****
0125 C.
0126 . C. ***** MDP `úÃîñî»ö%ÝñÊÃðñ¹ñèDCBC•x²è *****
0127 C. (%Á°îÝÓÝÃÝÈÝÞÝËÝÁÝÇÝèñÊ%¼ñ¼Ã»Ûñ¹ñè)
0128 . S. DC-BC dcbc-402:DCBC
0129 (MDP_known_event)
0130 C.
0131 C.
0132 . C. ***** ¥DÝ¹•İ Daily±¿İÑñÈ´Øñ¹ñèDCBC•x²è *****
0133 . S. DC-BC dcbc-153:DCBC
0134 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0135 C.
0136 C.
0137 . C. ;ãLOS¥Á¥\$¥Ã¥¬¼Ã»Û;ã
0138 C.
0139 . C. ***** LOS *****
0140 C.

*** OP Sequence for XRT ***

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2017/10/31 10:31:00.0 AOCs_OrE-point_Start_1_og [0x097]
                        AOCu_NM                    5 02-76 03 00 00 00 00
2017/10/31 15:00:00.0 AOCs_OrE-point_Start_2_og [0x098]
                        AOCu_NM                    5 02-76 04 00 00 00 00
2017/11/01 06:09:00.0 AOCs_OrE-point_Start_3_og [0x099]
                        AOCu_NM                    5 02-76 00 00 00 00 00
2017/11/01 06:15:00.0 XRT_TCIB_XRT_S_HTR_A_Dis_441_og [0x1b9]
                        TCIB_XRT_S_HTR_A_Dis      0 04-C0
2017/11/01 06:19:00.0 AOCs_OrE-point_Start_2_og [0x098]
                        AOCu_NM                    5 02-76 04 00 00 00 00
2017/11/01 12:14:54.0 XRT_CTRl_MANu_402_og [0x192]
                        MDP_XRT_CTRl_MANu         1 07-F0 c1
2017/11/01 12:14:56.0 XRT_CTRl_MANu_402_og [0x192]
                        MDP_XRT_CTRl_MANu         1 07-F0 c1
2017/11/01 12:14:58.0 XRT_FOCuS_PosITION_447_og [0x1bf]
                        XRT_FOCuS_PosITION         4 07-F8 22 ff aa 00
2017/11/01 12:15:00.0 AOCs_OrE-point_Start_4_og [0x09a]
                        AOCu_NM                    5 02-76 00 2e f9 2e f9
2017/11/01 12:15:18.0 XRT_FlD_Dis_409_og [0x199]
                        MDP_XRT_FlD_Dis           1 07-F0 d9
2017/11/01 12:15:20.0 XRT_FlRCTRl_Dis_418_og [0x1a2]
                        MDP_XRT_FlRCTRl_Dis        1 07-F0 c9
2017/11/01 12:17:56.0 XRT_ARS_Dis_445_og [0x1bd]
                        MDP_XRT_ARS_Dis           1 07-F0 d5
2017/11/01 12:17:58.0 XRT_QT_ProG_Set_444_og [0x1bc]
                        MDP_XRT_QT_ProG_Set        2 07-F0 c4 13
2017/11/01 12:18:00.0 XRT_CTRl_AuTO_408_og [0x198]
                        MDP_XRT_CTRl_AuTO         1 07-F0 c0
2017/11/01 12:24:54.0 XRT_CTRl_MANu_402_og [0x192]
                        MDP_XRT_CTRl_MANu         1 07-F0 c1
2017/11/01 12:24:56.0 XRT_CTRl_MANu_402_og [0x192]
                        MDP_XRT_CTRl_MANu         1 07-F0 c1
2017/11/01 12:24:58.0 XRT_FOCuS_PosITION_447_og [0x1bf]
                        XRT_FOCuS_PosITION         4 07-F8 22 ff aa 00
2017/11/01 12:25:00.0 AOCs_OrE-point_Start_5_og [0x09b]
                        AOCu_NM                    5 02-76 00 2e f9 d1 07
2017/11/01 12:25:18.0 XRT_FlD_Dis_409_og [0x199]
                        MDP_XRT_FlD_Dis           1 07-F0 d9
2017/11/01 12:25:20.0 XRT_FlRCTRl_Dis_418_og [0x1a2]
                        MDP_XRT_FlRCTRl_Dis        1 07-F0 c9
2017/11/01 12:27:56.0 XRT_ARS_Dis_445_og [0x1bd]
                        MDP_XRT_ARS_Dis           1 07-F0 d5
2017/11/01 12:27:58.0 XRT_QT_ProG_Set_431_og [0x1af]
                        MDP_XRT_QT_ProG_Set        2 07-F0 c4 0b
2017/11/01 12:28:00.0 XRT_CTRl_AuTO_408_og [0x198]
                        MDP_XRT_CTRl_AuTO         1 07-F0 c0
2017/11/01 12:34:54.0 XRT_CTRl_MANu_402_og [0x192]
                        MDP_XRT_CTRl_MANu         1 07-F0 c1
2017/11/01 12:34:56.0 XRT_CTRl_MANu_402_og [0x192]
                        MDP_XRT_CTRl_MANu         1 07-F0 c1
2017/11/01 12:34:58.0 XRT_FOCuS_PosITION_447_og [0x1bf]
                        XRT_FOCuS_PosITION         4 07-F8 22 ff aa 00
2017/11/01 12:35:00.0 AOCs_OrE-point_Start_6_og [0x09c]
                        AOCu_NM                    5 02-76 00 d1 07 d1 07
2017/11/01 12:35:18.0 XRT_FlD_Dis_409_og [0x199]
                        MDP_XRT_FlD_Dis           1 07-F0 d9
2017/11/01 12:35:20.0 XRT_FlRCTRl_Dis_418_og [0x1a2]
                        MDP_XRT_FlRCTRl_Dis        1 07-F0 c9
2017/11/01 12:37:56.0 XRT_ARS_Dis_445_og [0x1bd]
                        MDP_XRT_ARS_Dis           1 07-F0 d5
2017/11/01 12:37:58.0 XRT_QT_ProG_Set_427_og [0x1ab]
                        MDP_XRT_QT_ProG_Set        2 07-F0 c4 0f
2017/11/01 12:38:00.0 XRT_CTRl_AuTO_408_og [0x198]
                        MDP_XRT_CTRl_AuTO         1 07-F0 c0
2017/11/01 12:44:54.0 XRT_CTRl_MANu_402_og [0x192]
                        MDP_XRT_CTRl_MANu         1 07-F0 c1
2017/11/01 12:44:56.0 XRT_CTRl_MANu_402_og [0x192]
                        MDP_XRT_CTRl_MANu         1 07-F0 c1
2017/11/01 12:44:58.0 XRT_FOCuS_PosITION_447_og [0x1bf]
                        XRT_FOCuS_PosITION         4 07-F8 22 ff aa 00
2017/11/01 12:45:00.0 AOCs_OrE-point_Start_7_og [0x09d]
                        AOCu_NM                    5 02-76 00 d1 07 2e f9
2017/11/01 12:45:18.0 XRT_FlD_Dis_409_og [0x199]
                        MDP_XRT_FlD_Dis           1 07-F0 d9
2017/11/01 12:45:20.0 XRT_FlRCTRl_Dis_418_og [0x1a2]
                        MDP_XRT_FlRCTRl_Dis        1 07-F0 c9
2017/11/01 12:47:56.0 XRT_ARS_Dis_445_og [0x1bd]
                        MDP_XRT_ARS_Dis           1 07-F0 d5
2017/11/01 12:47:58.0 XRT_QT_ProG_Set_413_og [0x19d]
                        MDP_XRT_QT_ProG_Set        2 07-F0 c4 03
2017/11/01 12:48:00.0 XRT_CTRl_AuTO_408_og [0x198]
                        MDP_XRT_CTRl_AuTO         1 07-F0 c0
2017/11/01 12:54:54.0 XRT_CTRl_MANu_402_og [0x192]
                        MDP_XRT_CTRl_MANu         1 07-F0 c1
2017/11/01 12:54:56.0 XRT_CTRl_MANu_402_og [0x192]
                        MDP_XRT_CTRl_MANu         1 07-F0 c1
2017/11/01 12:54:58.0 XRT_FOCuS_ReCALIBRATE_416_og [0x1a0]
                        XRT_FOCuS_ReCAL            2 07-F8 78 00
2017/11/01 12:55:00.0 AOCs_OrE-point_Start_1_og [0x097]
                        AOCu_NM                    5 02-76 03 00 00 00 00
2017/11/01 12:58:58.0 XRT_FOCuS_PosITION_410_og [0x19a]

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2017/11/01	12:59:18.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
			MDP_XRT_FLD_ENA	1	07-F0	d8			
2017/11/01	12:59:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2017/11/01	12:59:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2017/11/01	12:59:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2017/11/01	12:59:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da			
2017/11/01	13:01:56.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e		
2017/11/01	13:01:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d		
2017/11/01	13:02:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2017/11/01	14:24:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/11/01	14:24:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/11/01	14:24:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2017/11/01	14:24:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2017/11/01	14:27:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2017/11/01	14:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2017/11/01	14:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2017/11/01	15:55:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/11/01	15:55:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/11/01	15:55:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2017/11/01	15:55:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2017/11/01	15:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2017/11/01	16:19:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2017/11/01	16:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/11/01	17:31:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/11/01	17:31:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/11/01	17:31:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2017/11/01	17:31:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2017/11/01	17:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2017/11/01	17:56:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/11/01	17:56:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/11/01	17:56:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2017/11/01	17:57:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCS_Ore-point_Start_3_OG [0x099]	5	02-76	00	00	00	00
			AOCS_Ore-point_Start_3_OG [0x099]	5	02-76	00	00	00	00
2017/11/01	17:57:18.0	XRT_FLD_DIS_425_OG [0x1a9]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2017/11/01	17:59:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2017/11/01	17:59:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2017/11/01	17:59:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a		
2017/11/01	18:00:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2017/11/01	18:06:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/11/01	18:06:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/11/01	18:06:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2017/11/01	18:07:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCS_Ore-point_Start_2_OG [0x098]	5	02-76	04	00	00	00
			AOCS_Ore-point_Start_2_OG [0x098]	5	02-76	04	00	00	00
2017/11/01	18:07:18.0	XRT_ROI_A_435_OG [0x1b3]	MDP_XRT_ROI_SET	6	07-F0	cd	05	85	83
			MDP_XRT_ROI_SET	6	07-F0	cd	06	85	83
			MDP_XRT_ROI_SET	6	07-F0	cd	07	85	83
			MDP_XRT_ROI_SET	6	07-F0	cd	08	80	80
			MDP_XRT_ROI_SET	6	07-F0	cd	0c	80	80
			MDP_XRT_ROI_SET	6	07-F0	cd	0d	80	80
			MDP_XRT_ROI_SET	6	07-F0	cd	0e	80	80
			MDP_XRT_ROI_SET	6	07-F0	cd	0f	80	80
2017/11/01	18:07:18.5	XRT_ROI_B_414_OG [0x19e]	MDP_XRT_ROI_SET	6	07-F0	cd	0f	80	80
			MDP_XRT_ROI_SET	6	07-F0	cd	10	80	80
2017/11/01	18:07:23.5	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			

2017/11/01	18:07:25.5	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2017/11/01	18:07:27.5	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2017/11/01	18:07:29.5	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2017/11/01	18:07:31.5	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/11/01	18:10:01.5	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02
2017/11/01	18:10:03.5	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2017/11/01	18:10:05.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/11/01	19:09:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/01	19:09:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/01	19:09:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/11/01	19:09:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/11/01	19:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/11/01	19:32:00.0	XRT_Custom_430_OG [0x1ae]					
2017/11/01	19:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/11/01	20:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/01	20:46:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/01	20:46:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/11/01	20:46:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/11/01	20:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/11/01	21:09:30.0	XRT_Custom_430_OG [0x1ae]					
2017/11/01	21:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/11/01	22:23:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/01	22:23:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/01	22:23:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/11/01	22:23:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/11/01	22:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/11/01	22:45:00.5	XRT_Custom_430_OG [0x1ae]					
2017/11/01	22:46:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/11/01	23:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/01	23:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/01	23:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2017/11/02	00:00:00.0	AOCS_Ore-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00	b1 cb 00 00
2017/11/02	00:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2017/11/02	00:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2017/11/02	00:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2017/11/02	00:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2017/11/02	00:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/11/02	00:00:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/02	00:00:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/02	00:00:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/11/02	00:00:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/11/02	00:02:56.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e
2017/11/02	00:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2017/11/02	00:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/11/02	00:11:30.0	XRT_Custom_430_OG [0x1ae]					
2017/11/02	00:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/11/02	01:27:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/02	01:27:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/02	01:27:04.0	XRT_FLD_RESET_415_OG [0x19f]					

2017/11/02	01:27:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/11/02	01:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/11/02	01:43:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/11/02	01:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/11/02	02:59:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/02	02:59:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/02	02:59:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/11/02	02:59:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/11/02	03:02:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/11/02	03:20:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/11/02	03:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/02	04:28:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/02	04:28:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/02	04:28:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/11/02	04:28:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/11/02	04:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/11/02	04:57:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/11/02	04:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/02	05:40:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/02	05:40:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/02	05:40:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2017/11/02	05:41:00.0	AOCS_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00
2017/11/02	05:41:18.0	XRT_FLD_DIS_425_OG [0x1a9]	MDP_XRT_FLD_DIS	1	07-F0	d9
2017/11/02	05:43:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2017/11/02	05:43:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2017/11/02	05:43:58.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2017/11/02	05:44:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/11/02	05:50:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/02	05:50:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/02	05:50:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2017/11/02	05:51:00.0	AOCS_OrE-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	00 12 35 ae c1
2017/11/02	05:51:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2017/11/02	05:51:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2017/11/02	05:51:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2017/11/02	05:51:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2017/11/02	05:51:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/11/02	05:53:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2017/11/02	05:53:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2017/11/02	05:54:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/11/02	06:08:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/02	06:08:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/02	06:08:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/11/02	06:08:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/11/02	06:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/11/02	06:34:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/11/02	06:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/02	07:48:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/02	07:48:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1

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2017/11/02	07:48:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/11/02	07:48:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/11/02	07:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/11/02	08:11:30.0	XRT_Custom_430_OG [0x1ae]							
2017/11/02	08:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/11/02	09:28:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/02	09:28:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/02	09:28:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/11/02	09:28:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/11/02	09:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/11/02	10:04:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				