

# XRT Timeline to be uploaded on 2017/11/02

Period: 2017/11/02 09:54:00 - 2017/11/07 11:12:00

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Normal mode

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## XOB #1B89: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
11/02 10:07:00 - 11/02 17:42:54	Track ( 740.1, 180.7) <sup>@ 11/02 10:04:00</sup>	# OP start + 10min, AR12686
11/02 23:20:00 - 11/03 05:59:54	Track ( 806.6, 188.2) <sup>@ 11/02 23:00:00</sup>	AR12686
11/03 06:13:00 - 11/03 17:59:54	Track ( 838.1, 192.6) <sup>@ 11/03 06:10:00</sup>	# AR12686

**PROG= 01 Inf.-time(s)**

<b>Subr= 1</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
<b>Seqn= 92</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384	(1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384	(1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384	(1064, 1048)	Q=98	0	0	2.0sec
<b>Subr= 2</b>	<b>5-time(s)</b>	<b>2.0sec</b>											
<b>Seqn= 75</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384	(1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	2.0sec
<b>Seqn= 96</b>	<b>4-time(s)</b>	<b>60.0sec</b>											
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384	(1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	1	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384	(1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	1	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384	(1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	1	2	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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## XOB #1BB4: Synoptic Q95 2x2 - Al/mesh(24/256/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(45/512/4096) + T

Term	Pointing (x, y)	Comment
11/02 17:46:00 - 11/02 17:52:54	Fixed ( 0.0, 0.0)	synoptic, shifted -17.0 min
11/03 06:03:00 - 11/03 06:09:54	Fixed ( 0.0, 0.0)	synoptic
11/03 18:03:00 - 11/03 18:09:54	Fixed ( 0.0, 0.0)	synoptic
11/04 06:20:00 - 11/04 06:26:54	Fixed ( 0.0, 0.0)	synoptic, shifted 17.0 min

**PROG= 10 1-time(s)**

<b>Subr= 1</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
<b>Seqn= 5</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512	(1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048	(1024, 1024)	DPCM	0	0	2.0sec
<b>Seqn= 1</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	24ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 99</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 33</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 23</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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## XOB #1BCF: CME watch - 4x4 - AEC 1/2 - Al-poly - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 180s cad (G-band/Leak first)

Term	Pointing (x, y)	Comment
11/02 17:56:00 - 11/02 22:58:30	Track ( -74.6, 148.4) <sup>@ 11/02 17:53:00</sup>	# EIS QS NW and coronal BP
11/03 18:13:00 - 11/03 22:59:54	Track ( 145.9, 150.8) <sup>@ 11/03 18:10:00</sup>	# EIS QW NW and coronal BP
11/03 23:03:00 - 11/04 06:16:54	Fixed ( 0.0, 880.0)	EIS N-pole CH obs

**PROG= 14 Inf.-time(s)**

<b>Subr= 1</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
<b>Seqn= 30</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512	(1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512	(1024, 1024)	Q=95	0	0	2.0sec
<b>Subr= 2</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
<b>Seqn= 7</b>	<b>20-time(s)</b>	<b>180.0sec</b>											
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	1	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048	(1024, 1024)	Q=98	2	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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**XOB #1BBA: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with**

Term	Pointing (x, y)		Comment									
11/04 06:30:00 - 11/04 09:00:30	Track ( -772.4, 98.8) @ 11/04 06:27:00		# AR at E-limb									
<b>PROG= 06 Inf.-time(s)</b>												
Subr= 1 1-time(s) 2.0sec												
Seqn= 92 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2 5-time(s) 2.0sec												
Seqn= 75 1-time(s) 2.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 96 4-time(s) 120.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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**Flare mode**

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**XOB #1B8E: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512**

Term	Pointing (x, y)		Comment									
11/02 10:07:00 - 11/02 17:42:54	Track ( 740.1, 180.7) @ 11/02 10:04:00		# OP start + 10min, AR12686									
11/02 17:56:00 - 11/02 22:58:30	Track ( -74.6, 148.4) @ 11/02 17:53:00		# EIS QS NW and coronal BP									
11/02 23:20:00 - 11/03 05:59:54	Track ( 806.6, 188.2) @ 11/02 23:00:00		AR12686									
11/03 06:13:00 - 11/03 17:59:54	Track ( 838.1, 192.6) @ 11/03 06:10:00		# AR12686									
11/03 18:13:00 - 11/03 22:59:54	Track ( 145.9, 150.8) @ 11/03 18:10:00		# EIS QW NW and coronal BP									
11/03 23:03:00 - 11/04 06:16:54	Fixed ( 0.0, 880.0)		EIS N-pole CH obs									
11/04 06:30:00 - 11/04 09:00:30	Track ( -772.4, 98.8) @ 11/04 06:27:00		# AR at E-limb									
<b>PROG= 13 30-time(s)</b>												
Subr= 1 20-time(s) 2.0sec												
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn=100 1-time(s) 10.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 10 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 87 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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**Active Region Search**

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NOT USED

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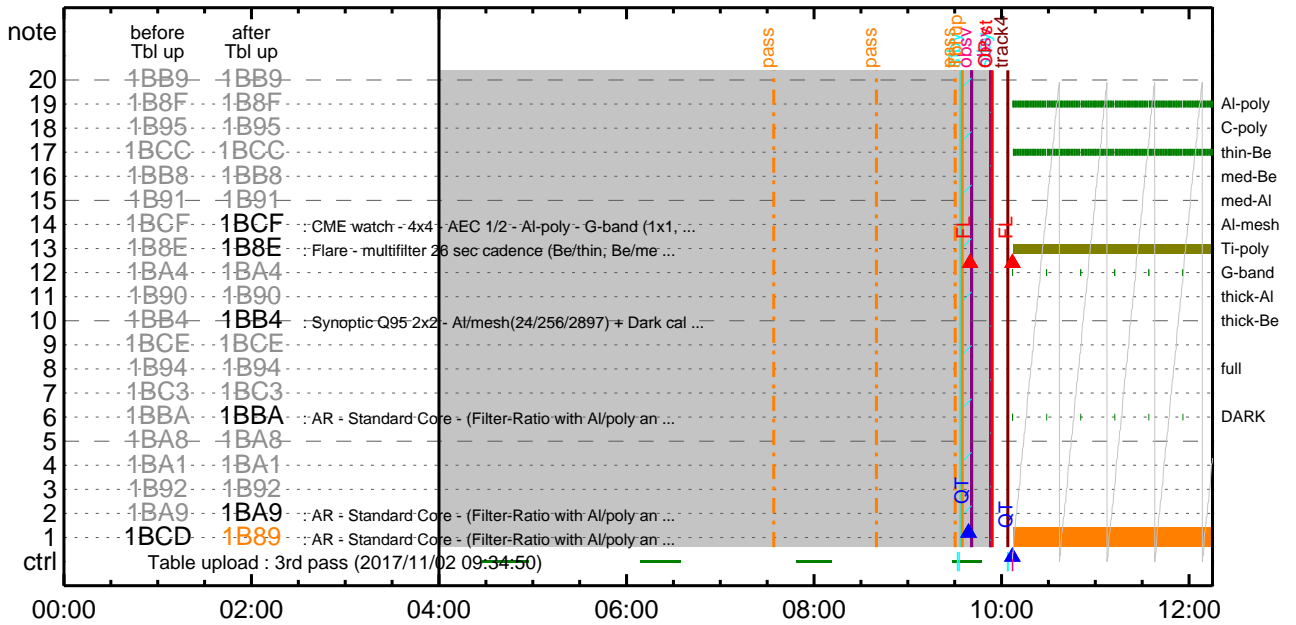
**Flare Detection**

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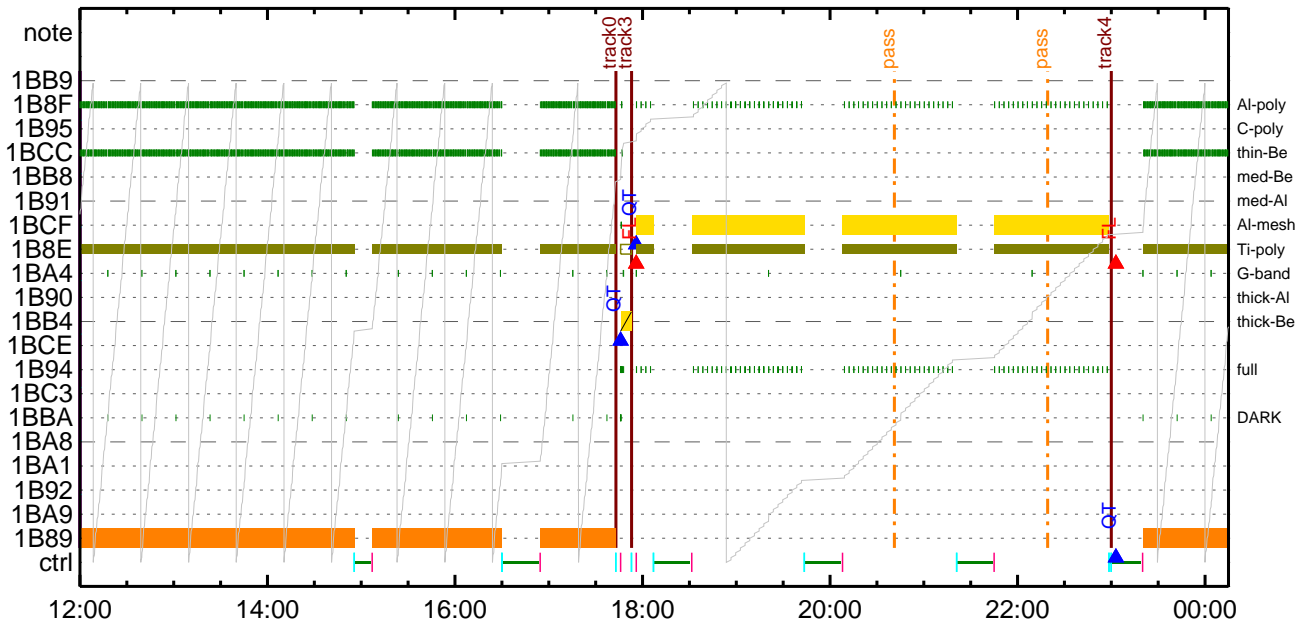
**FLD Patrol**

Term	Pointing (x, y)		Comment									
11/02 17:53:18 - 11/03 06:00:18	Track ( -74.6, 148.4) @ 11/02 17:53:00		# EIS QS NW and coronal BP									
11/03 06:10:18 - 11/03 18:00:18	Track ( 838.1, 192.6) @ 11/03 06:10:00		# AR12686									
11/03 18:10:18 - 11/04 06:17:18	Track ( 145.9, 150.8) @ 11/03 18:10:00		# EIS QW NW and coronal BP									
11/04 06:27:18 - 11/07 11:12:00	Track ( -772.4, 98.8) @ 11/04 06:27:00		# AR at E-limb									
Al-poly/Open	Al-poly/Open	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

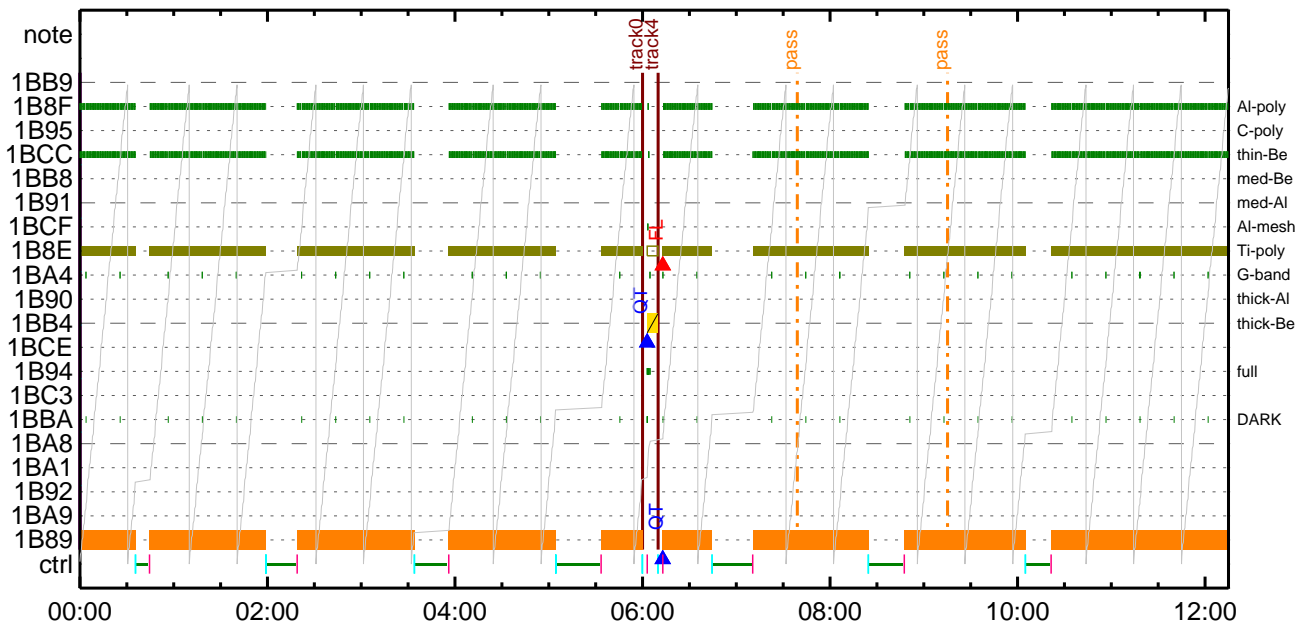
### CMDI #0104 2017/11/02



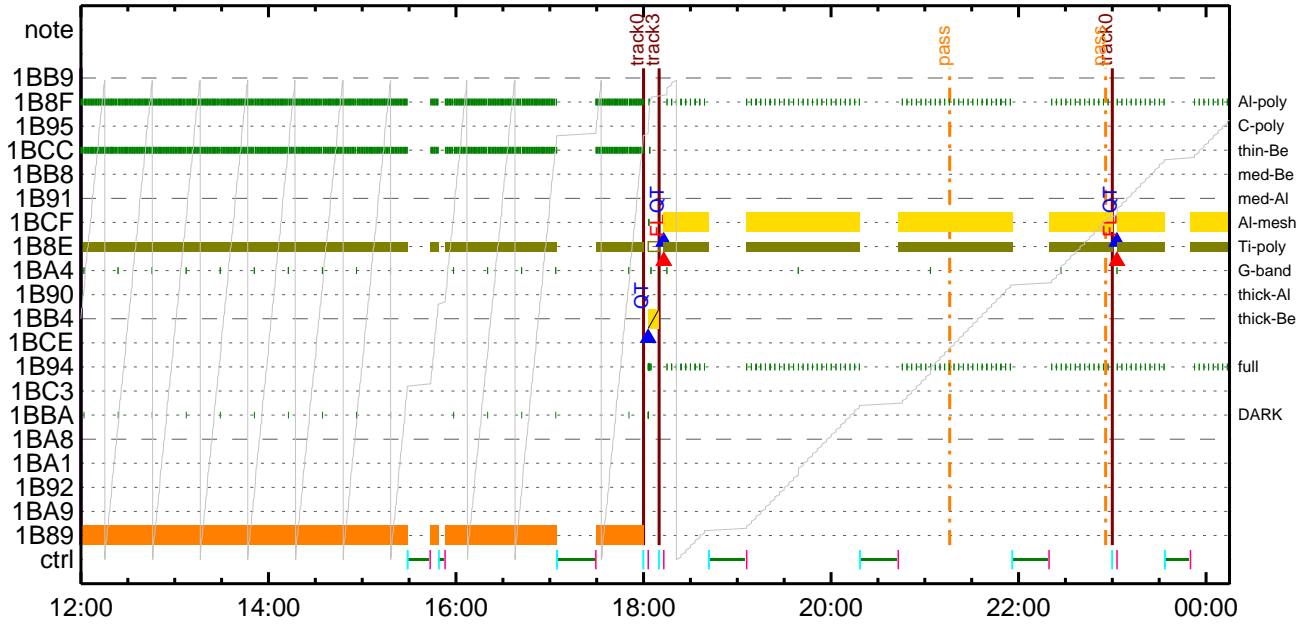
### CMDI #0104 2017/11/02



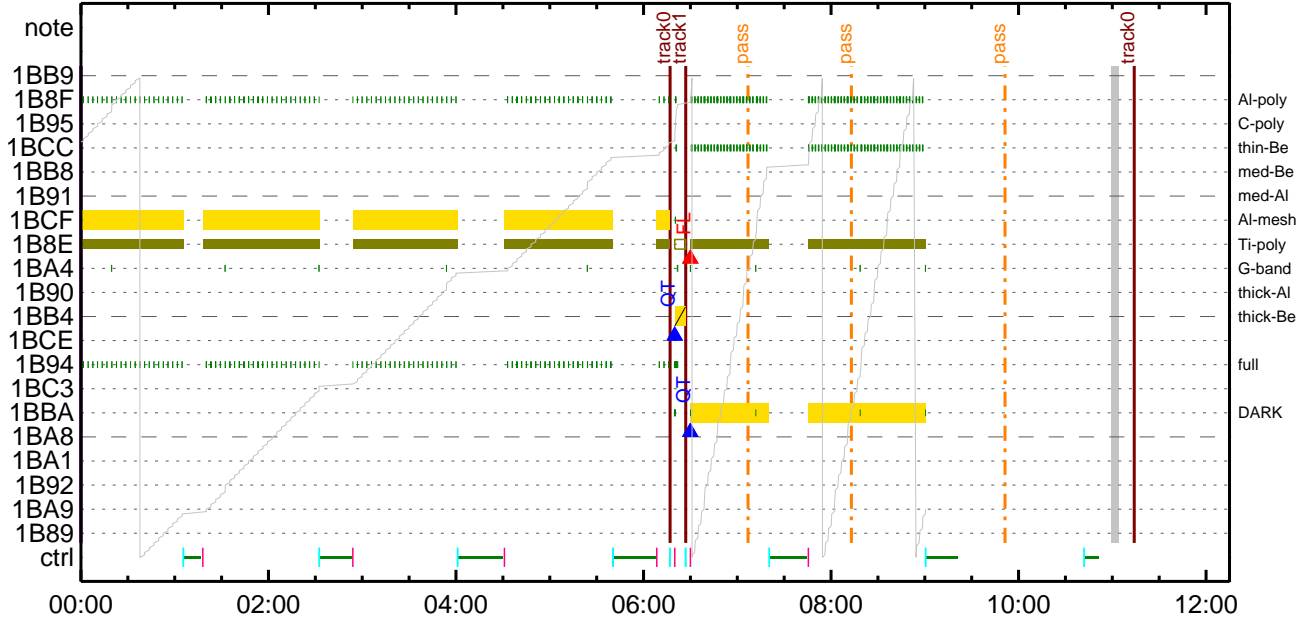
### CMDI #0104 2017/11/03



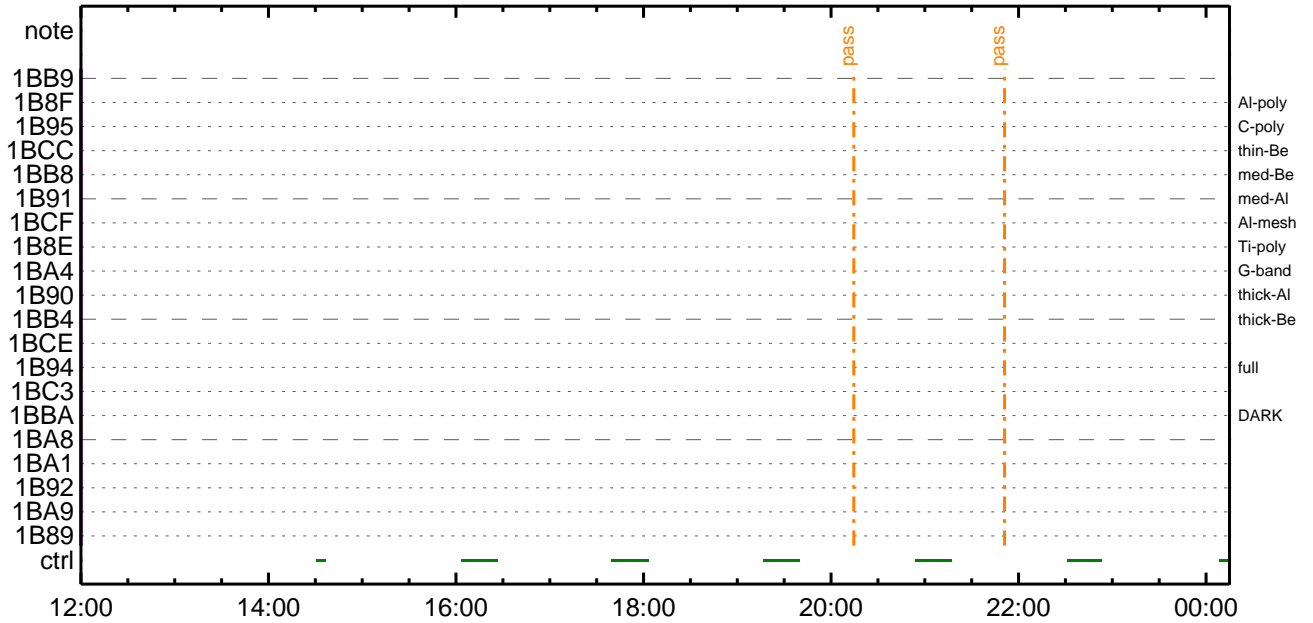
### CMDI #0104 2017/11/03



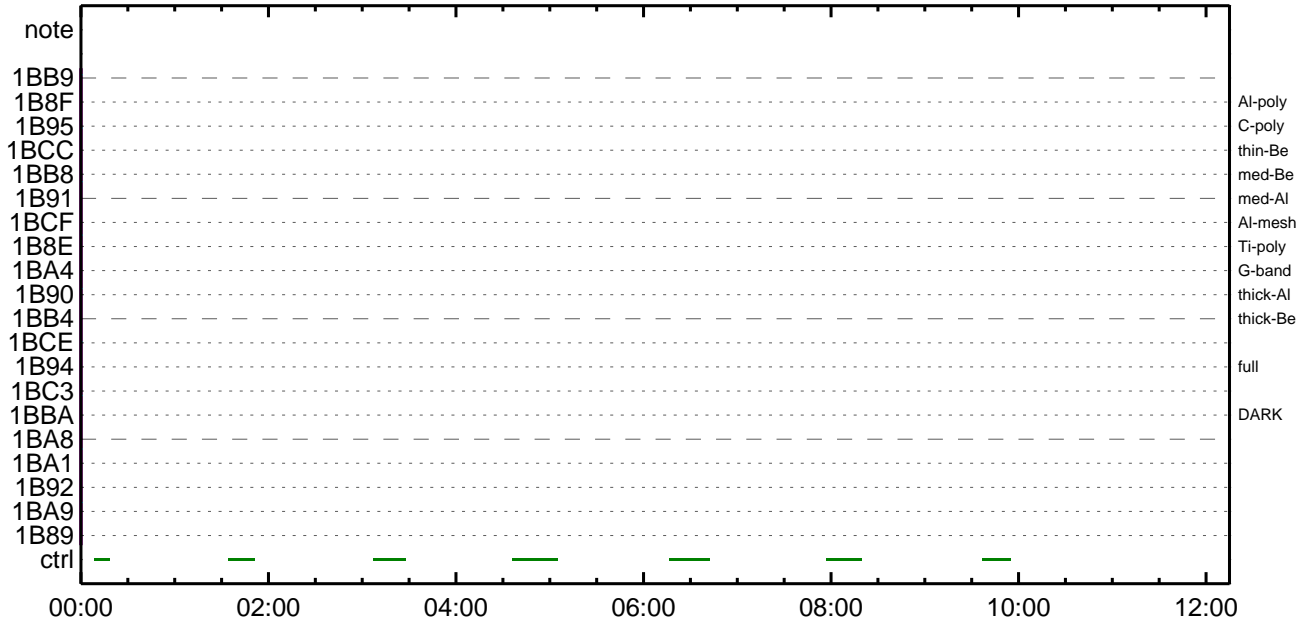
### CMDI #0104 2017/11/04



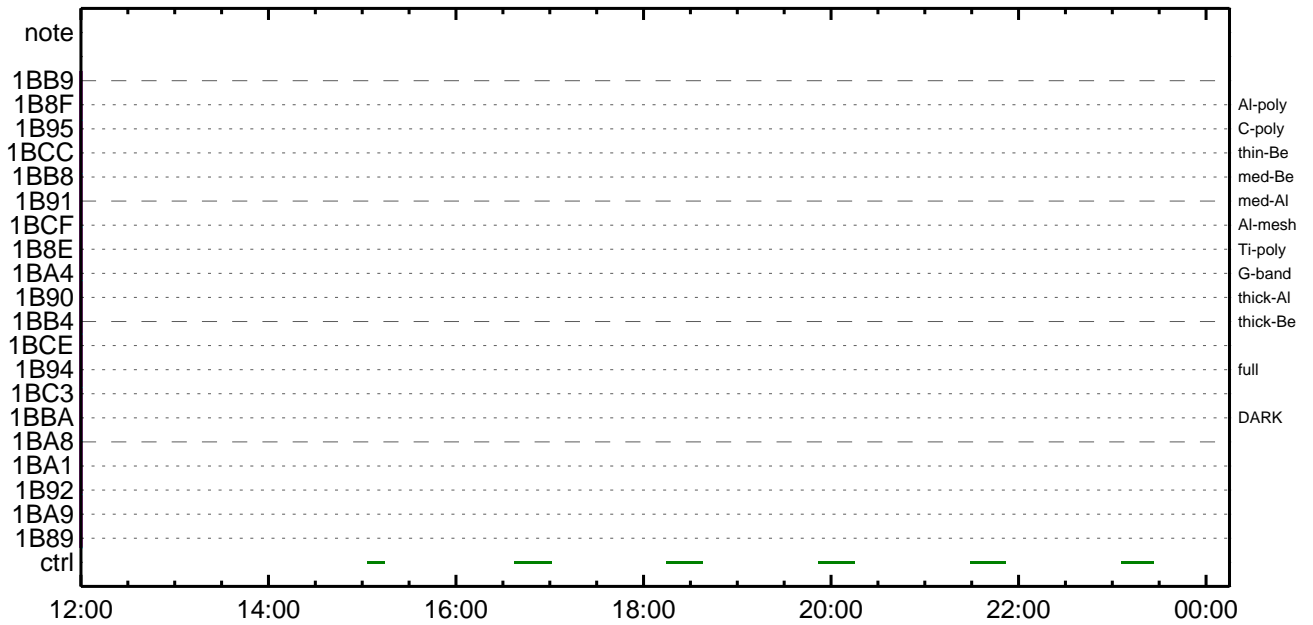
### CMDI #0104 2017/11/04



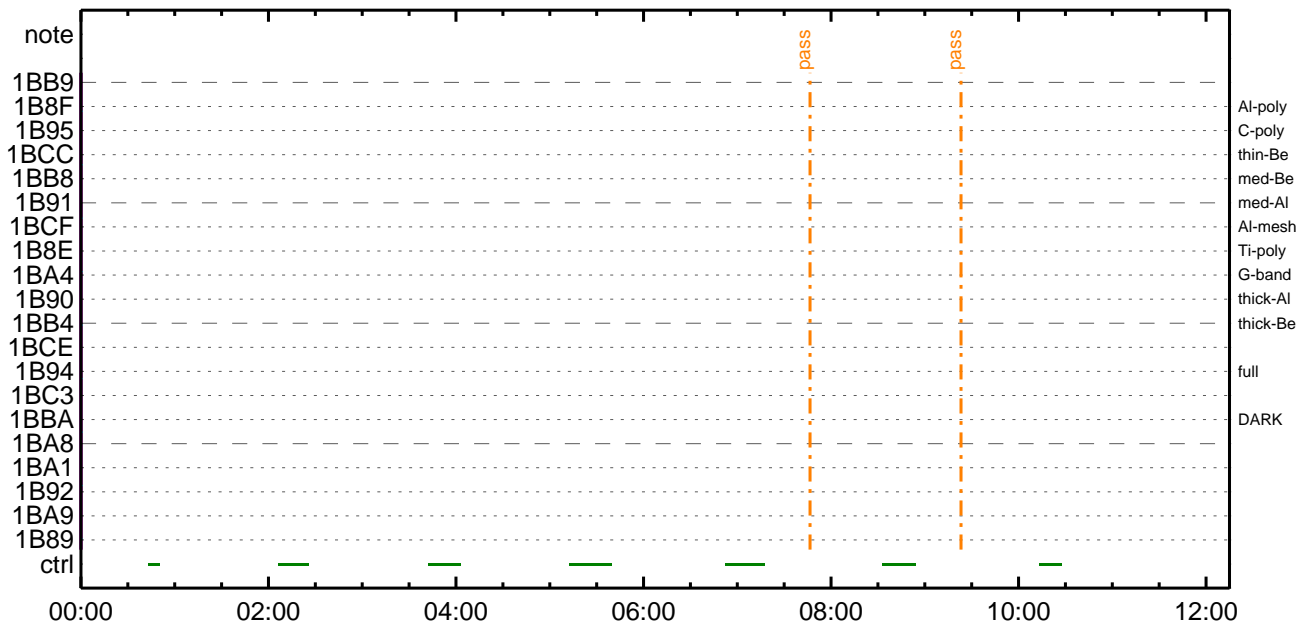
CMDI #0104 2017/11/05



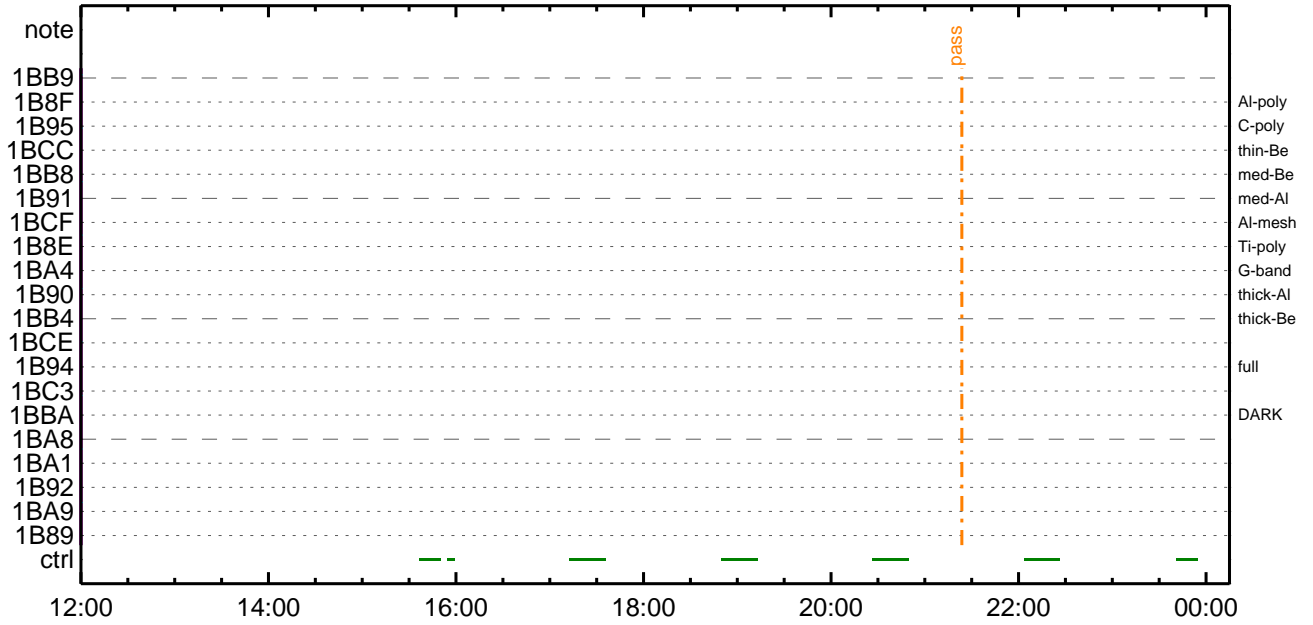
CMDI #0104 2017/11/05



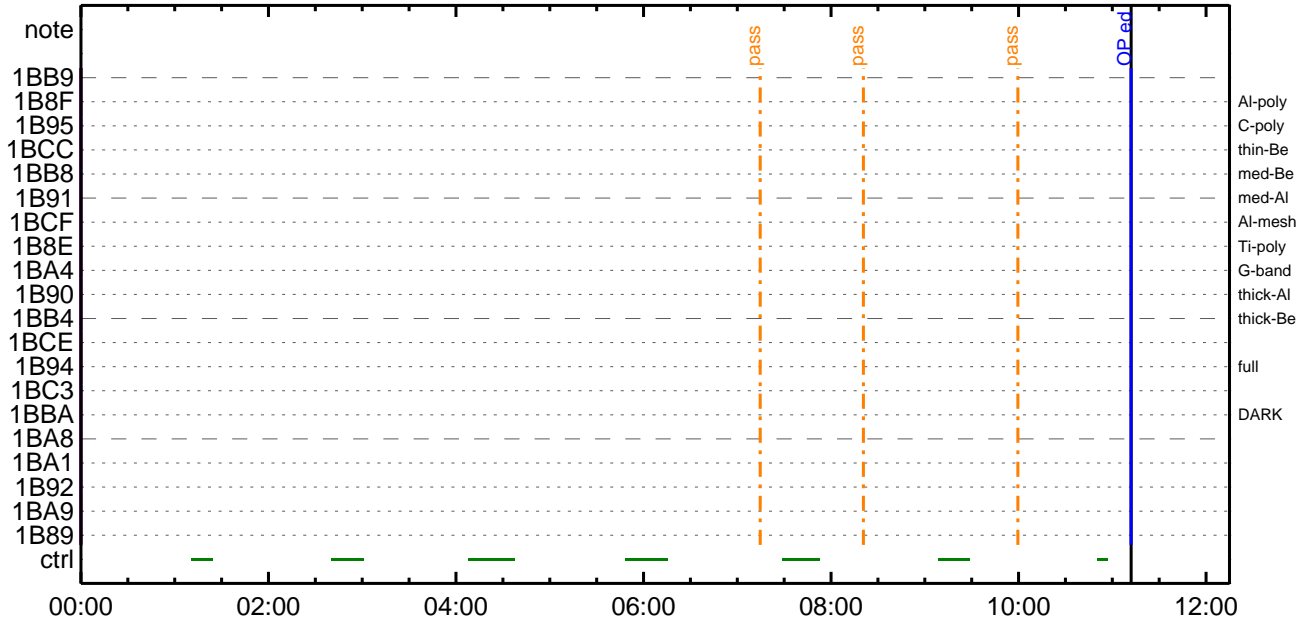
CMDI #0104 2017/11/06



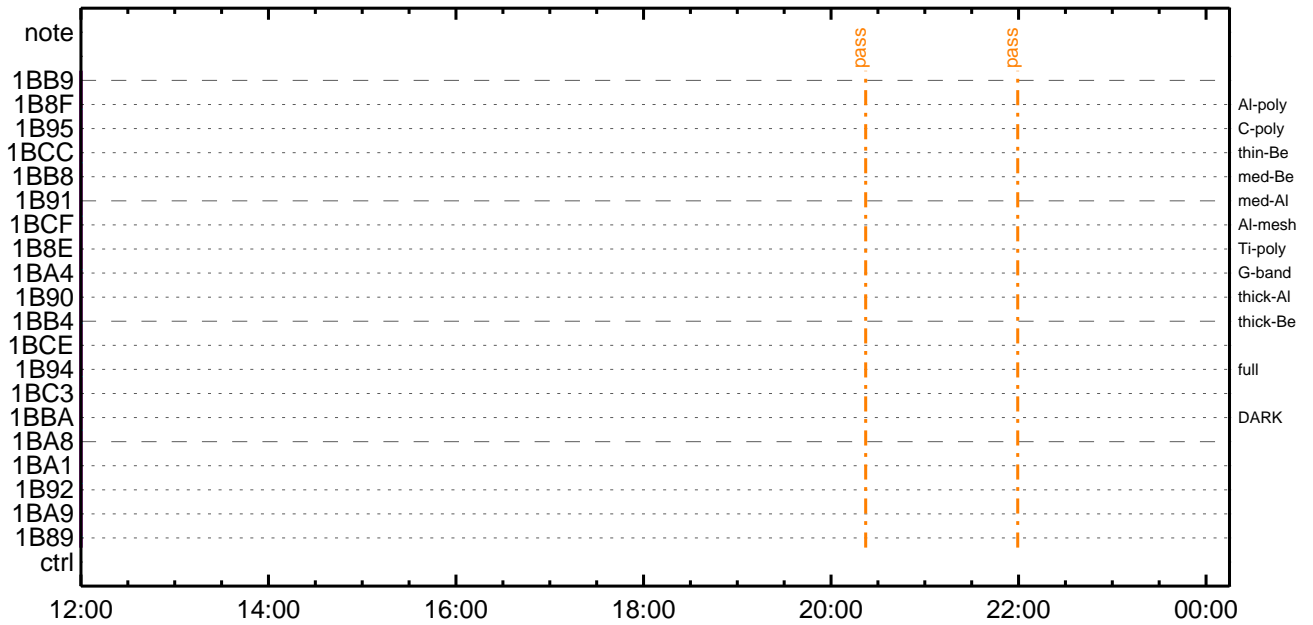
CMDI #0104 2017/11/06



CMDI #0104 2017/11/07



CMDI #0104 2017/11/07





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0096 C.          0203; SET 0EDUMP 01E±°iYÑY¹αÇ¹Öα|α³αE; E
0097 C.
0098 C. TIY³YF¥ÖYÉαδΔDİ¿ (UT)
0099 +. TI 2017-11-02 09:49:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.          φφ[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0102 C.
0103 +. TI 2017-11-02 09:49:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.          φφ[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0106 C.
0107 +. TI 2017-11-02 09:49:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.          φφ[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0110 C.
0111 +. TI 2017-11-02 09:53:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.          φφ[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0114 C.
0115 C. °E²¼αİÄê%îİÑαİYÁ¥§YÁY-¹àİÜ
0116 C.          φφ[HK1_TI_CMD_ENA/DIS]        EQ          ENA
0117 C.          φφ[HK1_TI_CMD_NUM]          EQ          4
0118 C.          φφ[HK1_NEXT_EXEC_PIM]        EQ          DHU
0119 C.          φφ[HK1_NEXT_EXEC_DC]        EQ          0xB3
0120 C.
0121 C. *****
0122 C. TIİİ°èYÄYÖYx
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC          (03 ab 03 01 02)
0128 C.          φφ[HK1_DMP_TOP_ADRS_1]        EQ          07
0129 C.          φφ[HK1_DMP_TOP_ADRS_0]        EQ          2B
0130 C.          φφ[HK1_DMP_BLOCK_NUM]        EQ          3
0131 C.          φφ[HK1_DMP_REPEAT_NUM]       EQ          0
0132 C.          φφ[HK1_DMA_DMP_PIM]        EQ          DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC          (07 0b f8)
0135 C.          φφ[HK1_PKT_FORM_NO]          EQ          7
0136 C.          φφ[HK1_PKT_GEN_TIME]         EQ          0.25 s
0137 C.          φφ[HK1_S_TLM_BIT_RATE]       EQ          32k
0138 C.          φφ[HK1_X_TLM_BIT_RATE]      EQ          4M
0139 C.          φφ[HK1_DMP_CHK_FLG]         EQ          EXEC
0140 C.
0141 C. YÄYÖYx¼³İ»αδ³İÇ§
0142 C.          φφ[HK1_DMP_CHK_FLG]         EQ          NON
0143 C.
0144 C. RAM ID=TI_TBLαİ%È¹Ç•è²İOKαδ³İÇ§
0145 C.
0146 C. DHU¥â;¼YÉ;È¼Y¼;¥İ;¼YÈ;Èαδİáα¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC          (02 0a f8)
0149 C.          φφ[HK1_PKT_FORM_NO]          EQ          2
0150 C.          φφ[HK1_PKT_GEN_TIME]         EQ          0.5S
0151 C.          φφ[HK1_S_TLM_BIT_RATE]       EQ          32K
0152 C.          φφ[HK1_X_TLM_BIT_RATE]      EQ          4M
0153 C.
0154 C.
0155 C. ***** XRT START *****
0156 C. Execute, after the success of OP upload.
0157 +. TI 2017-11-02 09:53:00.0
0158 DC 07-F0 MDP_XRT_MODE_STBY
0159 BC          (c3)
0160 C.          [   ] [HK1_TI_CMD_NUM]       EQ          1COUNTUP
0161 C.
0162 C. ***** XRT END *****
0163 C. Stop EIS observation and temporarily disable EIS mode changes
0164 C.
0165 C.
0166 C. ***** Start EIS operation (TI set) *****
0167 C. Execute, after the success of OP upload.
0168 C. Set EIS TI-commands
0169 +. TI 2017-11-02 09:53:30.0
0170 DC 07-FC EIS_MODE_MANU
0171 BC          (21 02)
0172 +. TI 2017-11-02 09:53:40.0
0173 DC 07-FC EIS_MODE_CHG_DIS
0174 BC          (22)
0175 C.          [   ] [HK1_TI_CMD_NUM]       EQ          2 COUNTUP
0176 C. ***** End EIS operation (TI set) *****
0177 C.
0178 C.
0179 C. *****
0180 C. SOT TI command set
0181 C. *****
0182 C. Execute, after the success of OP upload.
0183 +. TI 2017-11-02 09:53:16.0
0184 DC 07-F0 MDP_SOT_MODE_STBY
0185 BC          (41)
0186 C. -----
0187 C. HK1_TI_CMD_NUM          = 1 CNTUP [   ]
0188 C. -----
0189 C. ***** SOT END *****
0190 C.
0191 C. ***** MDP `úÄİİ»ö¼YαÈÄα¹αèDCBC•x²è *****
0192 C. (%â°İYÖYÄYÈYB¥ÈYÁYÇYèαÈ¼αα¼Ä»Üα¹αé)
0193 S. DC-BC dcbc-402:DCBC

```



```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
```



```

0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_CHG_ENA
0131 BC (20)
0132 . C. Verify EIS_MODE_CHG_FLG is ENA
0133 +. DC 07-FC EIS_MODE_MANU
0134 BC (21 02)
0135 . C. Verify EIS in MANUAL mode
0136 . C. Estimated OBSTBL upload time is 26s
0137 C. *****
0138 C. EIS START OBSTBL LOAD
0139 C. *****
0140 . S. RAM ram-820:EIS_OBSTBL
0141 ( )
0142 +. DC 07-FC EIS_DUMP_OBSTBL
0143 BC (07 07 07 00 00 70 00)
0144 C.
0145 C. Execute, after the success of OBSTBL upload.
0146 C. Set EIS TI-commands
0147 +. TI 2017-11-02 09:53:50.0
0148 DC 07-FC EIS_MODE_CHG_ENA
0149 BC (20)
0150 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0151 C. *****
0152 C. EIS END OBSTBL LOAD
0153 C. *****
0154 C.
0155 . C. ***** MDP [ ] DCBC * x2 è *****
0156 C. ( [%â°îYÓYAYEYPYÉYáYçYèâE%¼â»Ûâé)
0157 . S. DC-BC dcbc-402:DCBC
0158 (MDP_known_event)
0159 C.
0160 C.
0161 . C. ***** YDY1•ï Daily±¿ÎÑâÉ'Øâ¹âéDCBC*x²è *****
0162 . S. DC-BC dcbc-153:DCBC
0163 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164 C.
0165 C.
0166 . C. ;ãLOS¥Á¥S¥ÅY-¼Å»Û;ã
0167 C.
0168 . C. ***** LOS *****
0169 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-960 2017-11-02 13:05:57 130 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Ü;ã
0005 C.
0006 C. YÁY$;¼Y³YF¥ÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;ÈçðÅð•µ°®»ÍxÁÇçíYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÈ;ÈðÈ¼°ÇÔð•ñ¿i¹çñí;çÀ®, ùñ¹ðèðñçÁ+¿®ð•ñÈðñ³ñÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop SP table >
0018 +. DC 07-F0 MDP_SP_CTRL_MANU
0019 BC (61)
0020 C. -----
0021 C. MDP_SP_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload SP Observation Table>
0025 . S. RAM ram-284:MDP_OBS_S
0026 ()
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_S >
0029 +. DC 07-F0 MDP_DUMP_SPTBL
0030 BC (83 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_S verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2017-11-02 09:53:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 . C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 +. DC 07-F0 MDP_XRT_CTRL_MANU
0052 BC (c1)
0053 +. DC 07-F0 MDP_XRT_MODE_STBY
0054 BC (c3)
0055 . C. ----- Success Verify ? OK / NG_____
0056 C.
0057 C. XRT Obs. Table Upload
0058 . S. RAM ram-291:MDP_OBS_X
0059 ()
0060 C.
0061 +. DC 07-F0 MDP_DUMP_XRTTBL
0062 BC (84 07 00 00 00 3a d4)
0063 . C. ----- Comparison Check ? OK / ERR _____
0064 C.
0065 C.
0066 +. DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 01 b1 b1 04 04)
0068 +. DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 02 b1 b1 08 08)
0070 +. DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 03 b1 b1 08 08)
0072 +. DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 04 b1 b1 06 06)
0074 +. DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 06 85 83 06 06)
0076 +. DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 07 80 80 20 20)
0078 +. DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 08 80 80 20 08)
0080 +. DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 09 80 80 08 20)
0082 +. DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 0a 80 80 08 08)
0084 +. DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0f 80 80 06 06)
0086 +. DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 10 80 80 08 08)
0088 +. DC 07-F0 MDP_XRT_FLD_ENA
0089 BC (d8)
0090 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0091 BC (c8)
0092 +. DC 07-F0 MDP_XRT_ARS_DIS
0093 BC (d5)
0094 +. DC 07-F0 MDP_XRT_AEC_RESET
0095 BC (d0)
```

```
0096 +. DC 07-F0 MDP_XRT_FLD_RESET
0097 BC (da)
0098 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0099 BC (c4 02)
0100 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0101 BC (c5 0d)
0102 . C. ----- Success Verify ? OK / NG ____
0103 C.
0104 C.
0105 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0106 C.
0107 +. DC 07-F0 MDP_XRT_MODE_OBSV
0108 BC (c2)
0109 +. TI 2017-11-02 09:53:02.0
0110 DC 07-F0 MDP_XRT_MODE_OBSV
0111 BC (c2)
0112 . C. ----- Success Verify ? OK / NG ____
0113 C.
0114 C. ***** XRT END *****
0115 C.
0116 . C. ***** MDP `úÃîâî»ö¼ÝðËÂð¹ñèDCBC•x²è *****
0117 C. (%â°îÿÓÿÄÿËÿÏÿËÿáÿçÿèñ¼ñ¼Ä»Û¹ñè)
0118 . S. DC-BC dcbc-402:DCBC
0119 (MDP_known_event)
0120 C.
0121 C.
0122 . C. ***** ÿÐÿ¹•Ï Daily±¿ÎÑñË´Ø¹ñèDCBC•x²è *****
0123 . S. DC-BC dcbc-153:DCBC
0124 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0125 C.
0126 C.
0127 . C. ;ãLOSÿÁÿ$ÿÄÿ-¼Ä»Û;ã
0128 C.
0129 . C. ***** LOS *****
0130 C.
```

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XRT\_OGLIST\_0104.chk

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\*\*\* OP Sequence for XRT \*\*\*

2017/11/02	10:03:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/11/02	10:03:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/11/02	10:03:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2017/11/02	10:04:00.0	AOCS_OrE-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	04 00 00 00 00		
2017/11/02	10:04:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2017/11/02	10:04:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2017/11/02	10:04:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2017/11/02	10:04:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2017/11/02	10:04:26.0	XRT_FLD_RESET_433_OG [0x1b1]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2017/11/02	10:06:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01		
2017/11/02	10:06:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2017/11/02	10:07:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/11/02	14:55:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/11/02	14:55:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/11/02	14:55:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2017/11/02	14:55:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2017/11/02	14:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2017/11/02	15:06:00.0	XRT_Custom_430_OG [0x1ae]					
2017/11/02	15:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/11/02	16:30:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/11/02	16:30:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/11/02	16:30:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2017/11/02	16:30:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2017/11/02	16:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2017/11/02	16:53:30.0	XRT_Custom_430_OG [0x1ae]					
2017/11/02	16:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/11/02	17:42:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/11/02	17:42:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/11/02	17:42:58.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2017/11/02	17:43:00.0	AOCS_OrE-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2017/11/02	17:43:18.0	XRT_FLD_DIS_425_OG [0x1a9]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2017/11/02	17:45:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2017/11/02	17:45:56.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2017/11/02	17:45:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a		
2017/11/02	17:46:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/11/02	17:52:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/11/02	17:52:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/11/02	17:52:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2017/11/02	17:53:00.0	AOCS_OrE-point_Start_3_OG [0x099]					
		AOCU_NM	5	02-76	03 00 00 00 00		
2017/11/02	17:53:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2017/11/02	17:53:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2017/11/02	17:53:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2017/11/02	17:53:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2017/11/02	17:53:26.0	XRT_FLD_RESET_433_OG [0x1b1]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2017/11/02	17:55:56.0	XRT_QT_PROG_SET_407_OG [0x197]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e		
2017/11/02	17:55:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2017/11/02	17:56:00.0	XRT_CTRL_AUTO_408_OG [0x198]					

2017/11/02	18:07:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/02	18:07:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/02	18:07:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/11/02	18:07:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/11/02	18:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/11/02	18:30:30.0	XRT_Custom_430_OG [0x1ae]					
2017/11/02	18:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/11/02	19:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/02	19:43:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/02	19:43:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/11/02	19:43:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/11/02	19:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/11/02	20:07:00.0	XRT_Custom_430_OG [0x1ae]					
2017/11/02	20:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/11/02	21:21:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/02	21:21:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/02	21:21:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/11/02	21:21:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/11/02	21:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/11/02	21:44:00.0	XRT_Custom_430_OG [0x1ae]					
2017/11/02	21:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/11/02	22:58:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/02	22:58:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/02	22:58:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/11/02	22:58:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/11/02	22:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/02	22:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/02	22:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2017/11/02	23:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04 00 00 00 00	
2017/11/02	23:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2017/11/02	23:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2017/11/02	23:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2017/11/02	23:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2017/11/02	23:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/11/02	23:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/11/02	23:02:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01	
2017/11/02	23:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2017/11/02	23:19:00.0	XRT_Custom_430_OG [0x1ae]					
2017/11/02	23:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/11/03	00:35:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/03	00:35:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/03	00:35:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/11/03	00:35:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/11/03	00:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/11/03	00:43:30.0	XRT_Custom_430_OG [0x1ae]					
2017/11/03	00:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/11/03	01:59:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/03	01:59:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/03	01:59:04.0	XRT_FLD_RESET_415_OG [0x19f]					

2017/11/03	01:59:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/11/03	02:02:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/11/03	02:18:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/11/03	02:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/11/03	03:34:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/03	03:34:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/03	03:34:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/11/03	03:34:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/11/03	03:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/11/03	03:55:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/11/03	03:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/03	05:04:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/03	05:04:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/03	05:04:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/11/03	05:04:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/11/03	05:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/11/03	05:32:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/11/03	05:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/03	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/03	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/03	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2017/11/03	06:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2017/11/03	06:00:18.0	XRT_FLD_DIS_425_OG [0x1a9]	MDP_XRT_FLD_DIS	1	07-F0	d9
2017/11/03	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2017/11/03	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2017/11/03	06:02:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2017/11/03	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/11/03	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/03	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/03	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2017/11/03	06:10:00.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04 00 00 00 00
2017/11/03	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2017/11/03	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2017/11/03	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2017/11/03	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2017/11/03	06:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/11/03	06:12:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2017/11/03	06:12:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2017/11/03	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/11/03	06:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/03	06:44:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/03	06:44:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/11/03	06:44:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/11/03	06:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/11/03	07:09:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/11/03	07:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/03	08:24:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/03	08:24:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1



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2017/11/03	08:24:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/11/03	08:24:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/11/03	08:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/11/03	08:46:30.0	XRT_Custom_430_OG [0x1ae]							
2017/11/03	08:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/11/03	10:05:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/03	10:05:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/03	10:05:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/11/03	10:05:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/11/03	10:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/11/03	10:20:30.0	XRT_Custom_430_OG [0x1ae]							
2017/11/03	10:21:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/11/03	15:29:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/03	15:29:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/03	15:29:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/11/03	15:29:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/11/03	15:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/11/03	15:42:30.0	XRT_Custom_430_OG [0x1ae]							
2017/11/03	15:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/11/03	15:49:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/03	15:49:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/03	15:49:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/11/03	15:49:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/11/03	15:52:00.0	XRT_Custom_430_OG [0x1ae]							
2017/11/03	15:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/11/03	15:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/11/03	17:04:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/03	17:04:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/03	17:04:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/11/03	17:04:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/11/03	17:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/11/03	17:28:30.0	XRT_Custom_430_OG [0x1ae]							
2017/11/03	17:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/11/03	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/03	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/03	17:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2017/11/03	18:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2017/11/03	18:00:18.0	XRT_FLD_DIS_425_OG [0x1a9]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2017/11/03	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2017/11/03	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2017/11/03	18:02:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2017/11/03	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/11/03	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/03	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/03	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2017/11/03	18:10:00.0	AOCS_OrE-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2017/11/03	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2017/11/03	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2017/11/03	18:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				

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2017/11/03	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2017/11/03	18:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/11/03	18:12:56.0	XRT_QT_PROG_SET_407_OG [0x197]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2017/11/03	18:12:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2017/11/03	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/11/03	18:42:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/03	18:42:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/03	18:42:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/11/03	18:42:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/11/03	18:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/11/03	19:05:00.5	XRT_Custom_430_OG [0x1ae]			
2017/11/03	19:06:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/11/03	20:18:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/03	20:18:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/03	20:18:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/11/03	20:18:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/11/03	20:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/11/03	20:42:00.0	XRT_Custom_430_OG [0x1ae]			
2017/11/03	20:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/11/03	21:56:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/03	21:56:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/03	21:56:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/11/03	21:56:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/11/03	21:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/11/03	22:18:30.0	XRT_Custom_430_OG [0x1ae]			
2017/11/03	22:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/11/03	22:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/03	22:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/03	22:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2017/11/03	23:00:00.0	AOCS_OrE-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	00 b1 cb 00 00
2017/11/03	23:00:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2017/11/03	23:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2017/11/03	23:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2017/11/03	23:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2017/11/03	23:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/11/03	23:02:56.0	XRT_QT_PROG_SET_407_OG [0x197]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2017/11/03	23:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2017/11/03	23:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/11/03	23:33:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/03	23:33:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/03	23:33:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/11/03	23:33:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/11/03	23:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/11/03	23:49:00.0	XRT_Custom_430_OG [0x1ae]			
2017/11/03	23:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/11/04	01:05:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/04	01:05:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/04	01:05:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/11/04	01:05:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]			

2017/11/04	01:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/11/04	01:17:00.0	XRT_Custom_430_OG [0x1ae]					
2017/11/04	01:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/11/04	02:32:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/04	02:32:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/04	02:32:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2017/11/04	02:32:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/11/04	02:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/11/04	02:53:00.0	XRT_Custom_430_OG [0x1ae]					
2017/11/04	02:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/11/04	04:01:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/04	04:01:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/04	04:01:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2017/11/04	04:01:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/11/04	04:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/11/04	04:30:00.5	XRT_Custom_430_OG [0x1ae]					
2017/11/04	04:31:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/11/04	05:40:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/04	05:40:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/04	05:40:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2017/11/04	05:40:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/11/04	05:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/11/04	06:07:30.0	XRT_Custom_430_OG [0x1ae]					
2017/11/04	06:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/11/04	06:16:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/04	06:16:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/04	06:16:58.0	XRT_FOCUS_POSITION_403_OG [0x193]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2017/11/04	06:17:00.0	AOCS_Ore-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2017/11/04	06:17:18.0	XRT_FLD_DIS_425_OG [0x1a9]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2017/11/04	06:19:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2017/11/04	06:19:56.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2017/11/04	06:19:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a	
2017/11/04	06:20:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/11/04	06:26:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/04	06:26:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/04	06:26:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2017/11/04	06:27:00.0	AOCS_Ore-point_Start_5_OG [0x09b]					
			AOCU_NM	5	02-76	01 00 00 00 00	
2017/11/04	06:27:18.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2017/11/04	06:27:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2017/11/04	06:27:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2017/11/04	06:27:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2017/11/04	06:27:26.0	XRT_FLD_RESET_433_OG [0x1b1]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2017/11/04	06:29:56.0	XRT_QT_PROG_SET_404_OG [0x194]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 06	
2017/11/04	06:29:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2017/11/04	06:30:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/11/04	07:20:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/04	07:20:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/04	07:20:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	

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2017/11/04	07:20:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/11/04	07:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/11/04	07:44:30.0	XRT_Custom_430_OG [0x1ae]			
2017/11/04	07:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/11/04	09:00:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/04	09:00:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/04	09:00:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/11/04	09:00:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/11/04	09:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/11/04	10:42:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/04	10:42:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/04	10:42:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2017/11/04	10:42:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/11/04	10:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/11/04	11:14:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00