

# XRT Timeline to be uploaded on 2017/11/04

Period: 2017/11/04 11:04:00 - 2017/11/09 10:45:00

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## Normal mode

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### XOB #1BCC: CME watch - 4x4 - AEC 1/2 - Al-poly - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 360s cad (G-band/Leak firs

Term	Pointing (x, y)	Comment
11/04 11:17:00 - 11/04 15:59:54	Track ( -267.6, 37.0) @ 11/04 11:14:00	# OP start + 10min, EIS QS network/CH BP
11/04 16:27:30 - 11/04 17:39:30	Fixed ( -950.0, 150.0)	EIS spectral atlas
11/05 13:03:00 - 11/05 17:50:54	Track ( -30.1, 36.3) @ 11/05 13:00:00	EIS QS network/CH BP
11/06 18:20:00 - 11/06 22:04:00	Track ( 0.5, 0.5) @ 11/06 18:17:00	EIS sensitivity monitoring

**PROG= 17 Inf.-time(s)**

Subr= 1 1-time(s) 2.0sec	Seqn= 30 1-time(s) 2.0sec	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
		Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec

**Subr= 2 20-time(s) 360.0sec**

Seqn= 7 1-time(s) 2.0sec	Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	1	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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### XOB #1BA8: Synoptic 7 Filter w/ Al-mesh(24/256/2897), Al-poly(45/512/4096), Thin-Be(181/2048/11571) - Thick-Be(65536), Al-poly+Ti-poly(256/5795), Med-A

Term	Pointing (x, y)	Comment
11/04 18:06:30 - 11/04 18:13:24	Fixed ( 0.0, 0.0)	synoptic, shifted 3.5 min

**PROG= 05 1-time(s)**

Subr= 1 1-time(s) 2.0sec	Seqn= 5 1-time(s) 2.0sec	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

**Seqn= 1 1-time(s) 2.0sec**

Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

**Seqn= 99 1-time(s) 2.0sec**

Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

**Seqn= 67 1-time(s) 2.0sec**

thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

**Seqn= 23 1-time(s) 4.0sec**

Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

**Subr= 2 1-time(s) 2.0sec**

Seqn= 46 2-time(s) 2.0sec	Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

**Seqn= 65 2-time(s) 2.0sec**

med-Al/Open	med-Al/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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### XOB #1BA3: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, Al/Poly context, with G-band (1ms/1ms

Term	Pointing (x, y)	Comment
11/04 18:16:30 - 11/05 05:48:54	Track ( -820.7, 79.1) @ 11/04 18:13:30	Plage (AR 12687 ?, HOP 307)
11/05 06:02:00 - 11/05 12:59:54	Track ( -758.7, 73.5) @ 11/05 05:59:00	Plage (AR 12687 ?, HOP 307)

**PROG= 09 Inf.-time(s)**

Subr= 1 1-time(s) 2.0sec	Seqn= 92 1-time(s) 2.0sec	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
		Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

**Seqn= 42 3-time(s) 2.0sec**

Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

**Seqn= 32 40-time(s) 90.0sec**

thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	34.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec

Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	34.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1BB4: Synoptic Q95 2x2 - Al/mesh(24/256/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(45/512/4096) + T**

Term	Pointing (x, y)	Comment
11/05 05:52:00 - 11/05 05:58:54	Fixed ( 0.0, 0.0)	synoptic, shifted -11.0 min
11/05 17:54:00 - 11/05 18:00:54	Fixed ( 0.0, 0.0)	synoptic, shifted -9.0 min
11/06 06:03:00 - 11/06 06:09:54	Fixed ( 0.0, 0.0)	synoptic
11/06 18:10:00 - 11/06 18:16:54	Fixed ( 0.0, 0.0)	synoptic, shifted 7.0 min
11/07 06:28:00 - 11/07 06:34:54	Fixed ( 0.0, 0.0)	synoptic, shifted 25.0 min

**PROG= 10 1-time(s)**

- Subr= 1 1-time(s) 2.0sec
  - Seqn= 5 1-time(s) 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
  - Seqn= 1 1-time(s) 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Seqn= 99 1-time(s) 2.0sec
    - Al-poly/Open Al-poly/Open close Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Al-poly/Open Al-poly/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Al-poly/Open Al-poly/thick-Al close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Seqn= 33 1-time(s) 2.0sec
    - thin-Be/Open thin-Be/Open close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - thin-Be/Open thin-Be/Open close Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - thin-Be/Open thin-Be/Open close Safe Norm 22.6s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Seqn= 23 1-time(s) 2.0sec
    - Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
    - Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	
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**XOB #1B89: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with**

Term	Pointing (x, y)	Comment
11/05 18:04:00 - 11/06 05:59:54	Track ( -685.0, 68.4) @ 11/05 18:01:00	Plage (AR 12687 ?, HOP 307)
11/06 06:17:00 - 11/06 18:06:54	Track ( -601.0, 64.0) @ 11/06 06:10:00	Plage (AR 12687 ?, HOP 307)
11/07 04:39:00 - 11/07 06:24:54	Track ( -425.5, 58.1) @ 11/07 04:30:00	Plage (AR 12687 ?, HOP 307)
11/07 06:38:00 - 11/07 10:50:00	Track ( -408.0, 57.7) @ 11/07 06:35:00	Plage (AR 12687 ?, HOP 307)

**PROG= 01 Inf.-time(s)**

- Subr= 1 1-time(s) 2.0sec
  - Seqn= 92 1-time(s) 2.0sec
    - Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
    - Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
- Subr= 2 5-time(s) 2.0sec
  - Seqn= 75 1-time(s) 2.0sec
    - Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec
    - Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
    - thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec
    - thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
  - Seqn= 96 4-time(s) 60.0sec
    - Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec
    - thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec
    - Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 2.0sec
    - thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 2.0sec
    - Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec
    - thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	
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**XOB #1BA1: HOP336 1-filter - Al/poly -384x384, 512ms and 4s, 60s-cadence, G-band - 384x384 1ms**

Term	Pointing (x, y)	Comment
11/06 22:33:00 - 11/07 01:29:54	Track ( -21.0, 564.0) @ 11/06 22:30:00	HOP 336, N
11/07 01:33:00 - 11/07 04:08:00	Track ( -21.0, -786.0) @ 11/07 01:30:00	HOP 336, S

**PROG= 04 Inf.-time(s)**

- Subr= 1 1-time(s) 2.0sec
  - Seqn= 16 2-time(s) 2.0sec
    - Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
- Subr= 2 1-time(s) 2.0sec
  - Seqn= 90 1-time(s) 30.0sec
    - Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec
- Subr= 3 15-time(s) 2.0sec
  - Seqn= 68 1-time(s) 60.0sec

Al-poly/Open	Al-poly/Open	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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**Flare mode**

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**XOB #1B8E: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512**

Term	Pointing (x, y)	Comment
11/04 11:17:00 - 11/04 15:59:54	Track ( -267.6, 37.0) @ 11/04 11:14:00	# OP start + 10min, EIS QS network/CH BP
11/04 16:27:30 - 11/04 17:39:30	Fixed ( -950.0, 150.0)	EIS spectral atlas
11/04 18:16:30 - 11/05 05:48:54	Track ( -820.7, 79.1) @ 11/04 18:13:30	Plage (AR 12687 ?, HOP 307)
11/05 06:02:00 - 11/05 12:59:54	Track ( -758.7, 73.5) @ 11/05 05:59:00	Plage (AR 12687 ?, HOP 307)
11/05 13:03:00 - 11/05 17:50:54	Track ( -30.1, 36.3) @ 11/05 13:00:00	EIS QS network/CH BP
11/05 18:04:00 - 11/06 05:59:54	Track ( -685.0, 68.4) @ 11/05 18:01:00	Plage (AR 12687 ?, HOP 307)
11/06 06:17:00 - 11/06 18:06:54	Track ( -601.0, 64.0) @ 11/06 06:10:00	Plage (AR 12687 ?, HOP 307)
11/06 18:20:00 - 11/06 22:04:00	Track ( 0.5, 0.5) @ 11/06 18:17:00	EIS sensitivity monitoring
11/06 22:33:00 - 11/07 01:29:54	Track ( -21.0, 564.0) @ 11/06 22:30:00	HOP 336, N
11/07 01:33:00 - 11/07 04:08:00	Track ( -21.0, -786.0) @ 11/07 01:30:00	HOP 336, S
11/07 04:39:00 - 11/07 06:24:54	Track ( -425.5, 58.1) @ 11/07 04:30:00	Plage (AR 12687 ?, HOP 307)
11/07 06:38:00 - 11/07 10:50:00	Track ( -408.0, 57.7) @ 11/07 06:35:00	Plage (AR 12687 ?, HOP 307)

**PROG= 13 30-time(s)**

<b>Subr= 1 20-time(s) 2.0sec</b>													
└─ <b>Seqn= 11 1-time(s) 2.0sec</b>	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ <b>Seqn=100 1-time(s) 10.0sec</b>	thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
	med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>													
└─ <b>Seqn= 10 1-time(s) 2.0sec</b>	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ <b>Seqn= 11 1-time(s) 2.0sec</b>	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ <b>Seqn= 87 1-time(s) 2.0sec</b>	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

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**Active Region Search**

\* \* \* \* \*

NOT USED

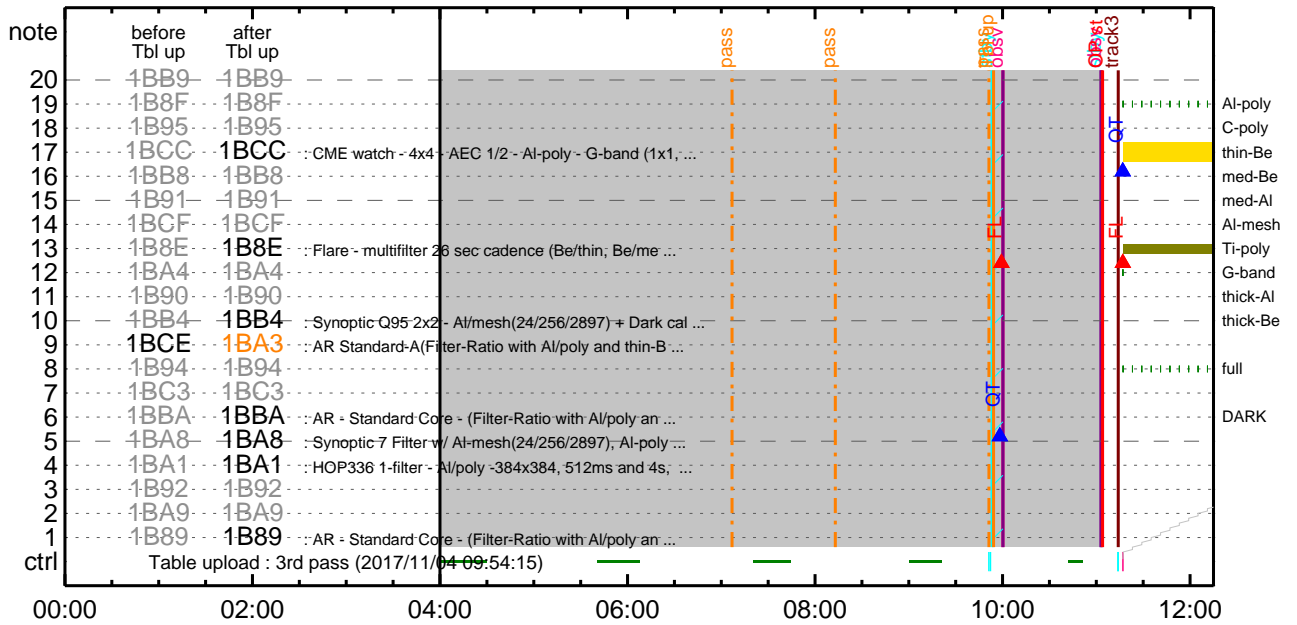
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**Flare Detection**

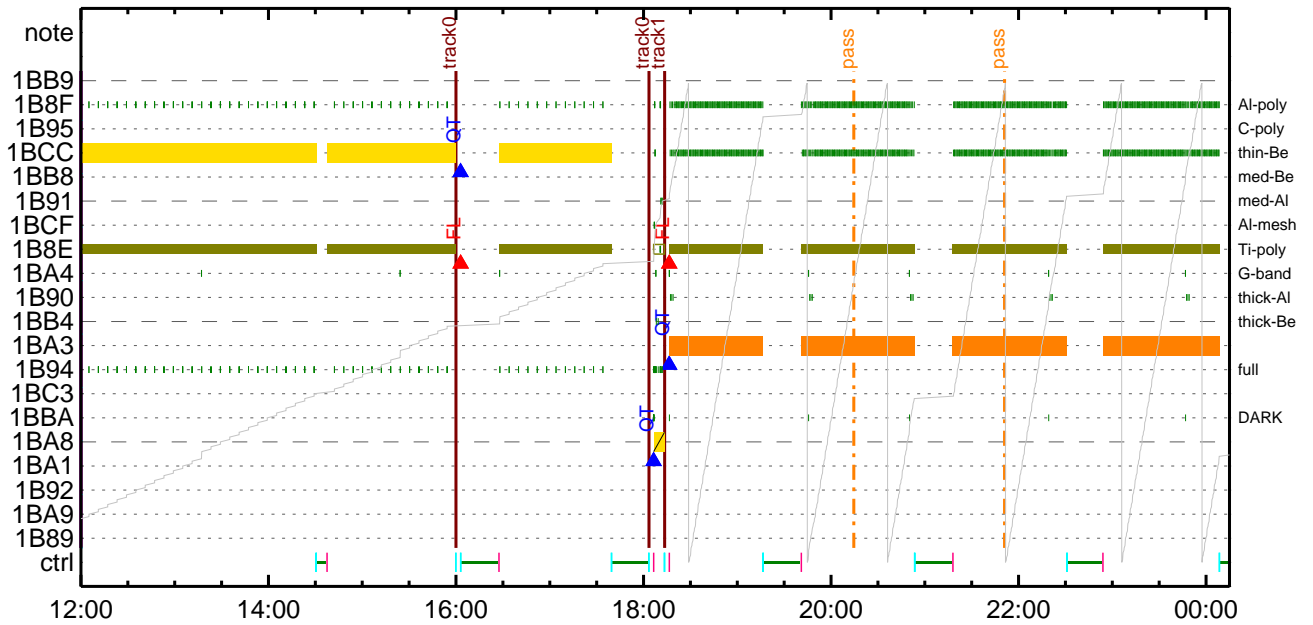
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<b>FLD Patrol</b>												
Term	Pointing (x, y)		Comment									
11/04 18:13:48 - 11/05 05:49:18	Track ( -820.7, 79.1) @ 11/04 18:13:30		Plage (AR 12687 ?, HOP 307)									
11/05 05:59:18 - 11/05 17:51:18	Track ( -758.7, 73.5) @ 11/05 05:59:00		Plage (AR 12687 ?, HOP 307)									
11/05 18:01:18 - 11/06 06:00:18	Track ( -685.0, 68.4) @ 11/05 18:01:00		Plage (AR 12687 ?, HOP 307)									
11/06 06:14:18 - 11/06 18:07:18	Track ( -601.0, 64.0) @ 11/06 06:10:00		Plage (AR 12687 ?, HOP 307)									
11/06 18:17:18 - 11/07 06:25:18	Track ( 0.5, 0.5) @ 11/06 18:17:00		EIS sensitivity monitoring									
11/07 06:35:18 - 11/09 10:45:00	Track ( -408.0, 57.7) @ 11/07 06:35:00		Plage (AR 12687 ?, HOP 307)									
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

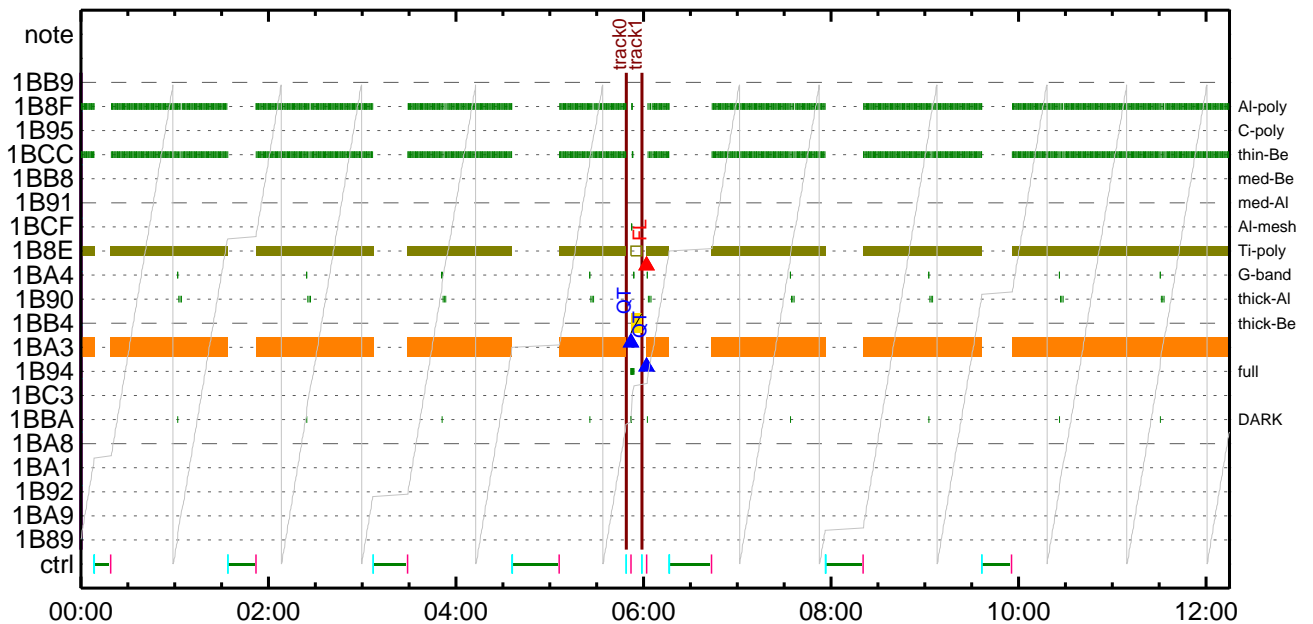
### CMDI #0110 2017/11/04



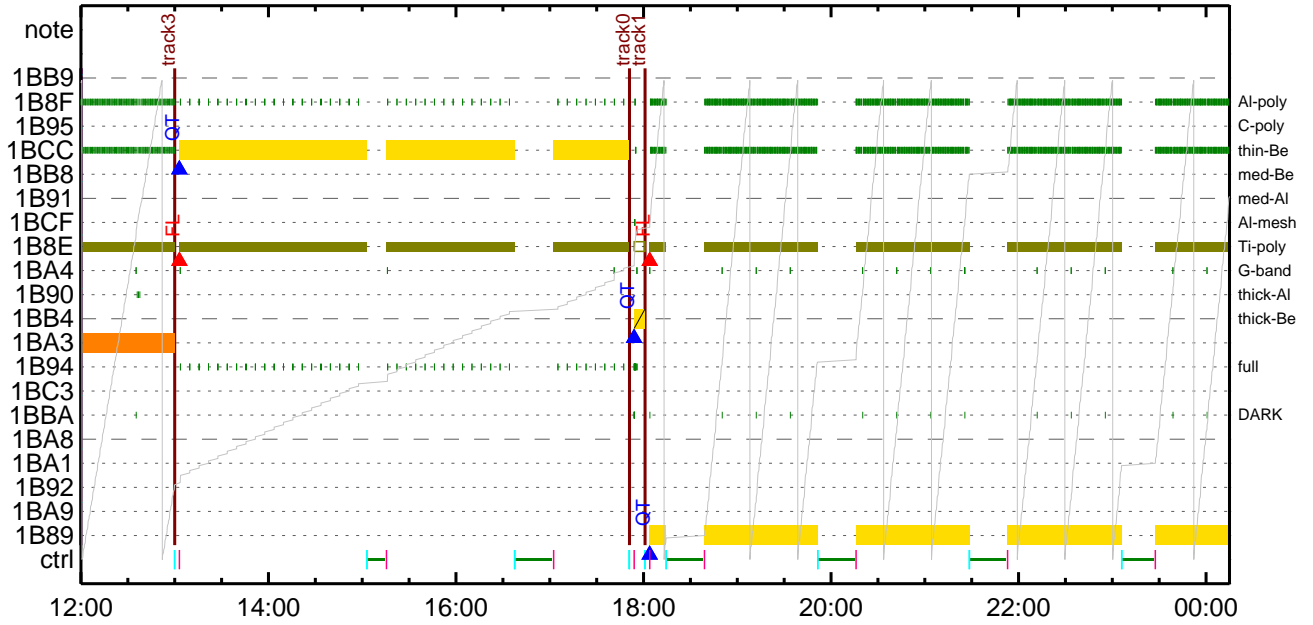
### CMDI #0110 2017/11/04



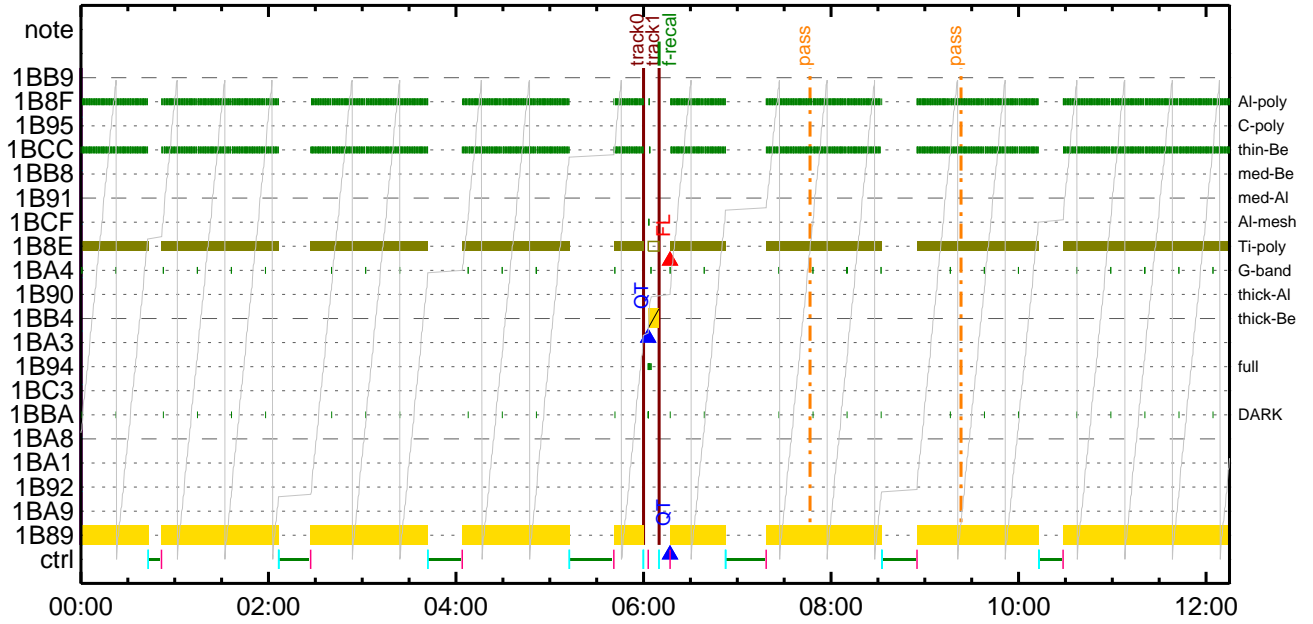
### CMDI #0110 2017/11/05



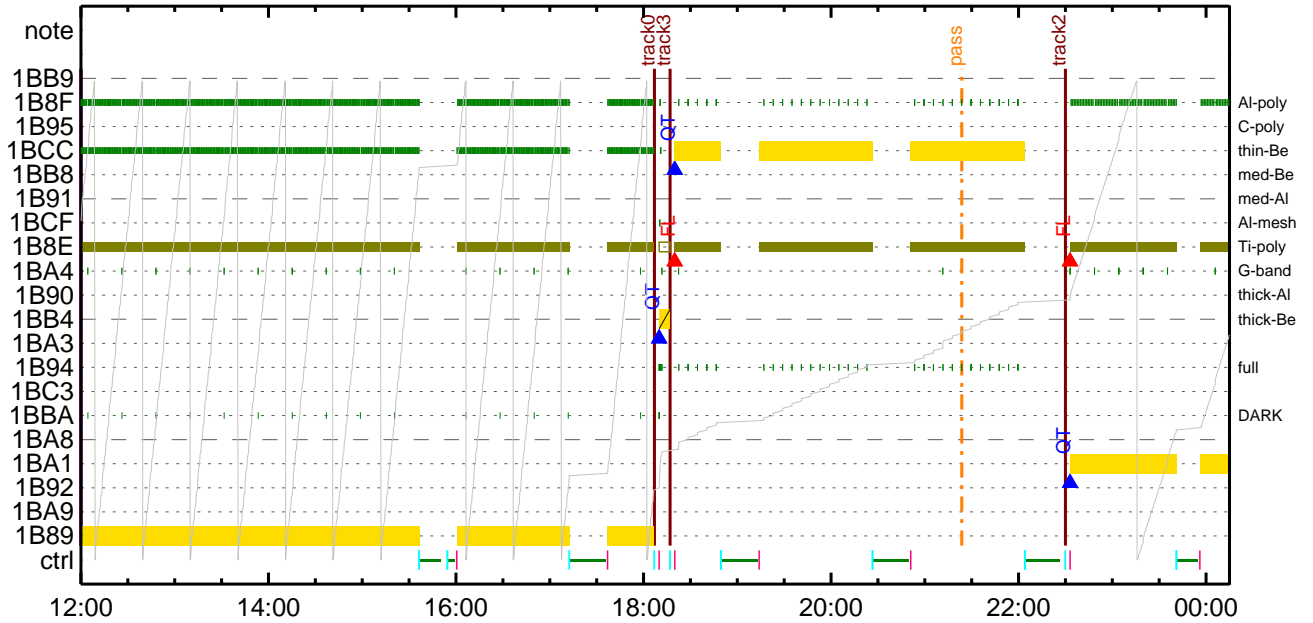
CMDI #0110 2017/11/05



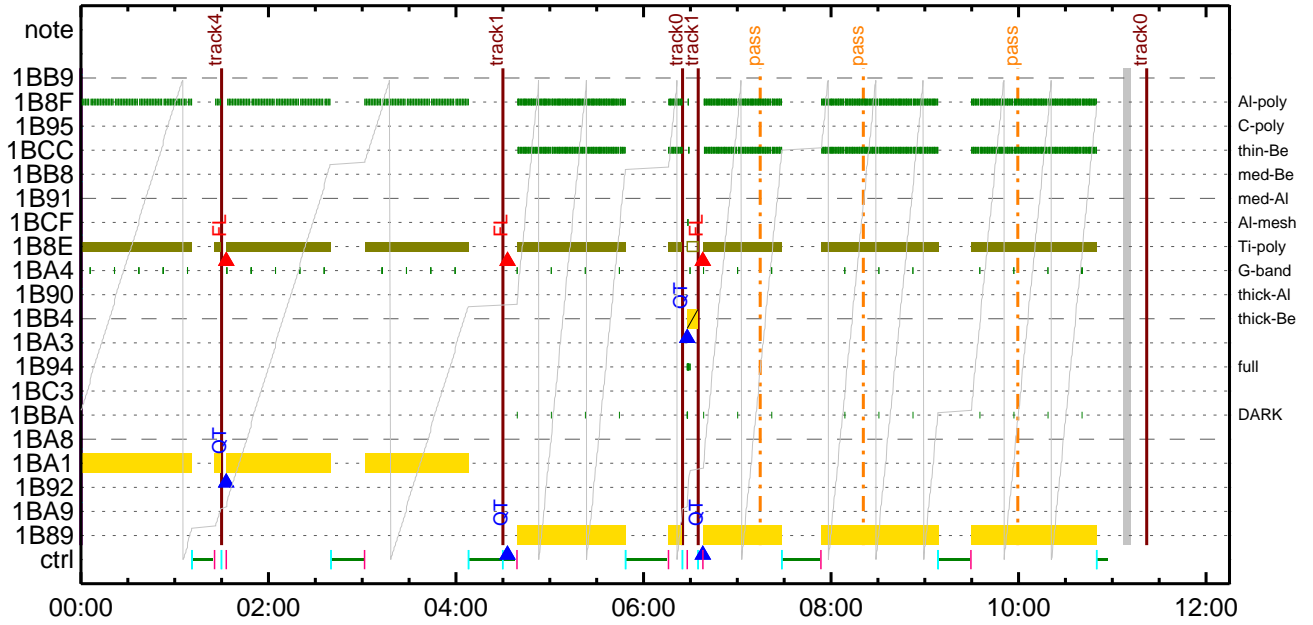
CMDI #0110 2017/11/06



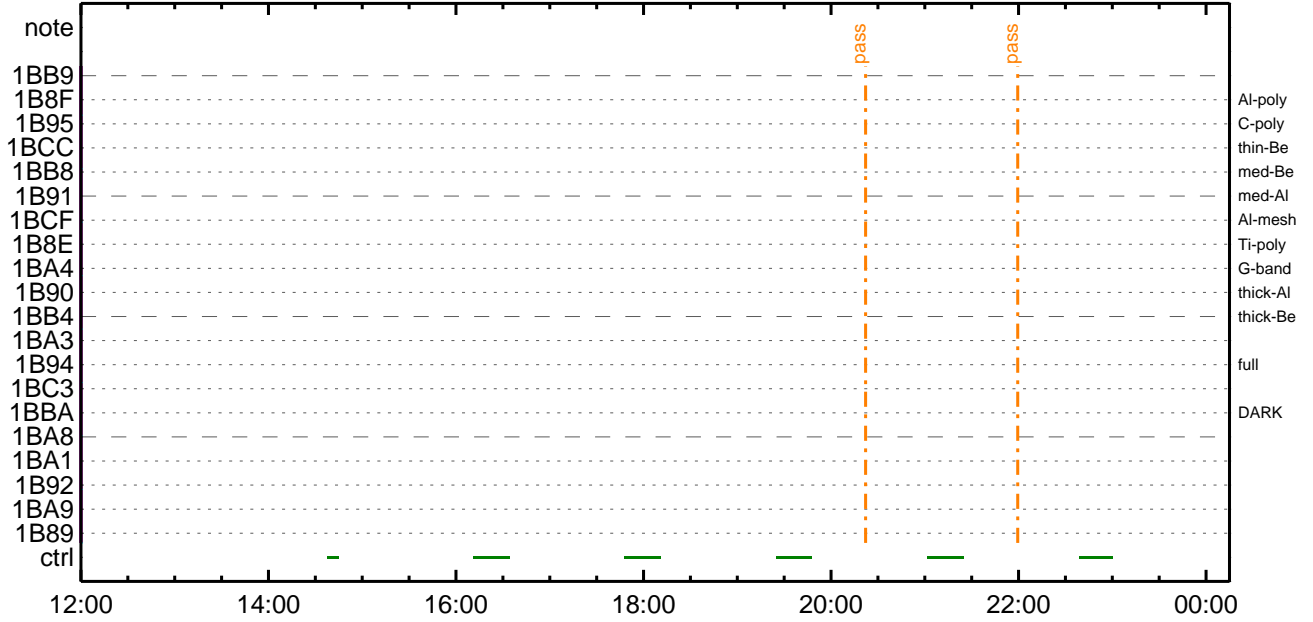
CMDI #0110 2017/11/06



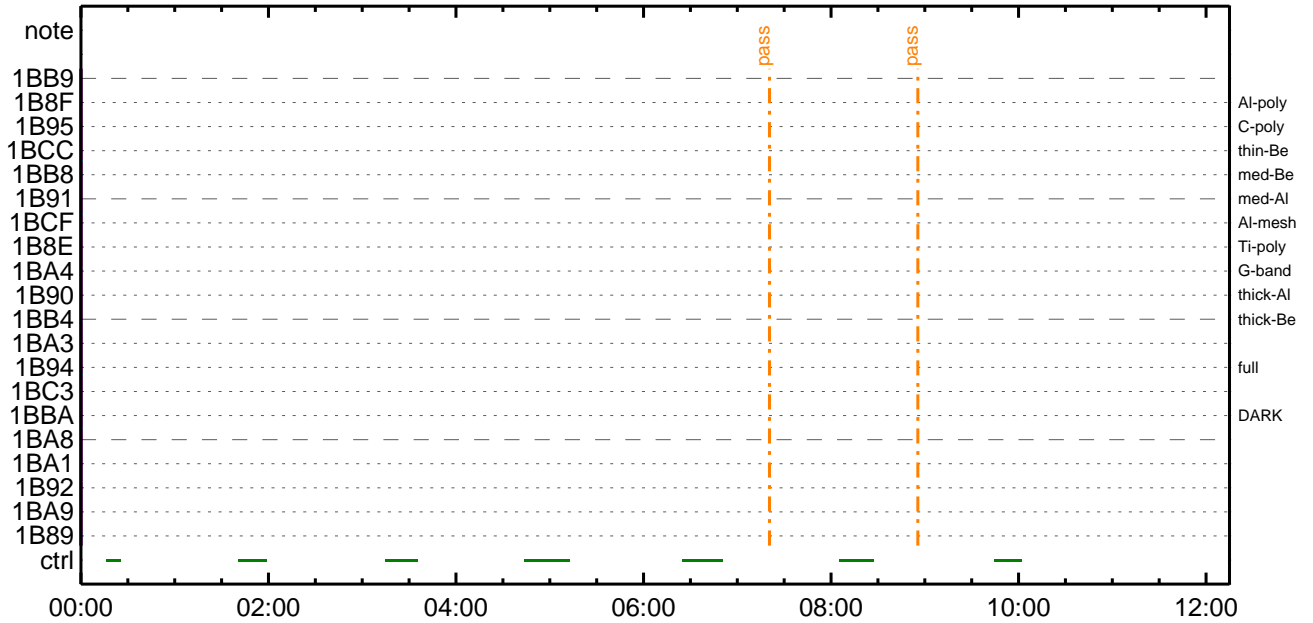
CMDI #0110 2017/11/07



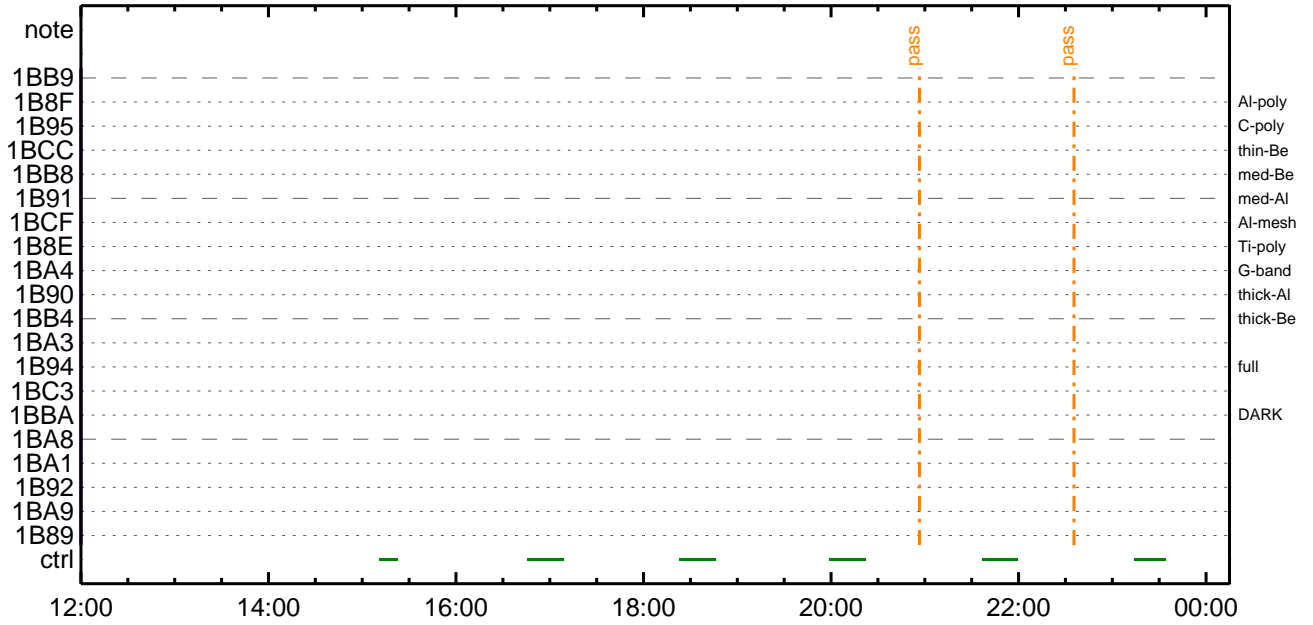
CMDI #0110 2017/11/07



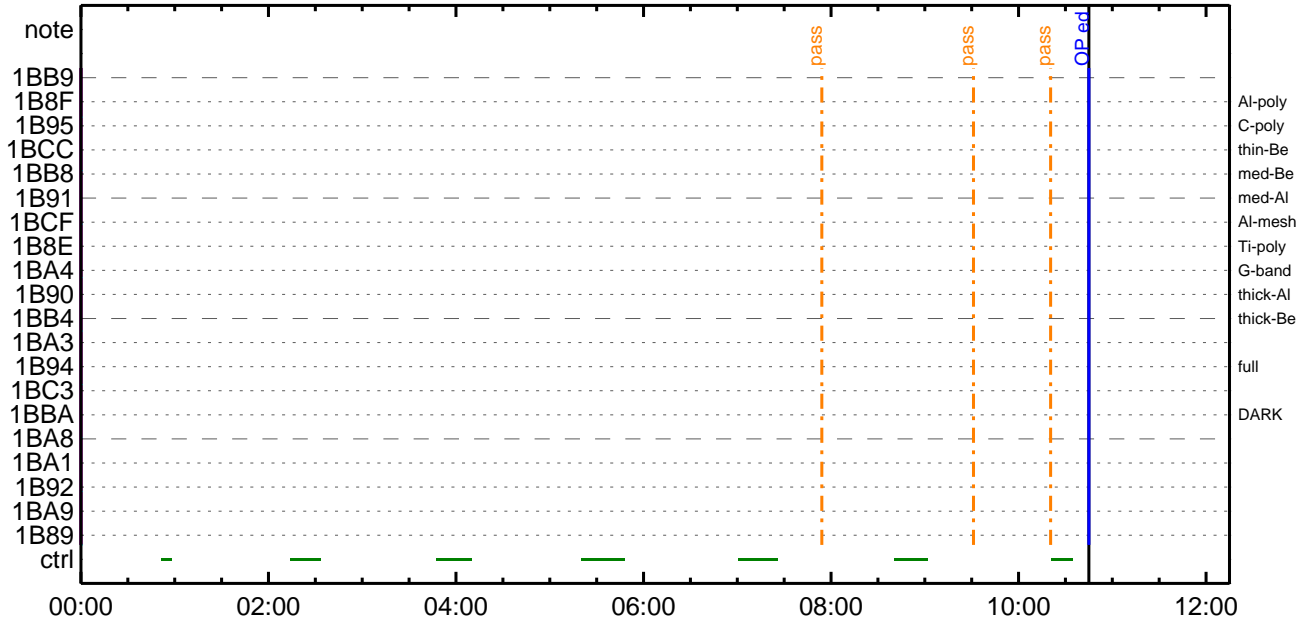
CMDI #0110 2017/11/08



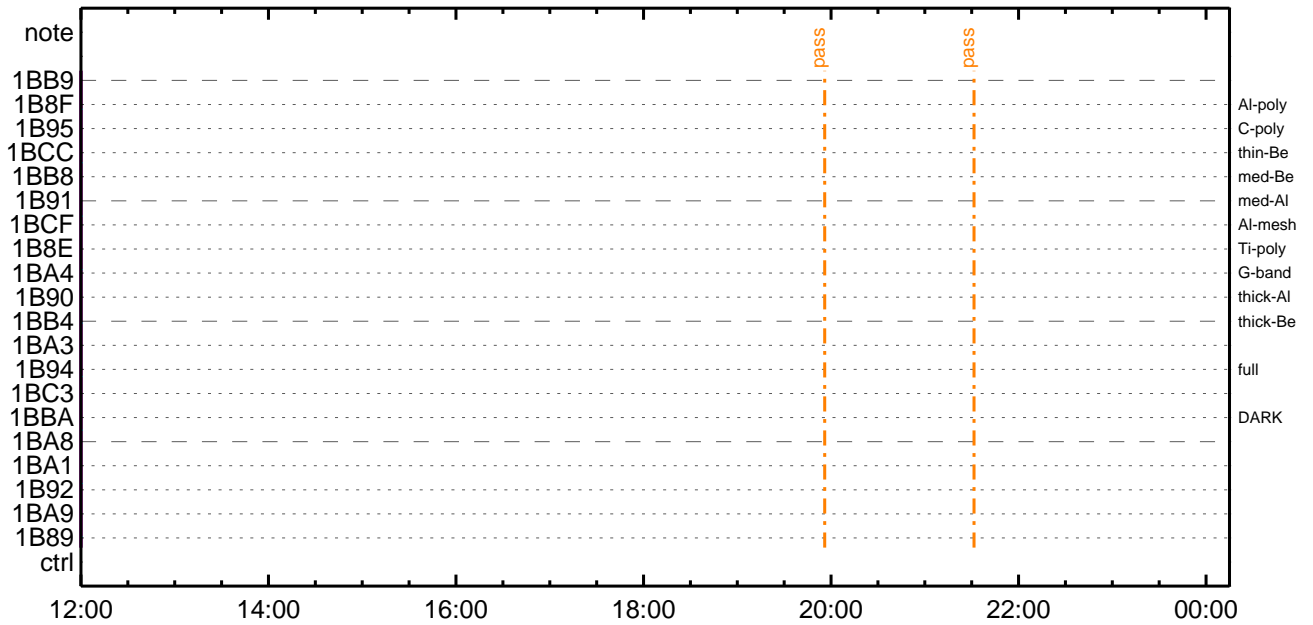
CMDI #0110 2017/11/08



CMDI #0110 2017/11/09



CMDI #0110 2017/11/09



(a) Spacecraft Operation Procedure (real-commands)

```

main-963 2017-11-04 12:22:29 194 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY~¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YFYÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èò¿òÀò•µ°Æ»Í×ÁÇòÍYçYÁY×YÍ;¼YÉ;ÈÈèµ•ííÉ;ÈòÈ¼°ÇÒò•ò¿¼í¹çòÍ;çÀ®, ùò¹òÈòòÇÁ+¿®ò•òÈòòò³òÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGYí;¼YÉ;|YÁYÓY×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGYí;¼YÉ;ã
0019 . S. OP op-963:OP
0020 ( )
0021 . S. OG og-963:OG
0022 ( )
0023 C.
0024 . C. ;ãNMOG&OPÍ°èYÁYóY×;ã
0025 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. YÀYÓY×½ªí»òð³ÍÇ§
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGòí¼È¹ç•è²ÍOKòð³ÍÇ§
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. YÀYÓY×½ªí»òð³ÍÇ§
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGòí¼È¹ç•è²ÍOKòð³ÍÇ§
0062 C.
0063 C. NMOG(0x210000-0x210FFF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. YÀYÓY×½ªí»òð³ÍÇ§
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG, RAM ID=OPòí¼È¹ç•è²ÍOKòð³ÍÇ§
0081 C.
0082 . C. ***** òÈ²¼òí¼Á´¶¼òÈÈ-òòÁ+¿®(¼áµ-YÀYÓY×½ªí»òð³ÍÇ§òðÁÓÃæç¾ªòµò¼í¹çòçòâ) *****
0083 C. DHUYã;¼YÉ;È¼Y½, Yí;¼YÉ;ÈòòÍáò¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE |§ OPOG UPLOADò-Á+¿®NGòí¼í¹ç;ç°È²¼òí¼TI-CMDÁ+¿®òí¼Á¹Ôò•òÈòòò³òÈ;f

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0096 C.                0p0z;çSET0EDUMP0İÆ±°iYÑY¹0ç¹00|0³0E;E
0097 C.
0098 . C. TIY³YF¥ÖYÉ00dÄDİ¿(UT)
0099 +. TI 2017-11-04 10:59:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0102 C.
0103 +. TI 2017-11-04 10:59:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0106 C.
0107 +. TI 2017-11-04 10:59:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0110 C.
0111 +. TI 2017-11-04 11:03:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0114 C.
0115 C. °E²¼0İÄè%îíÑ0İYÁY§YÄY-¹àİÜ
0116 C.                çç[HK1_TI_CMD_ENA/DIS]                EQ        ENA
0117 C.                çç[HK1_TI_CMD_NUM]                EQ        4
0118 C.                çç[HK1_NEXT_EXEC_PIM]                EQ        DHU
0119 C.                çç[HK1_NEXT_EXEC_DC]                EQ        0xB3
0120 C.
0121 . C. *****
0122 C. TIİİ°èYÄYÖY×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.                çç[HK1_DMP_TOP_ADRS_1]                EQ        07
0129 C.                çç[HK1_DMP_TOP_ADRS_0]                EQ        2B
0130 C.                çç[HK1_DMP_BLOCK_NUM]                EQ        3
0131 C.                çç[HK1_DMP_REPEAT_NUM]                EQ        0
0132 C.                çç[HK1_DMA_DMP_PIM]                EQ        DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.                çç[HK1_PKT_FORM_NO]                EQ        7
0136 C.                çç[HK1_PKT_GEN_TIME]                EQ        0.25 s
0137 C.                çç[HK1_S_TLM_BIT_RATE]                EQ        32k
0138 C.                çç[HK1_X_TLM_BIT_RATE]                EQ        4M
0139 C.                çç[HK1_DMP_CHK_FLG]                EQ        EXEC
0140 C.
0141 . C. YÄYÖY×¼ªİ»0d³İÇ§
0142 C.                çç[HK1_DMP_CHK_FLG]                EQ        NON
0143 C.
0144 . C. RAM ID=TI_TBL0İ%È¹ç•è²İOK0d³İÇ§
0145 C.
0146 . C. DHUYâ;¼YÉ;È¼Y¼,¥i;¼YÈ;È0dİá0¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.                çç[HK1_PKT_FORM_NO]                EQ        2
0150 C.                çç[HK1_PKT_GEN_TIME]                EQ        0.5S
0151 C.                çç[HK1_S_TLM_BIT_RATE]                EQ        32K
0152 C.                çç[HK1_X_TLM_BIT_RATE]                EQ        4M
0153 C.
0154 C.
0155 C. ***** XRT START *****
0156 C. Execute, after the success of OP upload.
0157 +. TI 2017-11-04 11:03:00.0
0158 DC 07-F0 MDP_XRT_MODE_STBY
0159 BC (c3)
0160 . C.                [ ] [HK1_TI_CMD_NUM]                EQ        1COUNTUP
0161 C.
0162 C. ***** XRT END *****
0163 . C. Stop EIS observation and temporarily disable EIS mode changes
0164 C.
0165 C.
0166 C. ***** Start EIS operation (TI set) *****
0167 C. Execute, after the success of OP upload.
0168 C. Set EIS TI-commands
0169 +. TI 2017-11-04 11:03:30.0
0170 DC 07-FC EIS_MODE_MANU
0171 BC (21 02)
0172 +. TI 2017-11-04 11:03:40.0
0173 DC 07-FC EIS_MODE_CHG_DIS
0174 BC (22)
0175 . C.                [ ] [HK1_TI_CMD_NUM]                EQ        2 COUNTUP
0176 C. ***** End EIS operation (TI set) *****
0177 C.
0178 C.
0179 C.
0180 . C. ***** MDP ´úÄî0İ»ö¼Y0EÄ00¹0E DCBC•x²è *****
0181 C. (%á°İYÖYÄYÉYF¥YÄYçYè0E¼00¼Ä»Ü0¹0è)
0182 . S. DC-BC dcbc-402:DCBC
0183 (MDP_known_event)
0184 C.
0185 C.
0186 . C. ***** YD¥¹•İ Daily±¿İÑ0E'00¹0E DCBC•x²è *****
0187 . S. DC-BC dcbc-153:DCBC
0188 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0189 C.
0190 C.
0191 . C. ;ãLOS¥ÄY§YÄY-¼Ä»Ü;ä
0192 C.
0193 . C. ***** LOS *****

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```
0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 +. DC 07-F0 MDP_XRT_CTRL_MANU
0104 BC (c1)
0105 + DC 07-F0 MDP_XRT_MODE_STBY
0106 BC (c3)
0107 . C. ----- Success Verify ?          OK / NG ____
0108 C.
0109 C. XRT Obs. Table Upload
0110 . S. RAM ram-291:MDP_OBS_X
0111 ( )
0112 C.
0113 +. DC 07-F0 MDP_DUMP_XRTTBL
0114 BC (84 07 00 00 00 3a d4)
0115 . C. ----- Comparison Check ?          OK / ERR ____
0116 C.
0117 C.
0118 +. DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 01 b1 b1 04 04)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 02 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 03 b1 b1 08 08)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 04 b1 b1 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 05 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 06 85 83 06 06)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 07 80 80 08 08)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 08 80 80 20 20)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 09 80 80 20 08)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0a 80 80 08 20)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0b 85 83 08 08)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 0f 80 80 06 06)
0142 + DC 07-F0 MDP_XRT_ROI_SET
0143 BC (cd 10 80 80 08 08)
0144 + DC 07-F0 MDP_XRT_FLD_ENA
0145 BC (d8)
0146 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0147 BC (c8)
0148 + DC 07-F0 MDP_XRT_ARS_DIS
0149 BC (d5)
0150 +. DC 07-F0 MDP_XRT_AEC_RESET
0151 BC (d0)
0152 +. DC 07-F0 MDP_XRT_FLD_RESET
0153 BC (da)
0154 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0155 BC (c4 06)
0156 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0157 BC (c5 0d)
0158 . C. ----- Success Verify ?          OK / NG ____
0159 C.
0160 C.
0161 . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0162 C.
0163 +. DC 07-F0 MDP_XRT_MODE_OBSV
0164 BC (c2)
0165 +. TI 2017-11-04 11:03:02.0
0166 DC 07-F0 MDP_XRT_MODE_OBSV
0167 BC (c2)
0168 . C. ----- Success Verify ?          OK / NG ____
0169 C.
0170 C. ***** XRT END *****
0171 C.
0172 . C. ***** MDP `ûÃîáí»ò¼YõÊÀð¹²³DCBC•x²è *****
0173 C. (¼áºîYóYÁYÈYËYÉYâYçYë¾¼α¼À»Ûα¹²è)
0174 . S. DC-BC dcbc-402:DCBC
0175 (MDP_known_event)
0176 C.
0177 C.
0178 . C. ***** YDY!•Ï Daily±¿ÍÑ³È´ø¹²³DCBC•x²è *****
0179 . S. DC-BC dcbc-153:DCBC
0180 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0181 C.
0182 C.
0183 . C. ¡ãLOSŸÁY$ŸÏY~¼À»Û;ã
0184 C.
0185 . C. ***** LOS *****
0186 C.
```

Nov 04, 17 12:22

XRT\_OGLIST\_0110.chk

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\*\*\* OP Sequence for XRT \*\*\*

2017/11/04	11:13:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/11/04	11:13:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/11/04	11:13:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2017/11/04	11:14:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	03 00 00 00 00		
2017/11/04	11:14:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2017/11/04	11:14:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2017/11/04	11:14:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2017/11/04	11:14:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2017/11/04	11:14:26.0	XRT_FLD_RESET_433_OG [0x1b1]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2017/11/04	11:16:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11		
2017/11/04	11:16:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2017/11/04	11:17:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/11/04	14:30:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/11/04	14:30:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/11/04	14:30:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2017/11/04	14:30:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2017/11/04	14:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2017/11/04	14:36:30.0	XRT_Custom_430_OG [0x1ae]					
2017/11/04	14:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/11/04	15:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/11/04	15:59:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/11/04	15:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2017/11/04	16:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 f2 a8 54 72		
2017/11/04	16:00:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2017/11/04	16:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2017/11/04	16:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2017/11/04	16:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2017/11/04	16:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2017/11/04	16:02:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11		
2017/11/04	16:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2017/11/04	16:03:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/11/04	16:03:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/11/04	16:03:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2017/11/04	16:03:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2017/11/04	16:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2017/11/04	16:26:30.0	XRT_Custom_430_OG [0x1ae]					
2017/11/04	16:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2017/11/04	17:39:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/11/04	17:39:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/11/04	17:39:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2017/11/04	17:39:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2017/11/04	17:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2017/11/04	18:03:24.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/11/04	18:03:26.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2017/11/04	18:03:28.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2017/11/04	18:03:30.0	AOCS_ORe-point_Start_3_OG [0x099]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2017/11/04	18:03:48.0	XRT_FLD_DIS_425_OG [0x1a9]					

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2017/11/04	18:06:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2017/11/04	18:06:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2017/11/04	18:06:28.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2017/11/04	18:06:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/11/04	18:13:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/04	18:13:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/04	18:13:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2017/11/04	18:13:30.0	AOCS_OrE-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	01 00 00 00 00
2017/11/04	18:13:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2017/11/04	18:13:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2017/11/04	18:13:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2017/11/04	18:13:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2017/11/04	18:13:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/11/04	18:16:26.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2017/11/04	18:16:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2017/11/04	18:16:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/11/04	19:16:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/04	19:16:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/04	19:16:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/11/04	19:16:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/11/04	19:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/11/04	19:40:00.0	XRT_Custom_430_OG [0x1ae]				
2017/11/04	19:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/11/04	20:53:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/04	20:53:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/04	20:53:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/11/04	20:53:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/11/04	20:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/11/04	21:17:00.0	XRT_Custom_430_OG [0x1ae]				
2017/11/04	21:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/11/04	22:31:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/04	22:31:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/04	22:31:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/11/04	22:31:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/11/04	22:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/11/04	22:53:00.0	XRT_Custom_430_OG [0x1ae]				
2017/11/04	22:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/11/05	00:08:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/05	00:08:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/05	00:08:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/11/05	00:08:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/11/05	00:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/11/05	00:18:00.0	XRT_Custom_430_OG [0x1ae]				
2017/11/05	00:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/11/05	01:34:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/05	01:34:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/05	01:34:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/11/05	01:34:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8



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2017/11/05	01:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/11/05	01:51:00.0	XRT_Custom_430_OG [0x1ae]							
2017/11/05	01:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/11/05	03:07:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/05	03:07:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/05	03:07:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/11/05	03:07:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/11/05	03:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/11/05	03:28:00.0	XRT_Custom_430_OG [0x1ae]							
2017/11/05	03:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/11/05	04:36:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/05	04:36:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/05	04:36:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/11/05	04:36:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/11/05	04:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/11/05	05:05:00.0	XRT_Custom_430_OG [0x1ae]							
2017/11/05	05:06:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/11/05	05:48:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/05	05:48:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/05	05:48:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2017/11/05	05:49:00.0	AOCS_OrE-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2017/11/05	05:49:18.0	XRT_FLD_DIS_425_OG [0x1a9]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2017/11/05	05:51:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2017/11/05	05:51:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2017/11/05	05:51:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2017/11/05	05:52:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/11/05	05:58:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/05	05:58:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/05	05:58:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2017/11/05	05:59:00.0	AOCS_OrE-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2017/11/05	05:59:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2017/11/05	05:59:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2017/11/05	05:59:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2017/11/05	05:59:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2017/11/05	05:59:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/11/05	06:01:56.0	XRT_QT_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09				
2017/11/05	06:01:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2017/11/05	06:02:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/11/05	06:16:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/05	06:16:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/05	06:16:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/11/05	06:16:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/11/05	06:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/11/05	06:42:30.0	XRT_Custom_430_OG [0x1ae]							
2017/11/05	06:43:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/11/05	07:56:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/05	07:56:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/05	07:56:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/11/05	07:56:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]							

2017/11/05	07:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/11/05	08:19:30.0	XRT_Custom_430_OG [0x1ae]					
2017/11/05	08:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/11/05	09:36:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/05	09:36:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/05	09:36:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2017/11/05	09:36:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/11/05	09:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/11/05	09:54:30.0	XRT_Custom_430_OG [0x1ae]					
2017/11/05	09:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/11/05	12:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/05	12:59:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/05	12:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2017/11/05	13:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]					
			AOCU_NM	5	02-76	03 00 00 00 00	
2017/11/05	13:00:18.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2017/11/05	13:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2017/11/05	13:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2017/11/05	13:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2017/11/05	13:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2017/11/05	13:02:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2017/11/05	13:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2017/11/05	13:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/11/05	15:03:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/05	15:03:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/05	15:03:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2017/11/05	15:03:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/11/05	15:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/11/05	15:14:30.0	XRT_Custom_430_OG [0x1ae]					
2017/11/05	15:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/11/05	16:37:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/05	16:37:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/05	16:37:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2017/11/05	16:37:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/11/05	16:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/11/05	17:01:30.0	XRT_Custom_430_OG [0x1ae]					
2017/11/05	17:02:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/11/05	17:50:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/05	17:50:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/05	17:50:58.0	XRT_FOCUS_POSITION_403_OG [0x193]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2017/11/05	17:51:00.0	AOCS_ORe-point_Start_3_OG [0x099]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2017/11/05	17:51:18.0	XRT_FLD_DIS_425_OG [0x1a9]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2017/11/05	17:53:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2017/11/05	17:53:56.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2017/11/05	17:53:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a	
2017/11/05	17:54:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/11/05	18:00:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/05	18:00:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/05	18:00:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	

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2017/11/05	18:01:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	01	00	00	00	00
2017/11/05	18:01:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2017/11/05	18:01:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2017/11/05	18:01:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2017/11/05	18:01:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2017/11/05	18:01:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/11/05	18:03:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	01			
2017/11/05	18:03:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2017/11/05	18:04:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/11/05	18:14:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/05	18:14:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/05	18:14:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/11/05	18:14:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/11/05	18:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/11/05	18:38:01.0	XRT_Custom_430_OG [0x1ae]							
2017/11/05	18:39:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/11/05	19:51:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/05	19:51:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/05	19:51:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/11/05	19:51:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/11/05	19:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/11/05	20:15:00.0	XRT_Custom_430_OG [0x1ae]							
2017/11/05	20:16:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/11/05	21:28:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/05	21:28:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/05	21:28:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/11/05	21:28:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/11/05	21:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/11/05	21:52:00.0	XRT_Custom_430_OG [0x1ae]							
2017/11/05	21:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/11/05	23:06:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/05	23:06:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/05	23:06:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/11/05	23:06:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/11/05	23:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/11/05	23:26:30.0	XRT_Custom_430_OG [0x1ae]							
2017/11/05	23:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/11/06	00:43:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/06	00:43:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/06	00:43:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/11/06	00:43:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/11/06	00:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/11/06	00:50:30.0	XRT_Custom_430_OG [0x1ae]							
2017/11/06	00:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/11/06	02:06:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/06	02:06:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/06	02:06:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/11/06	02:06:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/11/06	02:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				

2017/11/06	02:26:00.0	XRT_Custom_430_OG [0x1ae]						
2017/11/06	02:27:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2017/11/06	03:42:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/11/06	03:42:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/11/06	03:42:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2017/11/06	03:42:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2017/11/06	03:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2017/11/06	04:03:00.0	XRT_Custom_430_OG [0x1ae]						
2017/11/06	04:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2017/11/06	05:12:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/11/06	05:12:32.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/11/06	05:12:34.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2017/11/06	05:12:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2017/11/06	05:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2017/11/06	05:40:00.0	XRT_Custom_430_OG [0x1ae]						
2017/11/06	05:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2017/11/06	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/11/06	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/11/06	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2017/11/06	06:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2017/11/06	06:00:18.0	XRT_FLD_DIS_425_OG [0x1a9]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2017/11/06	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2017/11/06	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2017/11/06	06:02:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a			
2017/11/06	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2017/11/06	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/11/06	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/11/06	06:09:58.0	XRT_FOCUS_RECALIBRATE_416_OG [0x1a0]						
		XRT_FOCUS_RECAL	2	07-F8	78 00			
2017/11/06	06:10:00.0	AOCS_Ore-point_Start_4_OG [0x09a]						
		AOCU_NM	5	02-76	01 00 00 00 00			
2017/11/06	06:13:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2017/11/06	06:14:18.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2017/11/06	06:14:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2017/11/06	06:14:22.0	XRT_AEC_RESET_448_OG [0x1c0]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2017/11/06	06:14:24.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2017/11/06	06:14:26.0	XRT_FLD_RESET_433_OG [0x1b1]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2017/11/06	06:16:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01			
2017/11/06	06:16:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d			
2017/11/06	06:17:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2017/11/06	06:52:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/11/06	06:52:32.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/11/06	06:52:34.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2017/11/06	06:52:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2017/11/06	06:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2017/11/06	07:17:30.0	XRT_Custom_430_OG [0x1ae]						
2017/11/06	07:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2017/11/06	08:32:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/11/06	08:32:32.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2017/11/06	08:32:34.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2017/11/06	08:32:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]						

2017/11/06	08:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/11/06	08:54:00.0	XRT_Custom_430_OG [0x1ae]					
2017/11/06	08:55:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/11/06	10:13:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/06	10:13:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/06	10:13:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2017/11/06	10:13:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/11/06	10:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/11/06	10:27:30.0	XRT_Custom_430_OG [0x1ae]					
2017/11/06	10:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/11/06	15:36:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/06	15:36:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/06	15:36:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2017/11/06	15:36:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/11/06	15:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/11/06	15:54:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/06	15:54:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/06	15:54:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2017/11/06	15:54:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/11/06	15:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/11/06	15:59:30.0	XRT_Custom_430_OG [0x1ae]					
2017/11/06	16:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/11/06	17:12:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/06	17:12:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/06	17:12:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2017/11/06	17:12:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/11/06	17:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/11/06	17:36:00.0	XRT_Custom_430_OG [0x1ae]					
2017/11/06	17:37:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/11/06	18:06:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/06	18:06:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/06	18:06:58.0	XRT_FOCUS_POSITION_403_OG [0x193]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2017/11/06	18:07:00.0	AOCS_OrE-point_Start_3_OG [0x099]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2017/11/06	18:07:18.0	XRT_FLD_DIS_425_OG [0x1a9]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2017/11/06	18:09:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2017/11/06	18:09:56.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2017/11/06	18:09:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a	
2017/11/06	18:10:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/11/06	18:16:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/06	18:16:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/06	18:16:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2017/11/06	18:17:00.0	AOCS_OrE-point_Start_5_OG [0x09b]					
			AOCU_NM	5	02-76	03 03 8d 15 68	
2017/11/06	18:17:18.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2017/11/06	18:17:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2017/11/06	18:17:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2017/11/06	18:17:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2017/11/06	18:17:26.0	XRT_FLD_RESET_433_OG [0x1b1]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2017/11/06	18:19:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	

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2017/11/06	18:19:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2017/11/06	18:20:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/11/06	18:49:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/06	18:49:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/06	18:49:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/11/06	18:49:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/11/06	18:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/11/06	19:13:00.0	XRT_Custom_430_OG [0x1ae]							
2017/11/06	19:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/11/06	20:26:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/06	20:26:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/06	20:26:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/11/06	20:26:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/11/06	20:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/11/06	20:50:00.0	XRT_Custom_430_OG [0x1ae]							
2017/11/06	20:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/11/06	22:04:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/06	22:04:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/06	22:04:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/11/06	22:04:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/11/06	22:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/11/06	22:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/06	22:29:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/06	22:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2017/11/06	22:30:00.0	AOCs_OrE-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	02	00	00	00	00
2017/11/06	22:30:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2017/11/06	22:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2017/11/06	22:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2017/11/06	22:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2017/11/06	22:30:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/11/06	22:32:56.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2017/11/06	22:32:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2017/11/06	22:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/11/06	23:41:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/06	23:41:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/06	23:41:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/11/06	23:41:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/11/06	23:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/11/06	23:55:00.0	XRT_Custom_430_OG [0x1ae]							
2017/11/06	23:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/11/07	01:11:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/07	01:11:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/07	01:11:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/11/07	01:11:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/11/07	01:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/11/07	01:24:30.0	XRT_Custom_430_OG [0x1ae]							
2017/11/07	01:25:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/11/07	01:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/11/07	01:29:56.0	XRT_CTRL_MANU_402_OG [0x192]							

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2017/11/07	01:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/07	01:30:00.0	AOCS_OrE-point_Start_7_OG [0x09d]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2017/11/07	01:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	04 00 00 00 00	
2017/11/07	01:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2017/11/07	01:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2017/11/07	01:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2017/11/07	01:30:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2017/11/07	01:32:56.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/11/07	01:32:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2017/11/07	01:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2017/11/07	02:40:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/11/07	02:40:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/07	02:40:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/07	02:40:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/11/07	02:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/11/07	03:00:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/11/07	03:01:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2017/11/07	04:08:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/11/07	04:08:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/07	04:08:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/07	04:08:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/11/07	04:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/11/07	04:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/11/07	04:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/07	04:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/07	04:30:00.0	AOCS_OrE-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2017/11/07	04:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01 00 00 00 00	
2017/11/07	04:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2017/11/07	04:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2017/11/07	04:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2017/11/07	04:30:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2017/11/07	04:32:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/11/07	04:32:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01	
2017/11/07	04:38:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2017/11/07	04:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2017/11/07	05:48:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/11/07	05:48:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/07	05:48:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/07	05:48:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/11/07	05:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/11/07	06:15:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/11/07	06:16:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2017/11/07	06:24:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/11/07	06:24:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/07	06:24:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/11/07	06:25:00.0	AOCS_OrE-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2017/11/07	06:25:18.0	XRT_FLD_DIS_425_OG [0x1a9]	AOCU_NM	5	02-76	00 00 00 00 00	
2017/11/07	06:27:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9	

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2017/11/07	06:27:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
			MDP_XRT_ARS_DIS	1	07-F0	d5
2017/11/07	06:27:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2017/11/07	06:28:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/11/07	06:34:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/07	06:34:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/07	06:34:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2017/11/07	06:35:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	01 00 00 00 00
2017/11/07	06:35:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2017/11/07	06:35:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2017/11/07	06:35:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2017/11/07	06:35:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2017/11/07	06:35:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/11/07	06:37:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2017/11/07	06:37:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2017/11/07	06:38:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/11/07	07:28:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/07	07:28:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/07	07:28:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/11/07	07:28:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/11/07	07:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/11/07	07:52:30.0	XRT_Custom_430_OG [0x1ae]				
2017/11/07	07:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/11/07	09:08:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/07	09:08:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/07	09:08:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/11/07	09:08:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/11/07	09:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/11/07	09:28:30.0	XRT_Custom_430_OG [0x1ae]				
2017/11/07	09:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2017/11/07	10:50:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/07	10:50:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2017/11/07	10:50:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2017/11/07	10:50:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2017/11/07	10:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2017/11/07	11:22:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00