

XRT Timeline to be uploaded on 2018/02/06

Period: 2018/02/06 11:19:00 - 2018/02/10 10:25:00

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Normal mode

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XOB #1BDC: HOP341 - High cadence (10s Al/poly only) 384x384 at 1064 1048																
Term		Pointing (x, y)					Comment									
02/06 11:32:00 - 02/06 13:06:54		Track (567.9, -96.6) ^{02/06 11:29:00}					# OP start + 10min, HOP341 on AR12697									
PROG= 20 Inf.-time(s)																
└─ Subr= 1 1-time(s) 2.0sec																
└─ Seqn= 92 1-time(s) 2.0sec																
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec																
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec																
└─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec																
└─ Subr= 2 20-time(s) 2.0sec																
└─ Seqn= 74 60-time(s) 10.0sec																
└─ Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec																
Default Filter		Thicker Filter		VLS		mode		image		Exp.		CCD Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #1BDB: AR (Filter-Ratio with Al/poly and thin-Be) with PFB, 512x512 at 1064 1048, thick-Al context, with G-band (1ms/1ms leak), 120s cad																
Term		Pointing (x, y)					Comment									
02/06 13:10:00 - 02/06 18:02:24		Track (-845.1, -94.8) ^{02/06 13:07:00}					E-limb AR 12699									
02/06 18:53:00 - 02/07 04:56:54		Track (-816.1, -89.7) ^{02/06 18:50:00}					Cont,									
02/07 05:45:30 - 02/07 09:19:30		Track (-754.2, -80.4) ^{02/07 05:42:30}					Cont,									
02/07 15:42:00 - 02/07 17:58:30		Track (-690.4, -72.6) ^{02/07 15:39:00}					E-lim AR 12699									
02/07 18:35:00 - 02/08 06:06:24		Track (-670.7, -70.4) ^{02/07 18:32:00}					Cont,									
02/08 06:19:30 - 02/08 09:56:00		Track (-585.3, -62.3) ^{02/08 06:16:30}					Cont,									
PROG= 05 Inf.-time(s)																
└─ Subr= 1 1-time(s) 2.0sec																
└─ Seqn= 92 1-time(s) 2.0sec																
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec																
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec																
└─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec																
└─ Seqn= 71 3-time(s) 2.0sec																
└─ Open/thick-Al Open/thick-Be close Safe Norm 16.0s Obs 1x1 512x512 (1064, 1048) Q=98 3 0 2.0sec																
└─ Subr= 2 30-time(s) 120.0sec																
└─ Seqn= 89 1-time(s) 40.0sec																
└─ thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048) Q=95 2 0 2.0sec																
└─ Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 512x512 (1064, 1048) Q=95 2 0 14.0sec																
└─ Seqn= 58 1-time(s) 40.0sec																
└─ Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec																
└─ thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec																
└─ Seqn= 48 1-time(s) 2.0sec																
└─ Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec																
└─ thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec																
Default Filter		Thicker Filter		VLS		mode		image		Exp.		CCD Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #1BA7: Synoptic Q95 2x2 - Al/mesh(8/181/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(16/181/4096) + Th																
Term		Pointing (x, y)					Comment									
02/06 18:05:30 - 02/06 18:12:24		Fixed (0.0, 0.0)					synoptic, shifted 2.5 min and SOT focus test									
02/07 18:25:00 - 02/07 18:31:54		Fixed (0.0, 0.0)					synoptic, shifted 22.0 min									
02/08 06:09:30 - 02/08 06:16:24		Fixed (0.0, 0.0)					synoptic, shifted 6.5 min									
PROG= 01 1-time(s)																
└─ Subr= 1 1-time(s) 2.0sec																
└─ Seqn= 5 1-time(s) 2.0sec																
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec																
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec																
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec																
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec																
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec																
└─ Seqn= 26 1-time(s) 2.0sec																
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																
└─ Seqn= 73 1-time(s) 2.0sec																
└─ Al-poly/Open Al-poly/Open close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																
└─ Al-poly/Open Al-poly/Open close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																
└─ Al-poly/Open Al-poly/thick-Al close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																
└─ Seqn= 44 1-time(s) 2.0sec																
└─ thin-Be/Open thin-Be/Open close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																
└─ thin-Be/Open thin-Be/Open close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																
└─ thin-Be/Open thin-Be/Open close Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																
└─ Seqn= 23 1-time(s) 2.0sec																
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec																
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																
Default Filter		Thicker Filter		VLS		mode		image		Exp.		CCD Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #1B8C: CME watch - 4x4 - AEC 1/2/3 - Al-poly - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 90s cad (G-band/Leak first)												
Term	Pointing (x, y)			Comment								
02/06 18:15:30 - 02/06 18:49:54	Fixed (0.0, 0.0)			synoptic, shifted 2.5 min and SOT focus test								
02/07 05:00:00 - 02/07 05:32:24	Fixed (0.0, 0.0)			synoptic, shifted manually and SOT focus test								
PROG= 14 Inf.-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 30 1-time(s) 2.0sec												
Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=90 0 0 2.0sec												
Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=95 0 0 2.0sec												
Subr= 2 40-time(s) 90.0sec												
Seqn= 60 1-time(s) 2.0sec												
Al-poly/Open Al-poly/Open close Safe Norm 500ms Obs 4x4 2048x2048 (1024, 1024) DPCM 1 0 2.0sec												
Al-poly/Open Al-poly/Open close Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec												
Al-poly/Open Al-poly/Open close Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1BDD: Synoptic 7 Filter w/ Al-mesh(24/256/2897), Al-poly(24/362/4096), Thin-Be(181/2048/11571) - Thick-Be(65536), Al-poly+Ti-poly(256/5795), Med-A												
Term	Pointing (x, y)			Comment								
02/07 05:35:30 - 02/07 05:42:24	Fixed (0.0, 0.0)			synoptic, shifted manually and SOT focus test								
PROG= 12 1-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 5 1-time(s) 2.0sec												
Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec												
Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec												
Seqn= 1 1-time(s) 2.0sec												
Open/Al-mesh Open/Al-mesh close Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Open/Al-mesh Open/Al-mesh close Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Open/Al-mesh Open/Al-mesh close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Seqn= 41 1-time(s) 2.0sec												
Al-poly/Open Al-poly/Open close Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Al-poly/Open Al-poly/Open close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Al-poly/Open Al-poly/thick-Al close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Seqn= 67 1-time(s) 2.0sec												
thin-Be/Open thin-Be/Open close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
thin-Be/Open thin-Be/Open close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
thin-Be/Open thin-Be/Open close Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Seqn= 23 1-time(s) 4.0sec												
Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Subr= 2 1-time(s) 2.0sec												
Seqn= 46 2-time(s) 2.0sec												
Open/thick-Be Open/thick-Be close Safe Norm 64.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
Seqn= 40 2-time(s) 2.0sec												
Al-poly/Ti-poly Al-poly/thick-Al close Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
Al-poly/Ti-poly Al-poly/thick-Al close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
Seqn= 65 2-time(s) 2.0sec												
med-Al/Open med-Al/Open close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
med-Al/Open med-Al/Open close Safe Norm 32.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1B93: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 30s cadence, G-band - 384x384 1ms												
Term	Pointing (x, y)			Comment								
02/07 09:42:00 - 02/07 15:38:54	Fixed (-20.0, -951.0)			HOP81 on S-pole								
PROG= 04 Inf.-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 16 2-time(s) 2.0sec												
Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
Subr= 2 1-time(s) 2.0sec												
Seqn= 90 1-time(s) 30.0sec												
Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec												
Subr= 3 60-time(s) 2.0sec												
Seqn= 57 1-time(s) 30.0sec												
Open/Al-mesh Open/Al-mesh close Safe Norm 4.00s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec												
Al-poly/Open Al-poly/Open close Safe Norm 5.66s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

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Flare mode

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XOB #1B8E: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512												
Term	Pointing (x, y)			Comment								
02/06 13:10:00 - 02/06 18:02:24	Track (-845.1, -94.8) @ 02/06 13:07:00			E-limb AR 12699								
02/06 18:15:30 - 02/06 18:49:54	Fixed (0.0, 0.0)			synoptic, shifted 2.5 min and SOT focus test								
02/06 18:53:00 - 02/07 04:56:54	Track (-816.1, -89.7) @ 02/06 18:50:00			Cont,								

02/07 05:00:00 - 02/07 05:32:24 Fixed (0.0, 0.0) synoptic, shifted manually and SOT focus test
 02/07 05:45:30 - 02/07 09:19:30 Track (-754.2, -80.4) @ 02/07 05:42:30 Cont,
 02/07 09:42:00 - 02/07 15:38:54 Fixed (-20.0, -951.0) HOP81 on S-pole
 02/07 15:42:00 - 02/07 17:58:30 Track (-690.4, -72.6) @ 02/07 15:39:00 E-lim AR 12699
 02/07 18:35:00 - 02/08 06:06:24 Track (-670.7, -70.4) @ 02/07 18:32:00 Cont,
 02/08 06:19:30 - 02/08 09:56:00 Track (-585.3, -62.3) @ 02/08 06:16:30 Cont,

PROG= 13 30-time(s)

Subr=	1	20-time(s)	2.0sec										
Seqn=	11	1-time(s)	2.0sec										
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn=	100	1-time(s)	10.0sec										
	thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
	med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr=	2	1-time(s)	2.0sec										
Seqn=	10	1-time(s)	2.0sec										
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn=	11	1-time(s)	2.0sec										
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn=	87	1-time(s)	2.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

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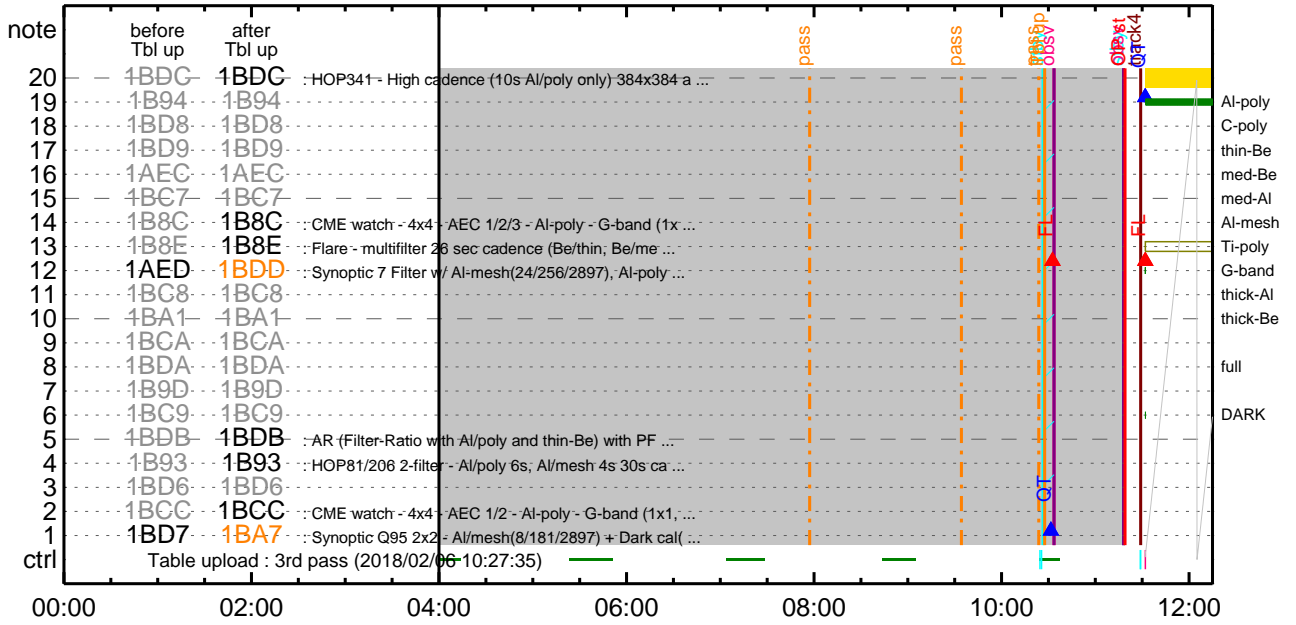
Flare Detection

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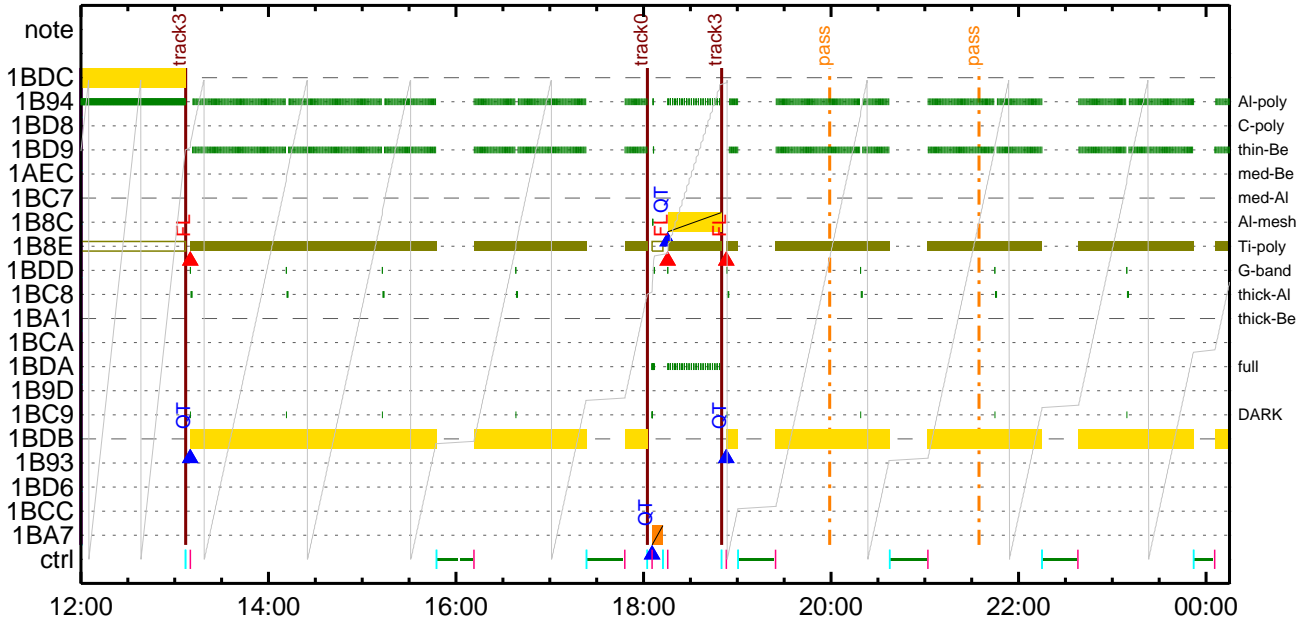
FLD Patrol

Term	Pointing (x, y)	Comment											
02/06 13:07:18 - 02/06 18:02:48	Track (-845.1, -94.8) @ 02/06 13:07:00	E-limb AR 12699											
02/06 18:12:48 - 02/07 05:32:48	Fixed (0.0, 0.0)	synoptic, shifted 2.5 min and SOT focus test											
02/07 05:42:48 - 02/07 18:22:18	Track (-754.2, -80.4) @ 02/07 05:42:30	Cont,											
02/07 18:32:18 - 02/08 06:06:48	Track (-670.7, -70.4) @ 02/07 18:32:00	Cont,											
02/08 06:16:48 - 02/10 10:25:00	Track (-585.3, -62.3) @ 02/08 06:16:30	Cont,											
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

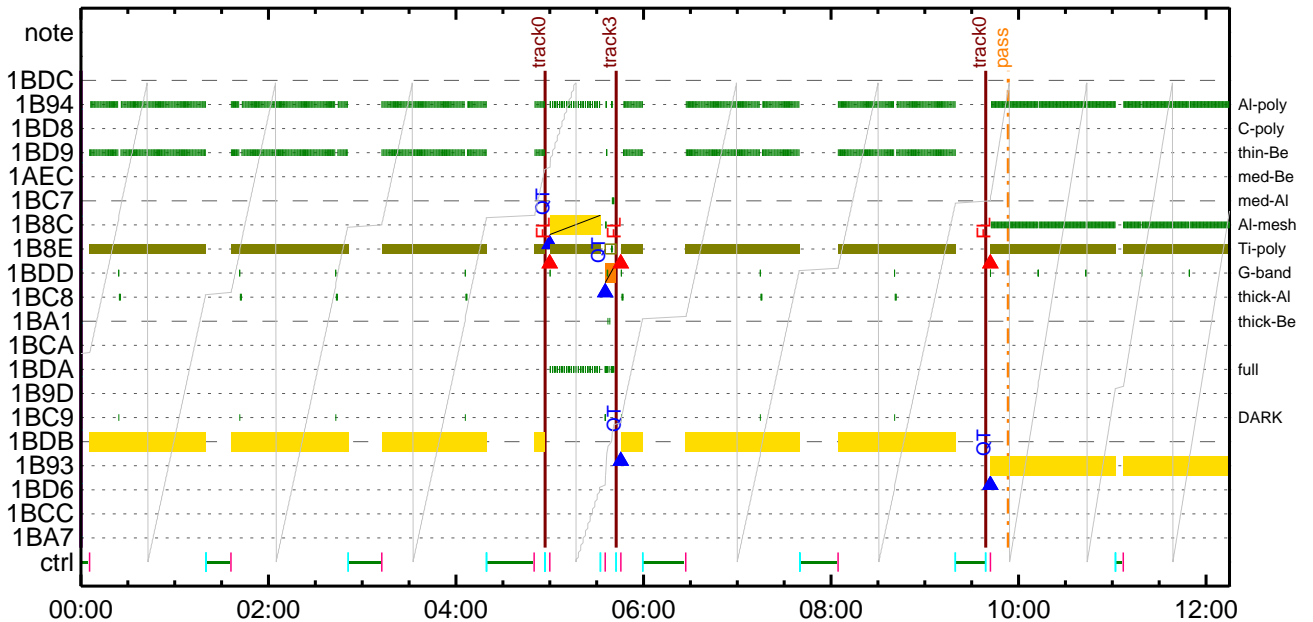
CMDI #0297 2018/02/06



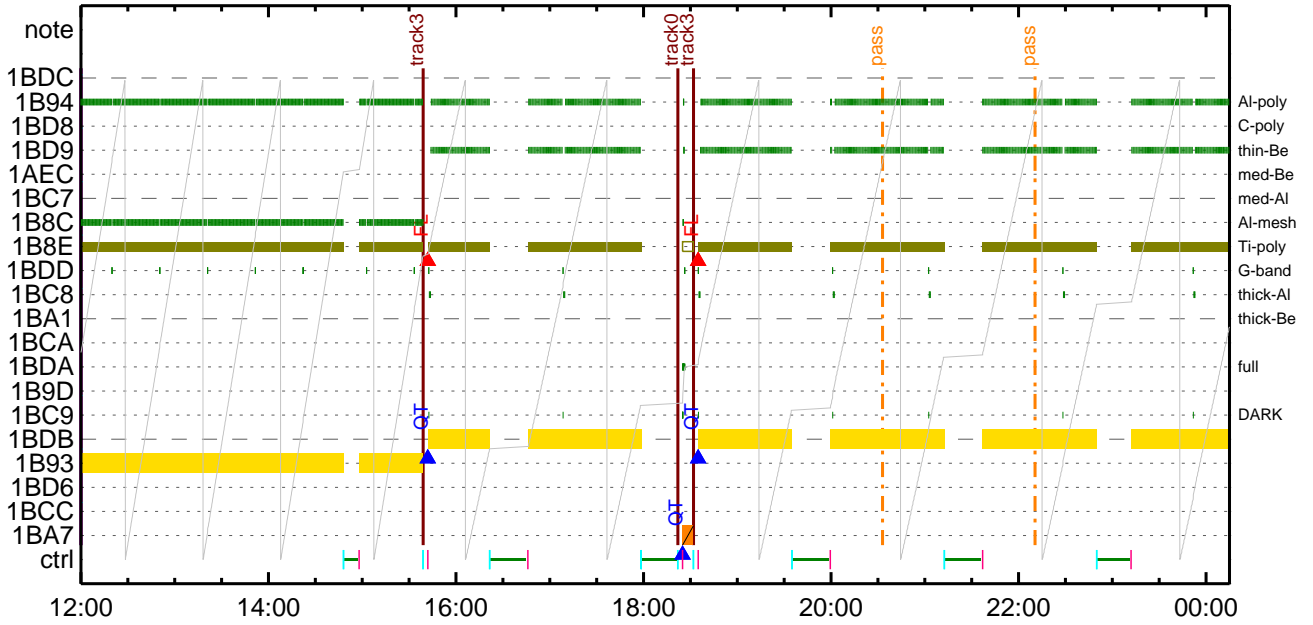
CMDI #0297 2018/02/06



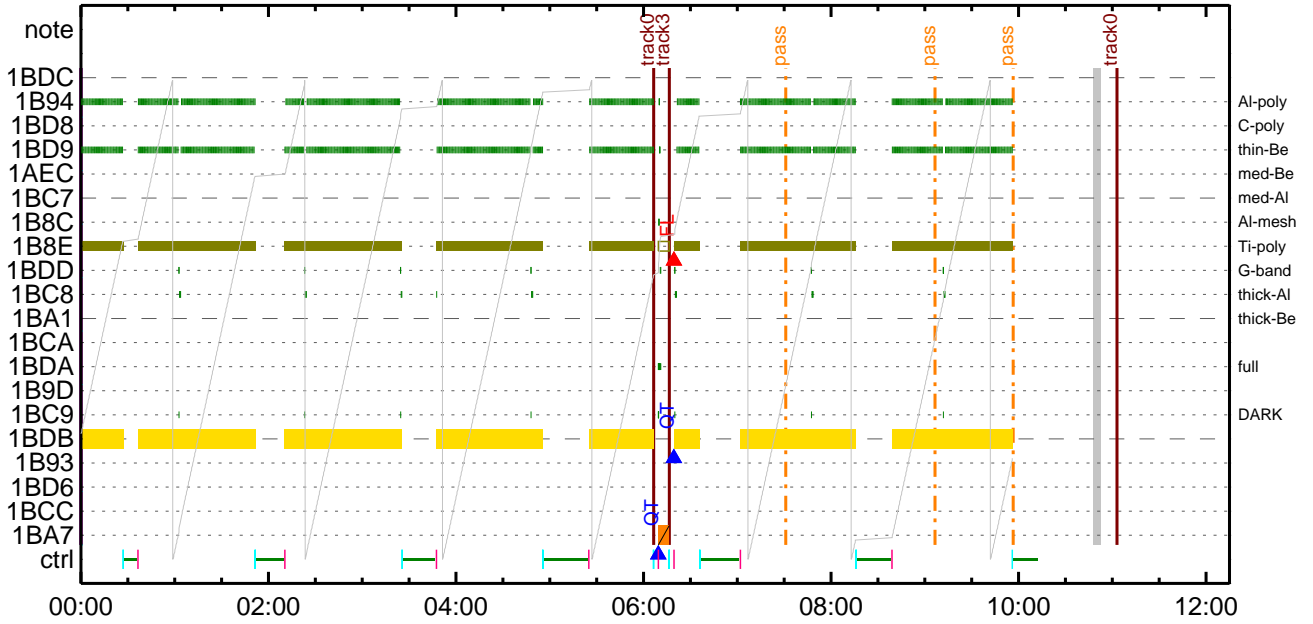
CMDI #0297 2018/02/07



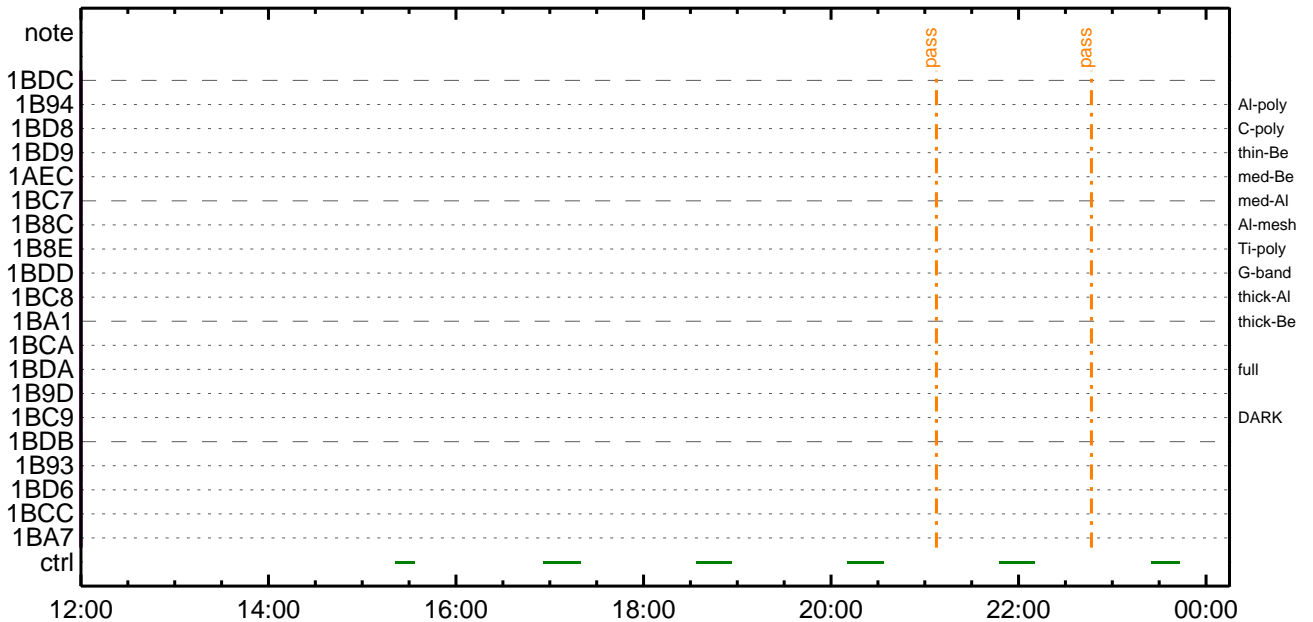
CMDI #0297 2018/02/07



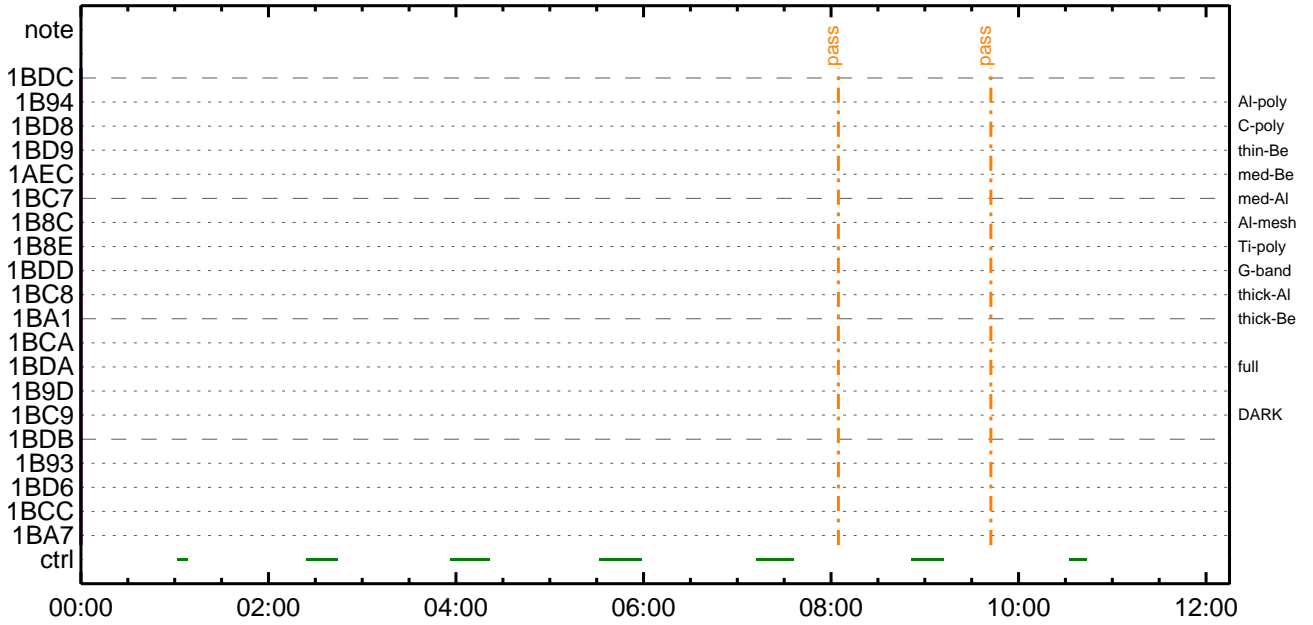
CMDI #0297 2018/02/08



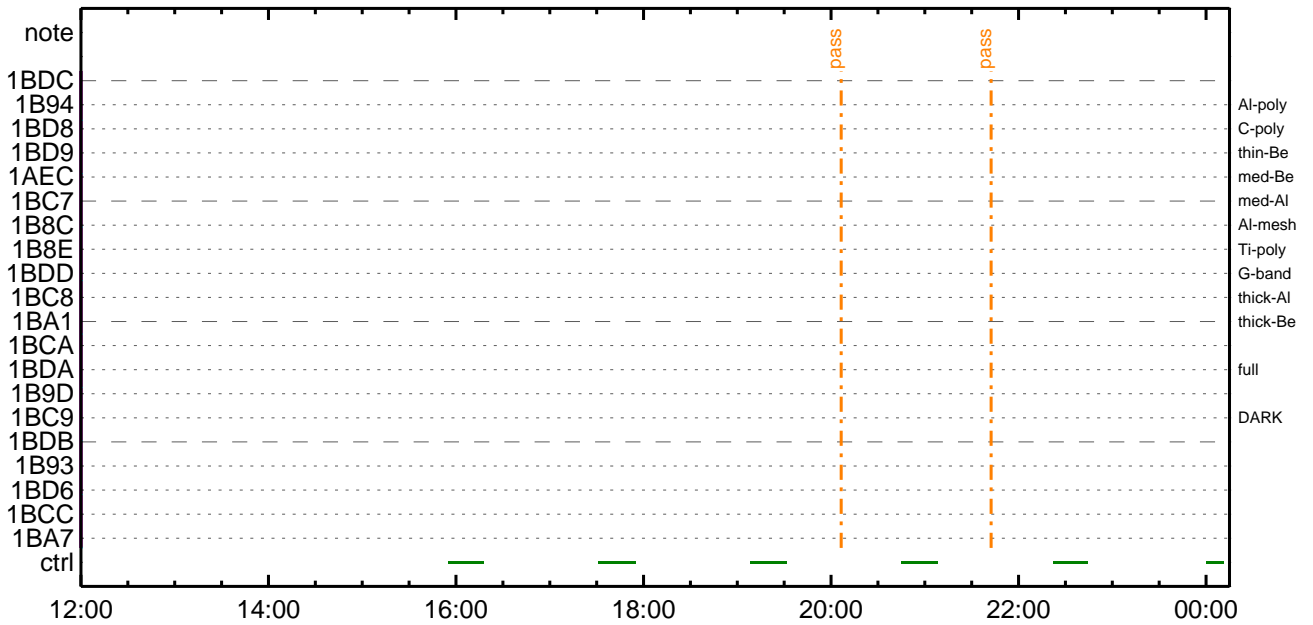
CMDI #0297 2018/02/08



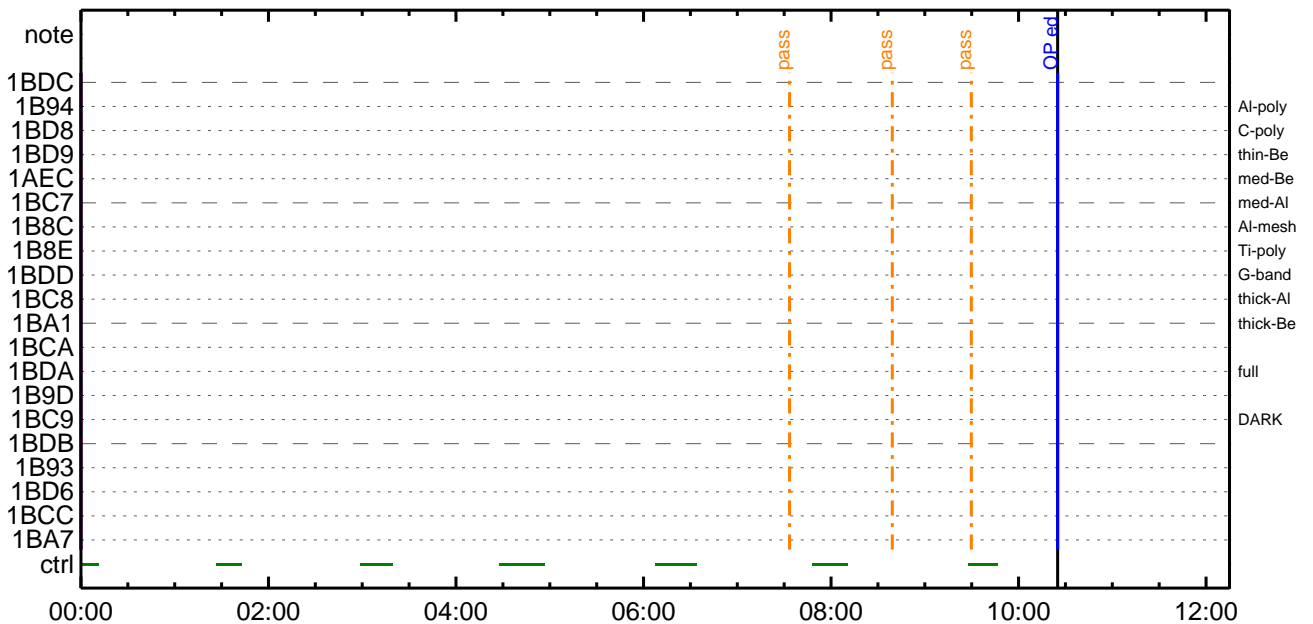
CMDI #0297 2018/02/09



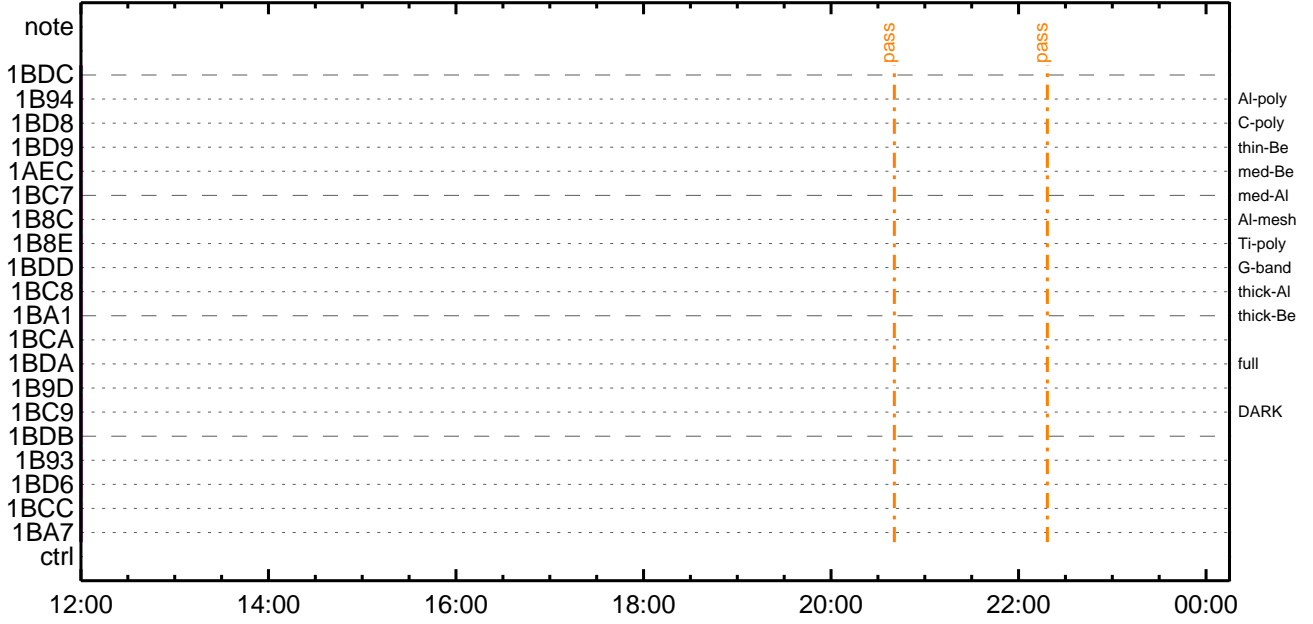
CMDI #0297 2018/02/09



CMDI #0297 2018/02/10



CMDI #0297 2018/02/10




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YA6Yx
0100 C. *****
0101 C.
0102 . C. ;ãOP/OGY1;4YE;ã
0103 . S. OP op-160:OP
0104 ( )
0105 . S. OG og-160:OG
0106 ( )
0107 C.
0108 . C. ;ãNMOG&OPfî°èYAY6Yx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 . C. YAY6Yx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 . C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 . C. YAY6Yx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 . C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 . C. YAY6Yx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 . C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 . C. ***** °E²¼òî½Ä´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAY6Yx½ê½çòðÁÓÆòÇ¼ª°¬òE¼î¹çòÇòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 . C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 . C. NOTICE ;§ OPOG UPLOAD²¬Á÷¿@NG²î½î¹ç;ç°E²¼òîTI-CMDÁ÷¿@²î½î¹Òª°²E²²²³²E;f
0180 C. °²²²;çSET²EEDUMP²î½±²îY¹²ç¹Òª|²³²E;f
0181 C.
0182 . C. TIY³Y²Y6YÉ²òðÁDî¿(UT)
0183 +. TI 2018-02-06 11:14:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2018-02-06 11:14:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2018-02-06 11:14:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2018-02-06 11:18:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]                      EQ        1COUNTUP
0198 C.
0199 C.   °Ê²¼ªïÄê%îññîîŷÄŷ§ŷÄŷ-¹äiÛ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]                    EQ        ENA
0201 C.          çç[HK1_TI_CMD_NUM]                        EQ        4
0202 C.          çç[HK1_NEXT_EXEC_PIM]                     EQ        DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]                      EQ        0xB3
0204 C.
0205 . C.  *****
0206 C.  TIîî°èŷÄŷÓŷ×
0207 C.  *****
0208 C.
0209 C.  TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PIM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]                    EQ        07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]                    EQ        2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]                     EQ        3
0215 C.          çç[HK1_DMP_REPEAT_NUM]                    EQ        0
0216 C.          çç[HK1_DMA_DMP_PIM]                       EQ        DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]                       EQ        7
0220 C.          çç[HK1_PKT_GEN_TIME]                       EQ        0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]                     EQ        32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]                     EQ        4M
0223 C.          çç[HK1_DMP_CHK_FLG]                       EQ        EXEC
0224 C.
0225 . C.  ŷÄŷÓŷ×½ªî»ð³îÇ§
0226 C.          çç[HK1_DMP_CHK_FLG]                       EQ        NON
0227 C.
0228 . C.  RAM ID=TI_TBLîî½Ê¹ç•è²îOKð³îÇ§
0229 C.
0230 . C.  DHUŷâ;¼ŷÉ;Ê½ŷ¼. ŷî;¼ŷÈ;Êððîãª¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]                       EQ        2
0234 C.          çç[HK1_PKT_GEN_TIME]                       EQ        0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]                     EQ        32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]                     EQ        4M
0237 C.
0238 C.
0239 C.  ***** XRT START *****
0240 C.  Execute, after the success of OP upload.
0241 +. TI 2018-02-06 11:18:00.0
0242 DC 07-F0 MDP_XRT_MODE_STBY
0243 BC      (c3)
0244 . C.          [ ] [HK1_TI_CMD_NUM]                     EQ        1COUNTUP
0245 C.
0246 C.  ***** XRT END *****
0247 C.
0248 . C.  ***** MDP ´ûÄîîî»ð¼ŷªÊÄðª¹èDCBC•x²è *****
0249 C.  (%á°îŷÓŷÄŷÈŷŷŷÄŷÇŷèè²¼ªª¼Ä»Ûª¹è)
0250 . S. DC-BC dcbc-402:DCBC
0251 (MDP_known_event)
0252 C.
0253 C.
0254 . C.  ***** ŷDŷ¹•î Daily±çîññè´Øª¹èDCBC•x²è *****
0255 . S. DC-BC dcbc-153:DCBC
0256 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0257 C.
0258 C.
0259 . C.  ;ãLOSŷÄŷ§ŷÄŷ-¼Ä»Û;ä
0260 C.
0261 . C.  ***** LOS *****
0262 C.

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0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOUC_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCS DUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOUC_ORB_UPD
0128 . C.
0129 . C. ***** AOCS Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOUC_ORB_PRPGT_START
0132 BC (16)
0133 + DC 02-8E AOUC_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 6212891.4 +- 1.0 (s) [ ]
0136 C.
0137 . C.
0138 C.
0139 . C. ***** MDP `uAÎnî»ô%ÿnÊADn¹nèDCBC•x²è *****
0140 C. (%â°iÿÓÿÃÿÈÿPÿËÿâÿçÿènÊ%¼n¼Ã»Ûn¹nè)
0141 . S. DC-BC dcbc-402:DCBC
0142 (MDP_known_event)
0143 C.
0144 C.
0145 . C. ***** ÿDÿ¹.Ï Daily±;îÑnÊ´Øn¹nèDCBC•x²è *****
0146 . S. DC-BC dcbc-153:DCBC
0147 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0148 C.
0149 C.
0150 . C. ;ãLOSÿÃÿSÿÿÿ¼Ã»Û;ã
0151 C.
0152 . C. ***** LOS *****
0153 C.

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0096 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0097 C.
0098 C.
0099 . C. ;äLOSŸÁŸSŸÄŸ-¼Ä»Û;ä
0100 C.
0101 . C. ***** LOS *****
0102 C.
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*** OP Sequence for XRT ***

2018/02/06	11:28:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/02/06	11:28:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/02/06	11:28:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2018/02/06	11:29:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 02 c0 01 ca				
2018/02/06	11:29:18.0	XRT_FLD_DIS_420_OG [0x1a4]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2018/02/06	11:31:50.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2018/02/06	11:31:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2018/02/06	11:31:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/02/06	11:31:56.0	XRT_QT_PROG_SET_409_OG [0x199]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2018/02/06	11:31:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2018/02/06	11:32:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/02/06	13:06:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/02/06	13:06:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/02/06	13:06:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2018/02/06	13:07:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	03 02 c0 01 ca				
2018/02/06	13:07:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2018/02/06	13:07:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2018/02/06	13:07:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2018/02/06	13:07:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/02/06	13:07:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/02/06	13:09:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2018/02/06	13:09:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2018/02/06	13:10:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/02/06	15:47:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/02/06	15:47:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/02/06	15:47:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/02/06	15:47:36.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/02/06	15:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/02/06	16:10:30.0	XRT_Custom_430_OG [0x1ae]							
2018/02/06	16:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/02/06	17:23:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/02/06	17:23:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/02/06	17:23:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/02/06	17:23:36.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/02/06	17:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/02/06	17:47:00.0	XRT_Custom_430_OG [0x1ae]							
2018/02/06	17:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/02/06	18:02:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/02/06	18:02:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/02/06	18:02:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2018/02/06	18:02:30.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2018/02/06	18:02:48.0	XRT_FLD_DIS_425_OG [0x1a9]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2018/02/06	18:05:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2018/02/06	18:05:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/02/06	18:05:28.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2018/02/06	18:05:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/02/06	18:12:24.0	XRT_CTRL_MANU_402_OG [0x192]							

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2018/02/06	18:12:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/02/06	18:12:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/02/06	18:12:48.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2018/02/06	18:12:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2018/02/06	18:12:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2018/02/06	18:12:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2018/02/06	18:12:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/02/06	18:15:26.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/02/06	18:15:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e
2018/02/06	18:15:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2018/02/06	18:49:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/02/06	18:49:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/02/06	18:49:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/02/06	18:50:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00
2018/02/06	18:50:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 02 c0	01 ca
2018/02/06	18:50:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2018/02/06	18:50:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2018/02/06	18:50:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2018/02/06	18:50:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/02/06	18:52:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/02/06	18:52:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05
2018/02/06	18:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2018/02/06	19:00:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/02/06	19:00:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/02/06	19:00:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/02/06	19:00:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/02/06	19:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/02/06	19:23:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/02/06	19:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2018/02/06	20:37:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/02/06	20:37:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/02/06	20:37:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/02/06	20:37:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/02/06	20:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/02/06	21:01:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/02/06	21:02:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2018/02/06	22:15:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/02/06	22:15:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/02/06	22:15:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/02/06	22:15:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/02/06	22:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/02/06	22:37:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/02/06	22:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2018/02/06	23:52:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/02/06	23:52:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/02/06	23:52:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/02/06	23:52:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/02/06	23:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	

2018/02/07	00:04:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/02/07	00:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2018/02/07	01:20:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/02/07	01:20:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/02/07	01:20:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/02/07	01:20:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/02/07	01:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/02/07	01:35:00.0	XRT_Custom_430_OG [0x1ae]					
2018/02/07	01:36:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2018/02/07	02:51:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/02/07	02:51:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/02/07	02:51:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/02/07	02:51:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	da	
2018/02/07	02:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e8	
2018/02/07	03:11:30.0	XRT_Custom_430_OG [0x1ae]					
2018/02/07	03:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2018/02/07	04:19:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/02/07	04:19:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/02/07	04:19:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	c1	
2018/02/07	04:19:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	da	
2018/02/07	04:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e8	
2018/02/07	04:49:00.0	XRT_Custom_430_OG [0x1ae]					
2018/02/07	04:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2018/02/07	04:56:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/02/07	04:56:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/02/07	04:56:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/02/07	04:57:00.0	AOCS_OrE-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2018/02/07	04:57:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 00 00 00 00	
2018/02/07	04:57:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2018/02/07	04:57:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2018/02/07	04:57:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2018/02/07	04:57:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/02/07	04:59:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/02/07	04:59:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e	
2018/02/07	05:00:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2018/02/07	05:32:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/02/07	05:32:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/02/07	05:32:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/02/07	05:32:48.0	XRT_FLD_DIS_425_OG [0x1a9]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2018/02/07	05:35:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2018/02/07	05:35:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2018/02/07	05:35:28.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/02/07	05:35:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c	
2018/02/07	05:42:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/02/07	05:42:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/02/07	05:42:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/02/07	05:42:30.0	AOCS_OrE-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2018/02/07	05:42:48.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 02 c0 01 ca	
			MDP_XRT_FLD_ENA	1	07-F0	d8	

2018/02/07	05:42:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2018/02/07	05:42:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2018/02/07	05:42:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2018/02/07	05:42:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/02/07	05:45:26.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05	
2018/02/07	05:45:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d	
2018/02/07	05:45:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/02/07	05:59:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/02/07	05:59:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/02/07	05:59:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/02/07	05:59:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/02/07	06:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/02/07	06:26:00.0	XRT_Custom_430_OG [0x1ae]						
2018/02/07	06:27:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/02/07	07:40:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/02/07	07:40:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/02/07	07:40:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/02/07	07:40:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/02/07	07:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/02/07	08:03:30.0	XRT_Custom_430_OG [0x1ae]						
2018/02/07	08:04:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/02/07	09:19:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/02/07	09:19:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/02/07	09:19:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/02/07	09:19:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/02/07	09:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/02/07	09:38:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/02/07	09:38:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/02/07	09:38:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2018/02/07	09:39:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00	54 8b 01 ca	
2018/02/07	09:39:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2018/02/07	09:39:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2018/02/07	09:39:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2018/02/07	09:39:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2018/02/07	09:39:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/02/07	09:41:56.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04	
2018/02/07	09:41:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d	
2018/02/07	09:42:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/02/07	11:02:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/02/07	11:02:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/02/07	11:02:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/02/07	11:02:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/02/07	11:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/02/07	11:06:00.0	XRT_Custom_430_OG [0x1ae]						
2018/02/07	11:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/02/07	14:48:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/02/07	14:48:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/02/07	14:48:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		

2018/02/07	14:48:06.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/02/07	14:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/02/07	14:57:00.0	XRT_Custom_430_OG [0x1ae]							
2018/02/07	14:58:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/02/07	15:38:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/02/07	15:38:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/02/07	15:38:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2018/02/07	15:39:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	03 02 c0 01 ca				
2018/02/07	15:39:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2018/02/07	15:39:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2018/02/07	15:39:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2018/02/07	15:39:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/02/07	15:39:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/02/07	15:41:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2018/02/07	15:41:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2018/02/07	15:42:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/02/07	16:21:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/02/07	16:21:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/02/07	16:21:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/02/07	16:21:36.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/02/07	16:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/02/07	16:45:00.0	XRT_Custom_430_OG [0x1ae]							
2018/02/07	16:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/02/07	17:58:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/02/07	17:58:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/02/07	17:58:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/02/07	17:58:36.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/02/07	18:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/02/07	18:21:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/02/07	18:21:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/02/07	18:21:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2018/02/07	18:22:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2018/02/07	18:22:18.0	XRT_FLD_DIS_425_OG [0x1a9]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2018/02/07	18:24:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2018/02/07	18:24:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/02/07	18:24:58.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2018/02/07	18:25:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/02/07	18:31:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/02/07	18:31:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/02/07	18:31:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2018/02/07	18:32:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	03 02 c0 01 ca				
2018/02/07	18:32:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2018/02/07	18:32:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2018/02/07	18:32:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2018/02/07	18:32:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/02/07	18:32:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/02/07	18:34:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2018/02/07	18:34:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							

2018/02/07	18:35:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/02/07	19:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/02/07	19:35:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/02/07	19:35:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/02/07	19:35:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/02/07	19:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/02/07	19:58:30.0	XRT_Custom_430_OG [0x1ae]					
2018/02/07	19:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/02/07	21:12:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/02/07	21:12:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/02/07	21:12:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/02/07	21:12:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/02/07	21:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/02/07	21:36:00.0	XRT_Custom_430_OG [0x1ae]					
2018/02/07	21:37:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/02/07	22:50:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/02/07	22:50:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/02/07	22:50:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/02/07	22:50:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/02/07	22:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/02/07	23:11:00.0	XRT_Custom_430_OG [0x1ae]					
2018/02/07	23:12:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/02/08	00:27:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/02/08	00:27:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/02/08	00:27:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/02/08	00:27:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/02/08	00:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/02/08	00:35:30.0	XRT_Custom_430_OG [0x1ae]					
2018/02/08	00:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/02/08	01:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/02/08	01:51:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/02/08	01:51:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/02/08	01:51:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/02/08	01:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/02/08	02:09:30.0	XRT_Custom_430_OG [0x1ae]					
2018/02/08	02:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/02/08	03:25:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/02/08	03:25:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/02/08	03:25:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/02/08	03:25:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/02/08	03:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/02/08	03:46:30.0	XRT_Custom_430_OG [0x1ae]					
2018/02/08	03:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/02/08	04:55:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/02/08	04:55:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/02/08	04:55:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/02/08	04:55:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/02/08	04:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/02/08	05:24:00.0	XRT_Custom_430_OG [0x1ae]					
2018/02/08	05:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					

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2018/02/08	06:06:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/02/08	06:06:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/02/08	06:06:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/02/08	06:06:30.0	AOCS_ORe-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2018/02/08	06:06:48.0	XRT_FLD_DIS_425_OG [0x1a9]	AOCU_NM	5	02-76	00 00 00 00 00
2018/02/08	06:09:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2018/02/08	06:09:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2018/02/08	06:09:28.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5
2018/02/08	06:09:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2018/02/08	06:16:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/02/08	06:16:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/02/08	06:16:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/02/08	06:16:30.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2018/02/08	06:16:48.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 02 c0 01 ca
2018/02/08	06:16:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2018/02/08	06:16:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2018/02/08	06:16:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2018/02/08	06:16:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2018/02/08	06:19:26.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/02/08	06:19:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2018/02/08	06:19:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2018/02/08	06:36:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/02/08	06:36:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/02/08	06:36:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/02/08	06:36:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/02/08	06:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/02/08	07:01:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/02/08	07:02:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2018/02/08	08:16:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/02/08	08:16:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/02/08	08:16:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/02/08	08:16:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/02/08	08:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/02/08	08:38:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/02/08	08:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2018/02/08	09:56:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/02/08	11:03:00.0	AOCS_ORe-point_Start_3_OG [0x099]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		AOCU_NM	5	02-76	00 00 00 00 00	00