

# XRT Timeline to be uploaded on 2018/03/03

Period: 2018/03/03 10:47:00 - 2018/03/08 11:00:00

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## Normal mode

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<b>XOB #1B93: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 30s cadence, G-band - 384x384 1ms</b>													
Term	Pointing (x, y)						Comment						
03/03 11:00:00 - 03/03 16:56:54	Fixed ( -20.0, -951.0)						# OP start + 10min, HOP206 at S-pole						
<b>PROG= 02 Inf.-time(s)</b>													
└─ <b>Subr= 1 1-time(s) 2.0sec</b>													
└─ <b>Seqn= 16 2-time(s) 2.0sec</b>													
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└─ <b>Subr= 2 1-time(s) 2.0sec</b>													
└─ <b>Seqn= 90 1-time(s) 30.0sec</b>													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
└─ <b>Subr= 3 60-time(s) 2.0sec</b>													
└─ <b>Seqn= 57 1-time(s) 30.0sec</b>													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #1BD4: AR (Filter-Ratio with Al/poly and thin-Be) with PFB, 512x512 at 1064 1048, thick-Al context, with G-band (1ms/1ms leak), 30s cad-2</b>													
Term	Pointing (x, y)						Comment						
03/03 17:00:00 - 03/03 18:01:54	Fixed ( 923.0, 98.0)						HOP307						
03/03 18:15:00 - 03/03 23:51:30	Fixed ( 923.0, 98.0)						HOP307						
03/04 03:12:00 - 03/04 05:31:54	Fixed ( 923.0, 98.0)						HOP307						

<b>PROG= 19 Inf.-time(s)</b>													
└─ <b>Subr= 1 1-time(s) 2.0sec</b>													
└─ <b>Seqn= 92 1-time(s) 2.0sec</b>													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ <b>Seqn= 71 3-time(s) 2.0sec</b>													
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	3	0	2.0sec
└─ <b>Subr= 2 100-time(s) 30.0sec</b>													
└─ <b>Seqn= 89 1-time(s) 15.0sec</b>													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0	14.0sec
└─ <b>Seqn= 58 1-time(s) 12.0sec</b>													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #1BB4: Synoptic Q95 2x2 - Al/mesh(24/256/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(45/512/4096) + T</b>													
Term	Pointing (x, y)						Comment						
03/03 18:09:00 - 03/03 18:11:54	Fixed ( 0.0, 0.0)						synoptic, shifted 2.0 min						
03/04 05:35:00 - 03/04 05:41:54	Fixed ( 0.0, 0.0)						synoptic, shifted -28.0 min						

<b>PROG= 15 1-time(s)</b>													
└─ <b>Subr= 1 1-time(s) 2.0sec</b>													
└─ <b>Seqn= 5 1-time(s) 2.0sec</b>													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ <b>Seqn= 1 1-time(s) 2.0sec</b>													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ <b>Seqn= 99 1-time(s) 2.0sec</b>													
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ <b>Seqn= 33 1-time(s) 2.0sec</b>													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ <b>Seqn= 23 1-time(s) 2.0sec</b>													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #1BE2: HOP349 - 3-filter Synoptics (Al-mesh[24/256/2897], Al-poly[45/512/4096], thin-Be[1024/11571/23142] with 512x512 G-band+Leak - 90min cad) +</b>													
Term	Pointing (x, y)						Comment						
03/04 00:08:00 - 03/04 02:50:30	Fixed ( 0.0, 0.0)						HOP349						
<b>PROG= 07 Inf.-time(s)</b>													

<b>Subr= 1 1-time(s) 300.0sec</b>													
<b>Seqn= 1 1-time(s) 2.0sec</b>													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 99 1-time(s) 2.0sec</b>													
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 33 1-time(s) 2.0sec</b>													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 30 1-time(s) 2.0sec</b>													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
<b>Subr= 2 18-time(s) 300.0sec</b>													
<b>Seqn= 8 1-time(s) 2.0sec</b>													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
<b>Seqn= 6 1-time(s) 2.0sec</b>													
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
<b>Seqn= 29 1-time(s) 2.0sec</b>													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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**Flare mode**

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**XOB #1B8E: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512**

Term	Pointing (x, y)	Comment
03/03 11:00:00 - 03/03 16:56:54	Fixed ( -20.0, -951.0)	# OP start + 10min, HOP206 at S-pole
03/03 17:00:00 - 03/03 18:01:54	Fixed ( 923.0, 98.0)	HOP307
03/03 18:15:00 - 03/03 23:51:30	Fixed ( 923.0, 98.0)	HOP307
03/04 00:08:00 - 03/04 02:50:30	Fixed ( 0.0, 0.0)	HOP349
03/04 03:12:00 - 03/04 05:31:54	Fixed ( 923.0, 98.0)	HOP307

**PROG= 13 30-time(s)**

<b>Subr= 1 20-time(s) 2.0sec</b>													
<b>Seqn= 11 1-time(s) 2.0sec</b>													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Seqn=100 1-time(s) 10.0sec</b>													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
	med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>													
<b>Seqn= 10 1-time(s) 2.0sec</b>													
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 11 1-time(s) 2.0sec</b>													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Seqn= 87 1-time(s) 2.0sec</b>													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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**Active Region Search**

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NOT USED

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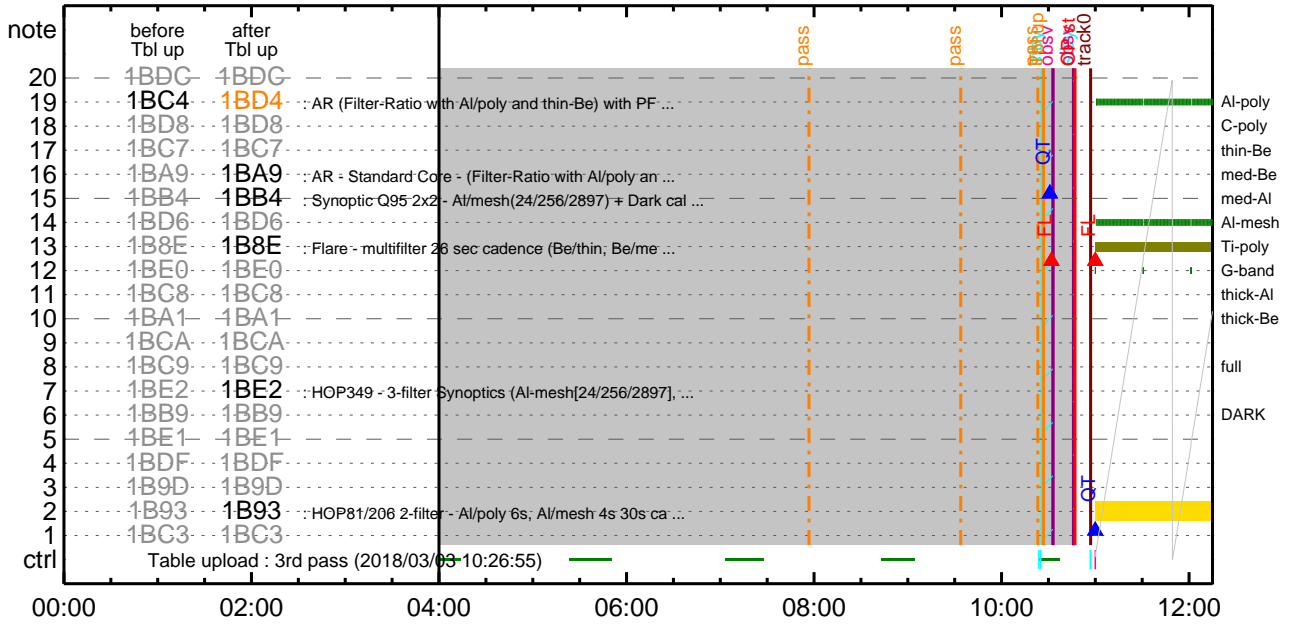
**Flare Detection**

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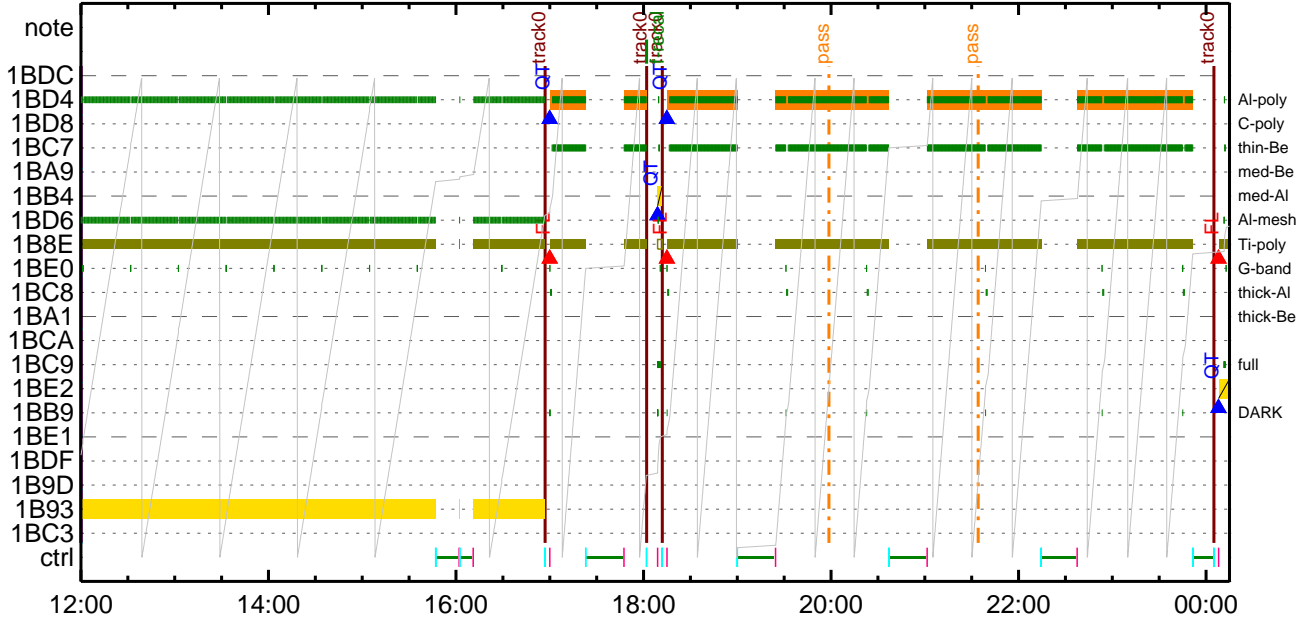
**FLD Patrol**

Term	Pointing (x, y)	Comment										
03/03 18:12:18 - 03/04 05:32:18	Fixed ( 923.0, 98.0)	HOP307										
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

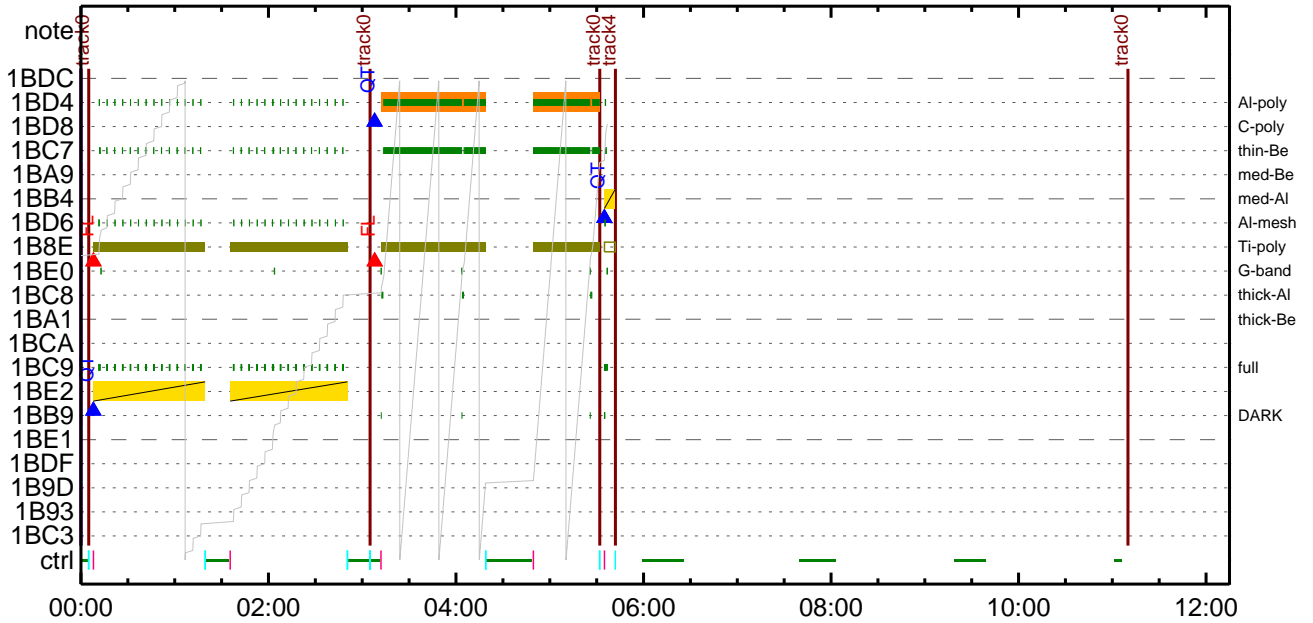
### CMDI #0348 2018/03/03



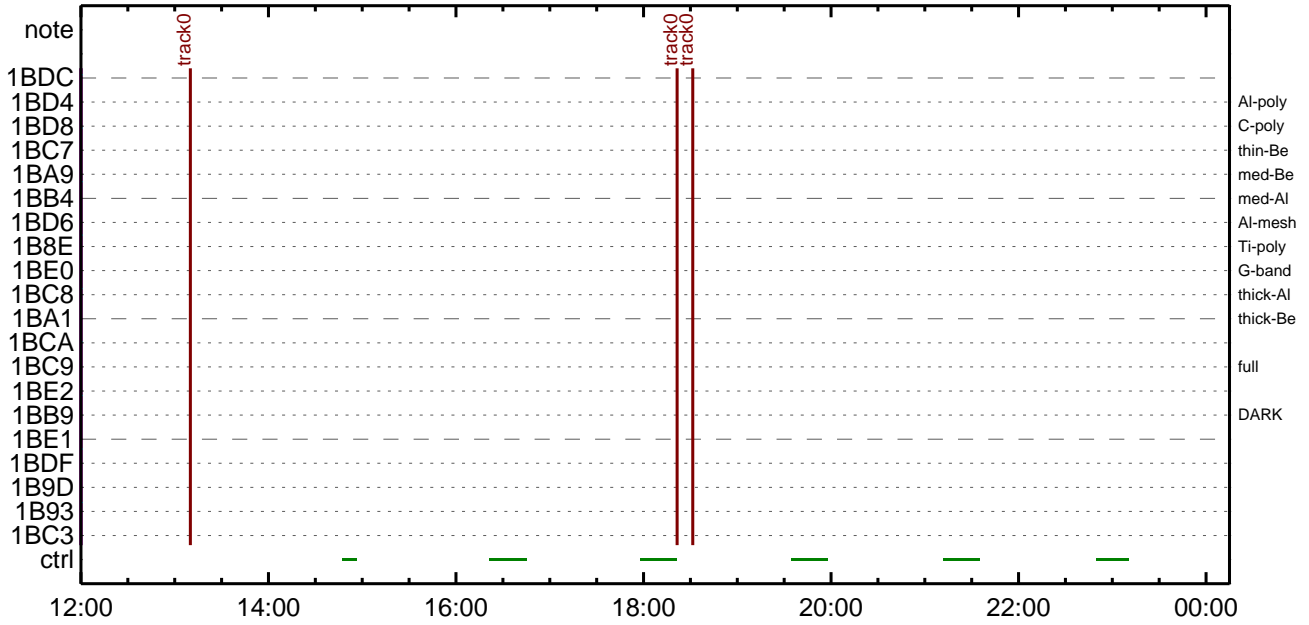
### CMDI #0348 2018/03/03



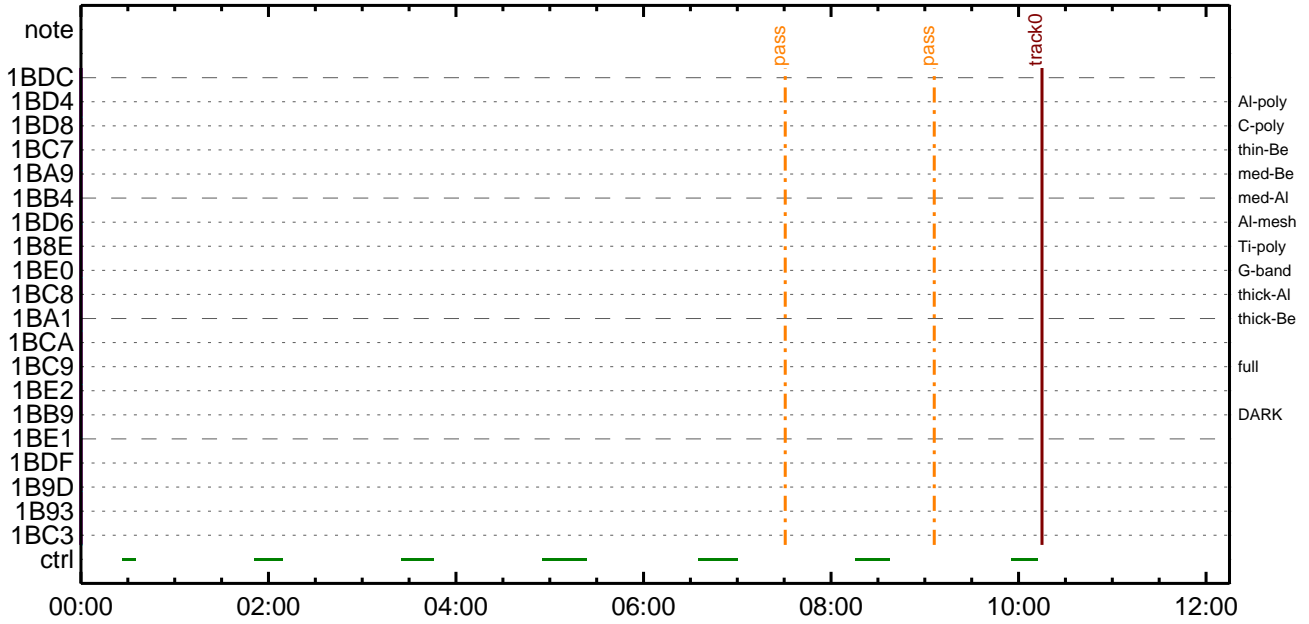
### CMDI #0348 2018/03/04



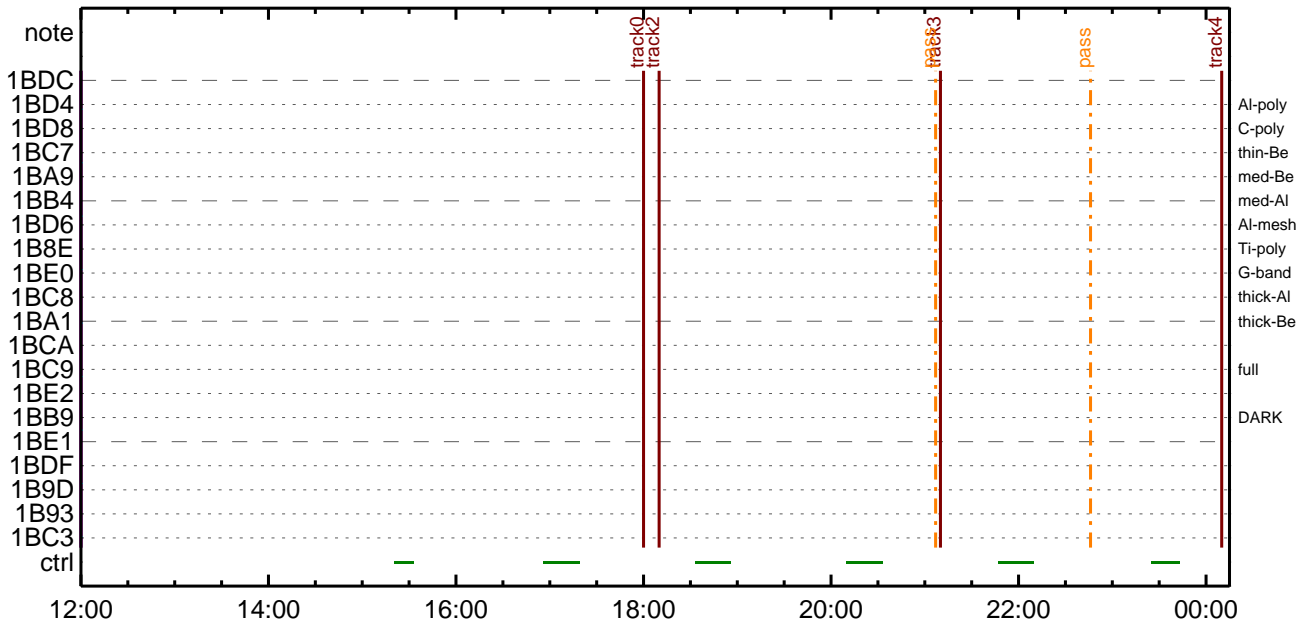
### CMDI #0348 2018/03/04



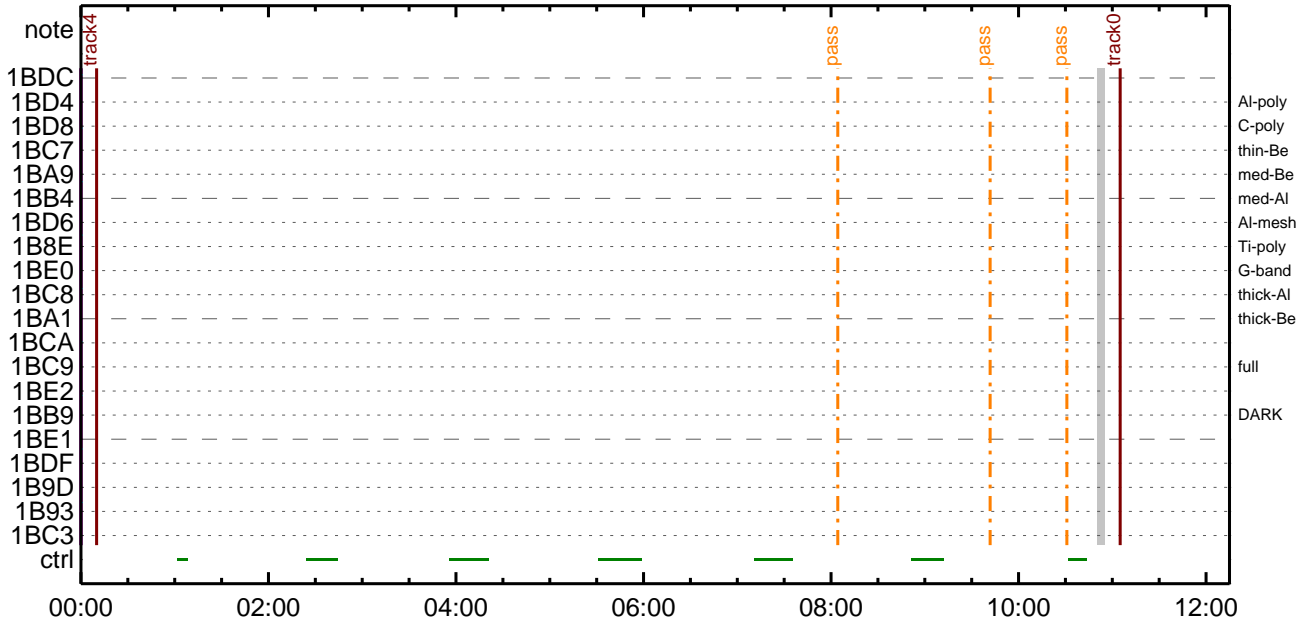
### CMDI #0348 2018/03/05



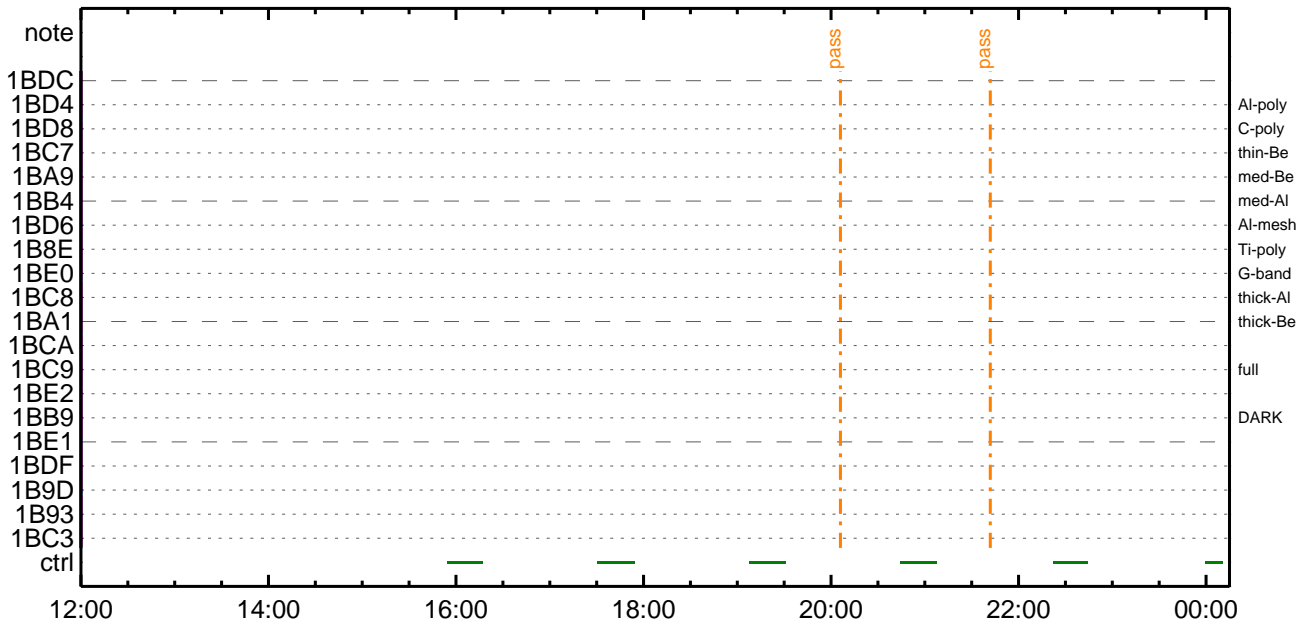
### CMDI #0348 2018/03/05



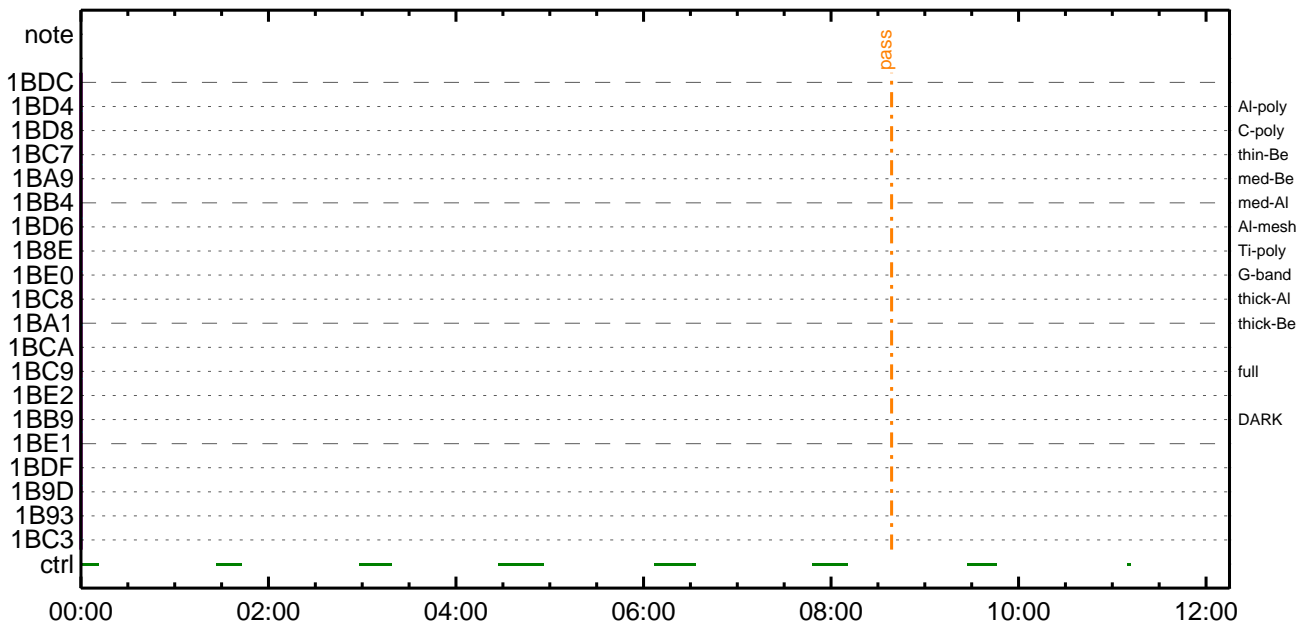
CMDI #0348 2018/03/06



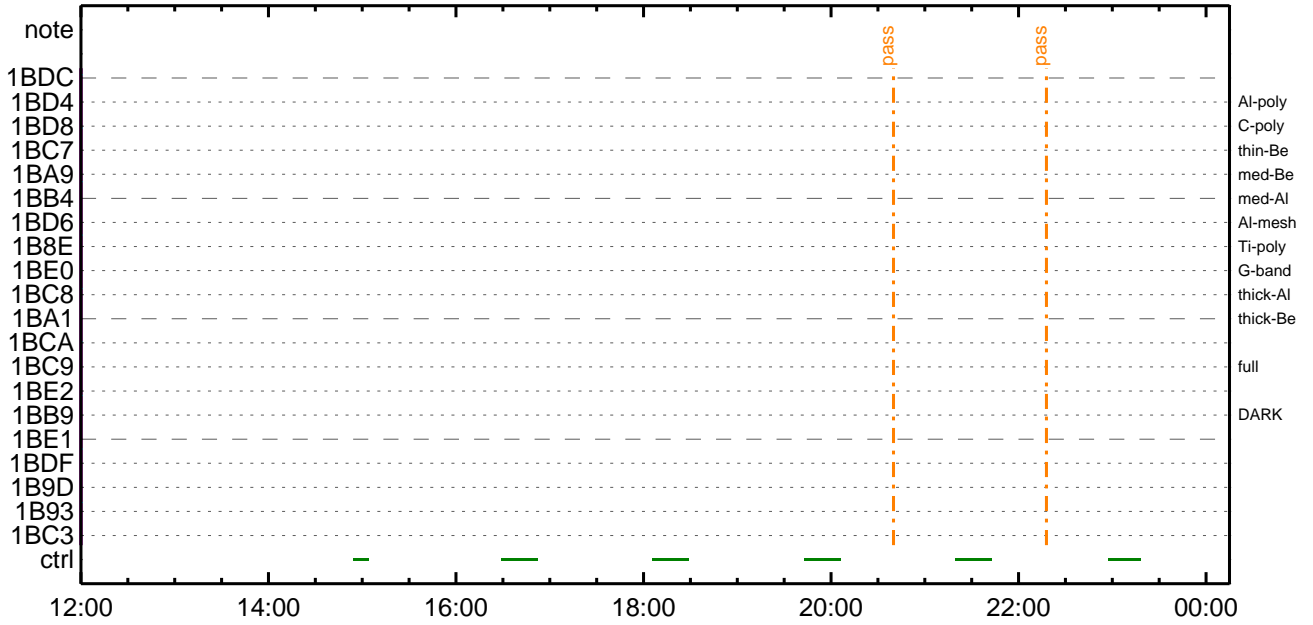
CMDI #0348 2018/03/06



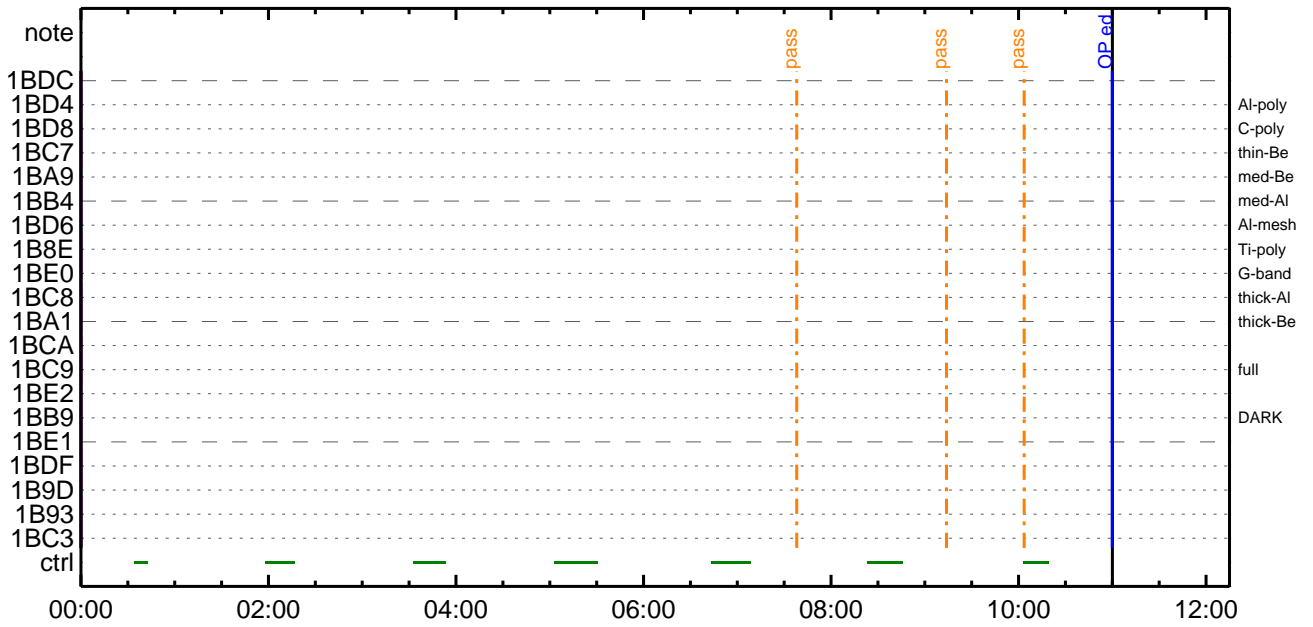
CMDI #0348 2018/03/07



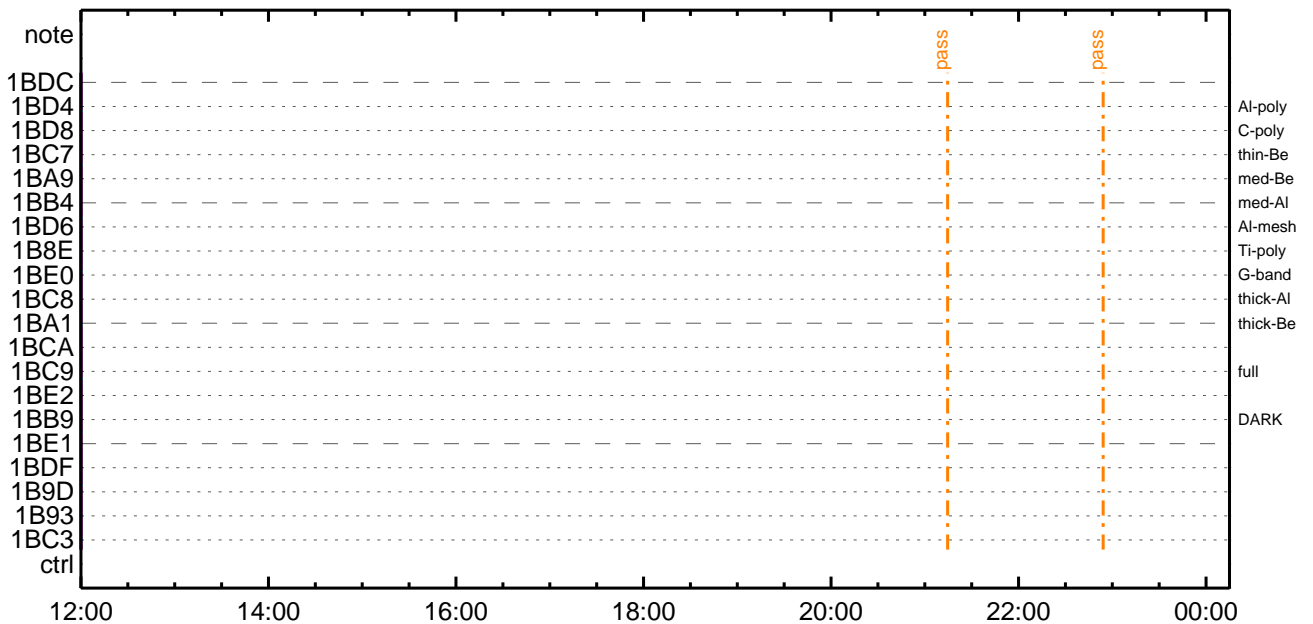
CMDI #0348 2018/03/07



CMDI #0348 2018/03/08



CMDI #0348 2018/03/08



(a) Spacecraft Operation Procedure (real-commands)

```
main-215 2018-03-03 13:34:55 273 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÄY$YÄY-¼Ä»Û;ä
0005 C.
0006 C. YÄYB;¼Y³YBYÖYÉÄ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Äí;Ëç¿ðÄð•µºÆ»Í×ÁÇðÍYçYÄY×YÍ;¼YÉ;ËËè%µ•ííË;ËðÈ¼ºÇÖð•ç¿ì¹ççÍ;çÄ®, ùñ¹ðèððçÇÄ+ç®ð•ðÈððð³ðÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÄ+ç®µ;ON
0016 C. *****
0017 C. ç“ °ÄÄ, í×ËYðÄLOððççðí»p´Öðð¹íí, ð•; çÉÖí×ðÈXÄÖONðí¹ÖðÈðìðÈððð³ðÈ;f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 +. DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 +. DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYÖYÉYÍYÄY-¾ÖÄÖð¬°ÄÄèð•ç¿çé; çºÈ²¼çíºÆÄ, ¼è%ççðð¼Ä¹Öç¹çé;f
0030 C.
0031 . C. *****
0032 C. DR PT1 Äí¼íºÆÄ,
0033 C. *****
0034 C. ç“ RESTART;ËPT1;Ëð•ç¿çç¼ì¹ççÍ; çºÈ²¼çí¼Ä¹Öççº; çDCBC-150ççËÈçç;f
0035 C.
0036 . C. ;ãPT1ºÆÄ, ³«»Ï;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 +. DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 +. DC 06-B3 DR_REP_START
0041 +. DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYçYÖYÄYËÄÜÄÖ;ËÄ•Äºöèð;Ë, äçîºÆÄ, °Æ³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 +. DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1ºÆÄ, ç-¼«ÆºÄä»ßççç¿, ä; çºÈ²¼çç¼Ä¹Öç¹çé;f
0055 C. YçYÖYÄYËÄÜÄÖððÄÄ•Äºöèð¬çç¼ì¹ççÍººÍ»ð¹çèððçÇÄÖçç;f
0056 C.
0057 . C. *****
0058 C. DR PT2 Äí¼íºÆÄ,
0059 C. *****
0060 C. ç“ RESTART;ËPT2;Ëð•ç¿çç¼ì¹ççÍ; çºÈ²¼çí¼Ä¹Öççº; çDCBC-151ççËÈçç;f
0061 C.
0062 . C. ;ãPT2ºÆÄ, ³«»Ï;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 +. DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 +. DC 06-B3 DR_REP_START
0067 +. DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYçYÖYÄYËÄÜÄÖ;ËÄ•Äºöèð;Ë, äçîºÆÄ, °Æ³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 +. DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DRºÆÄ, Ää»ß; çXÄ+ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDRºÆÄ, Ää»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 +. DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÄ+ç®µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 +. DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
```

```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;aOP/OGY1;4YE;a
0103 S. OP op-215:OP
0104 ( )
0105 S. OG og-215:OG
0106 ( )
0107 C.
0108 C. ;aNMOG&OPf^°eYAYOX;a
0109 C. NMOG(0x200000-0x207FFF;s 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C.          c{[HK1_DMP_TOP_ADRS_1]          EQ 40
0113 C.          c{[HK1_DMP_TOP_ADRS_0]          EQ 0
0114 C.          c{[HK1_DMP_BLOCK_NUM]          EQ 127
0115 C.          c{[HK1_DMP_REPEAT_NUM]         EQ 0
0116 C.          c{[HK1_DMA_DMP_PIM]            EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C.          c{[HK1_PKT_FORM_NO]             EQ 7
0120 C.          c{[HK1_PKT_GEN_TIME]           EQ 0.25 s
0121 C.          c{[HK1_S_TLM_BIT_RATE]         EQ 32k
0122 C.          c{[HK1_X_TLM_BIT_RATE]         EQ 4M
0123 C.          c{[HK1_DMP_CHK_FLG]           EQ EXEC
0124 C. YAYOXx1/2^I»oð³IÇS
0125 C.          c{[HK1_DMP_CHK_FLG]            EQ NON
0126 C. RAM ID=NMOGqfE¹ç•ë²IOKqð³IÇS
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;s 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C.          c{[HK1_DMP_TOP_ADRS_1]          EQ 41
0132 C.          c{[HK1_DMP_TOP_ADRS_0]          EQ 0
0133 C.          c{[HK1_DMP_BLOCK_NUM]          EQ 127
0134 C.          c{[HK1_DMP_REPEAT_NUM]         EQ 0
0135 C.          c{[HK1_DMA_DMP_PIM]            EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C.          c{[HK1_PKT_FORM_NO]             EQ 7
0139 C.          c{[HK1_PKT_GEN_TIME]           EQ 0.25 s
0140 C.          c{[HK1_S_TLM_BIT_RATE]         EQ 32k
0141 C.          c{[HK1_X_TLM_BIT_RATE]         EQ 4M
0142 C.          c{[HK1_DMP_CHK_FLG]           EQ EXEC
0143 C. YAYOXx1/2^I»oð³IÇS
0144 C.          c{[HK1_DMP_CHK_FLG]            EQ NON
0145 C. RAM ID=NMOGqfE¹ç•ë²IOKqð³IÇS
0146 C.
0147 C. NMOG(0x210000-0x2100FF;s 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C.          c{[HK1_DMP_TOP_ADRS_1]          EQ 42
0151 C.          c{[HK1_DMP_TOP_ADRS_0]          EQ 0
0152 C.          c{[HK1_DMP_BLOCK_NUM]          EQ 65
0153 C.          c{[HK1_DMP_REPEAT_NUM]         EQ 0
0154 C.          c{[HK1_DMA_DMP_PIM]            EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C.          c{[HK1_PKT_FORM_NO]             EQ 7
0158 C.          c{[HK1_PKT_GEN_TIME]           EQ 0.25 s
0159 C.          c{[HK1_S_TLM_BIT_RATE]         EQ 32k
0160 C.          c{[HK1_X_TLM_BIT_RATE]         EQ 4M
0161 C.          c{[HK1_DMP_CHK_FLG]           EQ EXEC
0162 C. YAYOXx1/2^I»oð³IÇS
0163 C.          c{[HK1_DMP_CHK_FLG]            EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPqfE¹ç•ë²IOKqð³IÇS
0165 C.
0166 C. ***** oE²¼oI%Ä´¶Á°oEÉ¬oÁ÷¿@ (¼áµ-YAYOXx1/2^e¼çoðÁÓæoÇ¼ª°¬oE¼i¹çoÇoâ) *****
0167 C. DHUÿâ;4YE;E¼Y¼, Y¼;4YE;EoðIã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C.          c{[HK1_PKT_FORM_NO]             EQ 2
0171 C.          c{[HK1_PKT_GEN_TIME]           EQ 0.5S
0172 C.          c{[HK1_S_TLM_BIT_RATE]         EQ 32K
0173 C.          c{[HK1_X_TLM_BIT_RATE]         EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE |s OPOG UPLOADq¬Á÷¿@NGuI¼i¹ç;ç°E²¼oI¶TI-CMDÁ÷¿@qI¼Á¹Ôq•qEoq³qE;f
0180 C.          qBq¿;çSETqEDUMPqIÆ±°iYNY¹qC¹Ôq|q³qE;f
0181 C.
0182 C. TIY³YB¼YOXEoðÁD¿¿(UT)
0183 +. TI 2018-03-03 10:42:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C.          c{[HK1_TI_CMD_NUM]              EQ 1COUNTUP
0186 C.
0187 +. TI 2018-03-03 10:42:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C.          c{[HK1_TI_CMD_NUM]              EQ 1COUNTUP
0190 C.
0191 +. TI 2018-03-03 10:42:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C.          c{[HK1_TI_CMD_NUM]              EQ 1COUNTUP

```





(a) Spacecraft Operation Procedure (real-commands)

```

main-216 2018-03-03 13:34:55 144 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSÝÁÝSÝÄÝ~¼Ä»Û;ã
0005 C.
0006 C. ÝÀÝß;¼Ý³ÝFÝÓÝÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿ãÁ·μ°È»Í×ÁÇ¿ÍÝÇÝÄÝ×ÝÍ;¼ÝÉ;ÈÈÈ¼μ·íÉ;ÈãÈ¼°Ç¿ã·¿¼ì¹¿ãÍ;¿À®, ù¿¹ãÈãßãÇÄ+¿®ã·ãÈãã¿ãÈ; É
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+¿@μ;ON
0016 C. *****
0017 C. ¿ °Ä, Í×ÈÝãÄLOSãßãÇãÍ»¿ Öã¿íÍ, μ.; ¿ÈÖÍ×ãÈXÁÓONãÍ¹ÖãÈíãÈãã¿ãÈ; É
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ¿¿[HK1_XPA_ON/OFF] EQ ON
0025 C. ¿¿[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ¿¿[HK1_XMOD_ON/OFF] EQ ON
0027 C. ¿¿[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDÝÓÝÉÝÍÝÄÝ~¼ÖÄÖã~ãÄêã·¿¿;¿; ¿°È²¼ãÍ°Ä, ¼ê¼¿ã¿¼Ä¹Öã¹¿¿; É
0030 C.
0031 . C. *****
0032 C. DR PT1 ÁÍ¼í°Ä,
0033 C. *****
0034 C. ¿ " RESTART;ÈPT1;Èã·¿¿ã¼ì¹¿ãÍ;¿°È²¼ãÍ¼Ä¹Öã»°; ¿DCBC-150¿¿¿È¿ã; É
0035 C.
0036 . C. ;ãPT1°Ä, ³«»Í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ¿¿[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0043 C. ¿¿[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0044 C. ¿¿[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãÝÇÝÓÝÈÝÄÝÁÝÛ;ÈÄ·Ä°¿öÈ¿;È, áãÍ°Ä, °È³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ¿¿[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0050 C. ¿¿[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0051 C. ¿¿[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°Ä, ã~¼«È°Ä»ßã·¿¿;ã; ¿°È²¼ã¿¼Ä¹Öã¹¿¿; É
0055 C. ÝÇÝÓÝÈÝÄÝÁÝÛ;ÈÄ·Ä°¿öÈ¿ã~¾áã¼ì¹¿ãÍ°Í»ã¹¿¿ãßãÇÄÖ¿ã; É
0056 C.
0057 . C. *****
0058 C. DR PT2 ÁÍ¼í°Ä,
0059 C. *****
0060 C. ¿ " RESTART;ÈPT2;Èã·¿¿ã¼ì¹¿ãÍ;¿°È²¼ãÍ¼Ä¹Öã»°; ¿DCBC-151¿¿¿È¿ã; É
0061 C.
0062 . C. ;ãPT2°Ä, ³«»Í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ¿¿[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0069 C. ¿¿[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0070 C. ¿¿[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãÝÇÝÓÝÈÝÄÝÁÝÛ;ÈÄ·Ä°¿öÈ¿;È, áãÍ°Ä, °È³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ¿¿[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0076 C. ¿¿[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0077 C. ¿¿[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°Ä, Ää»ß; ¿XÁ+¿@μ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°Ä, Ää»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ¿¿[HK1_REP_STA/STP] EQ STOP
0087 C. ¿¿[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ¿¿[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+¿@μ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ¿¿[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ¿¿[HK1_XPA_ON/OFF] EQ OFF

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```

0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 C.
0130 . C. ***** MDP 'ûÃîñî»ò¼ÿñÈÂðñ¹ñèDCBC•x²è *****
0131 C. (¼â°îÿÓÿÃÿÈÿPÿËÿáÿçÿèñÈ¼ñ¼Ã»Ûñ¹ñè)
0132 . S. DC-BC dcbc-402:DCBC
0133 (MDP_known_event)
0134 C.
0135 C.
0136 . C. ***** ÿDÿ¹•Ï Daily±;îÑñÈ'Øñ¹ñèDCBC•x²è *****
0137 . S. DC-BC dcbc-153:DCBC
0138 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0139 C.
0140 C.
0141 . C. ;ãLOSÿÃÿSÿÿÃÿ~¼Ã»Û;ã
0142 C.
0143 . C. ***** LOS *****
0144 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
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0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁY$;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;È¿òÀò•µ°È»Í×ÁÇóÍYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;ÈòÈ¼°ÇÒò•ò¿¼í¹çòÍ;çÀ®, ùò¹òèòòòçÁ+¿®ò•òÈòòò³òÈ;f
0011 +. DC 02-8E AOCS_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop SP table >
0018 +. DC 07-F0 MDP_SP_CTRL_MANU
0019 BC (61)
0020 C. -----
0021 C. MDP_SP_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload SP Observation Table>
0025 . S. RAM ram-281:MDP_OBS_S
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_S >
0029 +. DC 07-F0 MDP_DUMP_SPTBL
0030 BC (83 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_S verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2018-03-03 10:46:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 . C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 +. DC 07-F0 MDP_XRT_CTRL_MANU
0052 BC (c1)
0053 +. DC 07-F0 MDP_XRT_MODE_STBY
0054 BC (c3)
0055 . C. ----- Success Verify ? OK / NG_____
0056 C.
0057 C. XRT Obs. Table Upload
0058 . S. RAM ram-291:MDP_OBS_X
0059 ( )
0060 C.
0061 +. DC 07-F0 MDP_DUMP_XRTTBL
0062 BC (84 07 00 00 00 3a d4)
0063 . C. ----- Comparison Check ? OK / ERR _____
0064 C.
0065 C.
0066 +. DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 01 b1 b1 04 04)
0068 +. DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 02 b1 b1 08 08)
0070 +. DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 03 b1 b1 08 08)
0072 +. DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 04 b1 b1 06 06)
0074 +. DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 05 85 83 06 06)
0076 +. DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 06 85 83 06 06)
0078 +. DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 07 85 83 08 08)
0080 +. DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 08 80 80 20 20)
0082 +. DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 09 80 80 20 08)
0084 +. DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0a 80 80 08 20)
0086 +. DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 0b 80 80 08 08)
0088 +. DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 0f 80 80 06 06)
0090 +. DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 10 80 80 08 08)
0092 +. DC 07-F0 MDP_XRT_FLD_ENA
0093 BC (d8)
0094 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0095 BC (c8)
```

```
0096 + DC 07-F0 MDP_XRT_ARS_DIS
0097 BC (d5)
0098 + DC 07-F0 MDP_XRT_AEC_RESET
0099 BC (d0)
0100 + DC 07-F0 MDP_XRT_FLD_RESET
0101 BC (da)
0102 + DC 07-F0 MDP_XRT_QT_PROG_SET
0103 BC (c4 10)
0104 + DC 07-F0 MDP_XRT_FL_PROG_SET
0105 BC (c5 0d)
0106 . C. ----- Success Verify ? OK / NG ____
0107 C.
0108 C.
0109 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0110 C.
0111 + DC 07-F0 MDP_XRT_MODE_OBSV
0112 BC (c2)
0113 + TI 2018-03-03 10:46:02.0
0114 DC 07-F0 MDP_XRT_MODE_OBSV
0115 BC (c2)
0116 . C. ----- Success Verify ? OK / NG ____
0117 C.
0118 C. ***** XRT END *****
0119 C.
0120 . C. ***** MDP `úÃîñî»ö%ÝñÊÃðñ¹ñèDCBC•x²è *****
0121 C. (%á°îÝÓÝÃÝÈÝÞÝËÝáÝçÝèñÊ½¼ñ¼Â»Ûñ¹ñè)
0122 . S. DC-BC dcbc-402:DCBC
0123 (MDP_known_event)
0124 C.
0125 C.
0126 . C. ***** ÝDÝ¹•Ï Daily±¿ÍÑñÈ´Øñ¹ñèDCBC•x²è *****
0127 . S. DC-BC dcbc-153:DCBC
0128 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0129 C.
0130 C.
0131 . C. ¡ãLOSÝÁÝ$ÝÃÝ-¼Â»Ûñ¹ñè
0132 C.
0133 . C. ***** LOS *****
0134 C.
```

\*\*\* OP Sequence for XRT \*\*\*

```

2018/03/03 10:56:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/03/03 10:56:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/03/03 10:56:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2018/03/03 10:57:00.0 AOCS_Ore-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 00 54 8b 01 ca
2018/03/03 10:57:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2018/03/03 10:57:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2018/03/03 10:57:22.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2018/03/03 10:57:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2018/03/03 10:57:26.0 XRT_FLD_RESET_433_OG [0x1b1]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2018/03/03 10:59:56.0 XRT_QT_PROG_SET_437_OG [0x1b5]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 02
2018/03/03 10:59:58.0 XRT_FL_PROG_SET_440_OG [0x1b8]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 0d
2018/03/03 11:00:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2018/03/03 15:47:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/03/03 15:47:02.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/03/03 15:47:04.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2018/03/03 15:47:06.0 XRT_PREFLR_STRT_406_OG [0x196]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2018/03/03 15:50:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2018/03/03 16:01:00.0 XRT_Custom_430_OG [0x1ae]
2018/03/03 16:02:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2018/03/03 16:02:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/03/03 16:02:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/03/03 16:02:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2018/03/03 16:02:36.0 XRT_PREFLR_STRT_406_OG [0x196]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2018/03/03 16:05:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2018/03/03 16:10:00.0 XRT_Custom_430_OG [0x1ae]
2018/03/03 16:11:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2018/03/03 16:56:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/03/03 16:56:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/03/03 16:56:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2018/03/03 16:57:00.0 AOCS_Ore-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 00 f7 4c ad f4
2018/03/03 16:57:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2018/03/03 16:57:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2018/03/03 16:57:22.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2018/03/03 16:57:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2018/03/03 16:57:26.0 XRT_FLD_RESET_433_OG [0x1b1]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2018/03/03 16:59:56.0 XRT_QT_PROG_SET_444_OG [0x1bc]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 13
2018/03/03 16:59:58.0 XRT_FL_PROG_SET_440_OG [0x1b8]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 0d
2018/03/03 17:00:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2018/03/03 17:23:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/03/03 17:23:02.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/03/03 17:23:04.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2018/03/03 17:23:06.0 XRT_PREFLR_STRT_406_OG [0x196]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2018/03/03 17:26:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2018/03/03 17:46:30.0 XRT_Custom_430_OG [0x1ae]
2018/03/03 17:47:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2018/03/03 18:01:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/03/03 18:01:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1

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2018/03/03	18:01:58.0	XRT_FOCUS_RECALIBRATE_416_OG [0x1a0]							
		XRT_FOCUS_RECAL	2	07-F8	78	00			
2018/03/03	18:02:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	00	00
2018/03/03	18:05:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2018/03/03	18:06:18.0	XRT_FLD_DIS_425_OG [0x1a9]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2018/03/03	18:08:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2018/03/03	18:08:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/03/03	18:08:58.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f			
2018/03/03	18:09:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/03/03	18:11:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/03/03	18:11:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/03/03	18:11:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2018/03/03	18:12:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	f7	4c	ad	f4
2018/03/03	18:12:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2018/03/03	18:12:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2018/03/03	18:12:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2018/03/03	18:12:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/03/03	18:12:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/03/03	18:14:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	13			
2018/03/03	18:14:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2018/03/03	18:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/03/03	19:00:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/03/03	19:00:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/03/03	19:00:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/03/03	19:00:06.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/03/03	19:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/03/03	19:23:30.0	XRT_Custom_430_OG [0x1ae]							
2018/03/03	19:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/03/03	20:37:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/03/03	20:37:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/03/03	20:37:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/03/03	20:37:06.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/03/03	20:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/03/03	21:00:30.0	XRT_Custom_430_OG [0x1ae]							
2018/03/03	21:01:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/03/03	22:14:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/03/03	22:14:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/03/03	22:14:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/03/03	22:14:36.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/03/03	22:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/03/03	22:36:30.0	XRT_Custom_430_OG [0x1ae]							
2018/03/03	22:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/03/03	23:51:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/03/03	23:51:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/03/03	23:51:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/03/03	23:51:36.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/03/03	23:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/03/04	00:04:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/03/04	00:04:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				

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2018/03/04	00:04:58.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2018/03/04	00:05:00.0	AOCs_OrE-point_Start_3_OG [0x099] AOCU_NM	5	02-76	00 00 00 00 00
2018/03/04	00:05:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2018/03/04	00:05:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2018/03/04	00:05:22.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0
2018/03/04	00:05:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2018/03/04	00:05:26.0	XRT_FLD_RESET_433_OG [0x1b1] MDP_XRT_FLD_RESET	1	07-F0	da
2018/03/04	00:07:56.0	XRT_QT_PROG_SET_442_OG [0x1ba] MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2018/03/04	00:07:58.0	XRT_FL_PROG_SET_440_OG [0x1b8] MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2018/03/04	00:08:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/03/04	01:19:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/03/04	01:19:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/03/04	01:19:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2018/03/04	01:19:36.0	XRT_PREFLR_STRT_406_OG [0x196] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/03/04	01:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/03/04	01:34:30.0	XRT_Custom_430_OG [0x1ae]			
2018/03/04	01:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/03/04	02:50:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/03/04	02:50:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/03/04	02:50:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2018/03/04	02:50:36.0	XRT_PREFLR_STRT_406_OG [0x196] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/03/04	02:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/03/04	03:04:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/03/04	03:04:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/03/04	03:04:58.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2018/03/04	03:05:00.0	AOCs_OrE-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00 f7 4c ad f4
2018/03/04	03:05:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2018/03/04	03:05:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2018/03/04	03:05:22.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0
2018/03/04	03:05:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2018/03/04	03:05:26.0	XRT_FLD_RESET_433_OG [0x1b1] MDP_XRT_FLD_RESET	1	07-F0	da
2018/03/04	03:07:56.0	XRT_QT_PROG_SET_444_OG [0x1bc] MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2018/03/04	03:07:58.0	XRT_FL_PROG_SET_440_OG [0x1b8] MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2018/03/04	03:11:00.5	XRT_Custom_430_OG [0x1ae]			
2018/03/04	03:12:00.5	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/03/04	04:19:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/03/04	04:19:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/03/04	04:19:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2018/03/04	04:19:06.0	XRT_PREFLR_STRT_406_OG [0x196] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/03/04	04:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/03/04	04:48:30.0	XRT_Custom_430_OG [0x1ae]			
2018/03/04	04:49:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/03/04	05:31:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/03/04	05:31:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/03/04	05:31:58.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2018/03/04	05:32:00.0	AOCs_OrE-point_Start_3_OG [0x099] AOCU_NM	5	02-76	00 00 00 00 00
2018/03/04	05:32:18.0	XRT_FLD_DIS_425_OG [0x1a9] MDP_XRT_FLD_DIS	1	07-F0	d9
2018/03/04	05:34:54.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9



2018/03/04	05:34:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/03/04	05:34:58.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f			
2018/03/04	05:35:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/03/04	05:41:54.0	XRT_CTRL_MANU_431_OG [0x1af]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/03/04	05:42:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	04	00	00	00	00
2018/03/04	05:42:24.0	XRT_TCIB_XRT_S_HTR_A_ENA_439_OG [0x1b7]	TCIB_XRT_S_HTR_A_ENA	0	04-BC					
2018/03/04	11:10:00.0	AOCS_ORe-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00	ae	36	01	ca
2018/03/04	13:10:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	f7	4c	ad	f4
2018/03/04	18:21:30.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00	00	00	00
2018/03/04	18:31:30.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	f7	4c	ad	f4
2018/03/05	10:15:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00	56	4d	01	ca
2018/03/05	18:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00	00	00	00
2018/03/05	18:10:00.0	AOCS_ORe-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	02	00	00	00	00
2018/03/05	21:10:00.0	AOCS_ORe-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	03	00	00	00	00
2018/03/06	00:10:00.0	AOCS_ORe-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	04	02	c0	01	ca
2018/03/06	11:05:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00	00	00	00