

# XRT Timeline to be uploaded on 2018/03/27

Period: 2018/03/27 10:43:00 - 2018/03/31 10:55:00

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Normal mode

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## XOB #1BC7: CCD Monitor During Bakeout - G-band 1ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh(2048ms), Al/Poly(4096ms) - w leak image-1ms

Term	Pointing (x, y)	Comment
03/28 12:13:00 - 03/28 12:19:54	Fixed ( -528.4, -528.4)	XRT quadrant observation 1/4
<b>PROG= 05 1-time(s)</b>		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 51 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec	
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec	
└─ Open/thick-Be	Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec	
└─ Open/thick-Be	Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec	
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 3 2-time(s) 2.0sec		
└─ Open/Al-mesh	Open/Al-mesh close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Al-poly/Open	Al-poly/Open close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 34 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec	
└─ Open/G-band	Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

## XOB #1BC8: CCD Monitor During Bakeout - G-Band 1ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh (2048ms), Al/Poly (4096ms) - w leak image-1 ms

Term	Pointing (x, y)	Comment
03/28 12:23:00 - 03/28 12:29:54	Fixed ( 528.4, -528.4)	2/4
<b>PROG= 17 1-time(s)</b>		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 38 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec	
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec	
└─ Open/thick-Be	Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec	
└─ Open/thick-Be	Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec	
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 3 2-time(s) 2.0sec		
└─ Open/Al-mesh	Open/Al-mesh close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Al-poly/Open	Al-poly/Open close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 34 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec	
└─ Open/G-band	Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

## XOB #1BC9: CCD Monitor During Bakeout - G-Band 1ms - 1kx1k - Q90 - 3rd Quadrant - Al/mesh (2048ms), Al/Poly (4096ms) - w leak image-1 ms

Term	Pointing (x, y)	Comment
03/28 12:33:00 - 03/28 12:39:54	Fixed ( 528.4, 528.4)	3/4
<b>PROG= 11 1-time(s)</b>		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 21 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec	
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec	
└─ Open/thick-Be	Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec	
└─ Open/thick-Be	Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec	
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 3 2-time(s) 2.0sec		
└─ Open/Al-mesh	Open/Al-mesh close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Al-poly/Open	Al-poly/Open close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 34 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec	
└─ Open/G-band	Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

## XOB #1BCA: CCD Monitor During Bakeout - G-Band 1ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh (2048ms), Al/Poly (4096ms) - w leak image-1 ms

Term	Pointing (x, y)	Comment
03/28 12:43:00 - 03/28 12:49:54	Fixed ( -528.4, 528.4)	4/4
<b>PROG= 12 1-time(s)</b>		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 14 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec	
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec	
└─ Open/thick-Be	Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec	
└─ Open/thick-Be	Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec	
└─ Subr= 2 1-time(s) 2.0sec		



Seqn= 81	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Active Region Search

\* \* \* \* \*

NOT USED

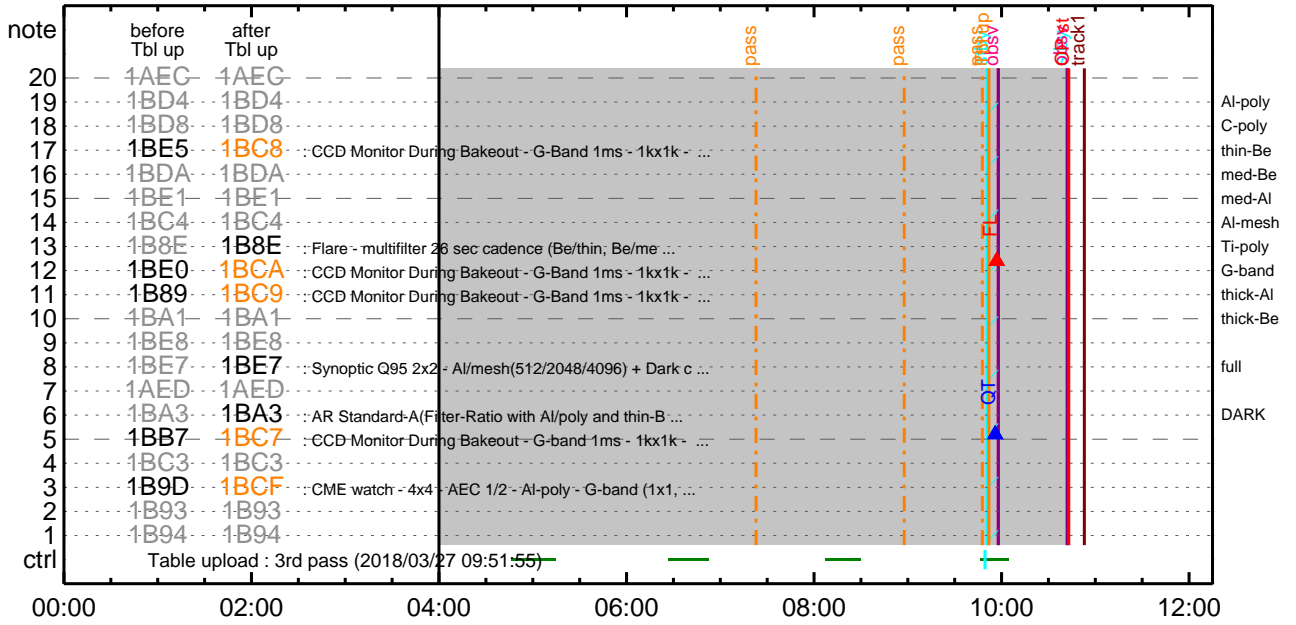
\* \* \* \* \*

### Flare Detection

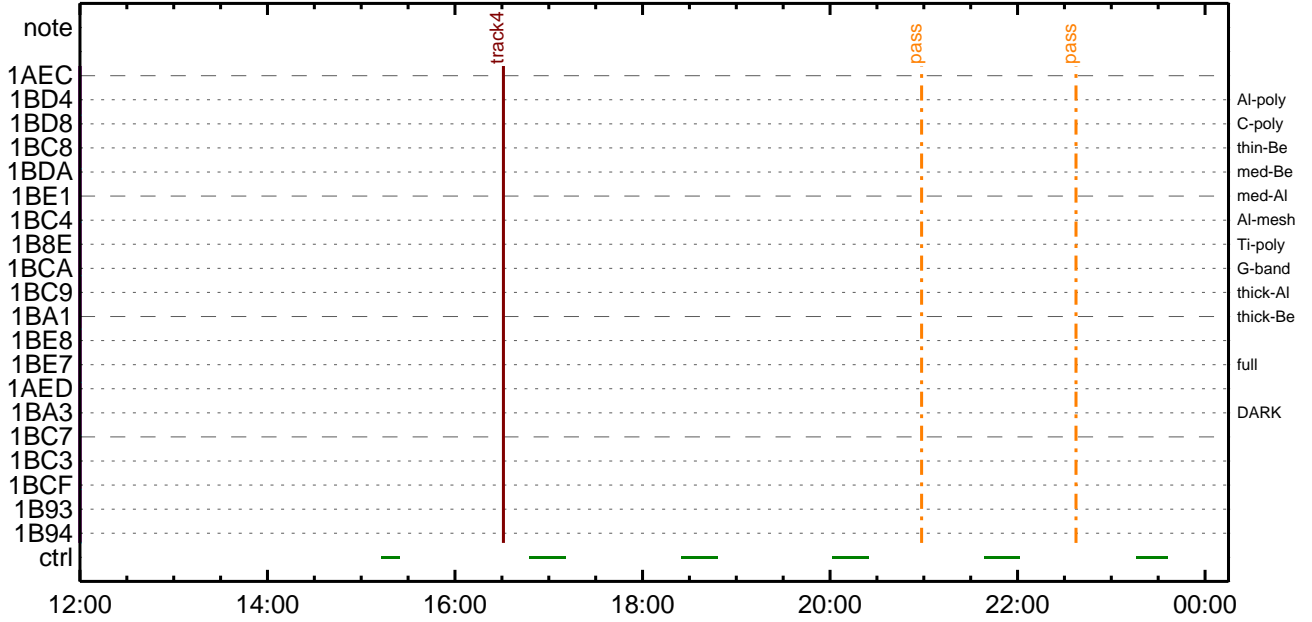
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FLD Patrol												
Term	Pointing (x, y)						Comment					
03/28 13:00:23 - 03/28 17:10:18	Track (	-30.8,	-31.0)	@ 03/28 12:50:00	DC QS obs							
03/28 17:20:18 - 03/29 05:31:48	Track (	10.9,	-31.2)	@ 03/28 17:20:00	# Cont,							
03/29 05:41:48 - 03/31 10:55:00	Track (	125.0,	-32.8)	@ 03/29 05:41:30	#Cont,							
Al-poly/Open	Al-poly/Open	close	Safe	Norm	8ms	Obs	8x8			Q=50		30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

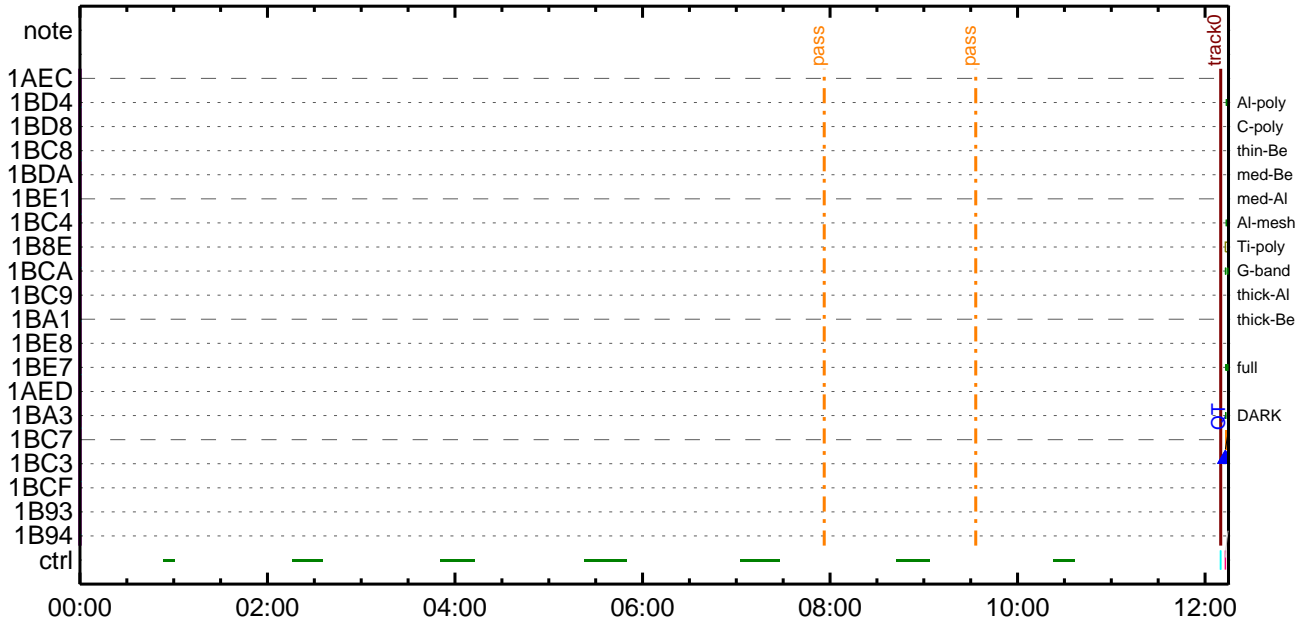
### CMDI #0396 2018/03/27



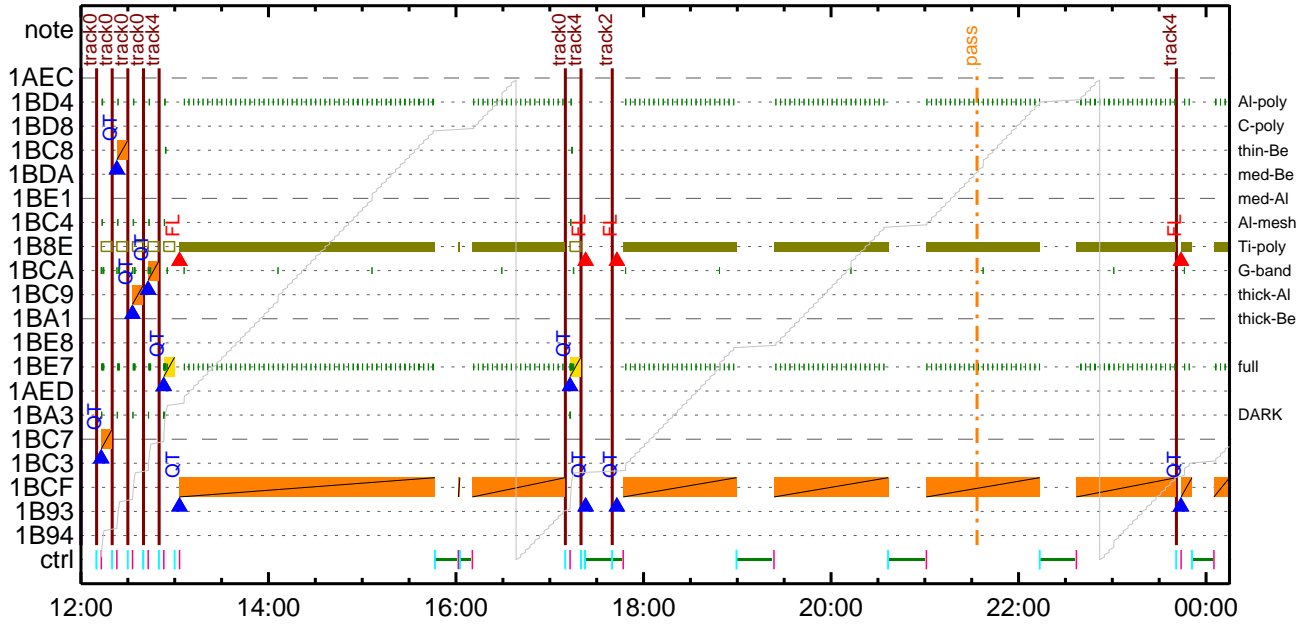
### CMDI #0396 2018/03/27



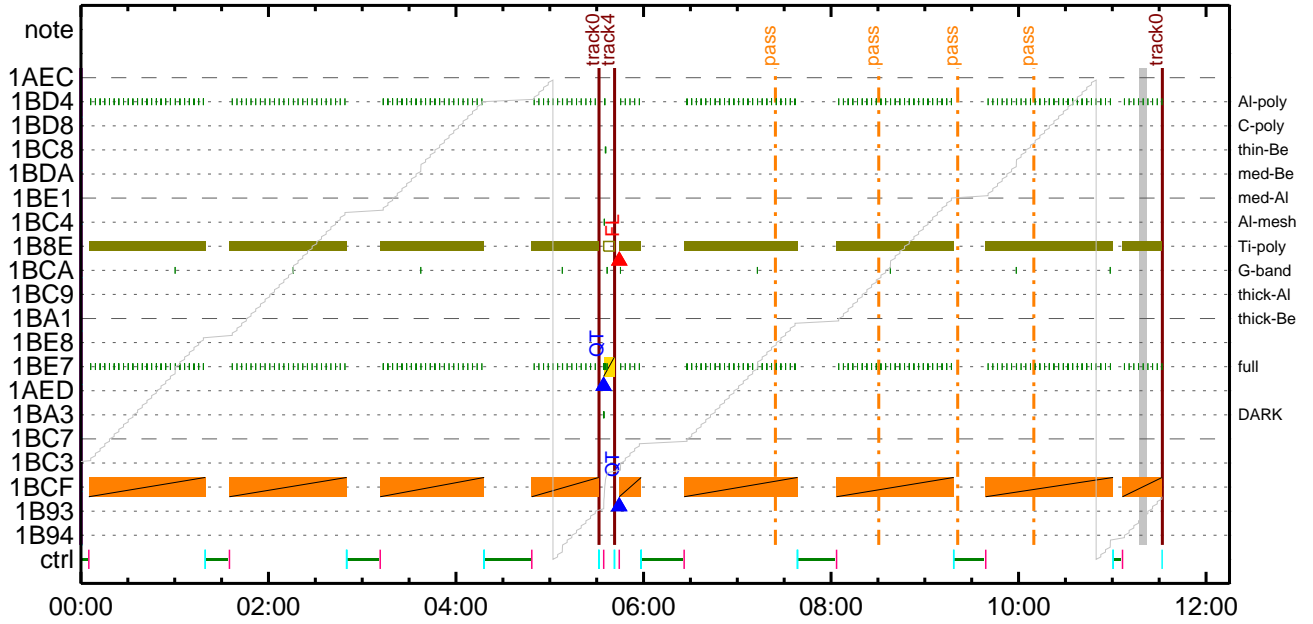
### CMDI #0396 2018/03/28



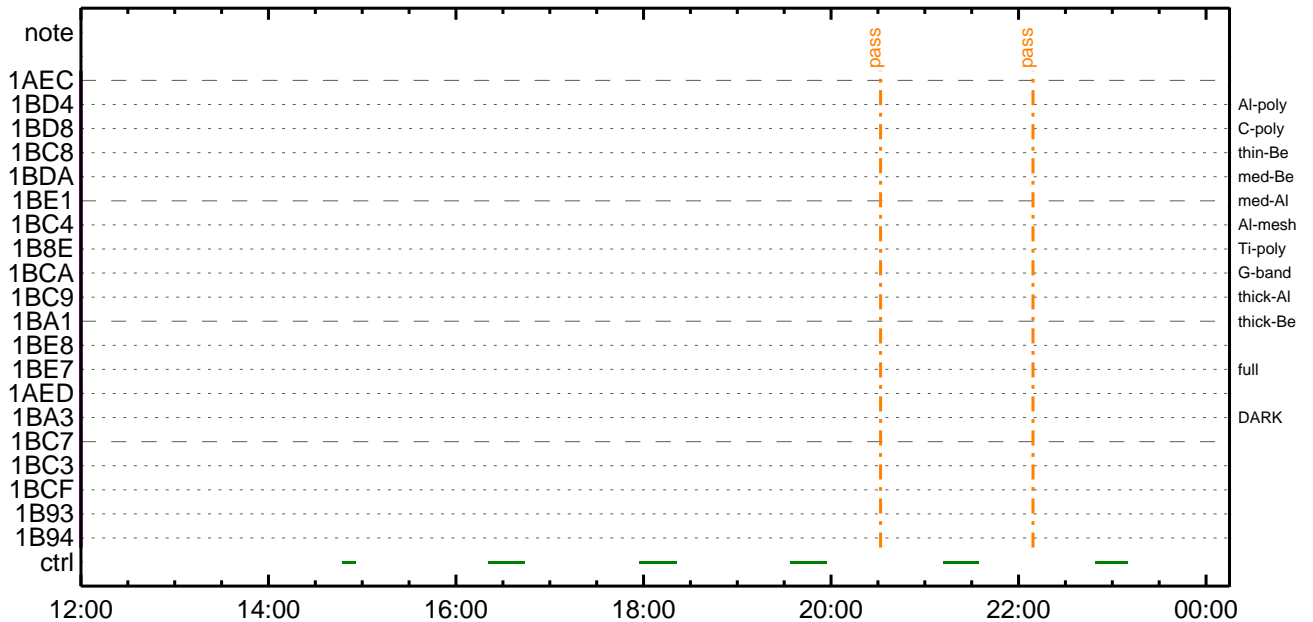
CMDI #0396 2018/03/28



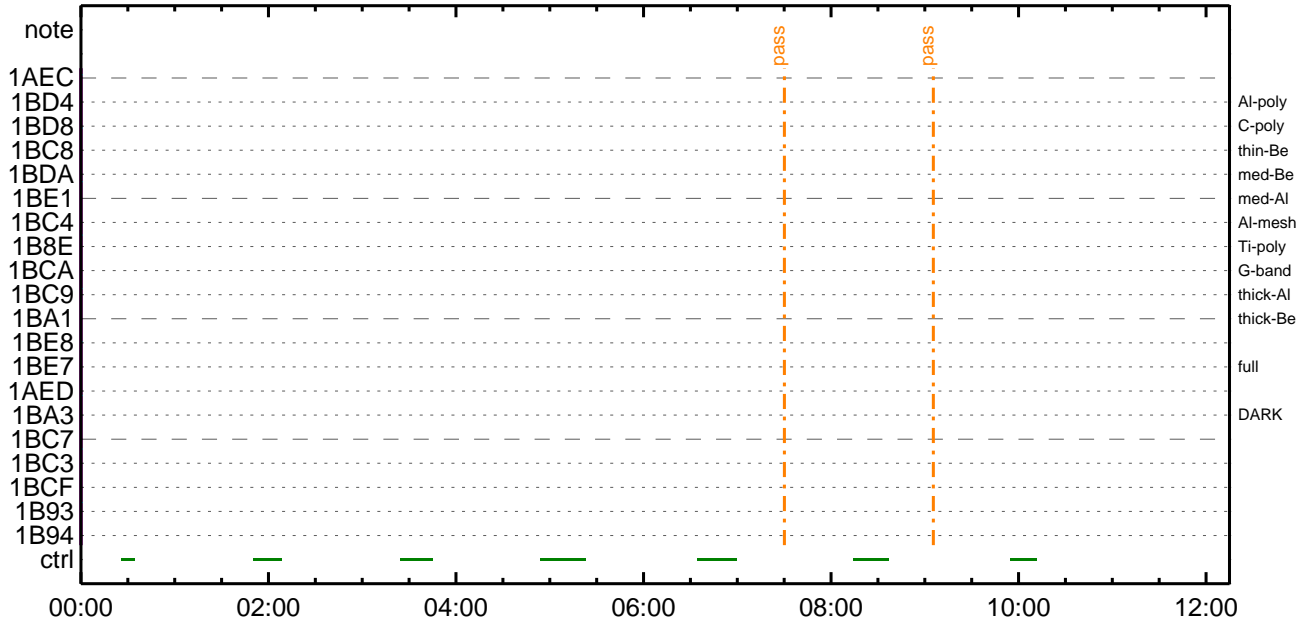
CMDI #0396 2018/03/29



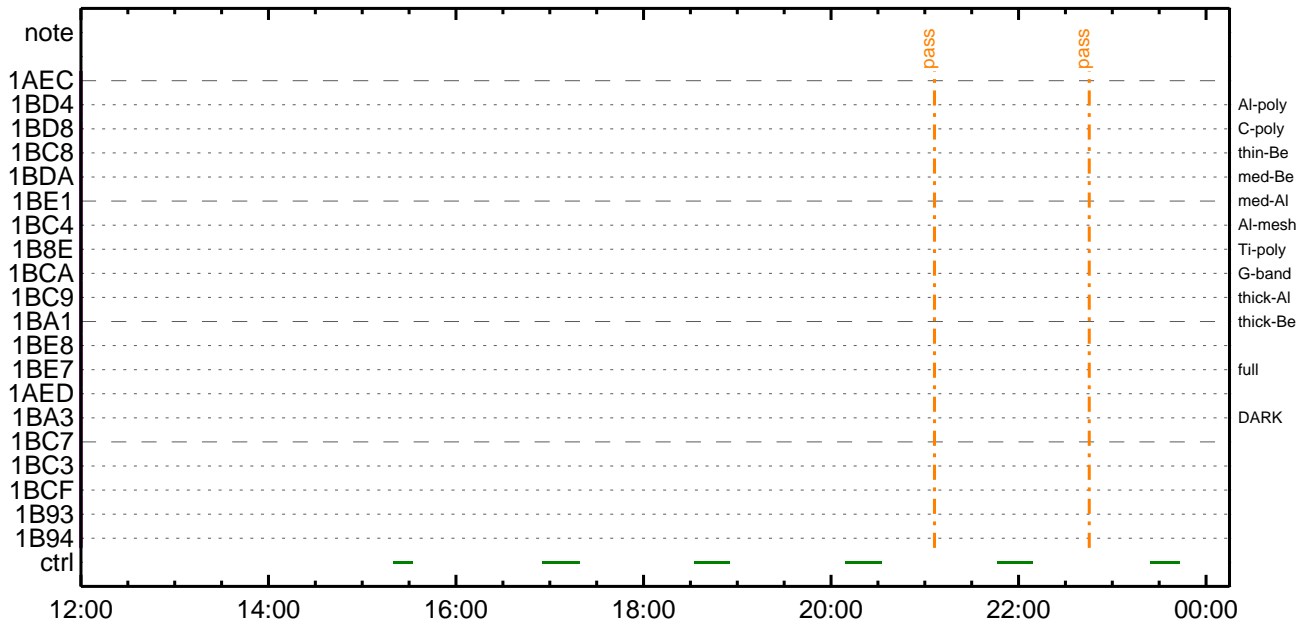
CMDI #0396 2018/03/29



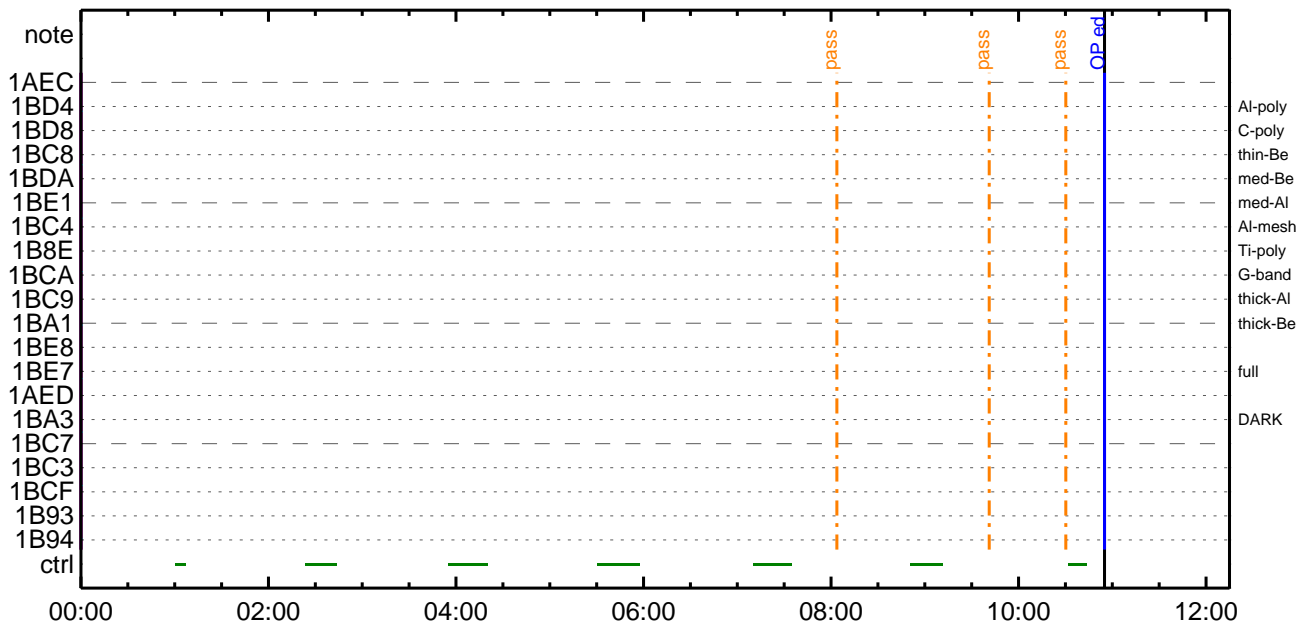
CMDI #0396 2018/03/30



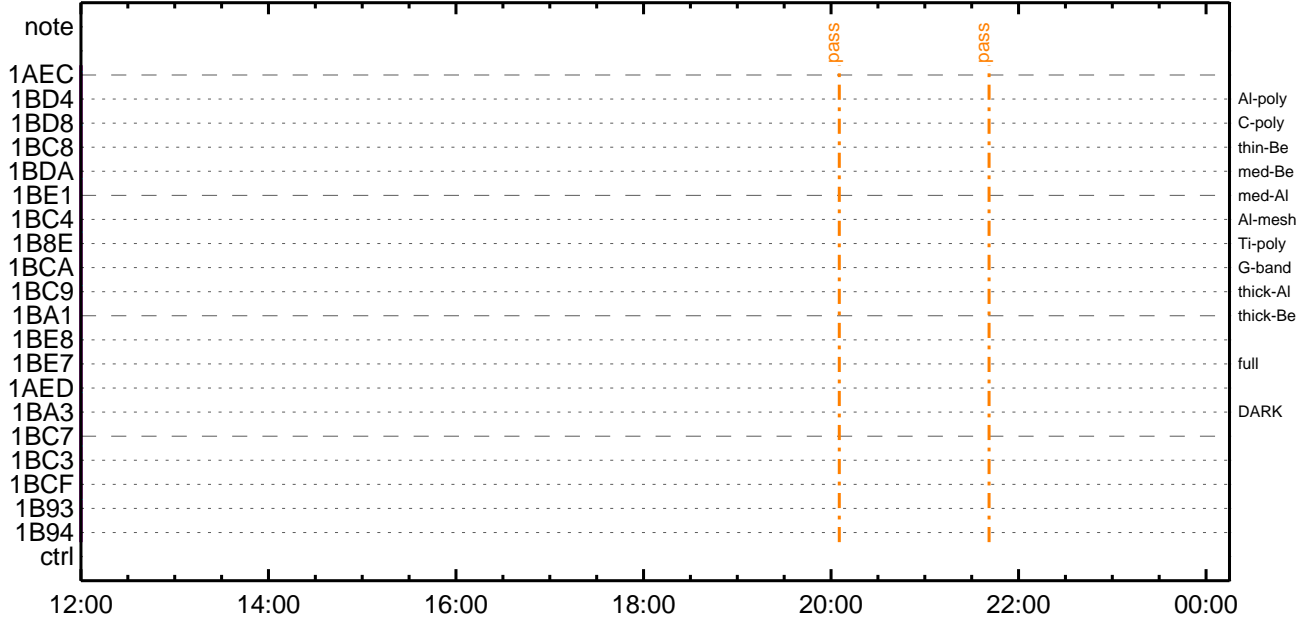
CMDI #0396 2018/03/30



CMDI #0396 2018/03/31



CMDI #0396 2018/03/31







```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOYx
0100 C. *****
0101 C.
0102 . C. ;ãOP/OGY1;4YE;ã
0103 . S. OP op-265:OP
0104 ( )
0105 . S. OG og-265:OG
0106 ( )
0107 C.
0108 . C. ;ãNMOG&OPîî°èYAYOYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 . C. YAYOYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 . C. RAM ID=NMOG²î½Ê¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 . C. YAYOYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 . C. RAM ID=NMOG²î½Ê¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 . C. YAYOYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 . C. RAM ID=NMOG, RAM ID=OP²î½Ê¹ç•è²îOKò³îÇ§
0165 C.
0166 . C. ***** °Ê²¼òî½Ã´¶Á°òÊÊ¬ò°Á÷¿® (½âµ-YAYOYx½ê½çòðÁÔÆòÇ½ª°²òè½î¹çòçòâ) *****
0167 C. DHUYâ;4YE;Ê½Y½, Yî;4YE;Êòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 . C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 . C. NOTICE ;§ OPOG UPLOAD²-Á÷¿®NG²î½î¹ç;ç°Ê²¼òî½TI-CMDÁ÷¿®²î½Á¹Ô²°²Ê²²²³òÊ;f
0180 C. ²²²¿;çSET²EDUMPAîÆ±²îYÑ¹²ç¹Ô²²²³²²Ê;f
0181 C.
0182 . C. TIY³Y²Y³Y²Y³Ê²òðÁDî¿(UT)
0183 +. TI 2018-03-27 10:38:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2018-03-27 10:38:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2018-03-27 10:38:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```





```

0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. ***** AOCs Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 + DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 2104721.8 +- 1.0 (s) [ ]
0136 C.
0137 . C.
0138 C.
0139 . C. ***** MDP `ûÃîñî»ò%ÿñÊÃð¹ñèDCBC•x²è *****
0140 C. (%ã°îÿÓÿÃÿÈÿPÿËÿÿáÿçÿèñÊ%¼ñ¼Ã»Ûñ¹ñè)
0141 . S. DC-BC dcbc-402:DCBC
0142 (MDP_known_event)
0143 C.
0144 C.
0145 . C. ***** ÿDÿ¹.Ï Daily±;îÑñÊ´Øñ¹ñèDCBC•x²è *****
0146 . S. DC-BC dcbc-153:DCBC
0147 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0148 C.
0149 C.
0150 . C. ;ãLOSÿÃÿSÿÿÿÿ¼Ã»Û;ã
0151 C.
0152 . C. ***** LOS *****
0153 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-267 2018-03-27 13:46:16 140 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY~¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. ÁíÈ¿òÄò•µ°È»Í×ÁÇóÍYçYÁY×Yí;¼YÉ;ÈÈèµ•íÉ;ÈòÈ¼°ÇÒò•ò¿¼í¹çòÍ;çÀ®, ùò¹òèòòòçÁ+¿®ò•òÈòòò³òÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop SP table >
0018 +. DC 07-F0 MDP_SP_CTRL_MANU
0019 BC (61)
0020 C. -----
0021 C. MDP_SP_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload SP Observation Table>
0025 . S. RAM ram-286:MDP_OBS_S
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_S >
0029 +. DC 07-F0 MDP_DUMP_SPTBL
0030 BC (83 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_S verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2018-03-27 10:42:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 +. DC 07-F0 MDP_XRT_CTRL_MANU
0052 BC (c1)
0053 +. DC 07-F0 MDP_XRT_MODE_STBY
0054 BC (c3)
0055 . C. ----- Success Verify ? OK / NG____
0056 C.
0057 C. XRT Obs. Table Upload
0058 . S. RAM ram-291:MDP_OBS_X
0059 ( )
0060 C.
0061 +. DC 07-F0 MDP_DUMP_XRTTBL
0062 BC (84 07 00 00 00 3a d4)
0063 . C. ----- Comparison Check ? OK / ERR ____
0064 C.
0065 C.
0066 +. DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 01 b1 b1 04 04)
0068 +. DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 02 b1 b1 08 08)
0070 +. DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 03 b1 b1 08 08)
0072 +. DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 04 b1 b1 06 06)
0074 +. DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 05 85 83 06 06)
0076 +. DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 06 85 83 06 06)
0078 +. DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 07 85 83 08 08)
0080 +. DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 08 c0 c0 10 10)
0082 +. DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 09 80 80 20 20)
0084 +. DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0a 40 c0 10 10)
0086 +. DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 0b 40 40 10 10)
0088 +. DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 0c c0 40 10 10)
0090 +. DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 0d 80 80 20 08)
0092 +. DC 07-F0 MDP_XRT_ROI_SET
0093 BC (cd 0e 80 80 08 20)
0094 +. DC 07-F0 MDP_XRT_ROI_SET
0095 BC (cd 0f 80 80 06 06)
```

```
0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 10 80 80 08 08)
0098 + DC 07-F0 MDP_XRT_FLD_ENA
0099 BC (d8)
0100 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0101 BC (c8)
0102 + DC 07-F0 MDP_XRT_ARS_DIS
0103 BC (d5)
0104 + DC 07-F0 MDP_XRT_AEC_RESET
0105 BC (d0)
0106 + DC 07-F0 MDP_XRT_FLD_RESET
0107 BC (da)
0108 + DC 07-F0 MDP_XRT_QT_PROG_SET
0109 BC (c4 06)
0110 + DC 07-F0 MDP_XRT_FL_PROG_SET
0111 BC (c5 0d)
0112 . C. ----- Success Verify ? OK / NG ____
0113 C.
0114 C.
0115 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0116 C.
0117 + DC 07-F0 MDP_XRT_MODE_OBSV
0118 BC (c2)
0119 + TI 2018-03-27 10:42:02.0
0120 DC 07-F0 MDP_XRT_MODE_OBSV
0121 BC (c2)
0122 . C. ----- Success Verify ? OK / NG ____
0123 C.
0124 C. ***** XRT END *****
0125 C.
0126 . C. ***** MDP `úÃîñî»ö%ÝñÊÃÐñ¹ñèDCBC•x²è *****
0127 C. (%Á°îÝÓÝÃÝÈÝÞÝËÝÁÝÇÝèñÊ%¼ñ¼Ã»Ûñ¹ñè)
0128 . S. DC-BC dcbc-402:DCBC
0129 (MDP_known_event)
0130 C.
0131 C.
0132 . C. ***** ¥ÐÝ¹•Ï Daily±¿ÍÑñÈ´Øñ¹ñèDCBC•x²è *****
0133 . S. DC-BC dcbc-153:DCBC
0134 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0135 C.
0136 C.
0137 . C. ¡ãLOS¥Á¥$¥Ã¥¬¼Ã»Ûñ¹ñè
0138 C.
0139 . C. ***** LOS *****
0140 C.
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\*\*\* OP Sequence for XRT \*\*\*

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2018/03/27 10:53:00.0 AOCs_Ore-point_Start_1_OG [0x097]
                        AOCU_NM                    5 02-76 01 02 c0 01 ca
2018/03/27 16:31:00.0 AOCs_Ore-point_Start_2_OG [0x098]
                        AOCU_NM                    5 02-76 04 02 a7 01 ca
2018/03/28 06:10:00.0 XRT_TCIB_XRT_S_HTR_A_DIS_438_OG [0x1b6]
                        TCIB_XRT_S_HTR_A_DIS 0 04-C0
2018/03/28 12:09:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2018/03/28 12:09:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2018/03/28 12:09:58.0 XRT_FOCUS_POSITION_447_OG [0x1bf]
                        XRT_FOCUS_POSITION        4 07-F8 22 ff aa 00
2018/03/28 12:10:00.0 AOCs_Ore-point_Start_3_OG [0x099]
                        AOCU_NM                    5 02-76 00 2e f9 2e f9
2018/03/28 12:10:18.0 XRT_FLD_DIS_441_OG [0x1b9]
                        MDP_XRT_FLD_DIS          1 07-F0 d9
2018/03/28 12:10:20.0 XRT_FLRCTRL_DIS_413_OG [0x19d]
                        MDP_XRT_FLRCTRL_DIS      1 07-F0 c9
2018/03/28 12:12:56.0 XRT_ARS_DIS_445_OG [0x1bd]
                        MDP_XRT_ARS_DIS          1 07-F0 d5
2018/03/28 12:12:58.0 XRT_QT_PROG_SET_449_OG [0x1c1]
                        MDP_XRT_QT_PROG_SET      2 07-F0 c4 05
2018/03/28 12:13:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO        1 07-F0 c0
2018/03/28 12:19:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2018/03/28 12:19:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2018/03/28 12:19:58.0 XRT_FOCUS_POSITION_447_OG [0x1bf]
                        XRT_FOCUS_POSITION        4 07-F8 22 ff aa 00
2018/03/28 12:20:00.0 AOCs_Ore-point_Start_4_OG [0x09a]
                        AOCU_NM                    5 02-76 00 2e f9 d1 07
2018/03/28 12:20:18.0 XRT_FLD_DIS_441_OG [0x1b9]
                        MDP_XRT_FLD_DIS          1 07-F0 d9
2018/03/28 12:20:20.0 XRT_FLRCTRL_DIS_413_OG [0x19d]
                        MDP_XRT_FLRCTRL_DIS      1 07-F0 c9
2018/03/28 12:22:56.0 XRT_ARS_DIS_445_OG [0x1bd]
                        MDP_XRT_ARS_DIS          1 07-F0 d5
2018/03/28 12:22:58.0 XRT_QT_PROG_SET_436_OG [0x1b4]
                        MDP_XRT_QT_PROG_SET      2 07-F0 c4 11
2018/03/28 12:23:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO        1 07-F0 c0
2018/03/28 12:29:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2018/03/28 12:29:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2018/03/28 12:29:58.0 XRT_FOCUS_POSITION_447_OG [0x1bf]
                        XRT_FOCUS_POSITION        4 07-F8 22 ff aa 00
2018/03/28 12:30:00.0 AOCs_Ore-point_Start_5_OG [0x09b]
                        AOCU_NM                    5 02-76 00 d1 07 d1 07
2018/03/28 12:30:18.0 XRT_FLD_DIS_441_OG [0x1b9]
                        MDP_XRT_FLD_DIS          1 07-F0 d9
2018/03/28 12:30:20.0 XRT_FLRCTRL_DIS_413_OG [0x19d]
                        MDP_XRT_FLRCTRL_DIS      1 07-F0 c9
2018/03/28 12:32:56.0 XRT_ARS_DIS_445_OG [0x1bd]
                        MDP_XRT_ARS_DIS          1 07-F0 d5
2018/03/28 12:32:58.0 XRT_QT_PROG_SET_428_OG [0x1ac]
                        MDP_XRT_QT_PROG_SET      2 07-F0 c4 0b
2018/03/28 12:33:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO        1 07-F0 c0
2018/03/28 12:39:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2018/03/28 12:39:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2018/03/28 12:39:58.0 XRT_FOCUS_POSITION_447_OG [0x1bf]
                        XRT_FOCUS_POSITION        4 07-F8 22 ff aa 00
2018/03/28 12:40:00.0 AOCs_Ore-point_Start_6_OG [0x09c]
                        AOCU_NM                    5 02-76 00 d1 07 2e f9
2018/03/28 12:40:18.0 XRT_FLD_DIS_441_OG [0x1b9]
                        MDP_XRT_FLD_DIS          1 07-F0 d9
2018/03/28 12:40:20.0 XRT_FLRCTRL_DIS_413_OG [0x19d]
                        MDP_XRT_FLRCTRL_DIS      1 07-F0 c9
2018/03/28 12:42:56.0 XRT_ARS_DIS_445_OG [0x1bd]
                        MDP_XRT_ARS_DIS          1 07-F0 d5
2018/03/28 12:42:58.0 XRT_QT_PROG_SET_418_OG [0x1a2]
                        MDP_XRT_QT_PROG_SET      2 07-F0 c4 0c
2018/03/28 12:43:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO        1 07-F0 c0
2018/03/28 12:49:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2018/03/28 12:49:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2018/03/28 12:49:58.0 XRT_FOCUS_POSITION_403_OG [0x193]
                        XRT_FOCUS_POSITION        4 07-F8 22 ff aa 00
2018/03/28 12:50:00.0 AOCs_Ore-point_Start_7_OG [0x09d]
                        AOCU_NM                    5 02-76 04 02 c0 01 ca
2018/03/28 12:50:18.0 XRT_FLD_DIS_425_OG [0x1a9]
                        MDP_XRT_FLD_DIS          1 07-F0 d9
2018/03/28 12:52:54.0 XRT_FLRCTRL_DIS_405_OG [0x195]
                        MDP_XRT_FLRCTRL_DIS      1 07-F0 c9
2018/03/28 12:52:56.0 XRT_ARS_DIS_423_OG [0x1a7]

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2018/03/28	12:52:58.0	XRT_QT_PROG_SET_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5				
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	08			
2018/03/28	12:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/03/28	12:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/03/28	12:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/03/28	12:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2018/03/28	13:00:18.0	XRT_ROI_A_420_OG [0x1a4]	MDP_XRT_ROI_SET	6	07-F0	cd	05	85	83	06
			MDP_XRT_ROI_SET	6	07-F0	cd	06	85	83	06
			MDP_XRT_ROI_SET	6	07-F0	cd	07	85	83	08
			MDP_XRT_ROI_SET	6	07-F0	cd	08	80	80	08
			MDP_XRT_ROI_SET	6	07-F0	cd	09	80	80	20
			MDP_XRT_ROI_SET	6	07-F0	cd	0d	80	80	20
			MDP_XRT_ROI_SET	6	07-F0	cd	0e	80	80	08
			MDP_XRT_ROI_SET	6	07-F0	cd	0f	80	80	06
2018/03/28	13:00:18.5	XRT_ROI_B_407_OG [0x197]	MDP_XRT_ROI_SET	6	07-F0	cd	0f	80	80	06
			MDP_XRT_ROI_SET	6	07-F0	cd	10	80	80	08
2018/03/28	13:00:23.5	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2018/03/28	13:00:25.5	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2018/03/28	13:00:27.5	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2018/03/28	13:00:29.5	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/03/28	13:00:31.5	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da				
2018/03/28	13:03:01.5	XRT_QT_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2018/03/28	13:03:03.5	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2018/03/28	13:03:05.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/03/28	15:46:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/03/28	15:46:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/03/28	15:46:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2018/03/28	15:46:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/03/28	15:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/03/28	16:00:30.0	XRT_Custom_430_OG [0x1ae]								
2018/03/28	16:01:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/03/28	16:02:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/03/28	16:02:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/03/28	16:02:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2018/03/28	16:02:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/03/28	16:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/03/28	16:09:30.0	XRT_Custom_430_OG [0x1ae]								
2018/03/28	16:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/03/28	17:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/03/28	17:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/03/28	17:09:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2018/03/28	17:10:00.0	AOCS_Ore-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00	00	00	00	00
2018/03/28	17:10:18.0	XRT_FLD_DIS_425_OG [0x1a9]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2018/03/28	17:12:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2018/03/28	17:12:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/03/28	17:12:58.0	XRT_QT_PROG_SET_443_OG [0x1bb]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	08			
2018/03/28	17:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/03/28	17:19:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/03/28	17:19:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/03/28	17:19:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2018/03/28	17:20:00.0	AOCS_Ore-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	04	02	c0	01	ca
2018/03/28	17:20:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				



2018/03/28	17:20:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2018/03/28	17:20:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2018/03/28	17:20:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/03/28	17:20:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/03/28	17:22:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/03/28	17:22:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/03/28	17:22:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/03/28	17:22:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/03/28	17:22:56.0	XRT_QT_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
2018/03/28	17:22:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2018/03/28	17:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/03/28	17:39:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/03/28	17:39:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/03/28	17:39:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2018/03/28	17:40:00.0	AOCS_OrE-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	02 02	c0 01 ca
2018/03/28	17:40:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2018/03/28	17:40:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2018/03/28	17:40:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2018/03/28	17:40:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/03/28	17:40:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/03/28	17:42:56.0	XRT_QT_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
2018/03/28	17:42:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2018/03/28	17:46:01.0	XRT_Custom_430_OG [0x1ae]					
2018/03/28	17:47:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/03/28	18:59:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/03/28	18:59:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/03/28	18:59:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/03/28	18:59:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/03/28	19:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/03/28	19:22:30.0	XRT_Custom_430_OG [0x1ae]					
2018/03/28	19:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/03/28	20:36:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/03/28	20:36:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/03/28	20:36:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/03/28	20:36:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/03/28	20:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/03/28	21:00:00.0	XRT_Custom_430_OG [0x1ae]					
2018/03/28	21:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/03/28	22:13:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/03/28	22:13:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/03/28	22:13:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/03/28	22:13:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/03/28	22:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/03/28	22:36:00.0	XRT_Custom_430_OG [0x1ae]					
2018/03/28	22:37:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/03/28	23:40:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/03/28	23:40:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/03/28	23:40:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2018/03/28	23:41:00.0	AOCS_OrE-point_Start_7_OG [0x09d]					

2018/03/28	23:41:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	04	02	c0	01	ca
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2018/03/28	23:41:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]								
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2018/03/28	23:41:22.0	XRT_AEC_RESET_448_OG [0x1c0]								
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2018/03/28	23:41:24.0	XRT_ARS_DIS_423_OG [0x1a7]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/03/28	23:41:26.0	XRT_FLD_RESET_433_OG [0x1b1]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2018/03/28	23:43:56.0	XRT_QT_PROG_SET_414_OG [0x19e]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2018/03/28	23:43:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]								
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2018/03/28	23:44:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/03/28	23:51:00.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/03/28	23:51:02.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/03/28	23:51:04.0	XRT_FLD_RESET_415_OG [0x19f]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2018/03/28	23:51:06.0	XRT_PREFLR_STRT_406_OG [0x196]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/03/28	23:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/03/29	00:04:01.0	XRT_Custom_430_OG [0x1ae]								
2018/03/29	00:05:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/03/29	01:19:30.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/03/29	01:19:32.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/03/29	01:19:34.0	XRT_FLD_RESET_415_OG [0x19f]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2018/03/29	01:19:36.0	XRT_PREFLR_STRT_406_OG [0x196]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/03/29	01:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/03/29	01:34:00.0	XRT_Custom_430_OG [0x1ae]								
2018/03/29	01:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/03/29	02:50:00.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/03/29	02:50:02.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/03/29	02:50:04.0	XRT_FLD_RESET_415_OG [0x19f]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2018/03/29	02:50:06.0	XRT_PREFLR_STRT_406_OG [0x196]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/03/29	02:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/03/29	03:10:30.0	XRT_Custom_430_OG [0x1ae]								
2018/03/29	03:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/03/29	04:18:00.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/03/29	04:18:02.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/03/29	04:18:04.0	XRT_FLD_RESET_415_OG [0x19f]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2018/03/29	04:18:06.0	XRT_PREFLR_STRT_406_OG [0x196]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/03/29	04:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/03/29	04:47:31.0	XRT_Custom_430_OG [0x1ae]								
2018/03/29	04:48:31.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/03/29	05:31:24.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/03/29	05:31:26.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/03/29	05:31:28.0	XRT_FOCUS_POSITION_403_OG [0x193]								
			XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2018/03/29	05:31:30.0	AOCS_Ore-point_Start_8_OG [0x09e]								
			AOCU_NM	5	02-76	00	00	00	00	00
2018/03/29	05:31:48.0	XRT_FLD_DIS_425_OG [0x1a9]								
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2018/03/29	05:34:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]								
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2018/03/29	05:34:26.0	XRT_ARS_DIS_423_OG [0x1a7]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/03/29	05:34:28.0	XRT_QT_PROG_SET_443_OG [0x1bb]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	08			
2018/03/29	05:34:30.0	XRT_CTRL_AUTO_408_OG [0x198]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/03/29	05:41:24.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/03/29	05:41:26.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/03/29	05:41:28.0	XRT_FOCUS_POSITION_403_OG [0x193]								
			XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	

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2018/03/29	05:41:30.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	04	02	c0	01	ca
2018/03/29	05:41:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0					d8
2018/03/29	05:41:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0					c8
2018/03/29	05:41:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0					d0
2018/03/29	05:41:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0					d5
2018/03/29	05:41:56.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2018/03/29	05:44:26.0	XRT_QT_PROG_SET_414_OG [0x19e]							
		MDP_XRT_QT_PROG_SET	2	07-F0					c4 03
2018/03/29	05:44:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0					c5 0d
2018/03/29	05:44:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2018/03/29	05:58:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2018/03/29	05:58:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2018/03/29	05:58:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2018/03/29	05:58:36.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2018/03/29	06:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2018/03/29	06:25:01.0	XRT_Custom_430_OG [0x1ae]							
2018/03/29	06:26:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2018/03/29	07:38:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2018/03/29	07:38:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2018/03/29	07:38:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2018/03/29	07:38:36.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2018/03/29	07:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2018/03/29	08:02:31.0	XRT_Custom_430_OG [0x1ae]							
2018/03/29	08:03:31.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2018/03/29	09:18:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2018/03/29	09:18:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2018/03/29	09:18:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2018/03/29	09:18:36.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2018/03/29	09:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2018/03/29	09:38:00.0	XRT_Custom_430_OG [0x1ae]							
2018/03/29	09:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2018/03/29	11:00:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2018/03/29	11:00:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2018/03/29	11:00:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2018/03/29	11:00:36.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2018/03/29	11:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2018/03/29	11:05:30.0	XRT_Custom_430_OG [0x1ae]							
2018/03/29	11:06:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2018/03/29	11:31:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2018/03/29	11:32:00.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00	00	00	00	00