

XRT Timeline to be uploaded on 2018/04/03

Period: 2018/04/03 10:29:00 - 2018/04/07 10:39:00

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Normal mode

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XOB #1BEA: HOP349 - 3-filter Synoptics (Al-mesh[24/256/2897], Al-poly[45/512/4096], thin-Be[1024/11571/23142] with 512x512 G-band+Leak - 90min cad) +

Term	Pointing (x, y)	Comment
04/03 10:51:00 - 04/03 13:44:54	Fixed (0.0, 0.0)	# OP start + 10min. HOP 349 at disk center.
04/04 10:33:00 - 04/04 13:29:54	Fixed (0.0, 0.0)	# HOP 349 at disk center.

PROG= 04 Inf.-time(s)

Subr= 1	1-time(s)	300.0sec											
Seqn= 1	1-time(s)	2.0sec											
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	24ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Seqn= 99	1-time(s)	2.0sec											
Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Seqn= 33	1-time(s)	2.0sec											
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Seqn= 30	1-time(s)	2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512	(1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512	(1024, 1024)	Q=95	0	0	2.0sec
Subr= 2	18-time(s)	150.0sec											
Seqn= 8	1-time(s)	2.0sec											
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048	(1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048	(1024, 1024)	DPCM	2	0	2.0sec
Seqn= 6	1-time(s)	2.0sec											
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048	(1024, 1024)	DPCM	2	0	2.0sec
Seqn= 29	1-time(s)	2.0sec											
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	3	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

XOB #1BE9: HOP81/206 2-filter - Al/poly 16s, Al/mesh 12s 30s cadence, G-band - 384x384 1ms

Term	Pointing (x, y)	Comment
04/03 13:48:00 - 04/03 20:29:54	Fixed (0.0, 910.0)	# ALMA coordination HOP 352 + HOP 81.

PROG= 19 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec											
Seqn= 16	2-time(s)	2.0sec											
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384	(1064, 1048)	DPCM	0	0	2.0sec
Subr= 2	1-time(s)	2.0sec											
Seqn= 90	1-time(s)	30.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384	(1064, 1048)	Q=90	0	0	2.0sec
Subr= 3	60-time(s)	2.0sec											
Seqn= 39	1-time(s)	30.0sec											
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	11.3s	Obs	1x1	384x384	(1064, 1048)	Q=90	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384	(1064, 1048)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

XOB #1BD9: Synoptic Q95 2x2 - Al/mesh(64/512/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(45/512/4096) + T

Term	Pointing (x, y)	Comment
04/03 20:33:00 - 04/03 20:39:54	Fixed (0.0, 0.0)	synoptic, shifted an hour or so.
04/04 05:50:00 - 04/04 05:56:54	Fixed (0.0, 0.0)	synoptic, shifted -13.0 min
04/04 19:23:00 - 04/04 19:29:54	Fixed (0.0, 0.0)	synoptic, shifted a couple of hours.
04/05 06:03:00 - 04/05 06:09:54	Fixed (0.0, 0.0)	synoptic

PROG= 15 1-time(s)

Subr= 1	1-time(s)	2.0sec											
Seqn= 5	1-time(s)	2.0sec											
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512	(1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048	(1024, 1024)	DPCM	0	0	2.0sec
Seqn= 36	1-time(s)	2.0sec											
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	63ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Seqn= 99	1-time(s)	2.0sec											
Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Seqn= 33	1-time(s)	2.0sec											

thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 23 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1BA4: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, Al/Poly context, with G-band (1ms/1ms)

Term	Pointing (x, y)	Comment
04/03 20:43:00 - 04/04 05:46:54	Track (-248.2, -66.8) @ 04/03 20:40:00	# AR 12703 observations.
04/04 06:00:00 - 04/04 10:29:54	Track (-165.3, -65.1) @ 04/04 05:57:00	# AR cont.
04/04 13:33:00 - 04/04 19:19:54	Track (-97.0, -64.3) @ 04/04 13:30:00	# ALMA coordination HOP 326.
04/04 19:33:00 - 04/05 05:59:54	Track (-42.1, -64.1) @ 04/04 19:30:00	# AR cont.
04/05 06:13:00 - 04/05 10:10:30	Track (55.4, -64.6) @ 04/05 06:10:00	# AR cont.

PROG= 01 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec												
Seqn= 92 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 42 1-time(s) 2.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 96 40-time(s) 2.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1B8E: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512)

Term	Pointing (x, y)	Comment
04/03 10:51:00 - 04/03 13:44:54	Fixed (0.0, 0.0)	# OP start + 10min. HOP 349 at disk center.
04/03 13:48:00 - 04/03 20:29:54	Fixed (0.0, 910.0)	# ALMA coordination HOP 352 + HOP 81.
04/03 20:43:00 - 04/04 05:46:54	Track (-248.2, -66.8) @ 04/03 20:40:00	# AR 12703 observations.
04/04 06:00:00 - 04/04 10:29:54	Track (-165.3, -65.1) @ 04/04 05:57:00	# AR cont.
04/04 10:33:00 - 04/04 13:29:54	Fixed (0.0, 0.0)	# HOP 349 at disk center.
04/04 13:33:00 - 04/04 19:19:54	Track (-97.0, -64.3) @ 04/04 13:30:00	# ALMA coordination HOP 326.
04/04 19:33:00 - 04/05 05:59:54	Track (-42.1, -64.1) @ 04/04 19:30:00	# AR cont.
04/05 06:13:00 - 04/05 10:10:30	Track (55.4, -64.6) @ 04/05 06:10:00	# AR cont.

PROG= 13 30-time(s)

Subr= 1 20-time(s) 2.0sec												
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn=100 1-time(s) 10.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 10 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 81 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

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Flare Detection

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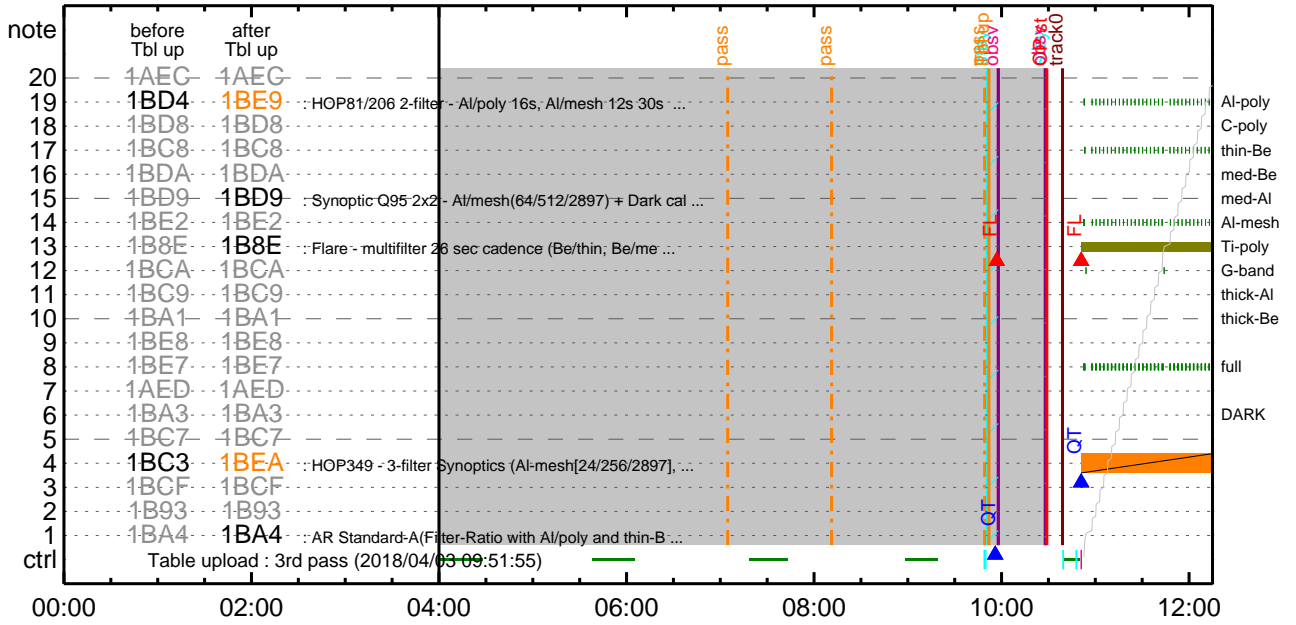
FLD Patrol

Term	Pointing (x, y)	Comment
04/03 20:40:18 - 04/04 05:47:18	Track (-248.2, -66.8) @ 04/03 20:40:00	# AR 12703 observations.
04/04 05:57:18 - 04/04 19:20:18	Track (-165.3, -65.1) @ 04/04 05:57:00	# AR cont.
04/04 19:30:18 - 04/05 06:00:18	Track (-42.1, -64.1) @ 04/04 19:30:00	# AR cont.

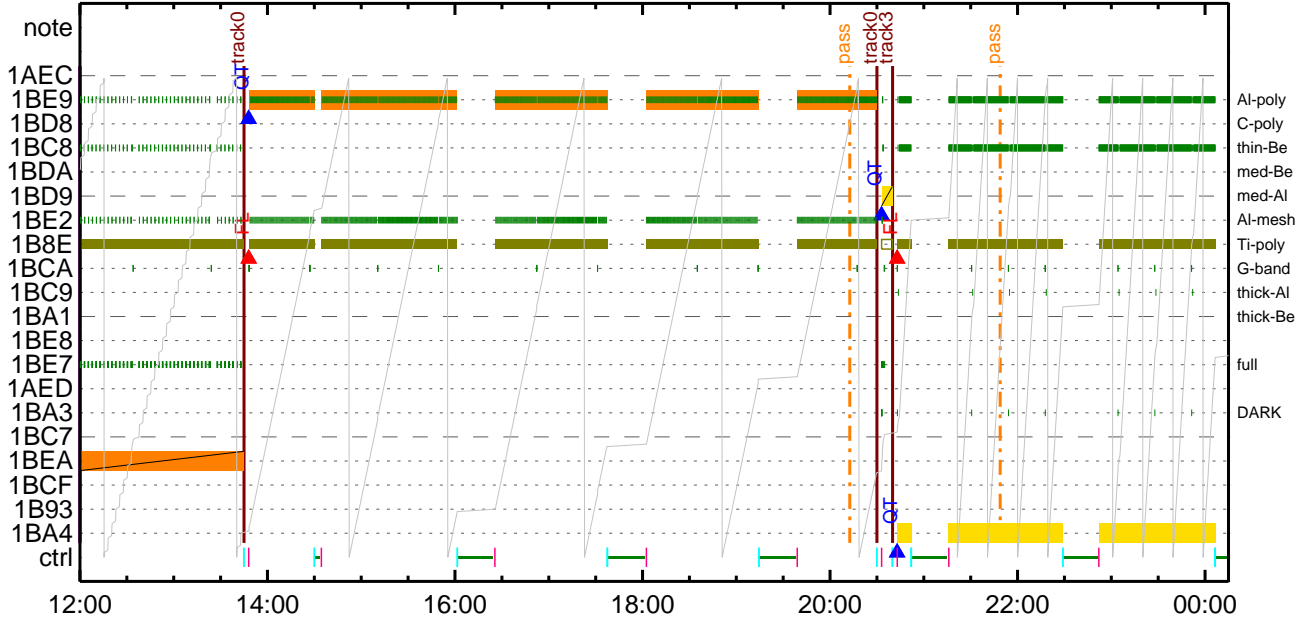
04/05 06:10:18 - 04/07 10:39:00 Track (55.4, -64.6) @ 04/05 06:10:00 # AR cont.

AI-poly/Open	AI-poly/Open	close	Safe	Norm	8ms	Obs	8x8		Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

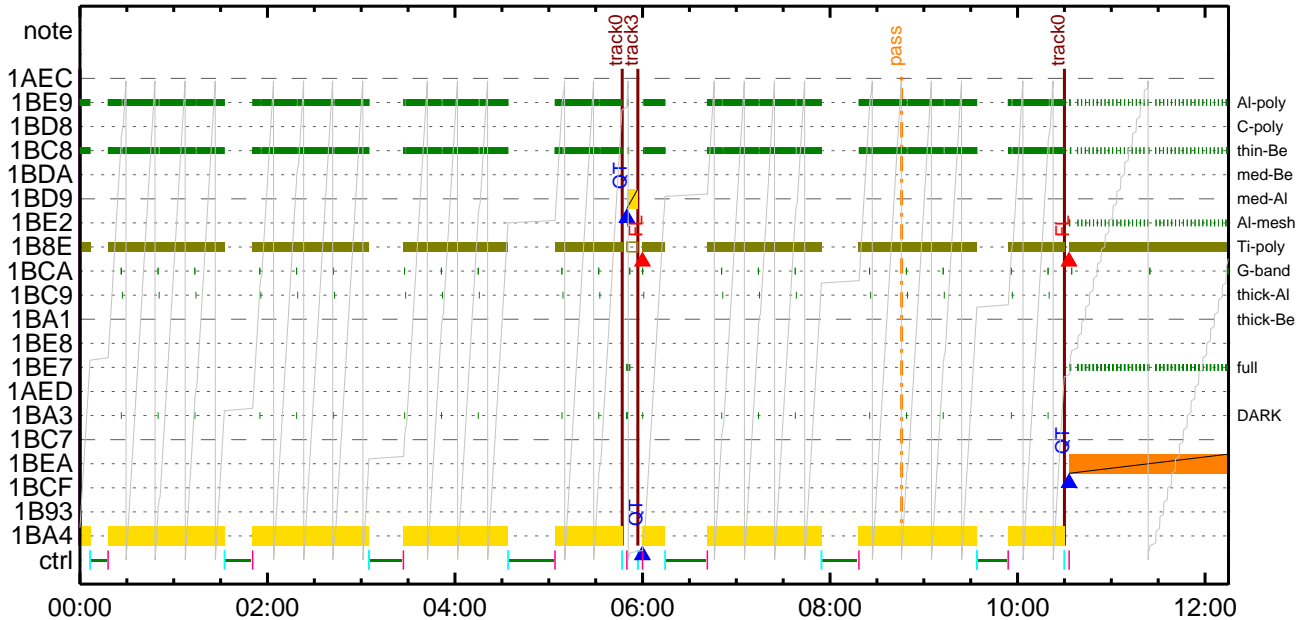
CMDI #0410 2018/04/03



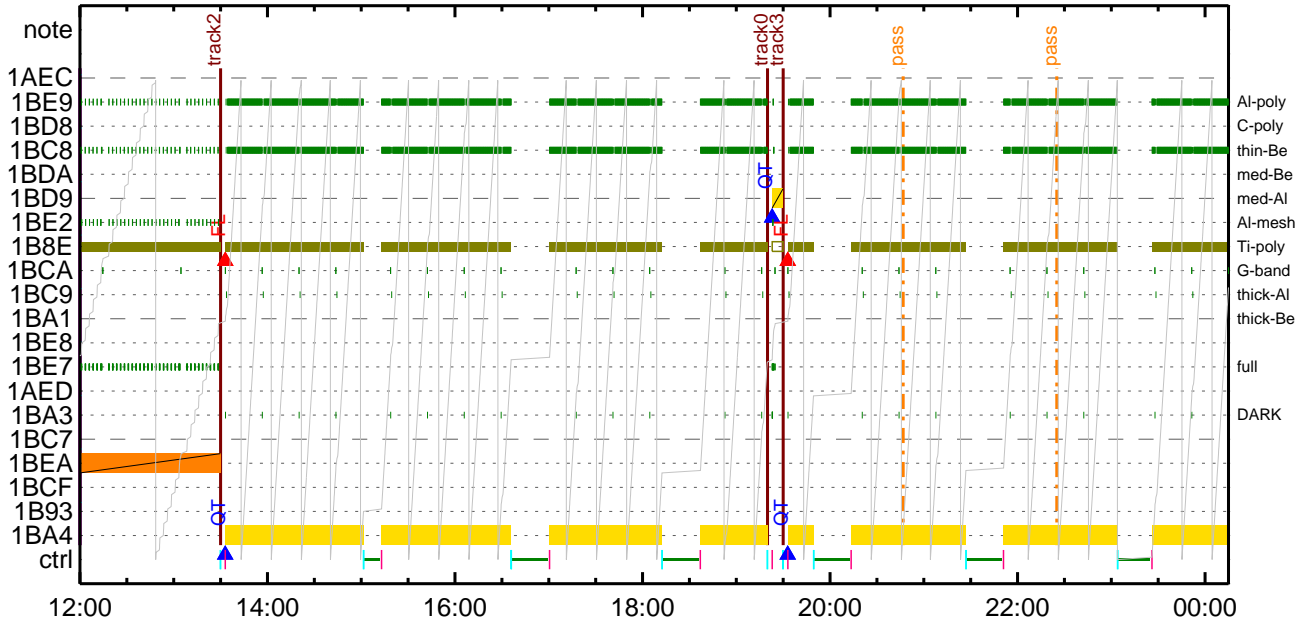
CMDI #0410 2018/04/03



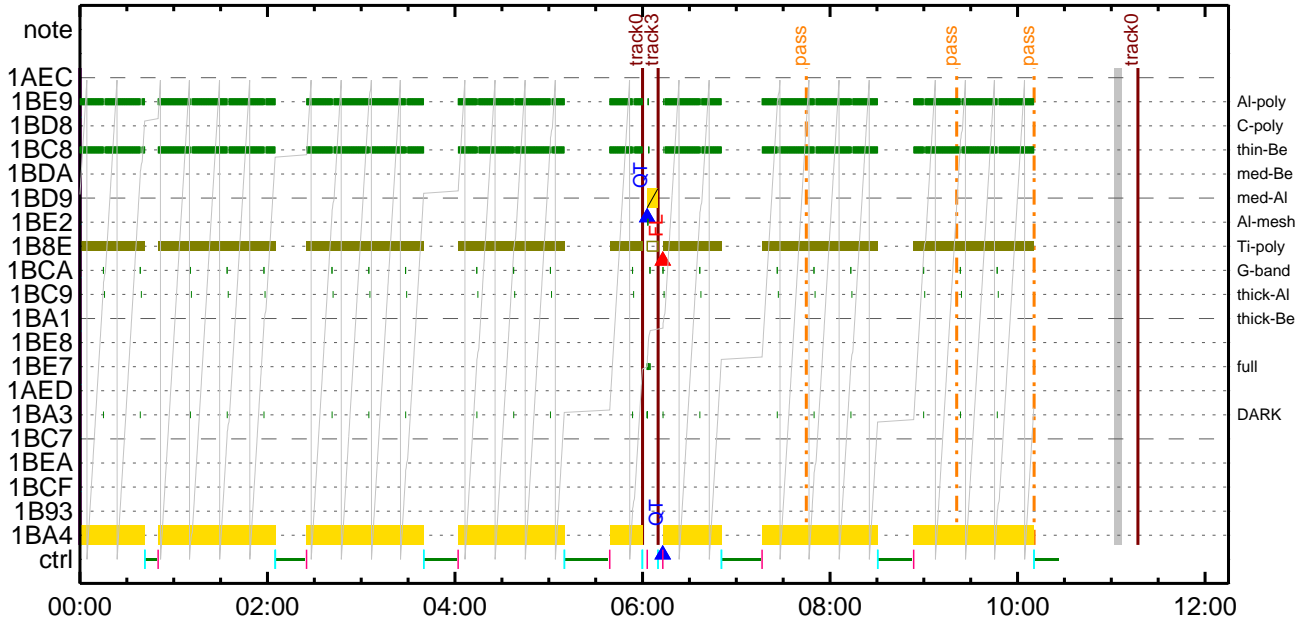
CMDI #0410 2018/04/04



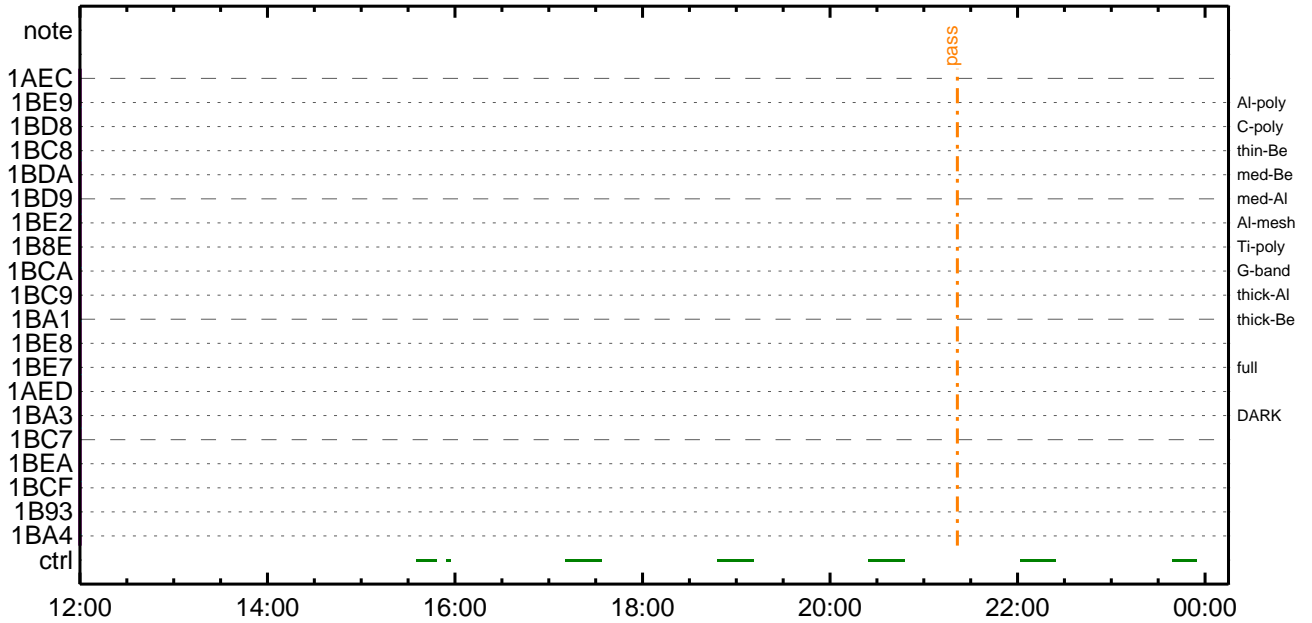
CMDI #0410 2018/04/04



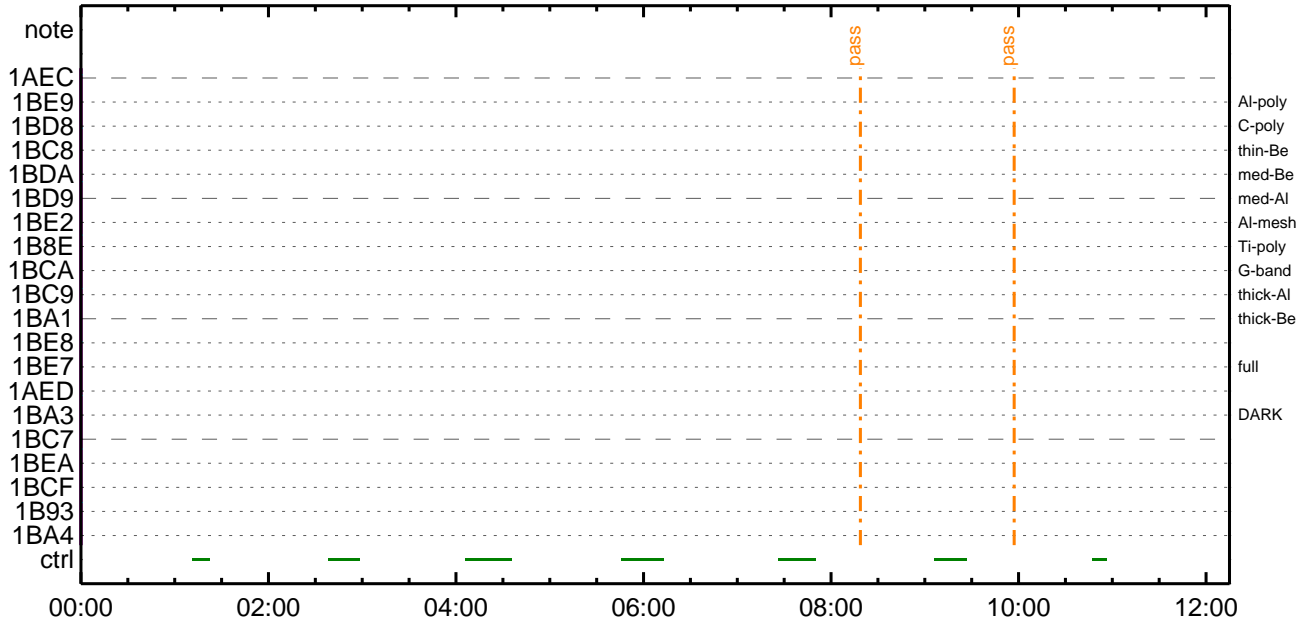
CMDI #0410 2018/04/05



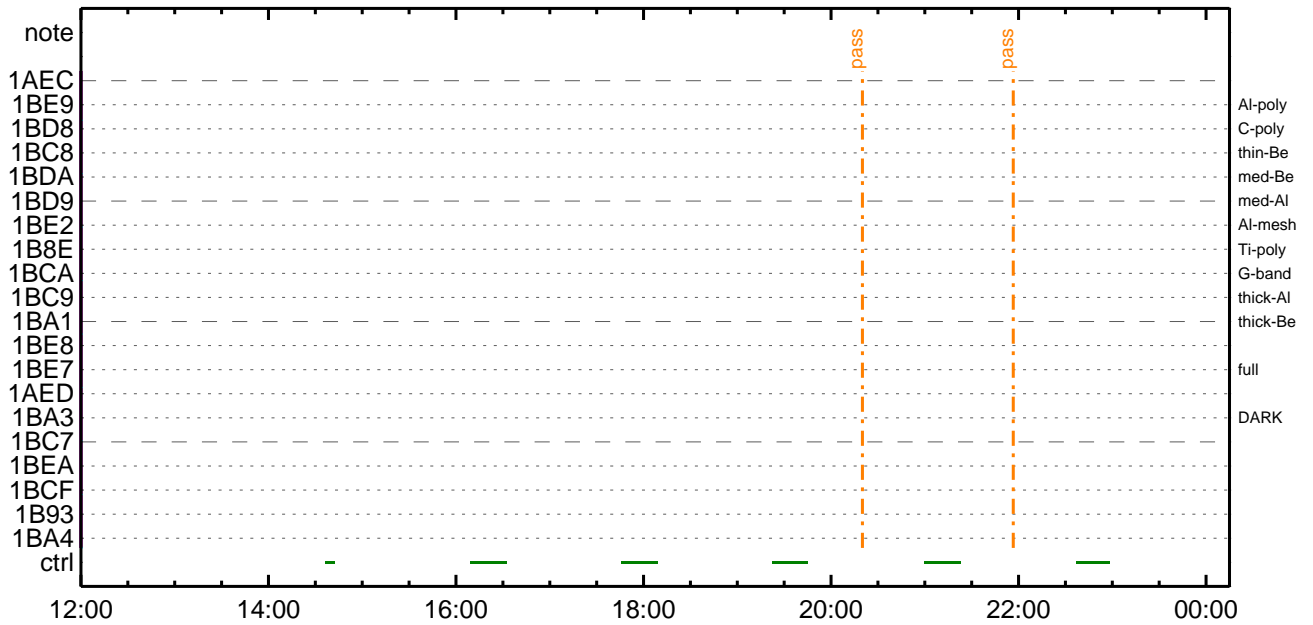
CMDI #0410 2018/04/05



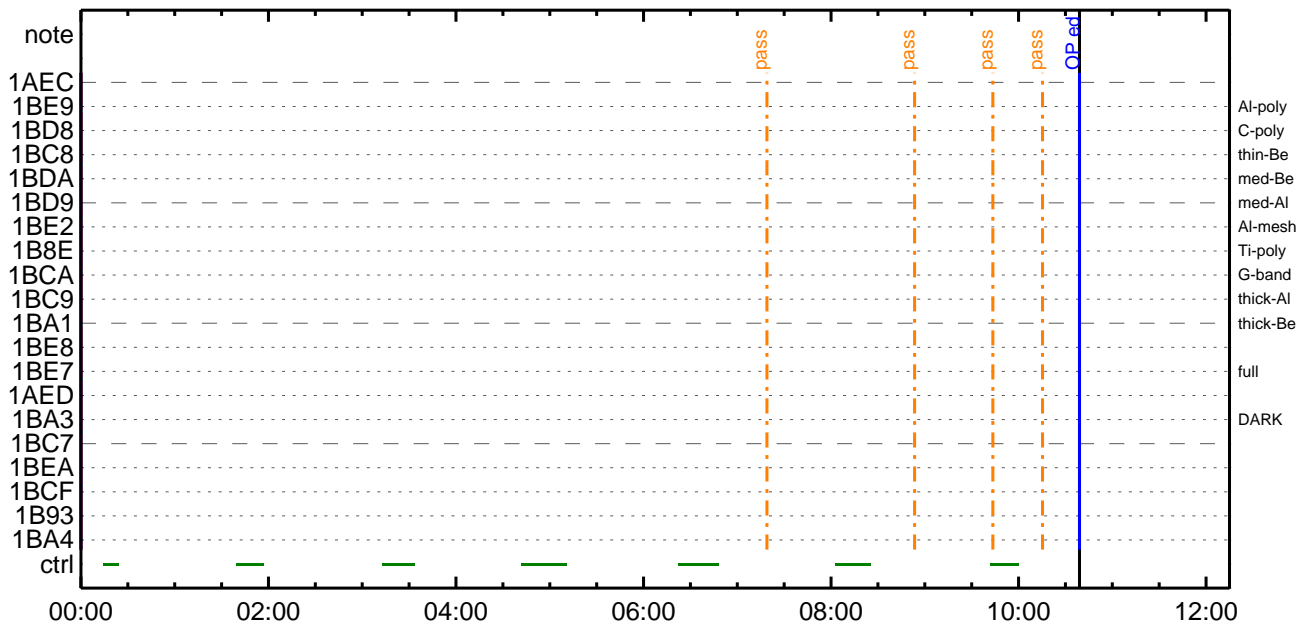
CMDI #0410 2018/04/06



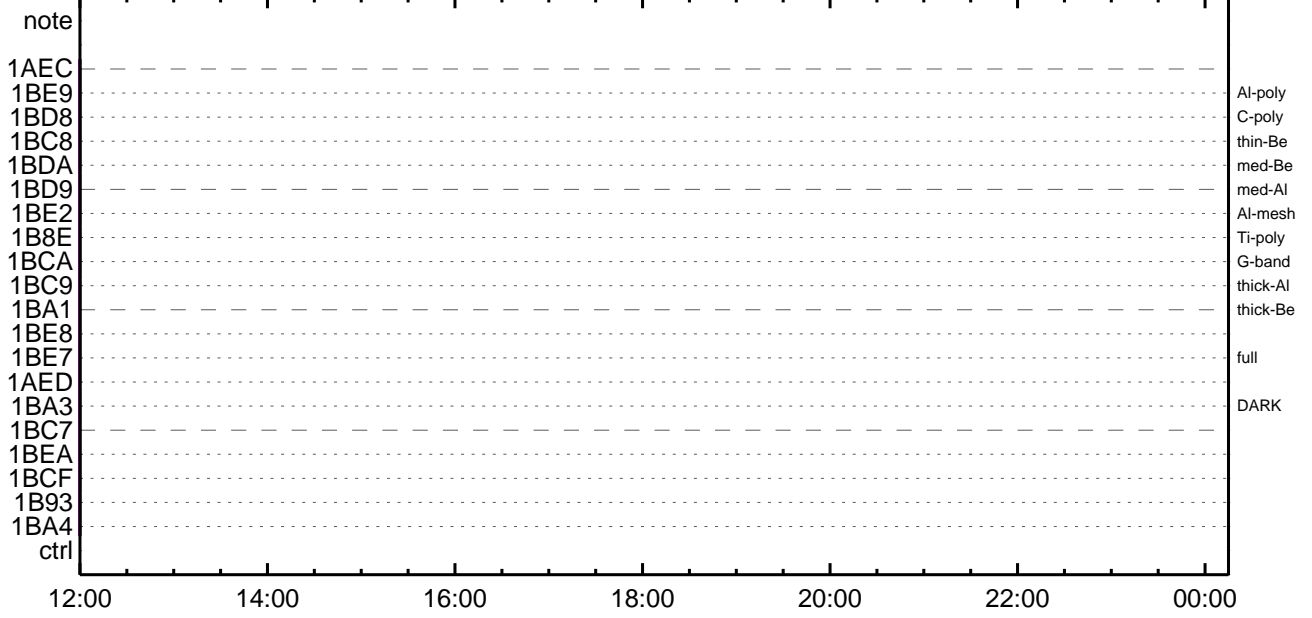
CMDI #0410 2018/04/06



CMDI #0410 2018/04/07



CMDI #0410 2018/04/07




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0096 C.                0302; SET0EDUMP01A±0iYNY¹0Ç¹0a|0³0E;E
0097 C.
0098 C. TIY³YFYY0YÉ00ðÄDİ¿(UT)
0099 +. TI 2018-04-03 10:24:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                ÇÇ[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0102 C.
0103 +. TI 2018-04-03 10:24:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                ÇÇ[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0106 C.
0107 +. TI 2018-04-03 10:24:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                ÇÇ[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0110 C.
0111 +. TI 2018-04-03 10:28:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                ÇÇ[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0114 C.
0115 C. 0E²¼0İÄê%îİÑ0İYÁY§YÄY-¹àİÜ
0116 C.                ÇÇ[HK1_TI_CMD_ENA/DIS]            EQ        ENA
0117 C.                ÇÇ[HK1_TI_CMD_NUM]              EQ        4
0118 C.                ÇÇ[HK1_NEXT_EXEC_PIM]            EQ        DHU
0119 C.                ÇÇ[HK1_NEXT_EXEC_DC]            EQ        0xB3
0120 C.
0121 C. *****
0122 C. TIİİ°èYÄY0Y×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC          (03 ab 03 01 02)
0128 C.                ÇÇ[HK1_DMP_TOP_ADRS_1]            EQ        07
0129 C.                ÇÇ[HK1_DMP_TOP_ADRS_0]            EQ        2B
0130 C.                ÇÇ[HK1_DMP_BLOCK_NUM]            EQ        3
0131 C.                ÇÇ[HK1_DMP_REPEAT_NUM]           EQ        0
0132 C.                ÇÇ[HK1_DMA_DMP_PIM]             EQ        DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC          (07 0b f8)
0135 C.                ÇÇ[HK1_PKT_FORM_NO]              EQ        7
0136 C.                ÇÇ[HK1_PKT_GEN_TIME]             EQ        0.25 s
0137 C.                ÇÇ[HK1_S_TLM_BIT_RATE]           EQ        32k
0138 C.                ÇÇ[HK1_X_TLM_BIT_RATE]          EQ        4M
0139 C.                ÇÇ[HK1_DMP_CHK_FLG]             EQ        EXEC
0140 C.
0141 C. YÄY0Y×½³İ»0ð³İÇ§
0142 C.                ÇÇ[HK1_DMP_CHK_FLG]             EQ        NON
0143 C.
0144 C. RAM ID=TI_TBL0İ%È¹Ç•è²İOK0ð³İÇ§
0145 C.
0146 C. DHUYâ;¼YÉ;È¼Y½, Yİ;¼YÈ;È0ðİá0¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC          (02 0a f8)
0149 C.                ÇÇ[HK1_PKT_FORM_NO]              EQ        2
0150 C.                ÇÇ[HK1_PKT_GEN_TIME]             EQ        0.5S
0151 C.                ÇÇ[HK1_S_TLM_BIT_RATE]           EQ        32K
0152 C.                ÇÇ[HK1_X_TLM_BIT_RATE]          EQ        4M
0153 C.
0154 C.
0155 C. ***** XRT START *****
0156 C. Execute, after the success of OP upload.
0157 +. TI 2018-04-03 10:28:00.0
0158 DC 07-F0 MDP_XRT_MODE_STBY
0159 BC          (c3)
0160 C.                [ ] [HK1_TI_CMD_NUM]            EQ        1COUNTUP
0161 C.
0162 C. ***** XRT END *****
0163 C.
0164 C. ***** MDP ´úÄİ0İ»ð¼Y0ÈÄ0¹0èDCBC•x²è *****
0165 C. (%â°İY0YÄYÉYFYYÉYáYÇYè0È%¼00¼Ä»Ü0¹0é)
0166 S. DC-BC dcbc-402:DCBC
0167 (MDP_known_event)
0168 C.
0169 C.
0170 C. ***** YDY¹•İ Daily±¿İÑ0È´00¹0èDCBC•x²è *****
0171 S. DC-BC dcbc-153:DCBC
0172 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0173 C.
0174 C.
0175 C. ;äLOS¥ÁY§YÄY-¼Ä»Ü;ä
0176 C.
0177 C. ***** LOS *****
0178 C.

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(a) Spacecraft Operation Procedure (real-commands)

```
main-281 2018-04-03 13:37:48 153 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŠYÄY-¼Ä»Û;ä
0005 C.
0006 C. YÄYß;¼Y³YFÝÓYÉÄ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;ÈçòÄâ•µ°Æ»Í×ÁÇóÍYçYÄY×YÍ;¼YÉ;ÈÈÈµ•íÉ;ÈÈÈ¼°ÇÖâ•ç¼í¹çâÍ;çÄ®, ùâ¹âèâèâçÄ+ç®â•âèâââçÈ; f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+ç®µ;ON
0016 C. *****
0017 C. ç° °ÆÀ, í×ÈYâáLOSâççâçâ»p´òâð¹íí, ç•; çÉóÍ×âÈXÁÖONâí¹òâÈâíâèâââçÈ; f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 +. DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 +. DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYÓYÉYÍYÄY-¾ôÄÖâççâçç; ç°È²¼âí°ÆÀ, ¼È¼ççâð¼Ä¹òâçç; f
0030 C.
0031 . C. *****
0032 C. DR PT1 Äí¼í°ÆÀ,
0033 C. *****
0034 C. ç° RESTART; ÈPT1; Èâ•ççâçç¼í¹çâÍ; ç°È²¼âí°ÆÀ¹òâçç; çDCBC-150âççÈèâ; f
0035 C.
0036 . C. ;ãPT1°ÆÀ, ³«»Í; ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 +. DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 +. DC 06-B3 DR_REP_START
0041 +. DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹ò, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹ò, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹ò, ;¼Ú)
0045 C.
0046 . C. ;ãYçYÓYÉYÄYÉÄÛÄØ; ÈÄ•Ä°²óÈð; È, ââí°ÆÀ, °Æ³«; ä
0047 +. DC 06-B3 DR_REP_START
0048 +. DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹ò, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹ò, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹ò, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÀ, çç¼«Æ°Äâ»ßâçç; á; ç°È²¼âí°ÆÀ¹òâçç; f
0055 C. YçYÓYÉYÄYÉÄÛÄØâÄ•Ä°²óÈðâçç¼í¹çâÍ°í»â¹âèâççÄòâ; f
0056 C.
0057 . C. *****
0058 C. DR PT2 Äí¼í°ÆÀ,
0059 C. *****
0060 C. ç° RESTART; ÈPT2; Èâ•ççâçç¼í¹çâÍ; ç°È²¼âí°ÆÀ¹òâçç; çDCBC-151âççÈèâ; f
0061 C.
0062 . C. ;ãPT2°ÆÀ, ³«»Í; ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 +. DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 +. DC 06-B3 DR_REP_START
0067 +. DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹ò, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹ò, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹ò, ;¼Ú)
0071 C.
0072 . C. ;ãYçYÓYÉYÄYÉÄÛÄØ; ÈÄ•Ä°²óÈð; È, ââí°ÆÀ, °Æ³«; ä
0073 +. DC 06-B3 DR_REP_START
0074 +. DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹ò, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹ò, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹ò, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÀ, Äâ»ß; çXÁ+ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÀ, Äâ»ß; ä
0084 +. DC 06-B4 DR_REP_STOP
0085 +. DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+ç®µ;OFF; ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 +. DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
```

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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. ***** AOCs Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 + DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 2705927.2 +- 1.0 (s) [ ]
0136 C.
0137 . C.
0138 C.
0139 . C. ***** MDP `ûÃîñî»ò%ÿñÊÃð¹ñèDCBC•x²è *****
0140 C. (%ã°îÿÓÿÃÿÈÿPÿËÿÿáÿçÿèñÊ%¼ñ¼Ã»Ûñ¹ñè)
0141 . S. DC-BC dcbc-402:DCBC
0142 (MDP_known_event)
0143 C.
0144 C.
0145 . C. ***** ÿDÿ¹.Ï Daily±;îÑñÊ´Øñ¹ñèDCBC•x²è *****
0146 . S. DC-BC dcbc-153:DCBC
0147 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0148 C.
0149 C.
0150 . C. ;ãLOSÿÁÿSÿÿÃÿ¹¼Ã»Û;ã
0151 C.
0152 . C. ***** LOS *****
0153 C.

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*** OP Sequence for XRT ***

```

2018/04/03 10:39:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                    AOCU_NM 5 02-76 00 00 00 00 00
2018/04/03 10:39:30.0 XRT_CTRL_MANU_400_OG [0x190]
                    MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/04/03 10:39:32.0 XRT_CTRL_MANU_402_OG [0x192]
                    MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/04/03 10:39:34.0 XRT_FLD_RESET_415_OG [0x19f]
                    MDP_XRT_FLD_RESET 1 07-F0 da
2018/04/03 10:39:36.0 XRT_PREFLR_STRT_406_OG [0x196]
                    MDP_XRT_PREFLR_STRT 1 07-F0 e8
2018/04/03 10:42:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                    MDP_XRT_PREFLR_STOP 1 07-F0 e9
2018/04/03 10:47:54.0 XRT_CTRL_MANU_402_OG [0x192]
                    MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/04/03 10:47:56.0 XRT_CTRL_MANU_402_OG [0x192]
                    MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/04/03 10:47:58.0 XRT_FOCUS_POSITION_403_OG [0x193]
                    XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2018/04/03 10:48:18.0 XRT_FLD_ENA_411_OG [0x19b]
                    MDP_XRT_FLD_ENA 1 07-F0 d8
2018/04/03 10:48:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                    MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2018/04/03 10:48:22.0 XRT_AEC_RESET_448_OG [0x1c0]
                    MDP_XRT_AEC_RESET 1 07-F0 d0
2018/04/03 10:48:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                    MDP_XRT_ARS_DIS 1 07-F0 d5
2018/04/03 10:48:26.0 XRT_FLD_RESET_433_OG [0x1b1]
                    MDP_XRT_FLD_RESET 1 07-F0 da
2018/04/03 10:50:56.0 XRT_QT_PROG_SET_444_OG [0x1bc]
                    MDP_XRT_QT_PROG_SET 2 07-F0 c4 04
2018/04/03 10:50:58.0 XRT_FL_PROG_SET_440_OG [0x1b8]
                    MDP_XRT_FL_PROG_SET 2 07-F0 c5 0d
2018/04/03 10:51:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                    MDP_XRT_CTRL_AUTO 1 07-F0 c0
2018/04/03 13:44:54.0 XRT_CTRL_MANU_402_OG [0x192]
                    MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/04/03 13:44:56.0 XRT_CTRL_MANU_402_OG [0x192]
                    MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/04/03 13:44:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                    XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2018/04/03 13:45:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                    AOCU_NM 5 02-76 00 af 1b 00 00
2018/04/03 13:45:18.0 XRT_FLD_ENA_411_OG [0x19b]
                    MDP_XRT_FLD_ENA 1 07-F0 d8
2018/04/03 13:45:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                    MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2018/04/03 13:45:22.0 XRT_AEC_RESET_448_OG [0x1c0]
                    MDP_XRT_AEC_RESET 1 07-F0 d0
2018/04/03 13:45:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                    MDP_XRT_ARS_DIS 1 07-F0 d5
2018/04/03 13:45:26.0 XRT_FLD_RESET_433_OG [0x1b1]
                    MDP_XRT_FLD_RESET 1 07-F0 da
2018/04/03 13:47:56.0 XRT_QT_PROG_SET_409_OG [0x199]
                    MDP_XRT_QT_PROG_SET 2 07-F0 c4 13
2018/04/03 13:47:58.0 XRT_FL_PROG_SET_440_OG [0x1b8]
                    MDP_XRT_FL_PROG_SET 2 07-F0 c5 0d
2018/04/03 13:48:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                    MDP_XRT_CTRL_AUTO 1 07-F0 c0
2018/04/03 14:30:00.0 XRT_CTRL_MANU_400_OG [0x190]
                    MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/04/03 14:30:02.0 XRT_CTRL_MANU_402_OG [0x192]
                    MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/04/03 14:30:04.0 XRT_FLD_RESET_415_OG [0x19f]
                    MDP_XRT_FLD_RESET 1 07-F0 da
2018/04/03 14:30:06.0 XRT_PREFLR_STRT_406_OG [0x196]
                    MDP_XRT_PREFLR_STRT 1 07-F0 e8
2018/04/03 14:33:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                    MDP_XRT_PREFLR_STOP 1 07-F0 e9
2018/04/03 14:33:30.0 XRT_Custom_430_OG [0x1ae]
2018/04/03 14:34:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                    MDP_XRT_CTRL_AUTO 1 07-F0 c0
2018/04/03 16:01:30.0 XRT_CTRL_MANU_400_OG [0x190]
                    MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/04/03 16:01:32.0 XRT_CTRL_MANU_402_OG [0x192]
                    MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/04/03 16:01:34.0 XRT_FLD_RESET_415_OG [0x19f]
                    MDP_XRT_FLD_RESET 1 07-F0 da
2018/04/03 16:01:36.0 XRT_PREFLR_STRT_406_OG [0x196]
                    MDP_XRT_PREFLR_STRT 1 07-F0 e8
2018/04/03 16:04:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                    MDP_XRT_PREFLR_STOP 1 07-F0 e9
2018/04/03 16:24:30.0 XRT_Custom_430_OG [0x1ae]
2018/04/03 16:25:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                    MDP_XRT_CTRL_AUTO 1 07-F0 c0
2018/04/03 17:37:30.0 XRT_CTRL_MANU_400_OG [0x190]
                    MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/04/03 17:37:32.0 XRT_CTRL_MANU_402_OG [0x192]
                    MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/04/03 17:37:34.0 XRT_FLD_RESET_415_OG [0x19f]
                    MDP_XRT_FLD_RESET 1 07-F0 da
2018/04/03 17:37:36.0 XRT_PREFLR_STRT_406_OG [0x196]

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2018/04/03	17:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/04/03	18:01:30.0	XRT_Custom_430_OG [0x1ae]					
2018/04/03	18:02:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/04/03	19:14:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/03	19:14:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/03	19:14:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2018/04/03	19:14:36.0	XRT_PREFLR_STRT_406_OG [0x196]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/04/03	19:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/04/03	19:38:00.0	XRT_Custom_430_OG [0x1ae]					
2018/04/03	19:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/04/03	20:29:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/03	20:29:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/03	20:29:58.0	XRT_FOCUS_POSITION_403_OG [0x193]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2018/04/03	20:30:00.0	AOCS_Or-point_Start_1_OG [0x097]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2018/04/03	20:30:18.0	XRT_FLD_DIS_425_OG [0x1a9]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2018/04/03	20:32:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2018/04/03	20:32:56.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/04/03	20:32:58.0	XRT_QT_PROG_SET_404_OG [0x194]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f	
2018/04/03	20:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/04/03	20:39:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/03	20:39:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/03	20:39:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2018/04/03	20:40:00.0	AOCS_Or-point_Start_3_OG [0x099]					
			AOCU_NM	5	02-76	03 00 00 00 00	
2018/04/03	20:40:18.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2018/04/03	20:40:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2018/04/03	20:40:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2018/04/03	20:40:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/04/03	20:40:26.0	XRT_FLD_RESET_433_OG [0x1b1]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2018/04/03	20:42:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 01	
2018/04/03	20:42:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2018/04/03	20:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/04/03	20:52:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/03	20:52:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/03	20:52:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2018/04/03	20:52:06.0	XRT_PREFLR_STRT_406_OG [0x196]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/04/03	20:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/04/03	21:15:00.5	XRT_Custom_430_OG [0x1ae]					
2018/04/03	21:16:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/04/03	22:29:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/03	22:29:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/03	22:29:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2018/04/03	22:29:06.0	XRT_PREFLR_STRT_406_OG [0x196]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/04/03	22:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/04/03	22:51:00.0	XRT_Custom_430_OG [0x1ae]					
2018/04/03	22:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/04/04	00:06:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/04	00:06:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/04	00:06:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	

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2018/04/04	00:06:36.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/04/04	00:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/04/04	00:17:00.0	XRT_Custom_430_OG [0x1ae]							
2018/04/04	00:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/04/04	01:32:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/04	01:32:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/04	01:32:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/04/04	01:32:36.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/04/04	01:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/04/04	01:49:30.0	XRT_Custom_430_OG [0x1ae]							
2018/04/04	01:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/04/04	03:05:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/04	03:05:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/04	03:05:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/04/04	03:05:06.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/04/04	03:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/04/04	03:26:00.0	XRT_Custom_430_OG [0x1ae]							
2018/04/04	03:27:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/04/04	04:34:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/04	04:34:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/04	04:34:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/04/04	04:34:06.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/04/04	04:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/04/04	05:03:00.0	XRT_Custom_430_OG [0x1ae]							
2018/04/04	05:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/04/04	05:46:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/04	05:46:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/04	05:46:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2018/04/04	05:47:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2018/04/04	05:47:18.0	XRT_FLD_DIS_425_OG [0x1a9]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2018/04/04	05:49:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2018/04/04	05:49:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/04/04	05:49:58.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f				
2018/04/04	05:50:00.5	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/04/04	05:56:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/04	05:56:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/04	05:56:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2018/04/04	05:57:00.0	AOCS_OrE-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2018/04/04	05:57:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2018/04/04	05:57:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2018/04/04	05:57:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2018/04/04	05:57:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/04/04	05:57:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/04/04	05:59:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2018/04/04	05:59:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2018/04/04	06:00:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/04/04	06:14:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/04	06:14:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/04	06:14:34.0	XRT_FLD_RESET_415_OG [0x19f]							

2018/04/04	06:14:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/04/04	06:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/04/04	06:40:30.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/04/04	06:41:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/04/04	07:54:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/04	07:54:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/04	07:54:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/04/04	07:54:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/04/04	07:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/04/04	08:17:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/04/04	08:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/04	09:34:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/04	09:34:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/04	09:34:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/04/04	09:34:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/04/04	09:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/04/04	09:53:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/04/04	09:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/04	10:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/04	10:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/04	10:29:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2018/04/04	10:30:00.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00
2018/04/04	10:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2018/04/04	10:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2018/04/04	10:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2018/04/04	10:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2018/04/04	10:30:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/04/04	10:32:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2018/04/04	10:32:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2018/04/04	10:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/04/04	13:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/04	13:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/04	13:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2018/04/04	13:30:00.0	AOCS_OrE-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	02 00 00 00 00
2018/04/04	13:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2018/04/04	13:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2018/04/04	13:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2018/04/04	13:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2018/04/04	13:30:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/04/04	13:32:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2018/04/04	13:32:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2018/04/04	13:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/04/04	15:01:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/04	15:01:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/04	15:01:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/04/04	15:01:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/04/04	15:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/04/04	15:12:00.0	XRT_Custom_430_OG [0x1ae]				

2018/04/04	15:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/04/04	16:36:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/04	16:36:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/04	16:36:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/04/04	16:36:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/04/04	16:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/04/04	16:59:30.0	XRT_Custom_430_OG [0x1ae]					
2018/04/04	17:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/04/04	18:12:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/04	18:12:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/04	18:12:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/04/04	18:12:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/04/04	18:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/04/04	18:36:00.0	XRT_Custom_430_OG [0x1ae]					
2018/04/04	18:37:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/04/04	19:19:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/04	19:19:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/04	19:19:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2018/04/04	19:20:00.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00	
2018/04/04	19:20:18.0	XRT_FLD_DIS_425_OG [0x1a9]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2018/04/04	19:22:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2018/04/04	19:22:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/04/04	19:22:58.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f	
2018/04/04	19:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/04/04	19:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/04	19:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/04	19:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2018/04/04	19:30:00.0	AOCS_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	03 00 00 00 00	
2018/04/04	19:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2018/04/04	19:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2018/04/04	19:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2018/04/04	19:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/04/04	19:30:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/04/04	19:32:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01	
2018/04/04	19:32:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2018/04/04	19:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/04/04	19:49:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/04	19:49:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/04	19:49:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/04/04	19:49:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/04/04	19:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/04/04	20:12:30.0	XRT_Custom_430_OG [0x1ae]					
2018/04/04	20:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/04/04	21:27:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/04	21:27:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/04	21:27:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/04/04	21:27:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/04/04	21:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	

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2018/04/04	21:50:00.0	XRT_Custom_430_OG [0x1ae]					
2018/04/04	21:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/04/04	23:04:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/04	23:04:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/04	23:04:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2018/04/04	23:04:06.0	XRT_PREFLR_STRT_406_OG [0x196]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/04/04	23:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/04/04	23:25:00.0	XRT_Custom_430_OG [0x1ae]					
2018/04/04	23:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/04/05	00:41:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/05	00:41:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/05	00:41:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2018/04/05	00:41:36.0	XRT_PREFLR_STRT_406_OG [0x196]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/04/05	00:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/04/05	00:49:00.0	XRT_Custom_430_OG [0x1ae]					
2018/04/05	00:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/04/05	02:05:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/05	02:05:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/05	02:05:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2018/04/05	02:05:06.0	XRT_PREFLR_STRT_406_OG [0x196]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/04/05	02:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/04/05	02:24:00.5	XRT_Custom_430_OG [0x1ae]					
2018/04/05	02:25:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/04/05	03:40:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/05	03:40:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/05	03:40:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2018/04/05	03:40:06.0	XRT_PREFLR_STRT_406_OG [0x196]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/04/05	03:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/04/05	04:01:00.0	XRT_Custom_430_OG [0x1ae]					
2018/04/05	04:02:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/04/05	05:10:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/05	05:10:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/05	05:10:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2018/04/05	05:10:06.0	XRT_PREFLR_STRT_406_OG [0x196]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/04/05	05:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/04/05	05:38:00.0	XRT_Custom_430_OG [0x1ae]					
2018/04/05	05:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/04/05	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/05	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/05	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2018/04/05	06:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2018/04/05	06:00:18.0	XRT_FLD_DIS_425_OG [0x1a9]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2018/04/05	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2018/04/05	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2018/04/05	06:02:58.0	XRT_QT_PROG_SET_404_OG [0x194]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f		
2018/04/05	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/04/05	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/05	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/05	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2018/04/05	06:10:00.0	AOCS_Ore-point_Start_3_OG [0x099]					

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2018/04/05	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03	00	00	00	00
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2018/04/05	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]								
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2018/04/05	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]								
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2018/04/05	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/04/05	06:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2018/04/05	06:12:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	01			
2018/04/05	06:12:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]								
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2018/04/05	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/04/05	06:50:30.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/05	06:50:32.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/05	06:50:34.0	XRT_FLD_RESET_415_OG [0x19f]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2018/04/05	06:50:36.0	XRT_PREFLR_STRT_406_OG [0x196]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/04/05	06:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/04/05	07:15:30.0	XRT_Custom_430_OG [0x1ae]								
2018/04/05	07:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/04/05	08:30:30.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/05	08:30:32.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/05	08:30:34.0	XRT_FLD_RESET_415_OG [0x19f]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2018/04/05	08:30:36.0	XRT_PREFLR_STRT_406_OG [0x196]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/04/05	08:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/04/05	08:52:30.0	XRT_Custom_430_OG [0x1ae]								
2018/04/05	08:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/04/05	10:10:30.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/05	10:10:32.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/05	10:10:34.0	XRT_FLD_RESET_415_OG [0x19f]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2018/04/05	10:10:36.0	XRT_PREFLR_STRT_406_OG [0x196]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/04/05	10:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/04/05	11:17:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00	00	00	00	00