

XRT Timeline to be uploaded on 2018/04/21

Period: 2018/04/21 10:43:00 - 2018/04/25 10:54:00

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Normal mode

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XOB #1BD4: AR (Filter-Ratio with Al/poly and thin-Be) with PFB, 512x512 at 1064 1048, thick-Al context, with G-band (1ms/1ms leak), 30s cad-2

Term	Pointing (x, y)	Comment
04/21 10:56:00 - 04/21 13:18:00	Track (-736.9, 83.3) @ 04/21 10:53:00	# OP start + 10min/HOP 321 AR12706
04/21 17:11:30 - 04/21 17:59:54	Track (-702.7, 86.5) @ 04/21 16:30:00	AR12706
04/21 18:48:00 - 04/21 22:59:54	Track (-692.2, 87.5) @ 04/21 18:10:00	AR12706
04/22 02:03:00 - 04/22 05:21:30	Track (-640.1, 91.6) @ 04/22 02:00:00	AR12706
04/22 06:13:00 - 04/22 11:29:54	Track (-610.9, 93.6) @ 04/22 06:10:00	#AR12706
04/22 18:13:30 - 04/22 22:59:54	Track (-521.2, 98.8) @ 04/22 18:10:30	#AR12706
04/23 02:03:00 - 04/23 04:17:30	Track (-458.9, 101.6) @ 04/23 02:00:00	AR12706

PROG= 10 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec										
Seqn= 92	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 71	3-time(s)	2.0sec										
Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	3	0	2.0sec
Subr= 2	100-time(s)	30.0sec										
Seqn= 89	1-time(s)	15.0sec										
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0	2.0sec
Seqn= 58	1-time(s)	12.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #1B93: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 30s cadence, G-band - 384x384 1ms

Term	Pointing (x, y)	Comment
04/21 13:35:00 - 04/21 16:29:54	Fixed (916.0, 207.0)	ALMA Cyc5 prominence

PROG= 11 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec										
Seqn= 16	2-time(s)	2.0sec										
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2	1-time(s)	2.0sec										
Seqn= 90	1-time(s)	30.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Subr= 3	60-time(s)	2.0sec										
Seqn= 57	1-time(s)	30.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #1BD9: Synoptic Q95 2x2 - Al/mesh(64/512/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(45/512/4096) + T

Term	Pointing (x, y)	Comment
04/21 18:03:00 - 04/21 18:09:54	Fixed (0.0, 0.0)	synoptic
04/22 06:03:00 - 04/22 06:09:54	Fixed (0.0, 0.0)	synoptic

PROG= 15 1-time(s)

Subr= 1	1-time(s)	2.0sec										
Seqn= 5	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 36	1-time(s)	2.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 99	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 33	1-time(s)	2.0sec										
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 23	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #1BE2: HOP349 - 3-filter Synoptics (Al-mesh[24/256/2897], Al-poly[45/512/4096], thin-Be[1024/11571/23142] with 512x512 G-band+Leak - 90min cad) +

Term	Pointing (x, y)	Comment
04/21 23:03:00 - 04/22 01:59:54	Fixed (0.0, 0.0)	HOP 349
04/22 23:03:00 - 04/23 01:59:54	Fixed (0.0, 0.0)	HOP 349
PROG= 16 Inf.-time(s)		
Subr= 1 1-time(s) 300.0sec		
Seqn= 1 1-time(s) 2.0sec		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 99 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/Open close	Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 33 1-time(s) 2.0sec		
thin-Be/Open	thin-Be/Open close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 22.6s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 30 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=95 0 0 2.0sec
Subr= 2 18-time(s) 300.0sec		
Seqn= 8 1-time(s) 2.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
Seqn= 6 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/Open close	Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
Seqn= 29 1-time(s) 2.0sec		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 250ms Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1BA1: HOP336 1-filter - Al/poly -384x384, 512ms and 4s, 60s-cadence, G-band - 384x384 1ms

Term	Pointing (x, y)	Comment
04/22 11:33:00 - 04/22 14:29:54	Track (-19.9, 644.0) @ 04/22 11:30:00	HOP 336 @40N
04/22 14:33:00 - 04/22 18:00:24	Track (-20.0, -646.0) @ 04/22 14:30:00	HOP 336 @45S
PROG= 19 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 16 2-time(s) 2.0sec		
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 90 1-time(s) 30.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec
Subr= 3 15-time(s) 2.0sec		
Seqn= 68 1-time(s) 60.0sec		
Al-poly/Open	Al-poly/Open close	Safe Norm 4.00s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1BD8: Synoptic 7 Filter w/ Al-mesh(64/512/2897), Al-poly(45/512/4096), Thin-Be(1024/11571/23142) - Thick-Be(65536), Al-poly+Ti-poly(512/8192), Med

Term	Pointing (x, y)	Comment
04/22 18:03:30 - 04/22 18:10:24	Fixed (0.0, 0.0)	synoptic, shifted 0.5 min
PROG= 18 1-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 5 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
Seqn= 36 1-time(s) 2.0sec		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 99 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/Open close	Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 33 1-time(s) 2.0sec		
thin-Be/Open	thin-Be/Open close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 22.6s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 23 1-time(s) 4.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 46 1-time(s) 2.0sec		

Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 17		1-time(s)	2.0sec									
med-Al/Open	med-Al/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 25		1-time(s)	2.0sec									
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1B8E: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
04/21 10:56:00 - 04/21 13:18:00	Track (-736.9, 83.3) @ 04/21 10:53:00	# OP start + 10min/HOP 321 AR12706
04/21 13:35:00 - 04/21 16:29:54	Fixed (916.0, 207.0)	ALMA Cyc5 prominence
04/21 17:11:30 - 04/21 17:59:54	Track (-702.7, 86.5) @ 04/21 16:30:00	AR12706
04/21 18:48:00 - 04/21 22:59:54	Track (-692.2, 87.5) @ 04/21 18:10:00	AR12706
04/21 23:03:00 - 04/22 01:59:54	Fixed (0.0, 0.0)	HOP 349
04/22 02:03:00 - 04/22 05:21:30	Track (-640.1, 91.6) @ 04/22 02:00:00	AR12706
04/22 06:13:00 - 04/22 11:29:54	Track (-610.9, 93.6) @ 04/22 06:10:00	#AR12706
04/22 11:33:00 - 04/22 14:29:54	Track (-19.9, 644.0) @ 04/22 11:30:00	HOP 336 @40N
04/22 14:33:00 - 04/22 18:00:24	Track (-20.0, -646.0) @ 04/22 14:30:00	HOP 336 @45S
04/22 18:13:30 - 04/22 22:59:54	Track (-521.2, 98.8) @ 04/22 18:10:30	#AR12706
04/22 23:03:00 - 04/23 01:59:54	Fixed (0.0, 0.0)	HOP 349
04/23 02:03:00 - 04/23 04:17:30	Track (-458.9, 101.6) @ 04/23 02:00:00	AR12706

PROG= 13 30-time(s)

Subr= 1		20-time(s)	2.0sec									
Seqn= 11		1-time(s)	2.0sec									
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn=100		1-time(s)	10.0sec									
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2		1-time(s)	2.0sec									
Seqn= 10		1-time(s)	2.0sec									
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11		1-time(s)	2.0sec									
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 81		1-time(s)	2.0sec									
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

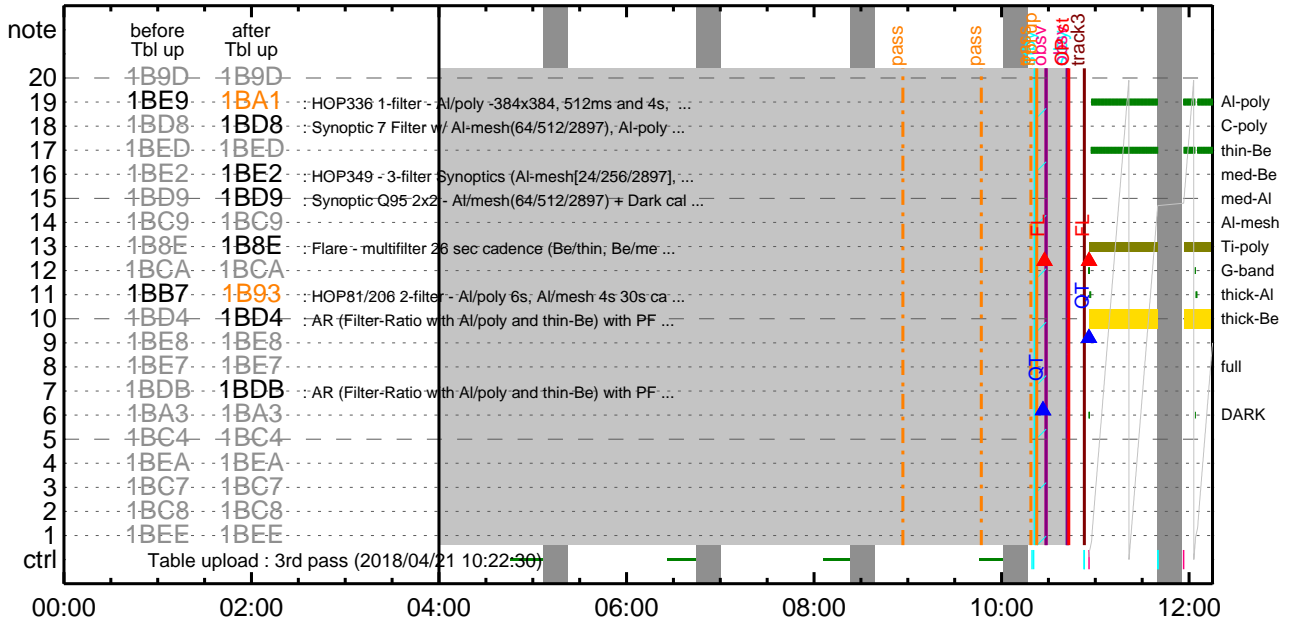
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Flare Detection

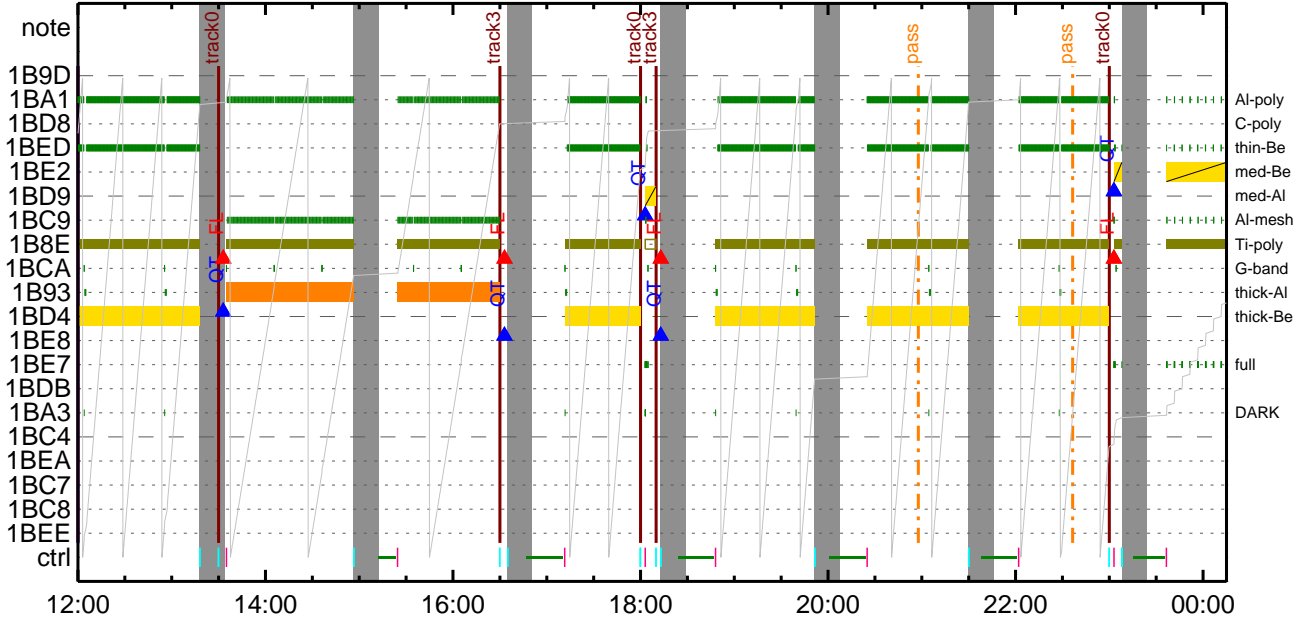
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FLD Patrol												
Term	Pointing (x, y)	Comment										
04/21 18:10:18 - 04/22 06:00:18	Track (-692.2, 87.5) @ 04/21 18:10:00	AR12706										
04/22 06:10:18 - 04/22 18:00:48	Track (-610.9, 93.6) @ 04/22 06:10:00	#AR12706										
04/22 18:10:48 - 04/25 10:54:00	Track (-521.2, 98.8) @ 04/22 18:10:30	#AR12706										
Al-poly/Open	Al-poly/Open	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

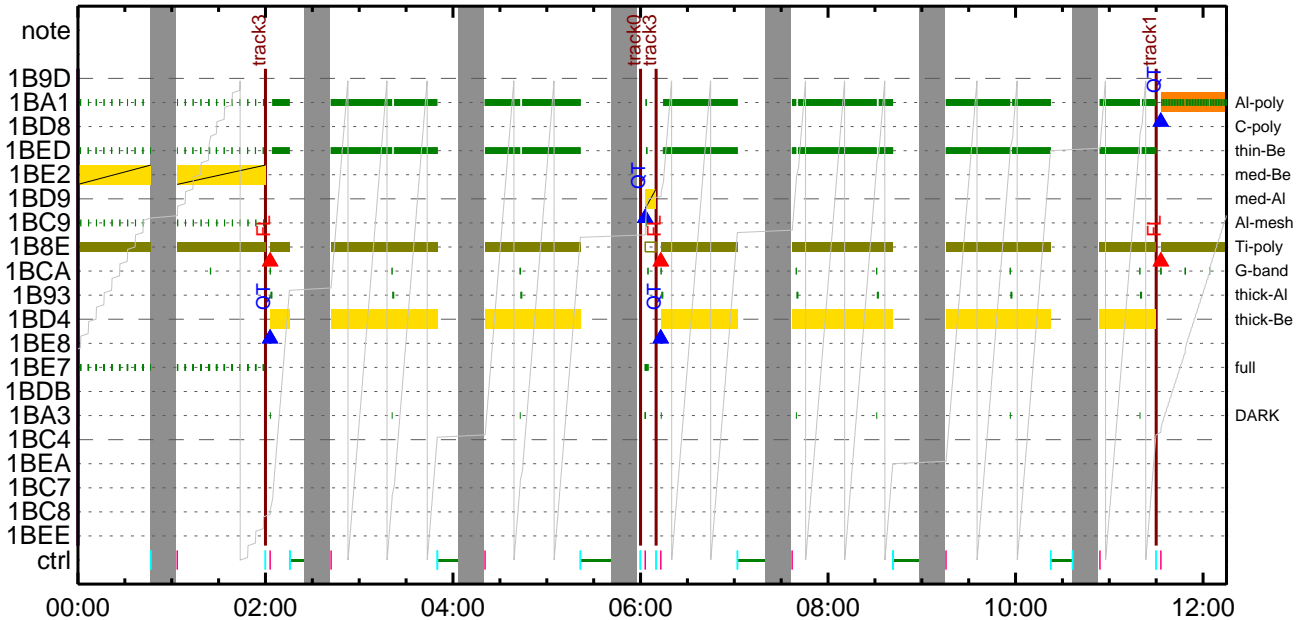
CMDI #0451 2018/04/21



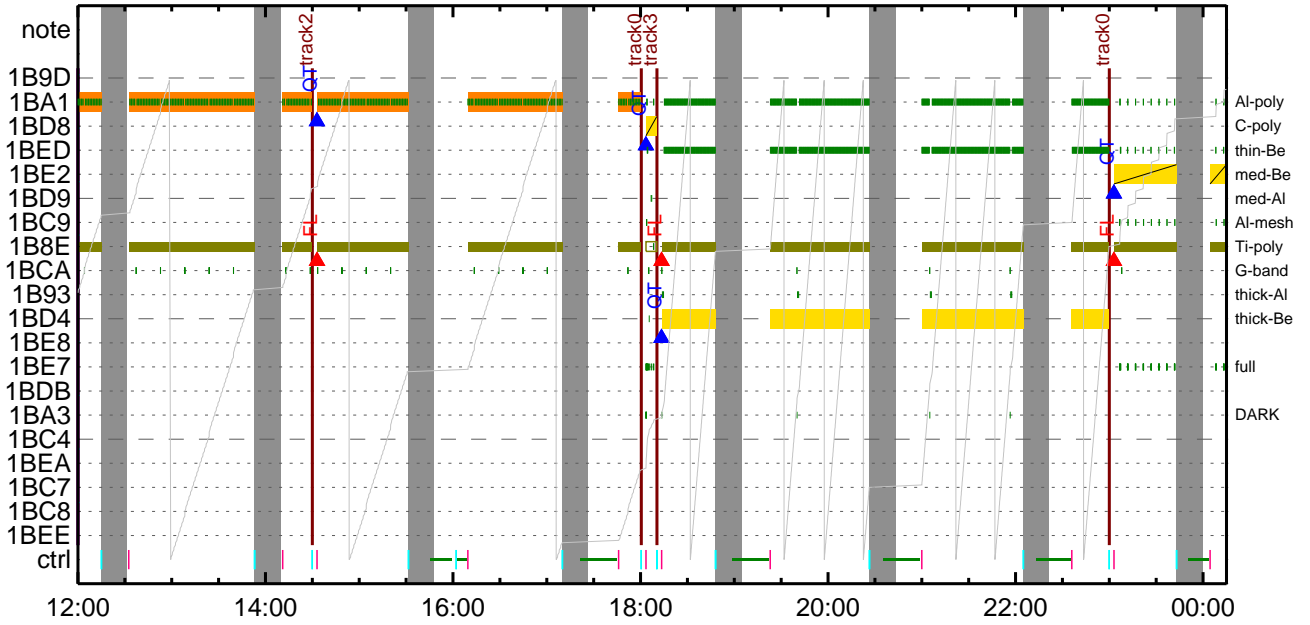
CMDI #0451 2018/04/21



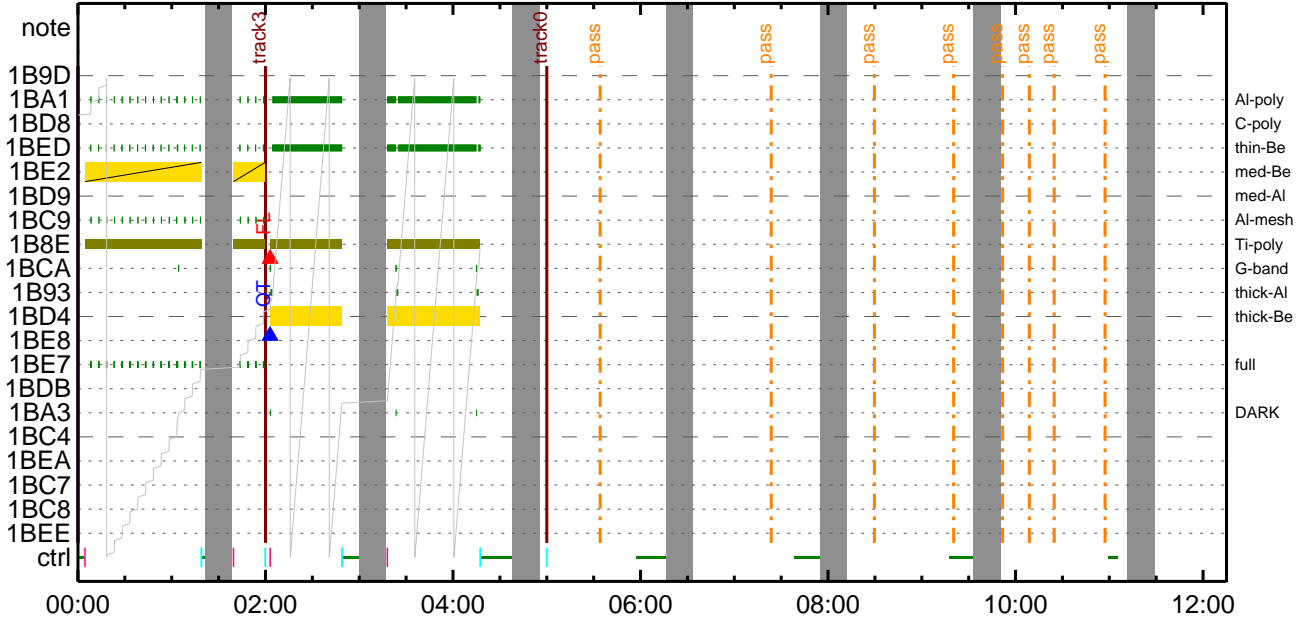
CMDI #0451 2018/04/22



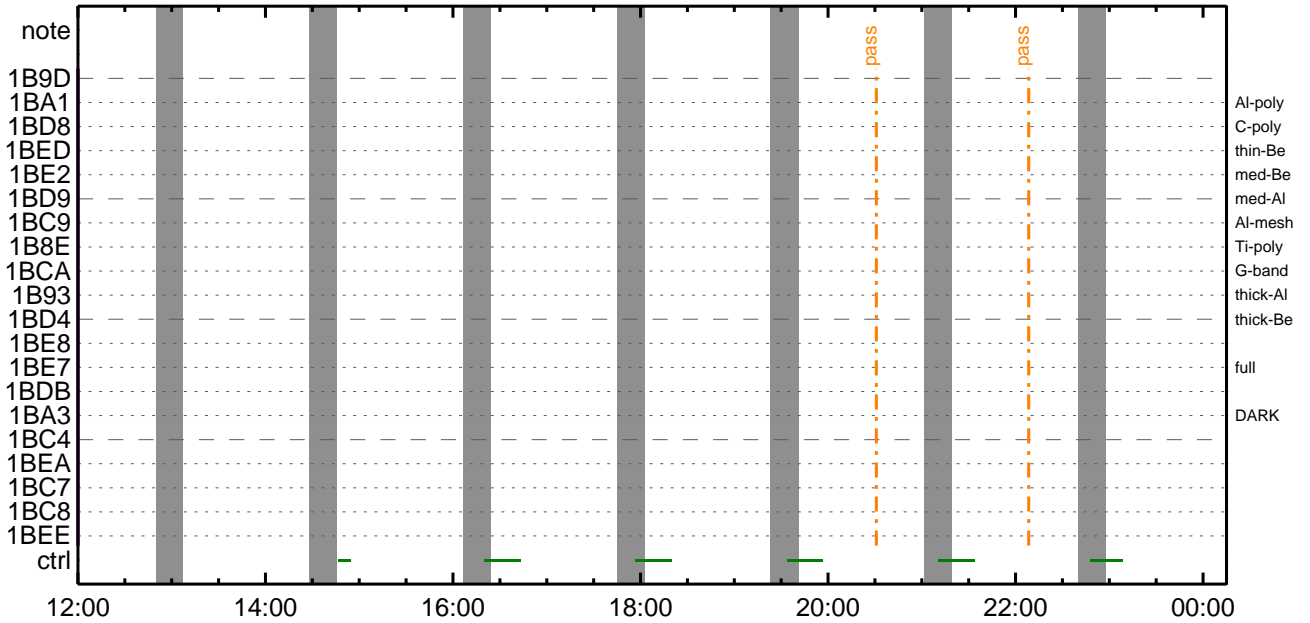
CMDI #0451 2018/04/22



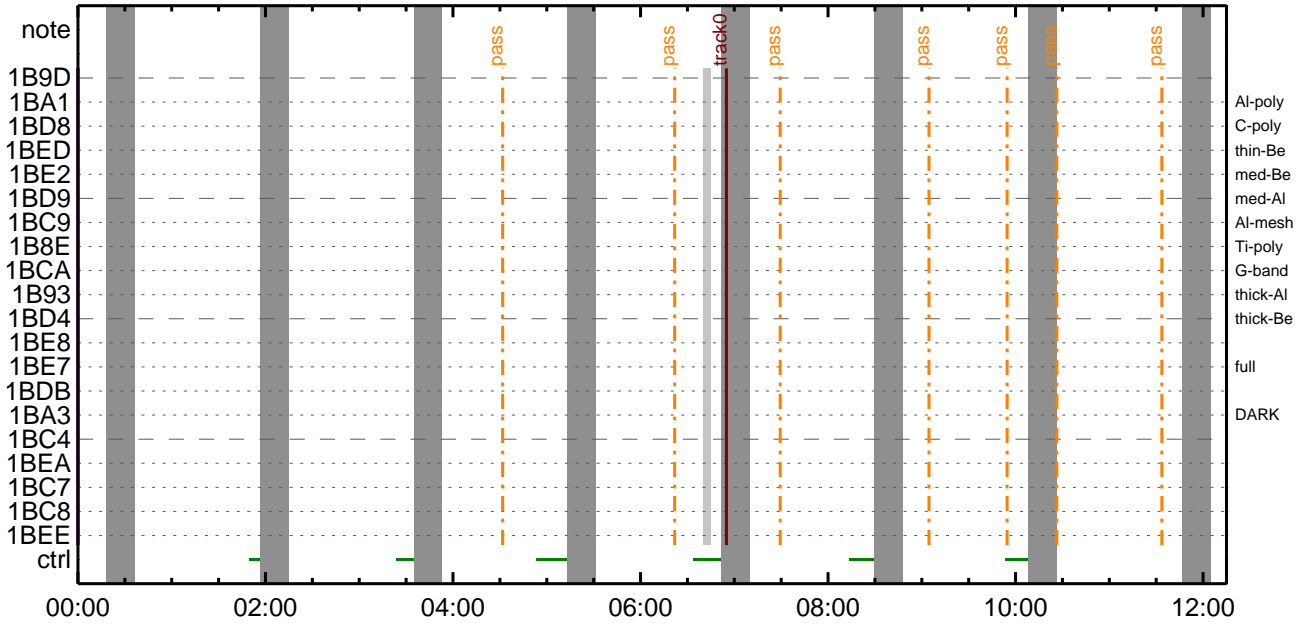
CMDI #0451 2018/04/23



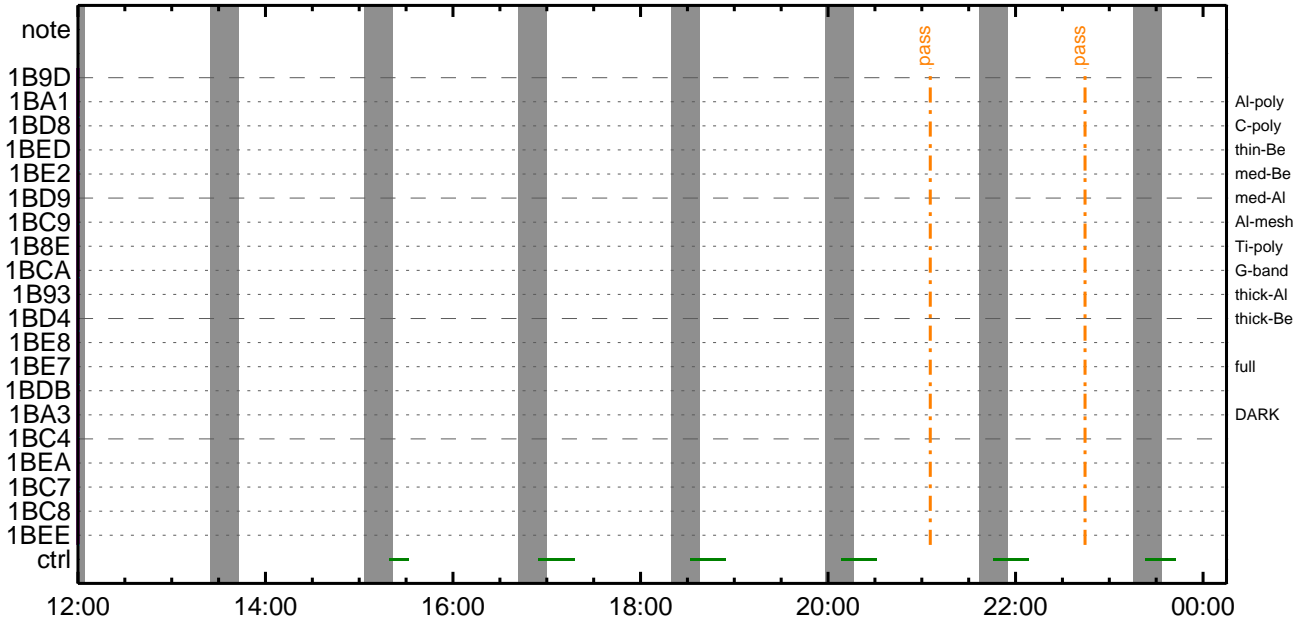
CMDI #0451 2018/04/23



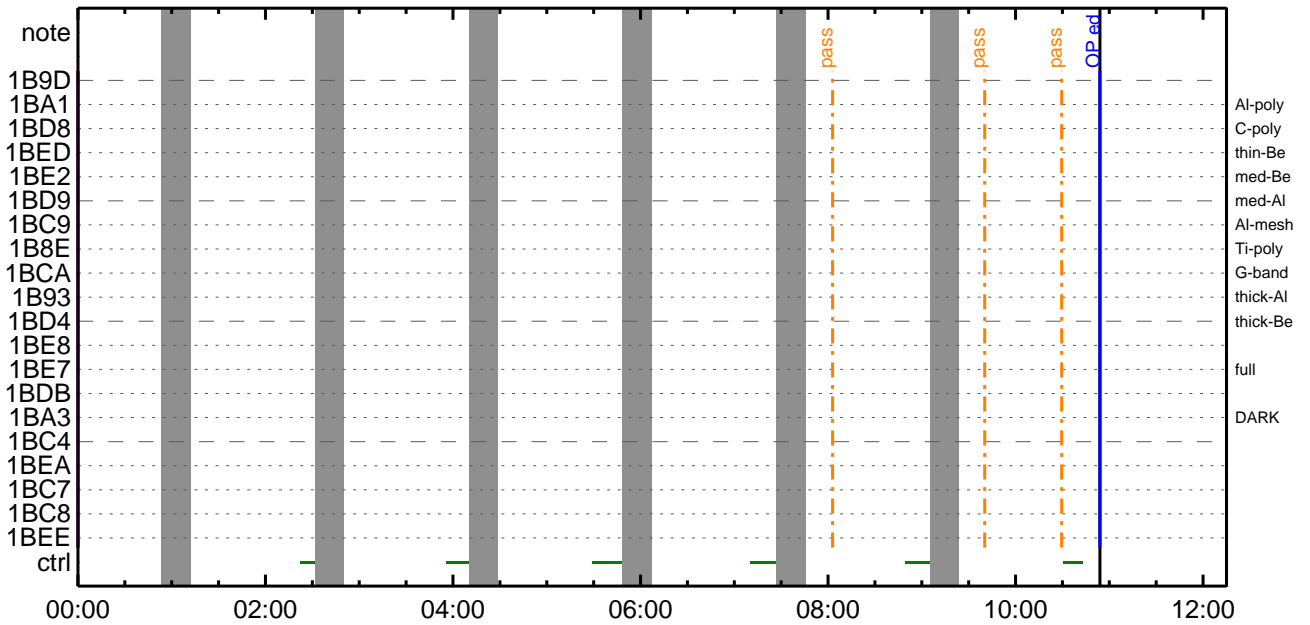
CMDI #0451 2018/04/24



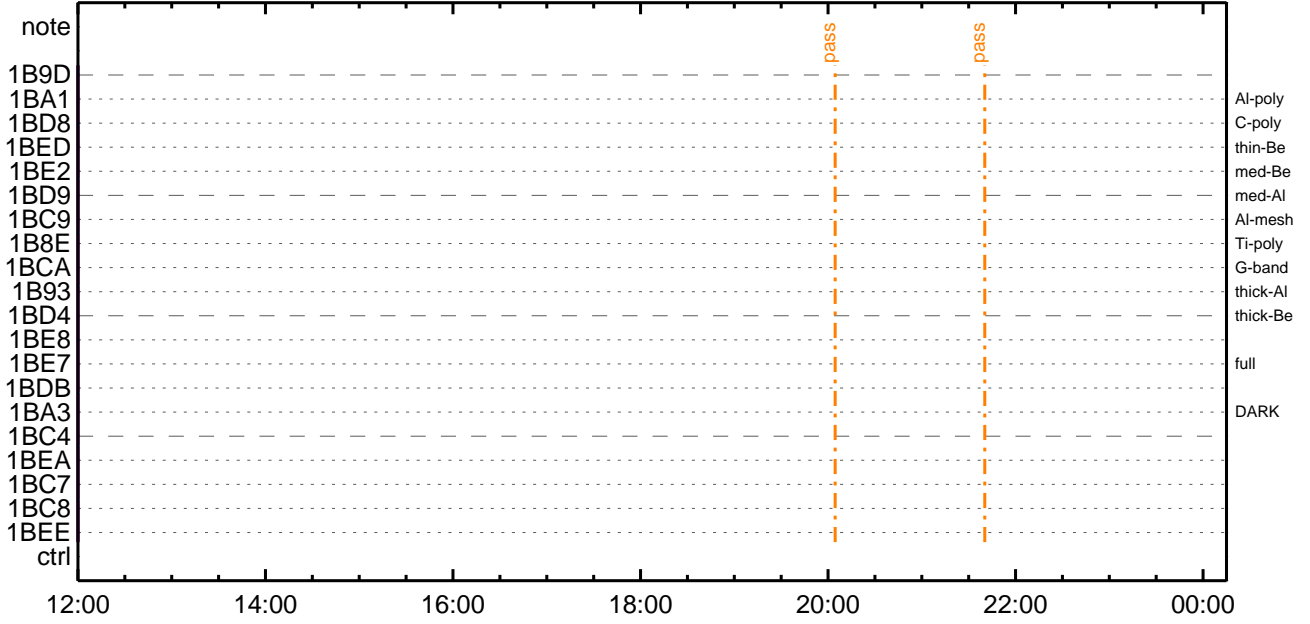
CMDI #0451 2018/04/24



CMDI #0451 2018/04/25



CMDI #0451 2018/04/25




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0096 C.
0097 C.
0098 C.
0099 C. *****
0100 C. *****
0101 C.
0102 C. ;ãOP/OGYí;¼YÉ;ä
0103 S. OP op-319:OP
0104 (
0105 S. OG og-319:OG
0106 (
0107 C.
0108 C. ;ãNMOG&OPíî°èYÀYóYx;ä
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0120 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YÀYóYx½ªî»ð³îÇ§
0125 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG□î¼E¹Ç•è²ïOK□ð³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0139 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YÀYóYx½ªî»ð³îÇ§
0144 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG□î¼E¹Ç•è²ïOK□ð³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0158 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YÀYóYx½ªî»ð³îÇ§
0163 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP□î¼E¹Ç•è²ïOK□ð³îÇ§
0165 C.
0166 C. ***** °ê²¼□î¼E¹Ç•è²ïOK□ð³îÇ§ *****
0167 C. DHUYâ;¼YÉ;È¼Y½;Yî;¼YÉ;È□ðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. ¢¢[HK1_PKT_FORM_NO] EQ 2
0171 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD□-Á÷¿@NG□î¼E¹Ç•è²ïOK□ð³îÇ§
0180 C. □ð³îÇ§;ÇSET□EEDUMP□î¼E¹Ç•è²ïOK□ð³îÇ§
0181 C.
0182 C. TIY³YþYóYÉ□ððÁDî¿(UT)
0183 +. TI 2018-04-21 10:38:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2018-04-21 10:38:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2018-04-21 10:38:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP

```

```
0194 . C.
0195 +. TI 2018-04-21 10:42:59.5
0196 DC 01-B2 DHU_OP_START
0197 . C.      꺠[HK1_TI_CMD_NUM]      EQ      1COUNTUP
0198 . C.
0199 . C.      °È²¼ºïÄê%íîñºîÿÁ¥§ÿÄÿ¹àïÛ
0200 . C.      꺠[HK1_TI_CMD_ENA/DIS]   EQ      ENA
0201 . C.      꺠[HK1_TI_CMD_NUM]      EQ      4
0202 . C.      꺠[HK1_NEXT_EXEC_PIM]    EQ      DHU
0203 . C.      꺠[HK1_NEXT_EXEC_DC]    EQ      0xB3
0204 . C.
0205 . C.      *****
0206 . C.      TIîï°èÿÄÿÓÿ×
0207 . C.      *****
0208 . C.
0209 . C.      TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 . C.      꺠[HK1_DMP_TOP_ADRS_1]   EQ      07
0213 . C.      꺠[HK1_DMP_TOP_ADRS_0]   EQ      2B
0214 . C.      꺠[HK1_DMP_BLOCK_NUM]    EQ      3
0215 . C.      꺠[HK1_DMP_REPEAT_NUM]  EQ      0
0216 . C.      꺠[HK1_DMA_DMP_PIM]      EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 . C.      꺠[HK1_PKT_FORM_NO]      EQ      7
0220 . C.      꺠[HK1_PKT_GEN_TIME]      EQ      0.25 s
0221 . C.      꺠[HK1_S_TLM_BIT_RATE]  EQ      32k
0222 . C.      꺠[HK1_X_TLM_BIT_RATE]  EQ      4M
0223 . C.      꺠[HK1_DMP_CHK_FLG]      EQ      EXEC
0224 . C.
0225 . C.      ¥ÄÿÓÿ×½ªî»º³îÇ§
0226 . C.      꺠[HK1_DMP_CHK_FLG]      EQ      NON
0227 . C.
0228 . C.      RAM ID=TI_TBLºîî½È¹ç•ë²îOKºº³îÇ§
0229 . C.
0230 . C.      DHUÿà;¼¥È;Ê%ÿ¼. ÿì;¼¥È;Ëººîãº¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 . C.      꺠[HK1_PKT_FORM_NO]      EQ      2
0234 . C.      꺠[HK1_PKT_GEN_TIME]      EQ      0.5S
0235 . C.      꺠[HK1_S_TLM_BIT_RATE]  EQ      32K
0236 . C.      꺠[HK1_X_TLM_BIT_RATE]  EQ      4M
0237 . C.
0238 . C.
0239 . C.      ***** XRT START *****
0240 . C.      Execute, after the success of OP upload.
0241 +. TI 2018-04-21 10:42:00.0
0242 DC 07-F0 MDP_XRT_MODE_STBY
0243 BC      (c3)
0244 . C.      [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0245 . C.
0246 . C.      ***** XRT END *****
0247 . C.
0248 . C.      ***** MDP ´ûÄîºî»ò%ÿºÈÄºº¹ºèDCBC•x²è *****
0249 . C.      (%áºîÿÓÿÄÿÈÿÿÿÿÿáÿçÿëºÈ%¼ºº%Ä»Ûº¹ºè)
0250 . S. DC-BC dcbc-402:DCBC
0251      (MDP_known_event)
0252 . C.
0253 . C.
0254 . C.      ***** ¥Ðÿ¹•î Daily±¿îñºè´Øº¹ºèDCBC•x²è *****
0255 . S. DC-BC dcbc-153:DCBC
0256      (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0257 . C.
0258 . C.
0259 . C.      ;ãLOSÿÁÿ§ÿÿÿ¹¼Ä»Û;ä
0260 . C.
0261 . C.      ***** LOS *****
0262 . C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-320 2018-04-21 11:53:06 60 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÀYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿òÁó•µ°È»Í×ÁÇóÍYçYÁY×Yí;¼YÉ;ÈÈ%µ•fíÉ;ÈòÈ¼°ÇÔó•¿¿¼í¹çóÍ;çÀ®, ùó¹òÈóòÇÁ+¿®ó•òÈóóó³òÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCS DUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 C.
0046 . C. ***** MDP `ûÁÍòÍ»ò¼YòÉÁòó¹òéDCBC•x²è *****
0047 C. (%á°íYÓYÁYB¥P¥H¥áYçYèÈ%¼óó¼Á»Ûó¹òé)
0048 . S. DC-BC dcbc-402:DCBC
0049 (MDP_known_event)
0050 C.
0051 C.
0052 . C. ***** YD¥¹•Í Daily+¿ÍÑóÈ´Øó¹òéDCBC•x²è *****
0053 . S. DC-BC dcbc-153:DCBC
0054 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0055 C.
0056 C.
0057 . C. ;ãLOS¥ÁY$YÁY-¼Á»Û;ä
0058 C.
0059 . C. ***** LOS *****
0060 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-321 2018-04-21 11:53:06 102 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÄY-¼Ä»Û;ä
0005 C.
0006 C. YÄYß;¼Y³YFÿÖYÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;Èò¿òÄò•µ°È»Í×ÁÇòÍYçYÄY×Yí;¼YÉ;ÈÈèµ•ííÉ;ÈòÈ¼°ÇÒò•ò¿¼í¹çòÍ;çÄ®, ùò¹òèòÈòÇÄ+¿ò•òÈòòò³òÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 +. DC 07-F0 MDP_XRT_CTRL_MANU
0020 BC (c1)
0021 +. DC 07-F0 MDP_XRT_MODE_STBY
0022 BC (c3)
0023 . C. ----- Success Verify ? OK / NG____
0024 C.
0025 C. XRT Obs. Table Upload
0026 . S. RAM ram-291:MDP_OBS_X
0027 ( )
0028 C.
0029 +. DC 07-F0 MDP_DUMP_XRTTBL
0030 BC (84 07 00 00 00 3a d4)
0031 . C. ----- Comparison Check ? OK / ERR ____
0032 C.
0033 C.
0034 +. DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 01 b1 b1 04 04)
0036 +. DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 02 b1 b1 08 08)
0038 +. DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 03 b1 b1 08 08)
0040 +. DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 04 b1 b1 06 06)
0042 +. DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 05 85 83 06 06)
0044 +. DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 06 85 83 06 06)
0046 +. DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 07 85 83 08 08)
0048 +. DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 08 80 80 20 20)
0050 +. DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 09 80 80 20 08)
0052 +. DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0a 80 80 08 20)
0054 +. DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0b 80 80 08 08)
0056 +. DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 0f 80 80 06 06)
0058 +. DC 07-F0 MDP_XRT_ROI_SET
0059 BC (cd 10 80 80 08 08)
0060 +. DC 07-F0 MDP_XRT_FLD_ENA
0061 BC (d8)
0062 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0063 BC (c8)
0064 +. DC 07-F0 MDP_XRT_ARS_DIS
0065 BC (d5)
0066 +. DC 07-F0 MDP_XRT_AEC_RESET
0067 BC (d0)
0068 +. DC 07-F0 MDP_XRT_FLD_RESET
0069 BC (da)
0070 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0071 BC (c4 07)
0072 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0073 BC (c5 0d)
0074 . C. ----- Success Verify ? OK / NG ____
0075 C.
0076 C.
0077 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0078 C.
0079 +. DC 07-F0 MDP_XRT_MODE_OBSV
0080 BC (c2)
0081 +. TI 2018-04-21 10:42:02.0
0082 DC 07-F0 MDP_XRT_MODE_OBSV
0083 BC (c2)
0084 . C. ----- Success Verify ? OK / NG ____
0085 C.
0086 C. ***** XRT END *****
0087 C.
0088 . C. ***** MDP 'üÄíòí»ò¼YòÈÄò¹òèDCBC•x²è *****
0089 C. (%á°íYÖYÄYÈYßYÉYçYèòÈ¼òò¼Ä»Ûò¹òè)
0090 . S. DC-BC dcbc-402:DCBC
0091 (MDP_known_event)
0092 C.
0093 C.
0094 . C. ***** YÄY¹•í Daily+¿íÑòÈ´Øò¹òèDCBC•x²è *****
0095 . S. DC-BC dcbc-153:DCBC
```

```
0096 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0097 C.
0098 C.
0099 . C. ;äLOSŸÁŸSŸÄŸ-¼Ä»Û;ä
0100 C.
0101 . C. ***** LOS *****
0102 C.
```

*** OP Sequence for XRT ***

```

2018/04/21 10:52:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/04/21 10:52:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/04/21 10:52:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2018/04/21 10:53:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 03 00 00 00 00
2018/04/21 10:53:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2018/04/21 10:53:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2018/04/21 10:53:22.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2018/04/21 10:53:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2018/04/21 10:53:26.0 XRT_FLD_RESET_433_OG [0x1b1]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2018/04/21 10:55:56.0 XRT_QT_PROG_SET_429_OG [0x1ad]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0a
2018/04/21 10:55:58.0 XRT_FL_PROG_SET_440_OG [0x1b8]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 0d
2018/04/21 10:56:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2018/04/21 11:40:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/04/21 11:40:02.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/04/21 11:40:04.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2018/04/21 11:40:06.0 XRT_PREFLR_STRT_406_OG [0x196]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2018/04/21 11:43:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2018/04/21 11:55:30.0 XRT_Custom_430_OG [0x1ae]
2018/04/21 11:56:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2018/04/21 13:18:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/04/21 13:18:02.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/04/21 13:18:04.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2018/04/21 13:18:06.0 XRT_PREFLR_STRT_406_OG [0x196]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2018/04/21 13:21:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2018/04/21 13:29:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/04/21 13:29:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/04/21 13:29:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2018/04/21 13:30:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 00 ed 9a ae 98
2018/04/21 13:30:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2018/04/21 13:30:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2018/04/21 13:30:22.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2018/04/21 13:30:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2018/04/21 13:30:26.0 XRT_FLD_RESET_433_OG [0x1b1]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2018/04/21 13:32:56.0 XRT_QT_PROG_SET_428_OG [0x1ac]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0b
2018/04/21 13:32:58.0 XRT_FL_PROG_SET_440_OG [0x1b8]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 0d
2018/04/21 13:34:00.0 XRT_Custom_430_OG [0x1ae]
2018/04/21 13:35:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2018/04/21 14:56:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/04/21 14:56:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/04/21 14:56:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2018/04/21 14:56:36.0 XRT_PREFLR_STRT_406_OG [0x196]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2018/04/21 14:59:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2018/04/21 15:23:30.0 XRT_Custom_430_OG [0x1ae]
2018/04/21 15:24:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2018/04/21 16:29:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/04/21 16:29:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/04/21 16:29:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00

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2018/04/21	16:30:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03	00	00	00	00
2018/04/21	16:30:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2018/04/21	16:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2018/04/21	16:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2018/04/21	16:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/04/21	16:30:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/04/21	16:32:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2018/04/21	16:32:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2018/04/21	16:35:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/21	16:35:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/21	16:35:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/04/21	16:35:06.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/04/21	16:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/04/21	17:10:30.0	XRT_Custom_430_OG [0x1ae]							
2018/04/21	17:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/04/21	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/21	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/21	17:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2018/04/21	18:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	00	00
2018/04/21	18:00:18.0	XRT_FLD_DIS_425_OG [0x1a9]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2018/04/21	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2018/04/21	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/04/21	18:02:58.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f			
2018/04/21	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/04/21	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/21	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/21	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2018/04/21	18:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03	00	00	00	00
2018/04/21	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2018/04/21	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2018/04/21	18:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2018/04/21	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/04/21	18:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/04/21	18:12:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2018/04/21	18:12:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2018/04/21	18:13:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/21	18:13:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/21	18:13:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/04/21	18:13:06.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/04/21	18:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/04/21	18:47:00.0	XRT_Custom_430_OG [0x1ae]							
2018/04/21	18:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/04/21	19:51:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/21	19:51:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/21	19:51:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/04/21	19:51:36.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/04/21	19:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/04/21	20:24:00.0	XRT_Custom_430_OG [0x1ae]							

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2018/04/21	20:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/04/21	21:30:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/21	21:30:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/21	21:30:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/04/21	21:30:06.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/04/21	21:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/04/21	22:01:00.0	XRT_Custom_430_OG [0x1ae]							
2018/04/21	22:02:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/04/21	22:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/21	22:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/21	22:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2018/04/21	23:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2018/04/21	23:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2018/04/21	23:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2018/04/21	23:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2018/04/21	23:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/04/21	23:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/04/21	23:02:56.0	XRT_QT_PROG_SET_446_OG [0x1be]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2018/04/21	23:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2018/04/21	23:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/04/21	23:08:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/21	23:08:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/21	23:08:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/04/21	23:08:06.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/04/21	23:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/04/21	23:35:30.0	XRT_Custom_430_OG [0x1ae]							
2018/04/21	23:36:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/04/22	00:46:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/22	00:46:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/22	00:46:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/04/22	00:46:36.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/04/22	00:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/04/22	01:02:30.0	XRT_Custom_430_OG [0x1ae]							
2018/04/22	01:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/04/22	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/22	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/22	01:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2018/04/22	02:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2018/04/22	02:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2018/04/22	02:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2018/04/22	02:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2018/04/22	02:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/04/22	02:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/04/22	02:02:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2018/04/22	02:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2018/04/22	02:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/04/22	02:15:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/22	02:15:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				

2018/04/22	02:15:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/04/22	02:15:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/04/22	02:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/04/22	02:41:00.0	XRT_Custom_430_OG [0x1ae]						
2018/04/22	02:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/04/22	03:50:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/22	03:50:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/22	03:50:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/04/22	03:50:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/04/22	03:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/04/22	04:19:30.0	XRT_Custom_430_OG [0x1ae]						
2018/04/22	04:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/04/22	05:21:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/22	05:21:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/22	05:21:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/04/22	05:21:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/04/22	05:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/04/22	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/22	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/22	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2018/04/22	06:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00		
2018/04/22	06:00:18.0	XRT_FLD_DIS_425_OG [0x1a9]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2018/04/22	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2018/04/22	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2018/04/22	06:02:58.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f		
2018/04/22	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/04/22	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/22	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/22	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2018/04/22	06:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 00 00 00 00		
2018/04/22	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2018/04/22	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2018/04/22	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2018/04/22	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2018/04/22	06:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/04/22	06:12:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a		
2018/04/22	06:12:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2018/04/22	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/04/22	07:02:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/22	07:02:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/22	07:02:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/04/22	07:02:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/04/22	07:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/04/22	07:36:00.0	XRT_Custom_430_OG [0x1ae]						
2018/04/22	07:37:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/04/22	08:41:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/22	08:41:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/22	08:41:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		

2018/04/22	08:41:36.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/04/22	08:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/04/22	09:14:30.0	XRT_Custom_430_OG [0x1ae]							
2018/04/22	09:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/04/22	10:22:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/22	10:22:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/22	10:22:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/04/22	10:22:36.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/04/22	10:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/04/22	10:36:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/22	10:36:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/22	10:36:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/04/22	10:36:36.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/04/22	10:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/04/22	10:53:00.0	XRT_Custom_430_OG [0x1ae]							
2018/04/22	10:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/04/22	11:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/22	11:29:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/22	11:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2018/04/22	11:30:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2018/04/22	11:30:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2018/04/22	11:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2018/04/22	11:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2018/04/22	11:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/04/22	11:30:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/04/22	11:32:56.0	XRT_QT_PROG_SET_409_OG [0x199]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2018/04/22	11:32:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2018/04/22	11:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/04/22	12:15:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/22	12:15:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/22	12:15:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/04/22	12:15:06.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/04/22	12:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/04/22	12:31:30.0	XRT_Custom_430_OG [0x1ae]							
2018/04/22	12:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/04/22	13:53:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/22	13:53:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/22	13:53:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/04/22	13:53:06.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/04/22	13:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/04/22	14:10:00.0	XRT_Custom_430_OG [0x1ae]							
2018/04/22	14:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/04/22	14:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/22	14:29:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/22	14:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2018/04/22	14:30:00.0	AOCS_Ore-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2018/04/22	14:30:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2018/04/22	14:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2018/04/22	14:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]							

2018/04/22	14:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2018/04/22	14:30:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/04/22	14:32:56.0	XRT_QT_PROG_SET_409_OG [0x199]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	13	
2018/04/22	14:32:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d	
2018/04/22	14:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/04/22	15:31:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/22	15:31:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/22	15:31:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/04/22	15:31:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/04/22	15:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/04/22	16:02:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/22	16:02:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/22	16:02:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/04/22	16:02:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/04/22	16:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/04/22	16:08:30.0	XRT_Custom_430_OG [0x1ae]						
2018/04/22	16:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/04/22	17:10:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/22	17:10:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/22	17:10:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/04/22	17:10:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/04/22	17:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/04/22	17:45:00.0	XRT_Custom_430_OG [0x1ae]						
2018/04/22	17:46:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/04/22	18:00:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/22	18:00:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/22	18:00:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2018/04/22	18:00:30.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00 00 00 00	
2018/04/22	18:00:48.0	XRT_FLD_DIS_425_OG [0x1a9]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2018/04/22	18:03:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2018/04/22	18:03:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2018/04/22	18:03:28.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12	
2018/04/22	18:03:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/04/22	18:10:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/22	18:10:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/22	18:10:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2018/04/22	18:10:30.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03	00 00 00 00	
2018/04/22	18:10:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2018/04/22	18:10:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2018/04/22	18:10:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2018/04/22	18:10:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2018/04/22	18:10:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/04/22	18:13:26.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a	
2018/04/22	18:13:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d	
2018/04/22	18:13:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/04/22	18:48:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/22	18:48:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		

2018/04/22	18:48:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2018/04/22	18:48:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2018/04/22	18:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2018/04/22	19:22:00.0	XRT_Custom_430_OG [0x1ae]							
2018/04/22	19:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2018/04/22	20:26:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/04/22	20:26:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/04/22	20:26:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2018/04/22	20:26:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2018/04/22	20:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2018/04/22	20:59:00.0	XRT_Custom_430_OG [0x1ae]							
2018/04/22	21:00:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2018/04/22	22:05:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/04/22	22:05:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/04/22	22:05:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2018/04/22	22:05:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2018/04/22	22:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2018/04/22	22:35:00.0	XRT_Custom_430_OG [0x1ae]							
2018/04/22	22:36:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2018/04/22	22:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/04/22	22:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/04/22	22:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2018/04/22	23:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00			
2018/04/22	23:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2018/04/22	23:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2018/04/22	23:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2018/04/22	23:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2018/04/22	23:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da			
2018/04/22	23:02:56.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10			
2018/04/22	23:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d			
2018/04/22	23:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2018/04/22	23:43:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/04/22	23:43:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/04/22	23:43:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2018/04/22	23:43:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2018/04/22	23:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2018/04/23	00:03:30.0	XRT_Custom_430_OG [0x1ae]							
2018/04/23	00:04:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2018/04/23	01:19:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/04/23	01:19:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/04/23	01:19:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2018/04/23	01:19:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2018/04/23	01:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2018/04/23	01:38:30.0	XRT_Custom_430_OG [0x1ae]							
2018/04/23	01:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2018/04/23	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/04/23	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/04/23	01:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2018/04/23	02:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 00 00 00 00			

2018/04/23	02:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2018/04/23	02:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2018/04/23	02:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2018/04/23	02:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/04/23	02:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/04/23	02:02:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2018/04/23	02:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2018/04/23	02:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/04/23	02:49:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/23	02:49:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/23	02:49:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/04/23	02:49:06.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/04/23	02:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/04/23	03:17:00.0	XRT_Custom_430_OG [0x1ae]							
2018/04/23	03:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/04/23	04:17:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/23	04:17:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/23	04:17:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/04/23	04:17:36.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/04/23	04:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/04/23	04:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/23	05:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2018/04/24	06:55:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				