

# XRT Timeline to be uploaded on 2018/04/26

Period: 2018/04/26 10:56:00 - 2018/05/01 11:09:00

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Normal mode

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## XOB #1BA4: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, Al/Poly context, with G-band (1ms/1ms)

Term	Pointing (x, y)	Comment
04/26 11:12:00 - 04/26 13:59:54	Track ( 261.4, 93.4) @ 04/26 11:06:00	# OP start + 10min/HOP307 AR12706
04/26 21:42:30 - 04/26 22:46:30	Track ( 350.6, 90.3) @ 04/26 21:15:00	HOP307 AR12706
04/27 02:24:00 - 04/27 05:57:54	Track ( 391.2, 88.7) @ 04/27 02:00:00	HOP307 AR12706
04/27 06:11:00 - 04/27 17:59:54	Track ( 425.8, 87.1) @ 04/27 06:08:00	#HOP307 AR12706

**PROG= 17 Inf.-time(s)**

Subr= 1	1-time(s)	2.0sec										
<b>Seqn= 92</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
Open/G-band	Open/G-band open	Safe Norm 1ms	Obs 1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec				
Open/G-band	Open/G-band close	Safe Norm 1ms	Obs 1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec				
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s	Obs 1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec				
<b>Seqn= 42</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
Al-poly/Open	thin-Be/Open close	Safe Norm 500ms	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec				
thin-Be/Open	med-Be/Open close	Safe Norm 5.66s	Obs 1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec				
Open/thick-Al	Open/thick-Al close	Safe Norm 16.0s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec				
<b>Seqn= 96</b>	<b>40-time(s)</b>	<b>2.0sec</b>										
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms	Obs 1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec				
thin-Be/Open	med-Be/Open close	Safe Norm 500ms	Obs 1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec				
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms	Obs 1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec				
thin-Be/Open	med-Be/Open close	Safe Norm 500ms	Obs 1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec				
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms	Obs 1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec				
thin-Be/Open	med-Be/Open close	Safe Norm 500ms	Obs 1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec				
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

## XOB #1BEF: HOP329 - Al/poly (Be-Thin) - 256x256 - 1x1 - 4sec cad - Gband - 348x384 - 1ms - AEC1

Term	Pointing (x, y)	Comment
04/26 14:03:00 - 04/26 17:40:24	Track ( 300.2, 90.2) @ 04/26 14:00:00	HOP329 ALMA Cyc5
04/26 18:28:30 - 04/26 21:08:30	Track ( 333.5, 89.0) @ 04/26 17:50:30	#HOP329 ALMA Cyc5 cont.

**PROG= 05 Inf.-time(s)**

Subr= 1	2-time(s)	2.0sec										
<b>Seqn= 92</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
Open/G-band	Open/G-band open	Safe Norm 1ms	Obs 1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec				
Open/G-band	Open/G-band close	Safe Norm 1ms	Obs 1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec				
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s	Obs 1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec				
<b>Subr= 2</b>	<b>250-time(s)</b>	<b>4.0sec</b>										
<b>Seqn= 20</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms	Obs 1x1	256x256 (1064, 1048)	Q=95	1	0	2.0sec				
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

## XOB #1BD9: Synoptic Q95 2x2 - Al/mesh(64/512/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(45/512/4096) + T

Term	Pointing (x, y)	Comment
04/26 17:44:56 - 04/26 17:50:24	Fixed ( 0.0, 0.0)	synoptic, shifted -19.5 min
04/27 06:02:26 - 04/27 06:07:54	Fixed ( 0.0, 0.0)	synoptic, shifted -2.0 min
04/27 18:04:26 - 04/27 18:09:54	Fixed ( 0.0, 0.0)	synoptic
04/28 06:19:56 - 04/28 06:25:24	Fixed ( 0.0, 0.0)	synoptic, shifted 15.5 min

**PROG= 15 1-time(s)**

Subr= 1	1-time(s)	2.0sec										
<b>Seqn= 5</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec				
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms	Obs 4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec				
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms	Obs 8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec				
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms	Obs 1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec				
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms	Obs 1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec				
<b>Seqn= 36</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
Open/Al-mesh	Open/Al-mesh close	Safe Norm 63ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
Open/Al-mesh	Open/Al-mesh close	Safe Norm 500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
Open/Al-mesh	Open/Al-mesh close	Safe Norm 2.83s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
<b>Seqn= 99</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
Al-poly/Open	Al-poly/Open close	Safe Norm 44ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
Al-poly/Open	Al-poly/Open close	Safe Norm 500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 4.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
<b>Seqn= 33</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
thin-Be/Open	thin-Be/Open close	Safe Norm 1.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
thin-Be/Open	thin-Be/Open close	Safe Norm 11.3s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
thin-Be/Open	thin-Be/Open close	Safe Norm 22.6s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
<b>Seqn= 23</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
Open/G-band	Open/G-band open	Safe Norm 1ms	Obs 1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec				
Open/G-band	Open/G-band close	Safe Norm 1ms	Obs 1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec				
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1BE2: HOP349 - 3-filter Synoptics (Al-mesh[24/256/2897], Al-poly[45/512/4096], thin-Be[1024/11571/23142] with 512x512 G-band+Leak - 90min cad) +**

Term	Pointing (x, y)	Comment
04/26 23:17:30 - 04/27 01:57:00	Fixed ( 0.0, 0.0)	HOP349
04/27 23:03:00 - 04/28 01:59:54	Fixed ( 0.0, 0.0)	HOP349
<b>PROG= 16 Inf.-time(s)</b>		
<b>Subr= 1 1-time(s) 300.0sec</b>		
<b>Seqn= 1 1-time(s) 2.0sec</b>		
Open/Al-mesh Open/Al-mesh close	Safe Norm 24ms	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh Open/Al-mesh close	Safe Norm 250ms	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh Open/Al-mesh close	Safe Norm 2.83s	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
<b>Seqn= 99 1-time(s) 2.0sec</b>		
Al-poly/Open Al-poly/Open close	Safe Norm 44ms	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open Al-poly/Open close	Safe Norm 500ms	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open Al-poly/thick-Al close	Safe Norm 4.00s	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
<b>Seqn= 33 1-time(s) 2.0sec</b>		
thin-Be/Open thin-Be/Open close	Safe Norm 1.00s	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open thin-Be/Open close	Safe Norm 11.3s	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open thin-Be/Open close	Safe Norm 22.6s	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
<b>Seqn= 30 1-time(s) 2.0sec</b>		
Open/G-band Open/G-band open	Safe Norm 1ms	Obs 1x1 512x512 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band Open/G-band close	Safe Norm 1ms	Obs 1x1 512x512 (1024, 1024) Q=95 0 0 2.0sec
<b>Subr= 2 18-time(s) 300.0sec</b>		
<b>Seqn= 8 1-time(s) 2.0sec</b>		
thin-Be/Open med-Be/Open close	Safe Norm 1.00s	Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
thin-Be/Open med-Be/Open close	Safe Norm 1.41s	Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
<b>Seqn= 6 1-time(s) 2.0sec</b>		
Al-poly/Open Al-poly/Open close	Safe Norm 125ms	Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
Al-poly/Open Al-poly/Open close	Safe Norm 1.00s	Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
<b>Seqn= 29 1-time(s) 2.0sec</b>		
Open/Al-mesh Open/Al-mesh close	Safe Norm 125ms	Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
Open/Al-mesh Open/Al-mesh close	Safe Norm 250ms	Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

**XOB #1BA3: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, Al/Poly context, with G-band (1ms/1ms)**

Term	Pointing (x, y)	Comment
04/27 18:13:00 - 04/27 22:59:54	Track ( 522.5, 82.0) @ 04/27 18:10:00	#HOP307 AR12706
04/28 02:03:00 - 04/28 05:37:30	Track ( 581.6, 78.3) @ 04/28 02:00:00	HOP307 AR12706
04/28 06:28:30 - 04/28 10:38:30	Track ( 613.5, 76.0) @ 04/28 06:25:30	#HOP307 AR12706
<b>PROG= 06 Inf.-time(s)</b>		
<b>Subr= 1 1-time(s) 2.0sec</b>		
<b>Seqn= 92 1-time(s) 2.0sec</b>		
Open/G-band Open/G-band open	Safe Norm 1ms	Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/G-band Open/G-band close	Safe Norm 1ms	Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/Ti-poly Open/thick-Al close	Safe Dark 16.0s	Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
<b>Seqn= 42 3-time(s) 2.0sec</b>		
Al-poly/Open thin-Be/Open close	Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
thin-Be/Open med-Be/Open close	Safe Norm 5.66s	Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
Open/thick-Al Open/thick-Al close	Safe Norm 16.0s	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
<b>Seqn= 32 40-time(s) 90.0sec</b>		
thin-Be/Open med-Be/Open close	Safe Norm 1.00s	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Al-poly/Open thin-Be/Open close	Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 34.0sec
thin-Be/Open med-Be/Open close	Safe Norm 1.00s	Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
Al-poly/Open thin-Be/Open close	Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 1 34.0sec
thin-Be/Open med-Be/Open close	Safe Norm 1.00s	Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
Al-poly/Open thin-Be/Open close	Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

\* \* \* \* \* **Flare mode** \* \* \* \* \*

**XOB #1B8E: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512)**

Term	Pointing (x, y)	Comment
04/26 11:12:00 - 04/26 13:59:54	Track ( 261.4, 93.4) @ 04/26 11:06:00	# OP start + 10min/HOP307 AR12706
04/26 14:03:00 - 04/26 17:40:24	Track ( 300.2, 90.2) @ 04/26 14:00:00	HOP329 ALMA Cyc5
04/26 18:28:30 - 04/26 21:08:30	Track ( 333.5, 89.0) @ 04/26 17:50:30	#HOP329 ALMA Cyc5 cont.
04/26 21:42:30 - 04/26 22:46:30	Track ( 350.6, 90.3) @ 04/26 21:15:00	HOP307 AR12706
04/26 23:17:30 - 04/27 01:57:00	Fixed ( 0.0, 0.0)	HOP349
04/27 02:24:00 - 04/27 05:57:54	Track ( 391.2, 88.7) @ 04/27 02:00:00	HOP307 AR12706
04/27 06:11:00 - 04/27 17:59:54	Track ( 425.8, 87.1) @ 04/27 06:08:00	#HOP307 AR12706
04/27 18:13:00 - 04/27 22:59:54	Track ( 522.5, 82.0) @ 04/27 18:10:00	#HOP307 AR12706
04/27 23:03:00 - 04/28 01:59:54	Fixed ( 0.0, 0.0)	HOP349
04/28 02:03:00 - 04/28 05:37:30	Track ( 581.6, 78.3) @ 04/28 02:00:00	HOP307 AR12706
04/28 06:28:30 - 04/28 10:38:30	Track ( 613.5, 76.0) @ 04/28 06:25:30	#HOP307 AR12706
<b>PROG= 13 30-time(s)</b>		
<b>Subr= 1 20-time(s) 2.0sec</b>		
<b>Seqn= 11 1-time(s) 2.0sec</b>		
Al-poly/Open Al-poly/thick-Al close	Safe Norm 125ms	Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
<b>Seqn=100 1-time(s) 10.0sec</b>		
thin-Be/Open med-Be/Open close	Safe Norm 125ms	Obs 1x1 384x384 (1024, 1024) Q=95 2 0 2.0sec

	med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Subr= 2</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
<b>Seqn= 10</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 11</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Seqn= 87</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Active Region Search

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NOT USED

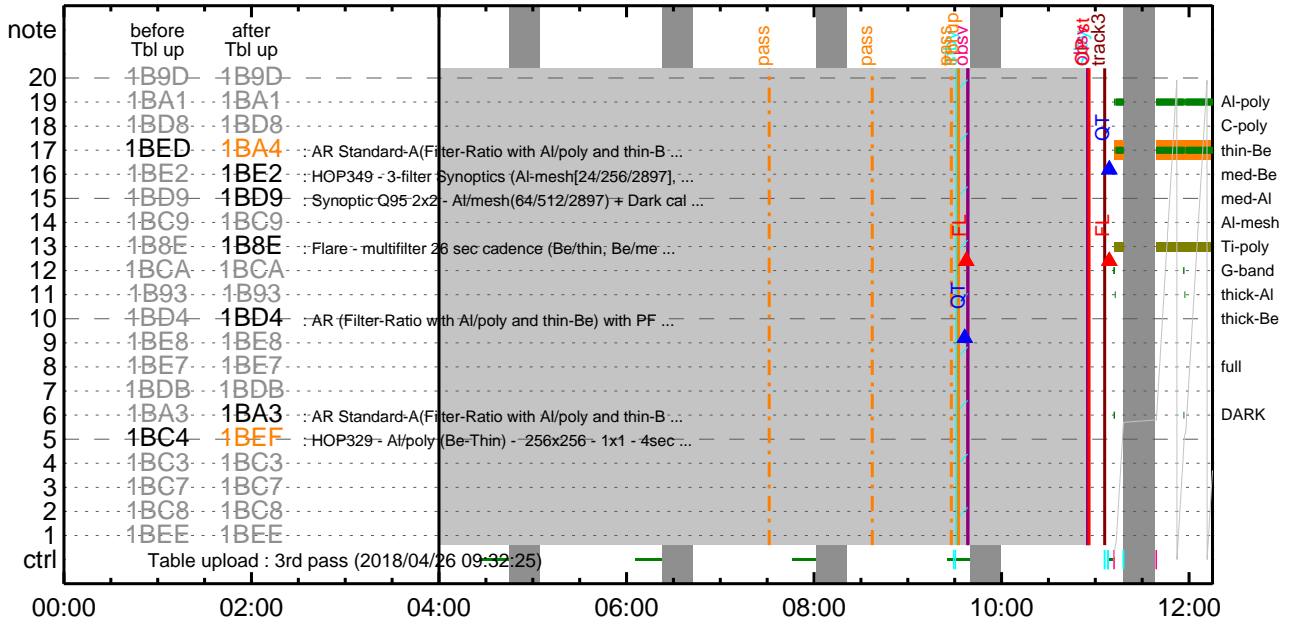
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### Flare Detection

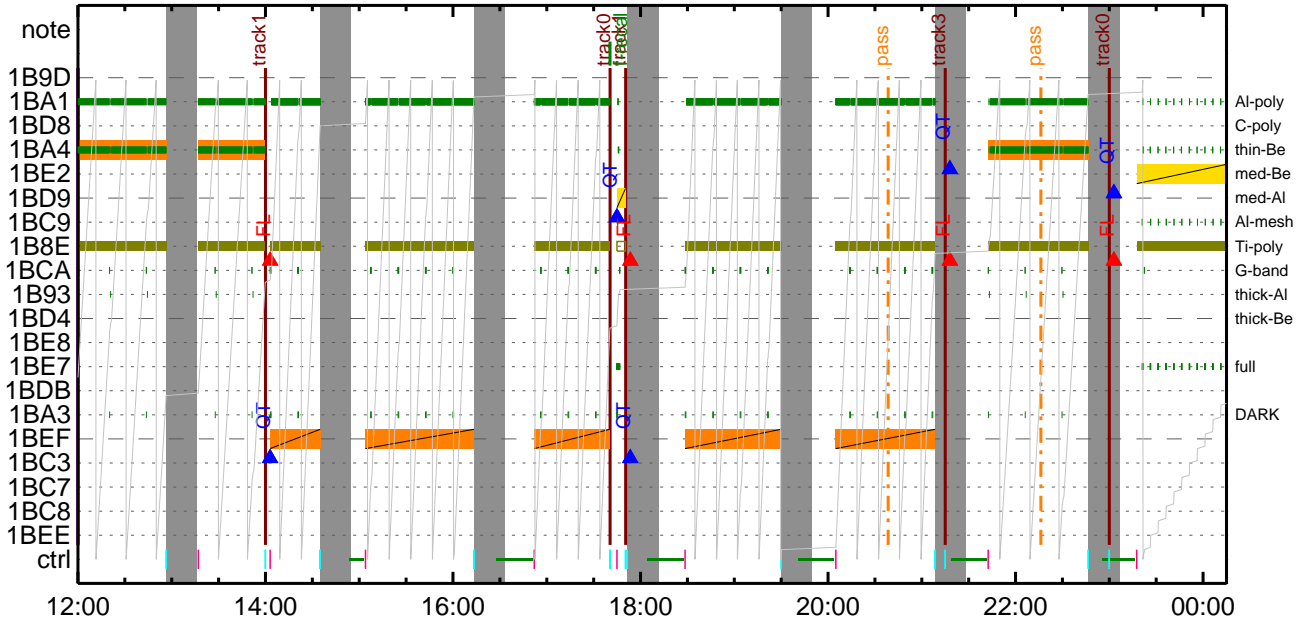
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FLD Patrol												
Term	Pointing (x, y)								Comment			
04/26 17:50:48 - 04/27 06:02:18	Track (	333.5,	89.0)	<small>@ 04/26 17:50:30</small>	#HOP329	ALMA	Cyc5	cont.				
04/27 06:08:18 - 04/27 18:04:18	Track (	425.8,	87.1)	<small>@ 04/27 06:08:00</small>	#HOP307	AR12706						
04/27 18:10:18 - 04/28 06:19:48	Track (	522.5,	82.0)	<small>@ 04/27 18:10:00</small>	#HOP307	AR12706						
04/28 06:25:48 - 05/01 11:09:00	Track (	613.5,	76.0)	<small>@ 04/28 06:25:30</small>	#HOP307	AR12706						
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	8ms	Obs	8x8		Q=50		80sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

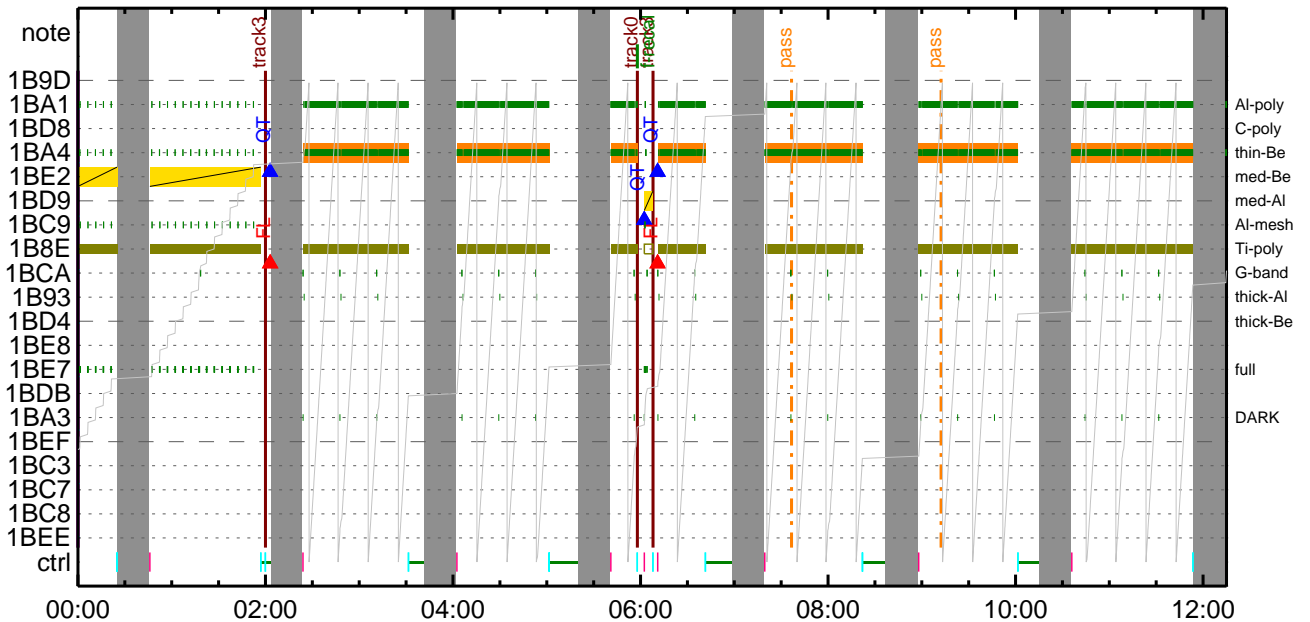
CMDI #0460 2018/04/26



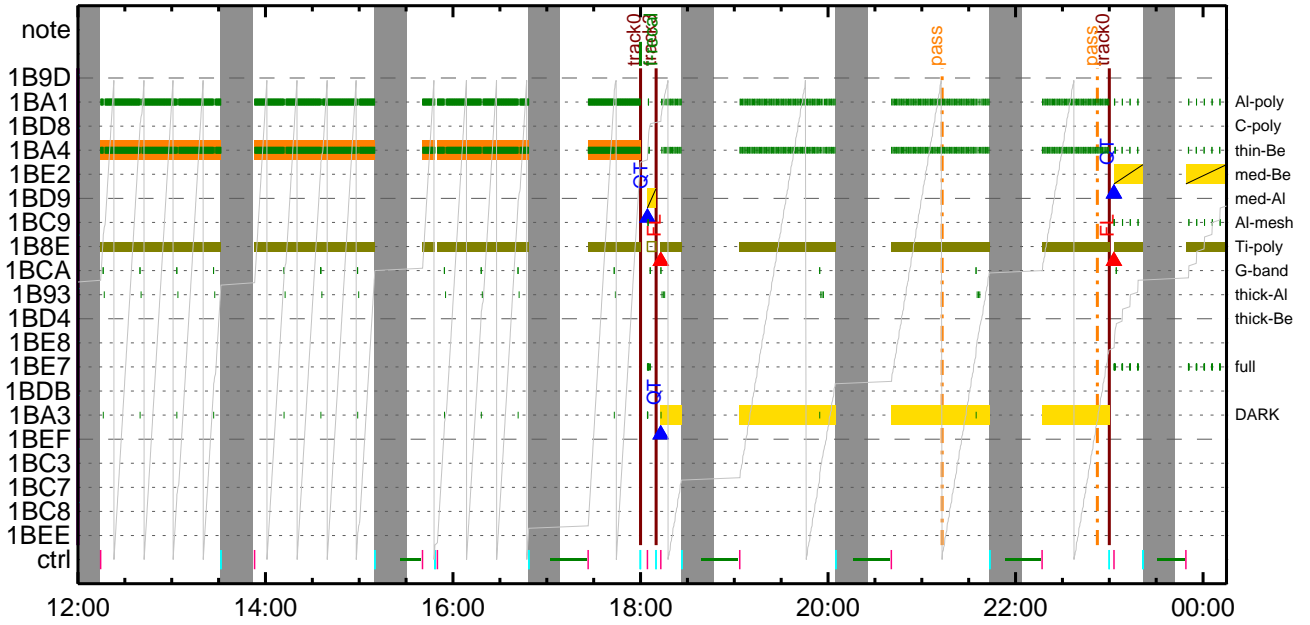
CMDI #0460 2018/04/26



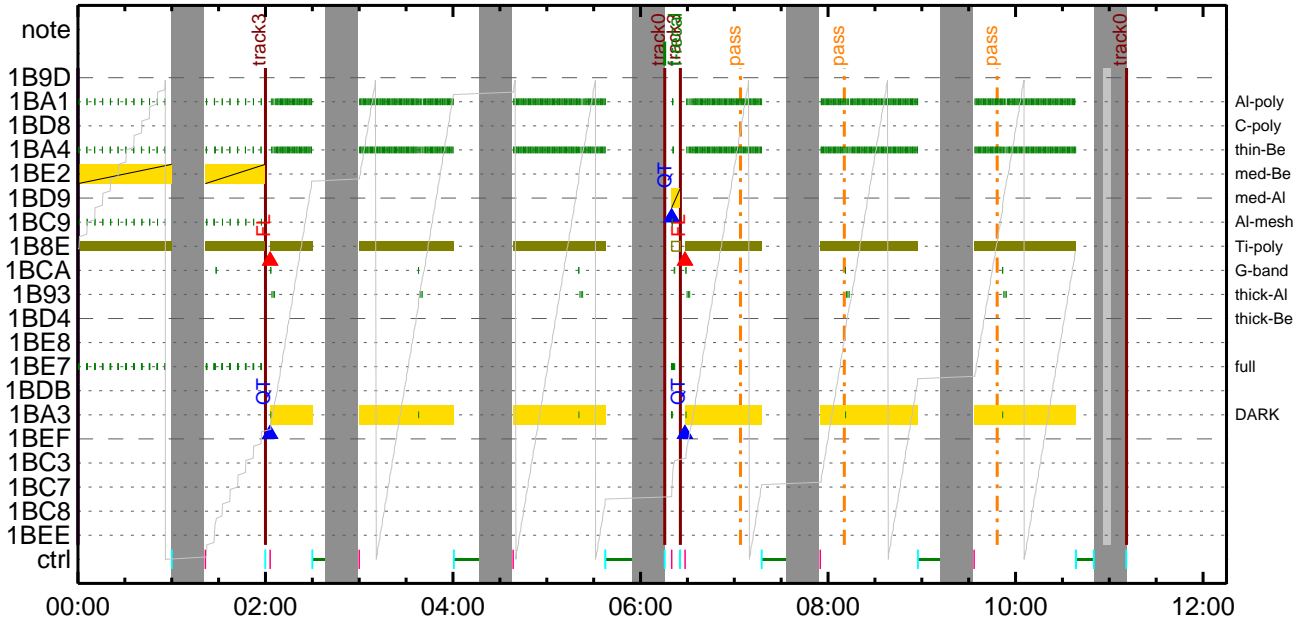
CMDI #0460 2018/04/27



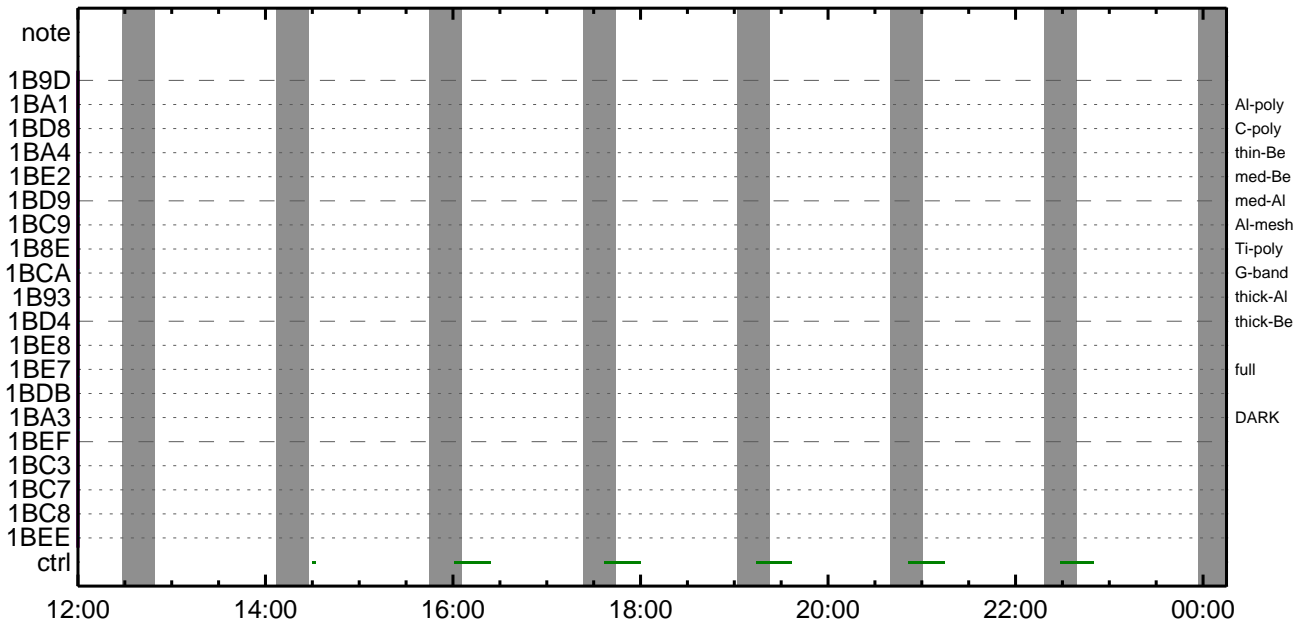
CMDI #0460 2018/04/27



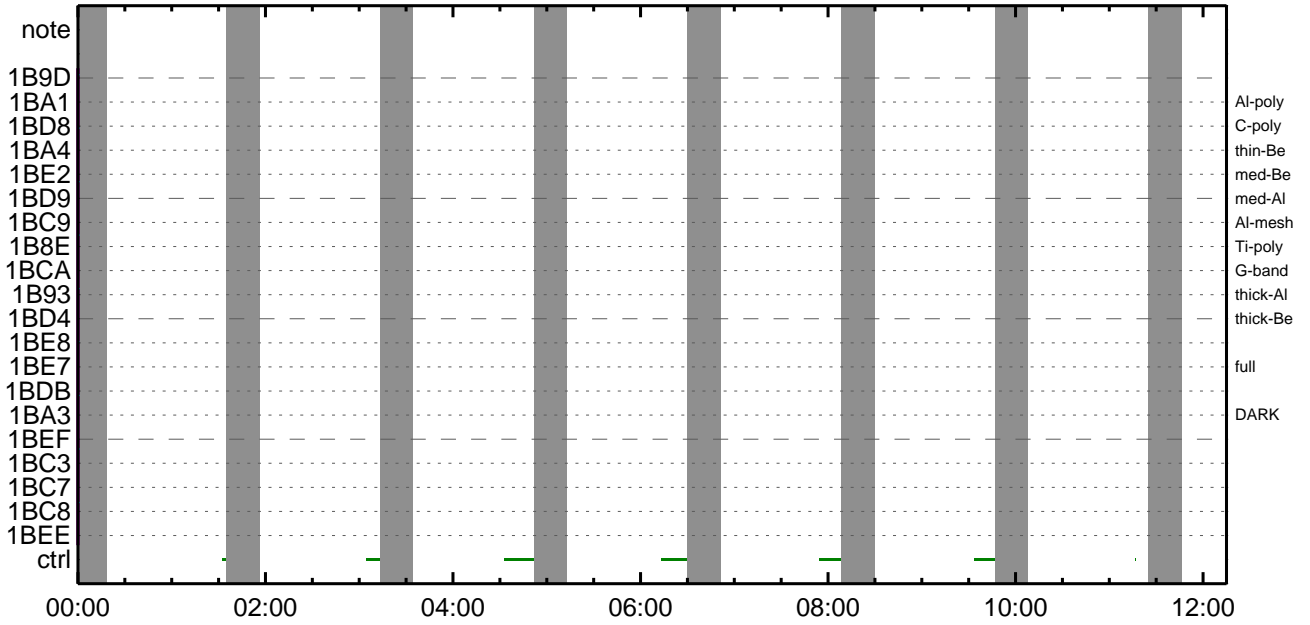
CMDI #0460 2018/04/28



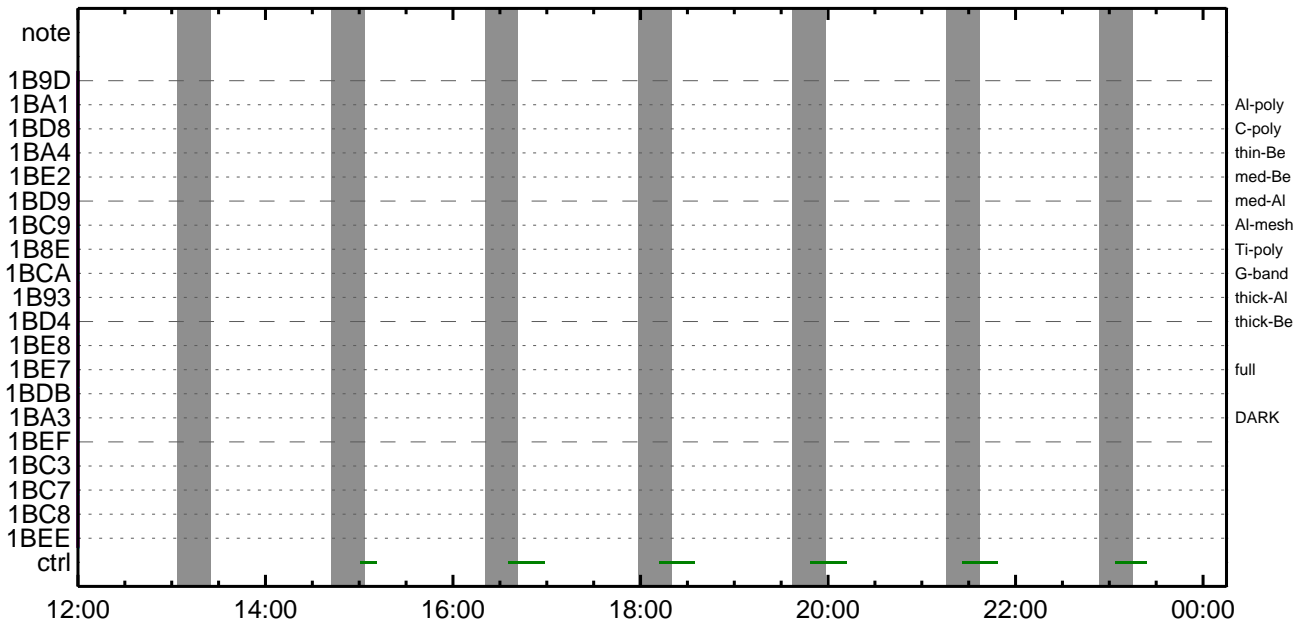
CMDI #0460 2018/04/28



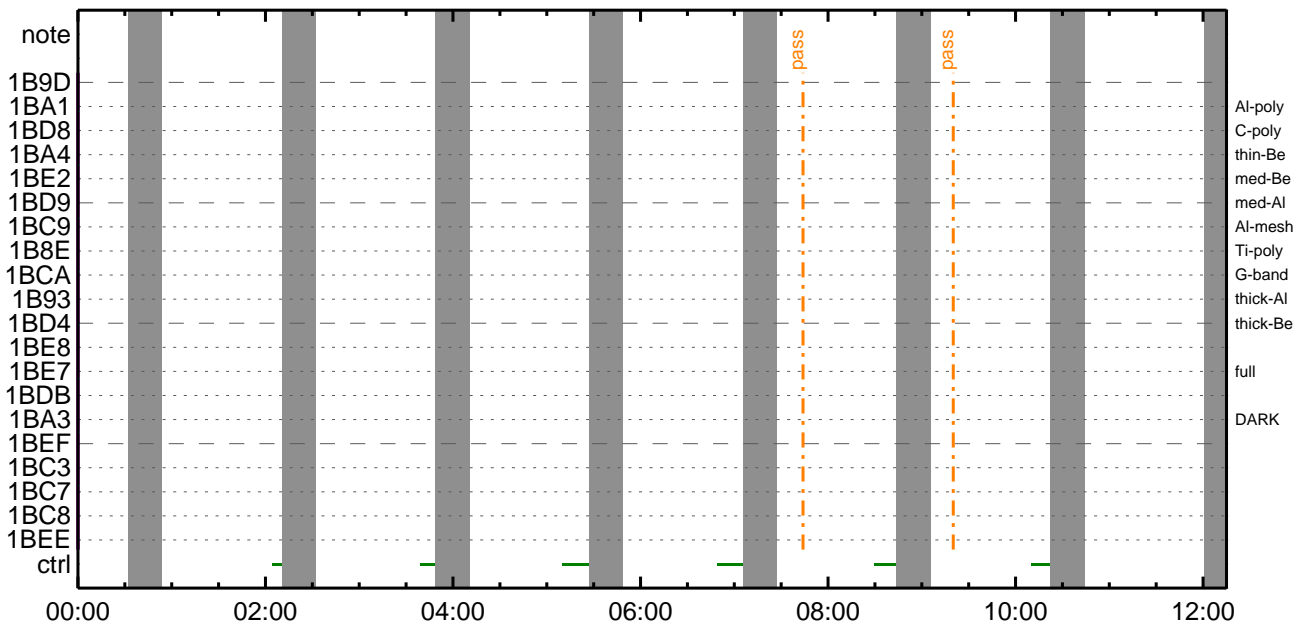
CMDI #0460 2018/04/29



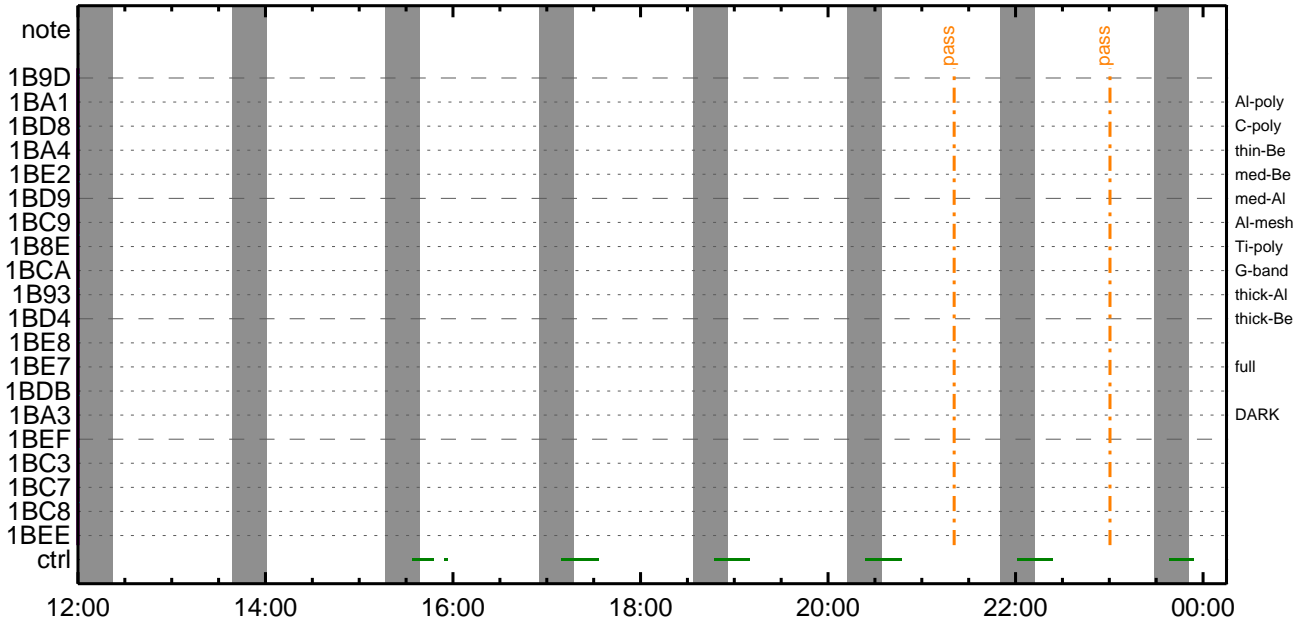
CMDI #0460 2018/04/29



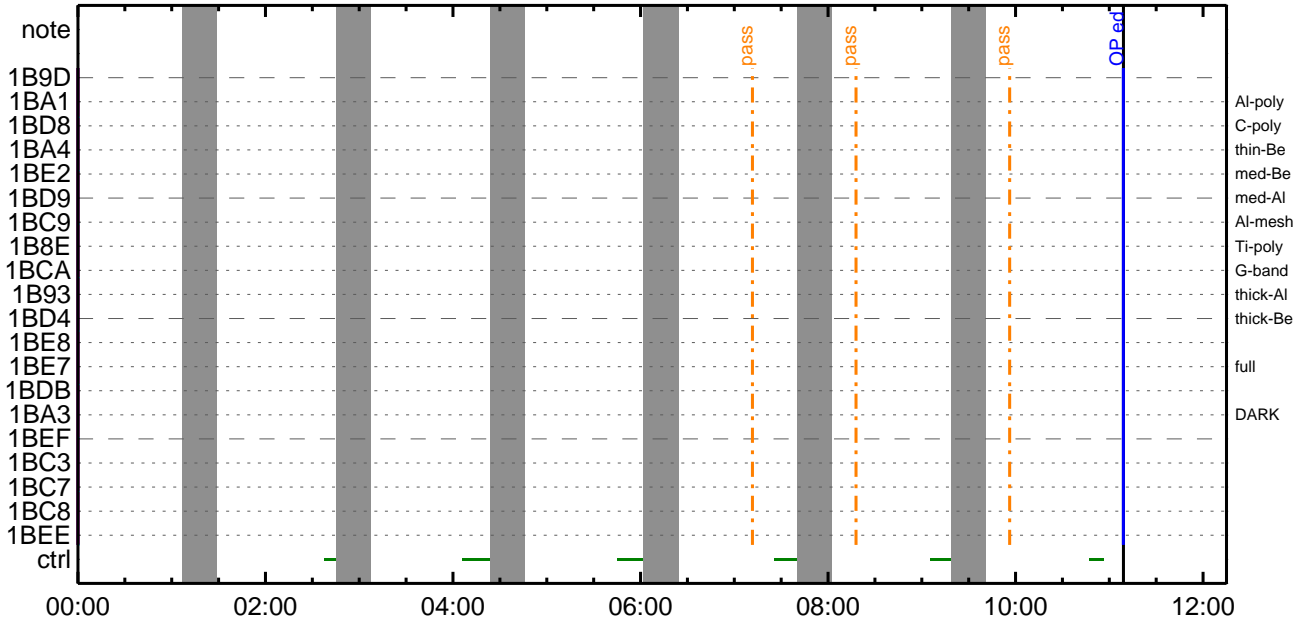
CMDI #0460 2018/04/30



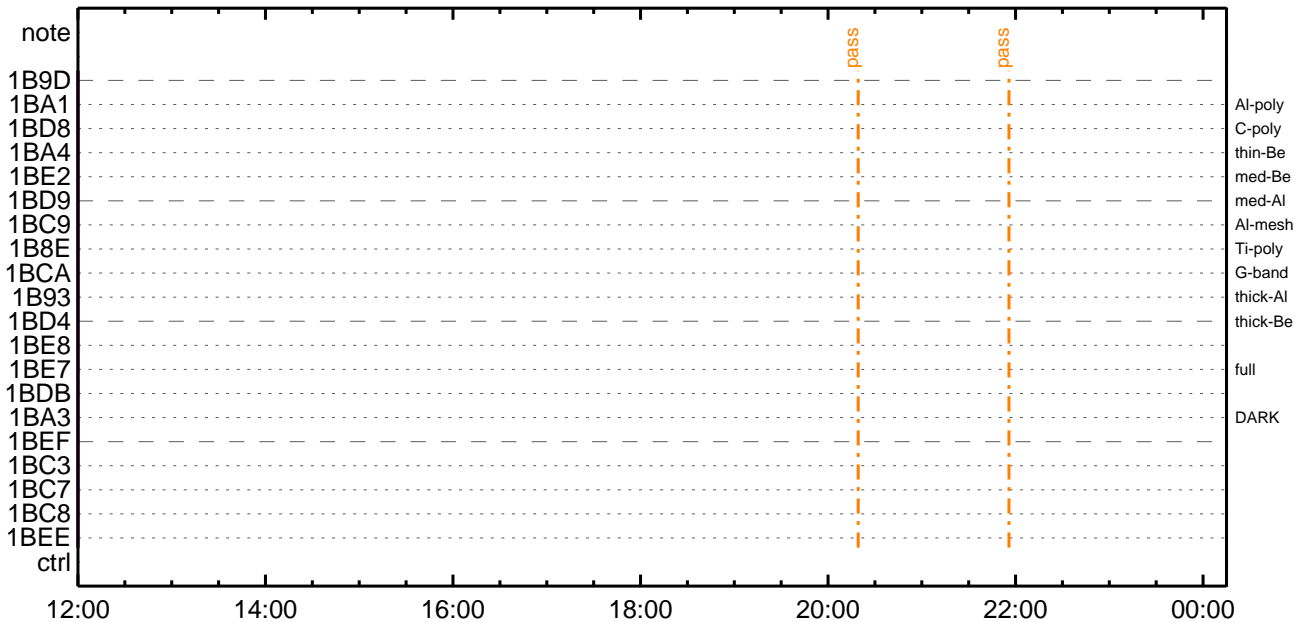
CMDI #0460 2018/04/30



CMDI #0460 2018/05/01



CMDI #0460 2018/05/01







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0096 C.          0x03AB00-0x03AEFF; § 1024byte
0097 C.
0098 C. TI_TBL(0x03AB00-0x03AEFF; § 1024byte)
0099 +. TI 2018-04-26 10:51:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0102 C.
0103 +. TI 2018-04-26 10:51:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0106 C.
0107 +. TI 2018-04-26 10:51:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0110 C.
0111 +. TI 2018-04-26 10:55:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0114 C.
0115 C.          *****
0116 C.          TI_TBL(0x03AB00-0x03AEFF; § 1024byte)
0117 C.          *****
0118 C.          *****
0119 C.          *****
0120 C.
0121 C.          *****
0122 C.          *****
0123 C.          *****
0124 C.
0125 C.          *****
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC          (03 ab 03 01 02)
0128 C.          çç[HK1_DMP_TOP_ADRS_1]      EQ          07
0129 C.          çç[HK1_DMP_TOP_ADRS_0]      EQ          2B
0130 C.          çç[HK1_DMP_BLOCK_NUM]       EQ          3
0131 C.          çç[HK1_DMP_REPEAT_NUM]      EQ          0
0132 C.          çç[HK1_DMA_DMP_PIM]        EQ          DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC          (07 0b f8)
0135 C.          çç[HK1_PKT_FORM_NO]         EQ          7
0136 C.          çç[HK1_PKT_GEN_TIME]        EQ          0.25 s
0137 C.          çç[HK1_S_TLM_BIT_RATE]      EQ          32k
0138 C.          çç[HK1_X_TLM_BIT_RATE]     EQ          4M
0139 C.          çç[HK1_DMP_CHK_FLG]        EQ          EXEC
0140 C.
0141 C.          *****
0142 C.          çç[HK1_DMP_CHK_FLG]        EQ          NON
0143 C.
0144 C.          *****
0145 C.
0146 C.          *****
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC          (02 0a f8)
0149 C.          çç[HK1_PKT_FORM_NO]         EQ          2
0150 C.          çç[HK1_PKT_GEN_TIME]        EQ          0.5S
0151 C.          çç[HK1_S_TLM_BIT_RATE]      EQ          32K
0152 C.          çç[HK1_X_TLM_BIT_RATE]     EQ          4M
0153 C.
0154 C.          *****
0155 C.          *****
0156 C.          *****
0157 C.          *****
0158 +. TI 2018-04-26 10:55:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC          (41)
0161 C.          *****
0162 C.          HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0163 C.          *****
0164 C.          *****
0165 C.
0166 C.          *****
0167 C.          *****
0168 +. TI 2018-04-26 10:55:00.0
0169 DC 07-F0 MDP_XRT_MODE_STBY
0170 BC          (c3)
0171 C.          [ ] [HK1_TI_CMD_NUM]      EQ          1COUNTUP
0172 C.
0173 C.          *****
0174 C.
0175 C.          *****
0176 C.          *****
0177 S. DC-BC dcbc-402:DCBC
0178 (MDP_known_event)
0179 C.
0180 C.
0181 C.          *****
0182 S. DC-BC dcbc-153:DCBC
0183 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0184 C.
0185 C.
0186 C.          *****
0187 C.
0188 C.          *****
0189 C.

```





(a) Spacecraft Operation Procedure (real-commands)

```
main-337 2018-04-26 11:42:19 138 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. ÁíÊ¿¿ãâ•µ°Æ»Í×ÁÇçíYçYÁY×Yí;¼YÉ;ÉÈ¿µ•ííÉ;ÈãÈ¼°ÇÔã•ã¿¼í¹çãÍ;çÀ®, ùã¹ãèãããÇÁ+¿®ã•ãÈããã³ãÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop SP table >
0018 +. DC 07-F0 MDP_SP_CTRL_MANU
0019 BC (61)
0020 C. -----
0021 C. MDP_SP_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload SP Observation Table>
0025 . S. RAM ram-284:MDP_OBS_S
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_S >
0029 +. DC 07-F0 MDP_DUMP_SPTBL
0030 BC (83 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_S verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2018-04-26 10:55:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 +. DC 07-F0 MDP_XRT_CTRL_MANU
0052 BC (c1)
0053 +. DC 07-F0 MDP_XRT_MODE_STBY
0054 BC (c3)
0055 . C. ----- Success Verify ? OK / NG_____
0056 C.
0057 C. XRT Obs. Table Upload
0058 . S. RAM ram-291:MDP_OBS_X
0059 ( )
0060 C.
0061 +. DC 07-F0 MDP_DUMP_XRTTBL
0062 BC (84 07 00 00 00 3a d4)
0063 . C. ----- Comparison Check ? OK / ERR _____
0064 C.
0065 C.
0066 +. DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 01 b1 b1 04 04)
0068 +. DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 02 b1 b1 08 08)
0070 +. DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 03 b1 b1 08 08)
0072 +. DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 04 b1 b1 06 06)
0074 +. DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 05 85 83 06 06)
0076 +. DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 06 85 83 06 06)
0078 +. DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 07 85 83 08 08)
0080 +. DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 08 85 83 04 04)
0082 +. DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 09 85 83 02 02)
0084 +. DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0a 80 80 20 20)
0086 +. DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 0b 80 80 20 08)
0088 +. DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 0c 80 80 08 20)
0090 +. DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 0d 80 80 08 08)
0092 +. DC 07-F0 MDP_XRT_ROI_SET
0093 BC (cd 0f 80 80 06 06)
0094 +. DC 07-F0 MDP_XRT_ROI_SET
0095 BC (cd 10 80 80 08 08)
```

```
0096 + DC 07-F0 MDP_XRT_FLD_ENA
0097 BC (d8)
0098 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0099 BC (c8)
0100 + DC 07-F0 MDP_XRT_ARS_DIS
0101 BC (d5)
0102 + DC 07-F0 MDP_XRT_AEC_RESET
0103 BC (d0)
0104 + DC 07-F0 MDP_XRT_FLD_RESET
0105 BC (da)
0106 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0107 BC (c4 0a)
0108 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0109 BC (c5 0d)
0110 . C. ----- Success Verify ? OK / NG ____
0111 C.
0112 C.
0113 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0114 C.
0115 +. DC 07-F0 MDP_XRT_MODE_OBSV
0116 BC (c2)
0117 +. TI 2018-04-26 10:55:02.0
0118 DC 07-F0 MDP_XRT_MODE_OBSV
0119 BC (c2)
0120 . C. ----- Success Verify ? OK / NG ____
0121 C.
0122 C. ***** XRT END *****
0123 C.
0124 . C. ***** MDP 'ûÃîñî»ö%ÿñÊÂðñ¹ñèDCBC•x²è *****
0125 C. (%â°îÿÓÿÃÿÈÿPÿËÿâÿçÿèñ%¼ññ¼Ã»Ûñ¹ñè)
0126 . S. DC-BC dcbc-402:DCBC
0127 (MDP_known_event)
0128 C.
0129 C.
0130 . C. ***** ÿDÿ¹•î Daily±;îññÊ'Øñ¹ñèDCBC•x²è *****
0131 . S. DC-BC dcbc-153:DCBC
0132 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0133 C.
0134 C.
0135 . C. ;ãLOSÿÃÿSÿËÿ-¼Ã»Û;ã
0136 C.
0137 . C. ***** LOS *****
0138 C.
```

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## XRT\_OGLIST\_0460.chk

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\*\*\* OP Sequence for XRT \*\*\*

2018/04/26	11:05:54.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/26	11:05:56.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/26	11:05:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]				
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2018/04/26	11:06:00.0	AOCS_Ore-point_Start_1_OG [0x097]				
		AOCU_NM	5	02-76	03 00 00 00 00	
2018/04/26	11:06:18.0	XRT_FLD_ENA_411_OG [0x19b]				
		MDP_XRT_FLD_ENA	1	07-F0	d8	
2018/04/26	11:06:20.5	XRT_FLRCTRL_ENA_412_OG [0x19c]				
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2018/04/26	11:06:22.5	XRT_AEC_RESET_448_OG [0x1c0]				
		MDP_XRT_AEC_RESET	1	07-F0	d0	
2018/04/26	11:06:24.5	XRT_ARS_DIS_423_OG [0x1a7]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/04/26	11:06:26.5	XRT_FLD_RESET_433_OG [0x1b1]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2018/04/26	11:08:00.5	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/26	11:08:02.5	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/26	11:08:04.5	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2018/04/26	11:08:06.5	XRT_PREFLR_STRT_406_OG [0x196]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/04/26	11:08:56.5	XRT_QT_PROG_SET_436_OG [0x1b4]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2018/04/26	11:08:58.5	XRT_FL_PROG_SET_440_OG [0x1b8]				
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2018/04/26	11:11:00.0	XRT_Custom_430_OG [0x1ae]				
2018/04/26	11:11:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/04/26	11:12:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/04/26	11:18:00.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/26	11:18:02.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/26	11:18:04.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2018/04/26	11:18:06.0	XRT_PREFLR_STRT_406_OG [0x196]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/04/26	11:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/04/26	11:38:00.0	XRT_Custom_430_OG [0x1ae]				
2018/04/26	11:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/04/26	12:56:30.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/26	12:56:32.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/26	12:56:34.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2018/04/26	12:56:36.0	XRT_PREFLR_STRT_406_OG [0x196]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/04/26	12:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/04/26	13:16:00.0	XRT_Custom_430_OG [0x1ae]				
2018/04/26	13:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/04/26	13:59:54.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/26	13:59:56.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/26	13:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]				
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2018/04/26	14:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]				
		AOCU_NM	5	02-76	01 02 c0 01 ca	
2018/04/26	14:00:18.0	XRT_FLD_ENA_411_OG [0x19b]				
		MDP_XRT_FLD_ENA	1	07-F0	d8	
2018/04/26	14:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]				
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2018/04/26	14:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]				
		MDP_XRT_AEC_RESET	1	07-F0	d0	
2018/04/26	14:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/04/26	14:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2018/04/26	14:02:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05	
2018/04/26	14:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]				
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2018/04/26	14:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/04/26	14:35:00.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/26	14:35:02.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/26	14:35:04.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	

2018/04/26	14:35:06.0	XRT_PREFLR_STRT_406_OG [0x196] MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2018/04/26	14:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2018/04/26	15:03:00.0	XRT_Custom_430_OG [0x1ae]								
2018/04/26	15:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2018/04/26	16:13:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1					
2018/04/26	16:13:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1					
2018/04/26	16:13:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da					
2018/04/26	16:13:36.0	XRT_PREFLR_STRT_406_OG [0x196] MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2018/04/26	16:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2018/04/26	16:51:00.0	XRT_Custom_430_OG [0x1ae]								
2018/04/26	16:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2018/04/26	17:40:24.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1					
2018/04/26	17:40:26.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1					
2018/04/26	17:40:28.0	XRT_FOCUS_RECALIBRATE_416_OG [0x1a0] XRT_FOCUS_RECAL	2	07-F8	78	00				
2018/04/26	17:40:30.0	AOCS_Ore-point_Start_3_OG [0x099] AOCU_NM	5	02-76	00	00	00	00	00	00
2018/04/26	17:44:28.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00		
2018/04/26	17:44:48.0	XRT_FLD_DIS_401_OG [0x191] MDP_XRT_FLD_DIS	1	07-F0	d9					
2018/04/26	17:44:50.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2018/04/26	17:44:52.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5					
2018/04/26	17:44:54.0	XRT_QT_PROG_SET_404_OG [0x194] MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f				
2018/04/26	17:44:56.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2018/04/26	17:50:24.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1					
2018/04/26	17:50:26.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1					
2018/04/26	17:50:28.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00		
2018/04/26	17:50:30.0	AOCS_Ore-point_Start_2_OG [0x098] AOCU_NM	5	02-76	01	02	c0	01	ca	
2018/04/26	17:50:48.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8					
2018/04/26	17:50:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2018/04/26	17:50:52.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0					
2018/04/26	17:50:54.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5					
2018/04/26	17:50:56.0	XRT_FLD_RESET_433_OG [0x1b1] MDP_XRT_FLD_RESET	1	07-F0	da					
2018/04/26	17:51:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1					
2018/04/26	17:51:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1					
2018/04/26	17:51:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da					
2018/04/26	17:51:36.0	XRT_PREFLR_STRT_406_OG [0x196] MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2018/04/26	17:53:26.0	XRT_QT_PROG_SET_449_OG [0x1c1] MDP_XRT_QT_PROG_SET	2	07-F0	c4	05				
2018/04/26	17:53:28.0	XRT_FL_PROG_SET_440_OG [0x1b8] MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d				
2018/04/26	17:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2018/04/26	18:27:30.0	XRT_Custom_430_OG [0x1ae]								
2018/04/26	18:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2018/04/26	19:30:00.5	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1					
2018/04/26	19:30:03.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1					
2018/04/26	19:30:05.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da					
2018/04/26	19:30:07.0	XRT_PREFLR_STRT_406_OG [0x196] MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2018/04/26	19:33:15.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2018/04/26	20:04:00.0	XRT_Custom_430_OG [0x1ae]								
2018/04/26	20:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2018/04/26	21:08:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1					
2018/04/26	21:08:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1					
2018/04/26	21:08:34.0	XRT_FLD_RESET_415_OG [0x19f]								

2018/04/26	21:08:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/04/26	21:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/04/26	21:14:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/04/26	21:14:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/26	21:14:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/26	21:15:00.0	AOCS_Ore-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2018/04/26	21:15:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 00 00 00 00	
2018/04/26	21:15:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2018/04/26	21:15:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2018/04/26	21:15:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2018/04/26	21:15:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/04/26	21:17:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/04/26	21:17:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2018/04/26	21:41:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2018/04/26	21:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2018/04/26	22:46:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/04/26	22:46:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/26	22:46:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/26	22:46:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/04/26	22:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/04/26	22:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/04/26	22:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/26	22:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/26	23:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2018/04/26	23:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 00 00 00 00	
2018/04/26	23:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2018/04/26	23:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2018/04/26	23:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2018/04/26	23:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/04/26	23:02:56.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/04/26	23:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10	
2018/04/26	23:16:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2018/04/26	23:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2018/04/27	00:25:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/04/27	00:25:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/27	00:25:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/27	00:25:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/04/27	00:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/04/27	00:45:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/04/27	00:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2018/04/27	01:57:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/04/27	01:57:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/27	01:57:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/27	01:57:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/04/27	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/04/27	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/27	01:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/27	02:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	



2018/04/27	02:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	AOCU_NM	5	02-76	03	00	00	00	00
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2018/04/27	02:00:18.0	XRT_FLD_ENA_411_OG [0x19b]								
		MDP_XRT_FLD_ENA		1	07-F0	d8				
2018/04/27	02:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]								
		MDP_XRT_FLRCTRL_ENA		1	07-F0	c8				
2018/04/27	02:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]								
		MDP_XRT_AEC_RESET		1	07-F0	d0				
2018/04/27	02:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]								
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2018/04/27	02:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]								
		MDP_XRT_FLD_RESET		1	07-F0	da				
2018/04/27	02:02:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]								
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	11			
2018/04/27	02:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]								
		MDP_XRT_FL_PROG_SET		2	07-F0	c5	0d			
2018/04/27	02:23:00.0	XRT_Custom_430_OG [0x1ae]								
2018/04/27	02:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2018/04/27	03:31:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2018/04/27	03:31:32.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2018/04/27	03:31:34.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET		1	07-F0	da				
2018/04/27	03:31:36.0	XRT_PREFLR_STRT_406_OG [0x196]								
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2018/04/27	03:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2018/04/27	04:01:30.0	XRT_Custom_430_OG [0x1ae]								
2018/04/27	04:02:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2018/04/27	05:01:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2018/04/27	05:01:32.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2018/04/27	05:01:34.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET		1	07-F0	da				
2018/04/27	05:01:36.0	XRT_PREFLR_STRT_406_OG [0x196]								
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2018/04/27	05:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2018/04/27	05:40:00.0	XRT_Custom_430_OG [0x1ae]								
2018/04/27	05:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2018/04/27	05:57:54.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2018/04/27	05:57:56.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2018/04/27	05:57:58.0	XRT_FOCUS_RECALIBRATE_416_OG [0x1a0]								
		XRT_FOCUS_RECAL		2	07-F8	78	00			
2018/04/27	05:58:00.0	AOCS_Ore-point_Start_3_OG [0x099]								
		AOCU_NM		5	02-76	00	00	00	00	00
2018/04/27	06:01:58.0	XRT_FOCUS_POSITION_403_OG [0x193]								
		XRT_FOCUS_POSITION		4	07-F8	22	ff	aa	00	
2018/04/27	06:02:18.0	XRT_FLD_DIS_401_OG [0x191]								
		MDP_XRT_FLD_DIS		1	07-F0	d9				
2018/04/27	06:02:20.0	XRT_FLRCTRL_DIS_405_OG [0x195]								
		MDP_XRT_FLRCTRL_DIS		1	07-F0	c9				
2018/04/27	06:02:22.0	XRT_ARS_DIS_423_OG [0x1a7]								
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2018/04/27	06:02:24.0	XRT_QT_PROG_SET_404_OG [0x194]								
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	0f			
2018/04/27	06:02:26.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2018/04/27	06:07:54.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2018/04/27	06:07:56.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2018/04/27	06:07:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]								
		XRT_FOCUS_POSITION		4	07-F8	22	fe	97	00	
2018/04/27	06:08:00.0	AOCS_Ore-point_Start_1_OG [0x097]								
		AOCU_NM		5	02-76	03	00	00	00	00
2018/04/27	06:08:18.0	XRT_FLD_ENA_411_OG [0x19b]								
		MDP_XRT_FLD_ENA		1	07-F0	d8				
2018/04/27	06:08:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]								
		MDP_XRT_FLRCTRL_ENA		1	07-F0	c8				
2018/04/27	06:08:22.0	XRT_AEC_RESET_448_OG [0x1c0]								
		MDP_XRT_AEC_RESET		1	07-F0	d0				
2018/04/27	06:08:24.0	XRT_ARS_DIS_423_OG [0x1a7]								
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2018/04/27	06:08:26.0	XRT_FLD_RESET_433_OG [0x1b1]								
		MDP_XRT_FLD_RESET		1	07-F0	da				
2018/04/27	06:10:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]								
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	11			
2018/04/27	06:10:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]								
		MDP_XRT_FL_PROG_SET		2	07-F0	c5	0d			
2018/04/27	06:11:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2018/04/27	06:41:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2018/04/27	06:41:32.0	XRT_CTRL_MANU_402_OG [0x192]								

2018/04/27	06:41:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/27	06:41:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/04/27	06:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/04/27	07:18:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/04/27	07:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/04/27	08:22:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/27	08:22:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/27	08:22:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/27	08:22:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/04/27	08:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/04/27	08:57:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/04/27	08:58:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/04/27	10:01:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/27	10:01:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/27	10:01:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/27	10:01:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/04/27	10:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/04/27	10:35:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/04/27	10:36:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/04/27	11:53:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/27	11:53:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/27	11:53:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/27	11:53:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/04/27	11:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/04/27	12:13:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/04/27	12:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/04/27	13:31:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/27	13:31:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/27	13:31:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/27	13:31:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/04/27	13:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/04/27	13:52:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/04/27	13:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/04/27	15:10:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/27	15:10:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/27	15:10:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/27	15:10:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/04/27	15:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/04/27	15:39:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/04/27	15:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/04/27	15:48:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/27	15:48:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/27	15:48:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/27	15:48:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/04/27	15:49:00.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/04/27	15:50:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/04/27	15:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/04/27	15:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/04/27	16:48:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/27	16:48:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/27	16:48:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1

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2018/04/27	16:48:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/04/27	16:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/04/27	17:25:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/04/27	17:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/04/27	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/27	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/27	17:59:58.0	XRT_FOCUS_RECALIBRATE_416_OG [0x1a0]	XRT_FOCUS_RECAL	2	07-F8	78 00
2018/04/27	18:00:00.0	AOCS_Orе-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00
2018/04/27	18:03:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2018/04/27	18:04:18.0	XRT_FLD_DIS_401_OG [0x191]	MDP_XRT_FLD_DIS	1	07-F0	d9
2018/04/27	18:04:20.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2018/04/27	18:04:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2018/04/27	18:04:24.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2018/04/27	18:04:26.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/04/27	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/27	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/27	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2018/04/27	18:10:00.0	AOCS_Orе-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 00 00 00 00
2018/04/27	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2018/04/27	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2018/04/27	18:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2018/04/27	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2018/04/27	18:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/04/27	18:12:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2018/04/27	18:12:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2018/04/27	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/04/27	18:26:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/27	18:26:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/27	18:26:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/04/27	18:26:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/04/27	18:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/04/27	19:02:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/04/27	19:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/04/27	20:05:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/27	20:05:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/27	20:05:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/04/27	20:05:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/04/27	20:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/04/27	20:39:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/04/27	20:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/04/27	21:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/27	21:43:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/27	21:43:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/04/27	21:43:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/04/27	21:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/04/27	22:16:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/04/27	22:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/04/27	22:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1

2018/04/27	22:59:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/27	22:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2018/04/27	23:00:00.0	AOCS_ORe-point_Start_3_OG [0x099] AOCU_NM	5	02-76	00 00 00 00 00
2018/04/27	23:00:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2018/04/27	23:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2018/04/27	23:00:22.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0
2018/04/27	23:00:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2018/04/27	23:00:26.0	XRT_FLD_RESET_433_OG [0x1b1] MDP_XRT_FLD_RESET	1	07-F0	da
2018/04/27	23:02:56.0	XRT_QT_PROG_SET_446_OG [0x1be] MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2018/04/27	23:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8] MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2018/04/27	23:03:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/04/27	23:21:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/27	23:21:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/27	23:21:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2018/04/27	23:21:36.0	XRT_PREFLR_STRT_406_OG [0x196] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/04/27	23:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/04/27	23:48:00.0	XRT_Custom_430_OG [0x1ae]			
2018/04/27	23:49:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/04/28	01:00:00.5	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/28	01:00:02.5	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/28	01:00:04.5	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2018/04/28	01:00:06.5	XRT_PREFLR_STRT_406_OG [0x196] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/04/28	01:03:14.5	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/04/28	01:20:30.0	XRT_Custom_430_OG [0x1ae]			
2018/04/28	01:21:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/04/28	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/28	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/28	01:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2018/04/28	02:00:00.0	AOCS_ORe-point_Start_1_OG [0x097] AOCU_NM	5	02-76	03 00 00 00 00
2018/04/28	02:00:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2018/04/28	02:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2018/04/28	02:00:22.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0
2018/04/28	02:00:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2018/04/28	02:00:26.0	XRT_FLD_RESET_433_OG [0x1b1] MDP_XRT_FLD_RESET	1	07-F0	da
2018/04/28	02:02:56.0	XRT_QT_PROG_SET_421_OG [0x1a5] MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2018/04/28	02:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8] MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2018/04/28	02:03:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/04/28	02:30:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/28	02:30:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/28	02:30:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2018/04/28	02:30:06.0	XRT_PREFLR_STRT_406_OG [0x196] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/04/28	02:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/04/28	02:59:00.0	XRT_Custom_430_OG [0x1ae]			
2018/04/28	03:00:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/04/28	04:00:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/28	04:00:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/28	04:00:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2018/04/28	04:00:36.0	XRT_PREFLR_STRT_406_OG [0x196] MDP_XRT_PREFLR_STRT	1	07-F0	e8

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2018/04/28	04:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/04/28	04:37:30.0	XRT_Custom_430_OG [0x1ae]							
2018/04/28	04:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/04/28	05:37:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/28	05:37:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/28	05:37:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/04/28	05:37:36.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/04/28	05:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/04/28	06:15:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/28	06:15:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/28	06:15:28.0	XRT_FOCUS_RECALIBRATE_416_OG [0x1a0]							
		XRT_FOCUS_RECAL	2	07-F8	78 00				
2018/04/28	06:15:30.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2018/04/28	06:19:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2018/04/28	06:19:48.0	XRT_FLD_DIS_401_OG [0x191]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2018/04/28	06:19:50.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2018/04/28	06:19:52.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/04/28	06:19:54.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f				
2018/04/28	06:19:56.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/04/28	06:25:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/28	06:25:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/28	06:25:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2018/04/28	06:25:30.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2018/04/28	06:25:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2018/04/28	06:25:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2018/04/28	06:25:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2018/04/28	06:25:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/04/28	06:25:56.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/04/28	06:28:26.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2018/04/28	06:28:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2018/04/28	06:28:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/04/28	07:17:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/28	07:17:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/28	07:17:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/04/28	07:17:36.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/04/28	07:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/04/28	07:54:00.0	XRT_Custom_430_OG [0x1ae]							
2018/04/28	07:55:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/04/28	08:57:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/28	08:57:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/28	08:57:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/04/28	08:57:36.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/04/28	09:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/04/28	09:32:30.0	XRT_Custom_430_OG [0x1ae]							
2018/04/28	09:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/04/28	10:38:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/28	10:38:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/28	10:38:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/04/28	10:38:36.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				

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2018/04/28	10:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/04/28	10:50:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/28	10:50:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/28	10:50:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2018/04/28	10:50:06.0	XRT_PREFLR_STRT_406_OG [0x196]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/04/28	10:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/04/28	11:10:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/28	11:10:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/28	11:11:00.5	AOCS_OrE-point_Start_3_OG [0x099]					
		AOCU_NM	5	02-76	00 00 00 00 00		