

# XRT Timeline to be uploaded on 2018/04/28

Period: 2018/04/28 11:01:00 - 2018/05/03 11:13:00

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Normal mode

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## XOB #1BF1: HOP349 - 3-filter Synoptics (Al-mesh[512/2048/4096], Al-poly[512/4096/8192], thin-Be[3897/16384/32768] with 512x512 G-band+Leak - 45 min c

Term	Pointing (x, y)	Comment
04/28 11:14:00 - 04/28 14:06:30	Fixed ( 0.0, 0.0)	# OP start + 10min HOP349 disk center
04/29 11:03:00 - 04/29 13:59:54	Fixed ( 0.0, 0.0)	# HOP349 disk center
04/30 10:44:30 - 04/30 13:09:54	Fixed ( 0.0, 0.0)	# HOP349 disk center
<b>PROG= 02 Inf.-time(s)</b>		
└─ Subr= 1 1-time(s) 300.0sec		
└─ Seqn= 12 1-time(s) 2.0sec		
└─ Open/Al-mesh Open/Al-mesh close	Safe Norm 500ms	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Al-mesh Open/Al-mesh close	Safe Norm 2.00s	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Al-mesh Open/Al-mesh close	Safe Norm 4.00s	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 82 1-time(s) 2.0sec		
└─ Al-poly/Open Al-poly/Open close	Safe Norm 500ms	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Al-poly/Open Al-poly/thick-Al close	Safe Norm 4.00s	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Al-poly/Open Al-poly/Open close	Safe Norm 8.00s	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 52 1-time(s) 2.0sec		
└─ thin-Be/Open thin-Be/Open close	Safe Norm 2.83s	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ thin-Be/Open thin-Be/Open close	Safe Norm 16.0s	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ thin-Be/Open thin-Be/Open close	Safe Norm 32.0s	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 30 1-time(s) 2.0sec		
└─ Open/G-band Open/G-band open	Safe Norm 1ms	Obs 1x1 512x512 (1024, 1024) Q=90 0 0 2.0sec
└─ Open/G-band Open/G-band close	Safe Norm 1ms	Obs 1x1 512x512 (1024, 1024) Q=95 0 0 2.0sec
└─ Subr= 2 30-time(s) 90.0sec		
└─ Seqn= 8 1-time(s) 2.0sec		
└─ thin-Be/Open med-Be/Open close	Safe Norm 1.00s	Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
└─ thin-Be/Open med-Be/Open close	Safe Norm 1.41s	Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
└─ Seqn= 6 1-time(s) 2.0sec		
└─ Al-poly/Open Al-poly/Open close	Safe Norm 125ms	Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
└─ Al-poly/Open Al-poly/Open close	Safe Norm 1.00s	Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
└─ Seqn= 29 1-time(s) 2.0sec		
└─ Open/Al-mesh Open/Al-mesh close	Safe Norm 125ms	Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
└─ Open/Al-mesh Open/Al-mesh close	Safe Norm 250ms	Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

## XOB #1BA4: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, Al/Poly context, with G-band (1ms/1ms

Term	Pointing (x, y)	Comment
04/28 14:33:00 - 04/28 17:23:30	Track ( 651.6, 73.5) @ 04/28 14:11:00	# AR12706
04/28 18:13:30 - 04/29 05:45:54	Track ( 678.1, 71.3) @ 04/28 18:10:30	# AR12706
04/29 05:59:00 - 04/29 10:59:54	Track ( 749.9, 64.5) @ 04/29 05:56:00	# AR12706
04/29 20:13:00 - 04/30 05:09:30	Track ( 820.8, 56.0) @ 04/29 19:35:00	# AR12706
04/30 13:13:00 - 04/30 18:04:24	Track ( 890.7, 44.6) @ 04/30 13:10:00	# AR12706
<b>PROG= 17 Inf.-time(s)</b>		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 92 1-time(s) 2.0sec		
└─ Open/G-band Open/G-band open	Safe Norm 1ms	Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
└─ Open/G-band Open/G-band close	Safe Norm 1ms	Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
└─ Open/Ti-poly Open/thick-Al close	Safe Dark 16.0s	Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
└─ Seqn= 42 1-time(s) 2.0sec		
└─ Al-poly/Open thin-Be/Open close	Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ thin-Be/Open med-Be/Open close	Safe Norm 5.66s	Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
└─ Open/thick-Al Open/thick-Al close	Safe Norm 16.0s	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ Seqn= 96 40-time(s) 2.0sec		
└─ Al-poly/Open thin-Be/Open close	Safe Norm 250ms	Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec
└─ thin-Be/Open med-Be/Open close	Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec
└─ Al-poly/Open thin-Be/Open close	Safe Norm 250ms	Obs 1x1 384x384 (1064, 1048) Q=95 1 1 2.0sec
└─ thin-Be/Open med-Be/Open close	Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 1 1 2.0sec
└─ Al-poly/Open thin-Be/Open close	Safe Norm 250ms	Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec
└─ thin-Be/Open med-Be/Open close	Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

## XOB #1BD9: Synoptic Q95 2x2 - Al/mesh(64/512/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(45/512/4096) + T

Term	Pointing (x, y)	Comment
04/28 18:03:30 - 04/28 18:10:24	Fixed ( 0.0, 0.0)	synoptic, shifted 0.5 min
04/29 05:49:00 - 04/29 05:55:54	Fixed ( 0.0, 0.0)	synoptic, shifted -14.0 min
04/29 19:28:00 - 04/29 19:34:54	Fixed ( 0.0, 0.0)	synoptic, shifted 1 orbit
04/30 18:07:30 - 04/30 18:14:24	Fixed ( 0.0, 0.0)	synoptic, shifted 4.5 min
05/01 06:27:00 - 05/01 06:33:54	Fixed ( 0.0, 0.0)	synoptic, shifted 24.0 min
<b>PROG= 15 1-time(s)</b>		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 5 1-time(s) 2.0sec		
└─ Open/Ti-poly Open/thick-Al close	Safe Dark 500ms	Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Open/Ti-poly Open/thick-Al close	Safe Dark 500ms	Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec

	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
	<b>Seqn= 36</b>		<b>1-time(s)</b>			<b>2.0sec</b>							
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	<b>Seqn= 99</b>		<b>1-time(s)</b>			<b>2.0sec</b>							
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	<b>Seqn= 33</b>		<b>1-time(s)</b>			<b>2.0sec</b>							
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	<b>Seqn= 23</b>		<b>1-time(s)</b>			<b>2.0sec</b>							
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1BF2: Al/poly (Thin-Be) - 384x384 - 1x1 - 8sec cad - Gband - 348x384 - 1ms - AEC1**

Term	Pointing (x, y)		Comment										
04/29 14:03:00 - 04/29 19:24:54	Track ( 775.4, 66.0) @ 04/29 14:00:00		# HOP326 with ALMA										
<b>PROG= 14 Inf.-time(s)</b>													
	<b>Subr= 1</b>		<b>2-time(s)</b>			<b>2.0sec</b>							
	<b>Seqn= 92</b>		<b>1-time(s)</b>			<b>2.0sec</b>							
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	<b>Subr= 2</b>		<b>200-time(s)</b>			<b>8.0sec</b>							
	<b>Seqn= 18</b>		<b>1-time(s)</b>			<b>2.0sec</b>							
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
	<b>Subr= 3</b>		<b>200-time(s)</b>			<b>8.0sec</b>							
	<b>Seqn= 18</b>		<b>1-time(s)</b>			<b>2.0sec</b>							
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1BD8: Synoptic 7 Filter w/ Al-mesh(64/512/2897), Al-poly(45/512/4096), Thin-Be(1024/11571/23142) - Thick-Be(65536), Al-poly+Ti-poly(512/8192), Med**

Term	Pointing (x, y)		Comment										
04/30 06:03:00 - 04/30 06:09:54	Fixed ( 0.0, 0.0)		synoptic										
<b>PROG= 18 1-time(s)</b>													
	<b>Subr= 1</b>		<b>1-time(s)</b>			<b>2.0sec</b>							
	<b>Seqn= 5</b>		<b>1-time(s)</b>			<b>2.0sec</b>							
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
	<b>Seqn= 36</b>		<b>1-time(s)</b>			<b>2.0sec</b>							
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	<b>Seqn= 99</b>		<b>1-time(s)</b>			<b>2.0sec</b>							
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	<b>Seqn= 33</b>		<b>1-time(s)</b>			<b>2.0sec</b>							
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	<b>Seqn= 23</b>		<b>1-time(s)</b>			<b>4.0sec</b>							
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	<b>Subr= 2</b>		<b>1-time(s)</b>			<b>2.0sec</b>							
	<b>Seqn= 46</b>		<b>1-time(s)</b>			<b>2.0sec</b>							
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	<b>Seqn= 17</b>		<b>1-time(s)</b>			<b>2.0sec</b>							
	med-Al/Open	med-Al/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	med-Al/Open	med-Al/Open	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	<b>Seqn= 25</b>		<b>1-time(s)</b>			<b>2.0sec</b>							
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1AEC: G-Band Alignment with North Pole Q90 2x2 (G-band and VLS=CLS) - 1msec (Al/poly) - 4096msec - 5min cadence - Partial Sun-wNGT**

Term	Pointing (x, y)		Comment										
04/30 06:25:00 - 04/30 08:09:54	Fixed ( 0.0, 930.0)		# Co-alignment N-limb										

<b>PROG= 20 1-time(s)</b>													
└─ <b>Subr= 1 24-time(s) 300.0sec</b>													
└─ <b>Seqn= 98 1-time(s) 2.0sec</b>													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec
└─ <b>Seqn= 63 1-time(s) 2.0sec</b>													
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec
└─ <b>Seqn= 45 1-time(s) 2.0sec</b>													
	Al-poly/Open	med-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x1536 (1024, 768)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1AED: G-Band Alignment with East limb Q90 2x2 (G-band and VLS=CLS) - 1msec - (Al/poly) 1443msec - 8 min cadence-wNGT**

Term	Pointing (x, y)		Comment										
04/30 08:25:00 - 04/30 10:09:54	Fixed ( -970.0, 0.0)		# Co-alignment E-limb										
<b>PROG= 03 1-time(s)</b>													
└─ <b>Subr= 1 15-time(s) 480.0sec</b>													
└─ <b>Seqn= 19 1-time(s) 2.0sec</b>													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	2x2	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec
└─ <b>Seqn= 43 1-time(s) 2.0sec</b>													
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec
└─ <b>Seqn= 70 1-time(s) 2.0sec</b>													
	Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	1536x2048 (1280, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1BA1: HOP336 1-filter - Al/poly -384x384, 512ms and 4s, 60s-cadence, G-band - 384x384 1ms**

Term	Pointing (x, y)		Comment										
04/30 18:17:30 - 04/30 21:14:24	Fixed ( -20.0, 669.0)		# HOP336 #1										
04/30 21:17:30 - 05/01 00:14:24	Fixed ( -20.0, -629.0)		# HOP336 #2										
<b>PROG= 19 Inf.-time(s)</b>													
└─ <b>Subr= 1 1-time(s) 2.0sec</b>													
└─ <b>Seqn= 16 2-time(s) 2.0sec</b>													
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└─ <b>Subr= 2 1-time(s) 2.0sec</b>													
└─ <b>Seqn= 90 1-time(s) 30.0sec</b>													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
└─ <b>Subr= 3 15-time(s) 2.0sec</b>													
└─ <b>Seqn= 68 1-time(s) 60.0sec</b>													
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1BA3: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, Al/Poly context, with G-band (1ms/1ms)**

Term	Pointing (x, y)		Comment										
05/01 00:17:30 - 05/01 05:45:30	Track ( 921.5, 37.1) @ 05/01 00:14:30		# AR12706										
05/01 06:37:00 - 05/01 10:46:30	Track ( 934.3, 32.9) @ 05/01 06:34:00		# AR12706										
<b>PROG= 06 Inf.-time(s)</b>													
└─ <b>Subr= 1 1-time(s) 2.0sec</b>													
└─ <b>Seqn= 92 1-time(s) 2.0sec</b>													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ <b>Seqn= 42 3-time(s) 2.0sec</b>													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ <b>Seqn= 32 40-time(s) 90.0sec</b>													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	34.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	34.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

\* \* \* \* \* **Flare mode** \* \* \* \* \*

**XOB #1B8E: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512)**

Term	Pointing (x, y)		Comment									
04/28 11:14:00 - 04/28 14:06:30	Fixed ( 0.0, 0.0)		# OP start + 10min HOP349 disk center									
04/28 14:33:00 - 04/28 17:23:30	Track ( 651.6, 73.5) @ 04/28 14:11:00		# AR12706									
04/28 18:13:30 - 04/29 05:45:54	Track ( 678.1, 71.3) @ 04/28 18:10:30		# AR12706									
04/29 05:59:00 - 04/29 10:59:54	Track ( 749.9, 64.5) @ 04/29 05:56:00		# AR12706									
04/29 11:03:00 - 04/29 13:59:54	Fixed ( 0.0, 0.0)		# HOP349 disk center									
04/29 14:03:00 - 04/29 19:24:54	Track ( 775.4, 66.0) @ 04/29 14:00:00		# HOP326 with ALMA									
04/29 20:13:00 - 04/30 05:09:30	Track ( 820.8, 56.0) @ 04/29 19:35:00		# AR12706									
04/30 10:44:30 - 04/30 13:09:54	Fixed ( 0.0, 0.0)		# HOP349 disk center									
04/30 13:13:00 - 04/30 18:04:24	Track ( 890.7, 44.6) @ 04/30 13:10:00		# AR12706									

04/30 18:17:30 - 04/30 21:14:24 Fixed ( -20.0, 669.0) # HOP336 #1  
 04/30 21:17:30 - 05/01 00:14:24 Fixed ( -20.0, -629.0) # HOP336 #2  
 05/01 00:17:30 - 05/01 05:45:30 Track ( 921.5, 37.1) @ 05/01 00:14:30 # AR12706  
 05/01 06:37:00 - 05/01 10:46:30 Track ( 934.3, 32.9) @ 05/01 06:34:00 # AR12706

**PROG= 13 30-time(s)**

Subr= 1		20-time(s)		2.0sec													
Seqn= 11		1-time(s)		2.0sec													
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec					
Seqn=100		1-time(s)		10.0sec													
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec					
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec					
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec					
Subr= 2		1-time(s)		2.0sec													
Seqn= 10		1-time(s)		2.0sec													
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec					
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec					
Seqn= 11		1-time(s)		2.0sec													
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec					
Seqn= 87		1-time(s)		2.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec					
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec					
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec					
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec					
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval						

\* \* \* \* \*

**Active Region Search**

\* \* \* \* \*

NOT USED

\* \* \* \* \*

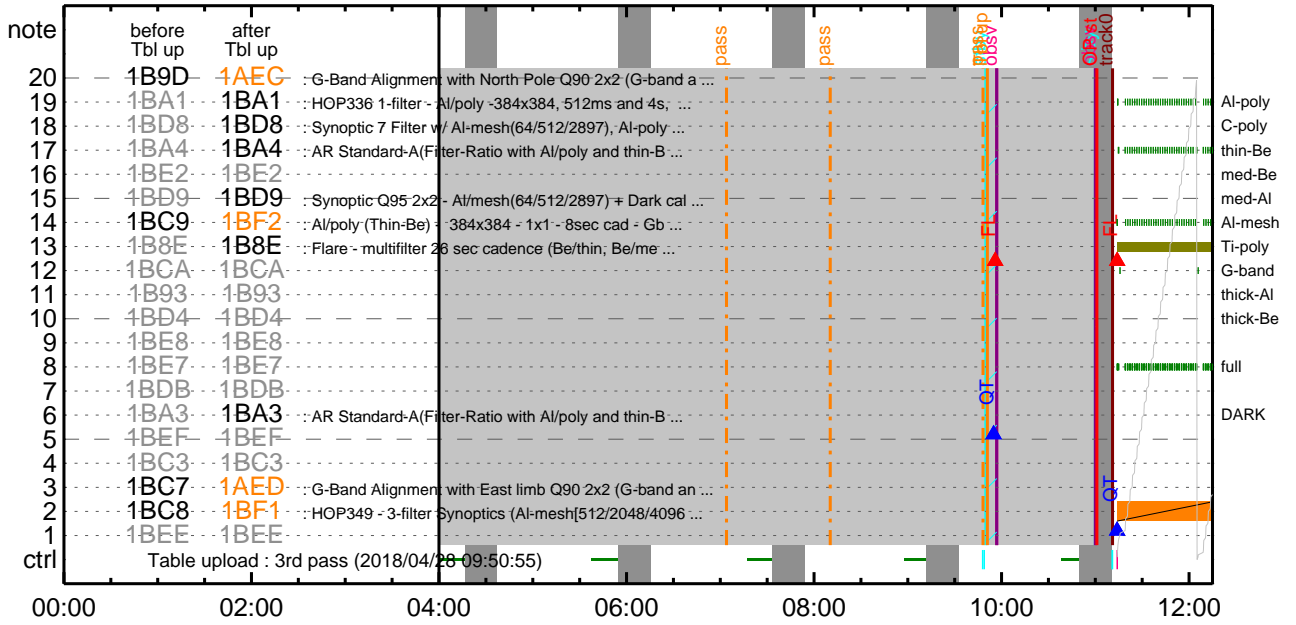
**Flare Detection**

\* \* \* \* \*

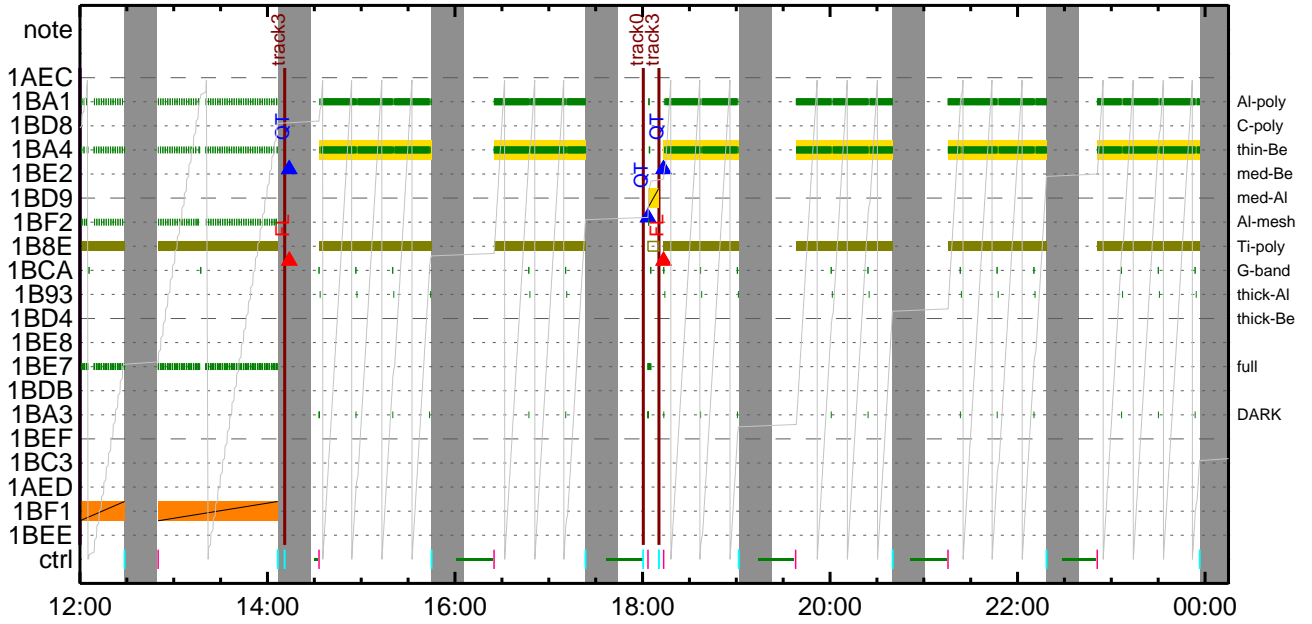
**FLD Patrol**

Term	Pointing (x, y)		Comment											
04/28 18:10:48 - 04/29 05:46:18	Track ( 678.1, 71.3)	@ 04/28 18:10:30	# AR12706											
04/29 05:56:18 - 04/29 19:25:18	Track ( 749.9, 64.5)	@ 04/29 05:56:00	# AR12706											
04/29 19:35:18 - 04/30 06:00:18	Track ( 820.8, 56.0)	@ 04/29 19:35:00	# AR12706											
04/30 10:10:18 - 04/30 18:04:48	Fixed ( 0.0, 0.0)		# HOP349 disk center											
04/30 18:14:48 - 05/01 06:24:18	Fixed ( -20.0, 669.0)		# HOP336 #1											
05/01 06:34:18 - 05/03 11:13:00	Track ( 934.3, 32.9)	@ 05/01 06:34:00	# AR12706											
Al-poly/Open	Al-poly/Open	close	Safe	Norm	8ms	Obs	8x8	Q=50	80sec					
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval			

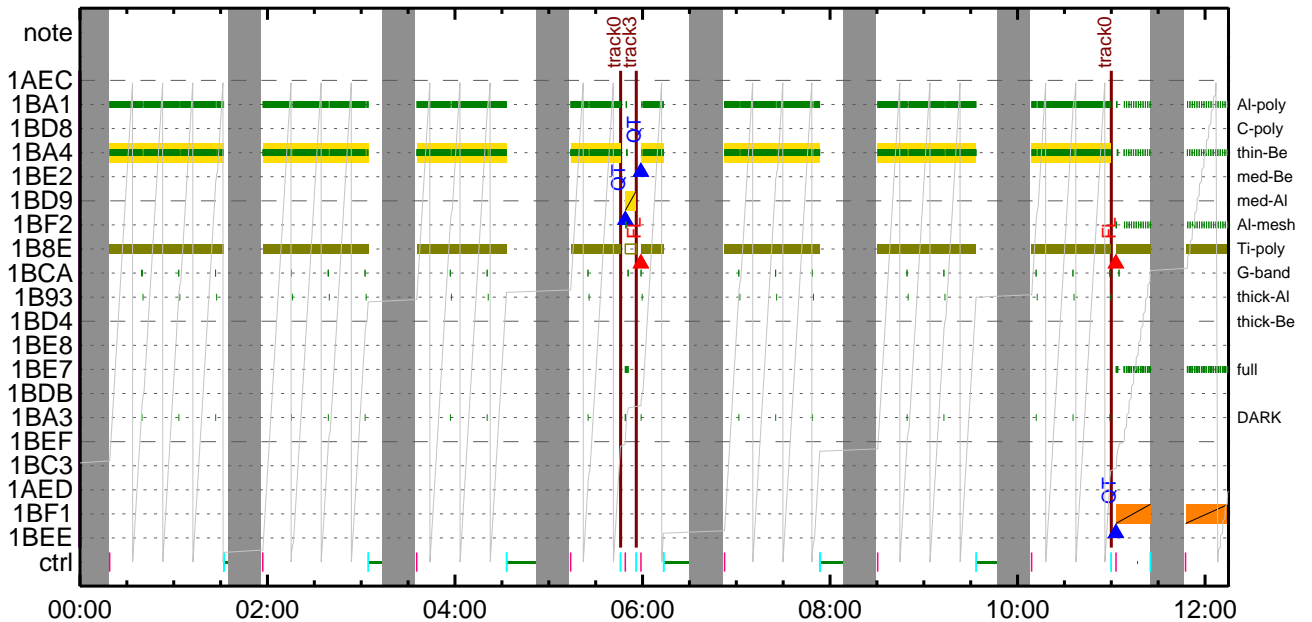
### CMDI #0464 2018/04/28



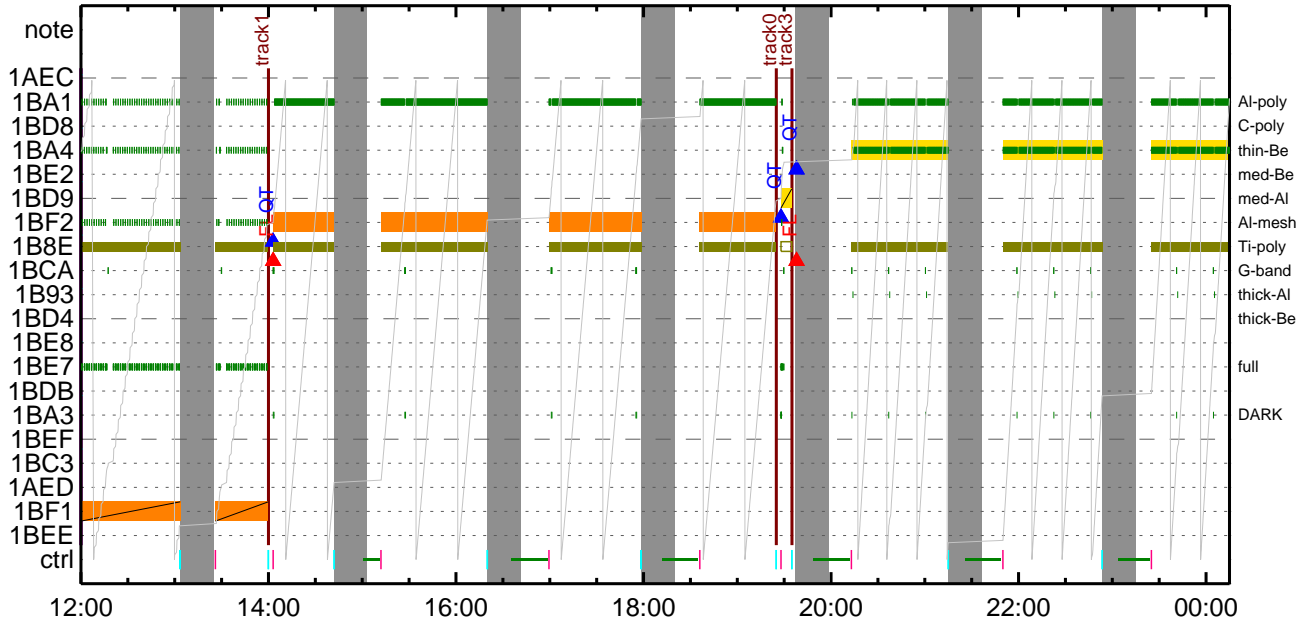
### CMDI #0464 2018/04/28



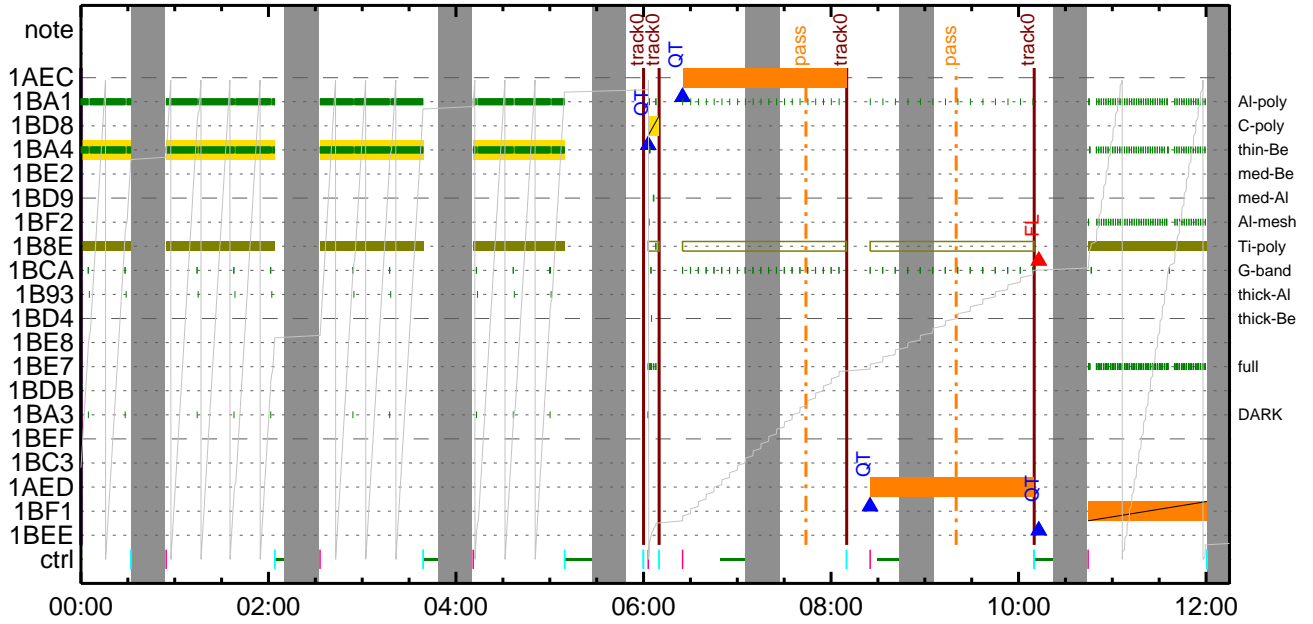
### CMDI #0464 2018/04/29



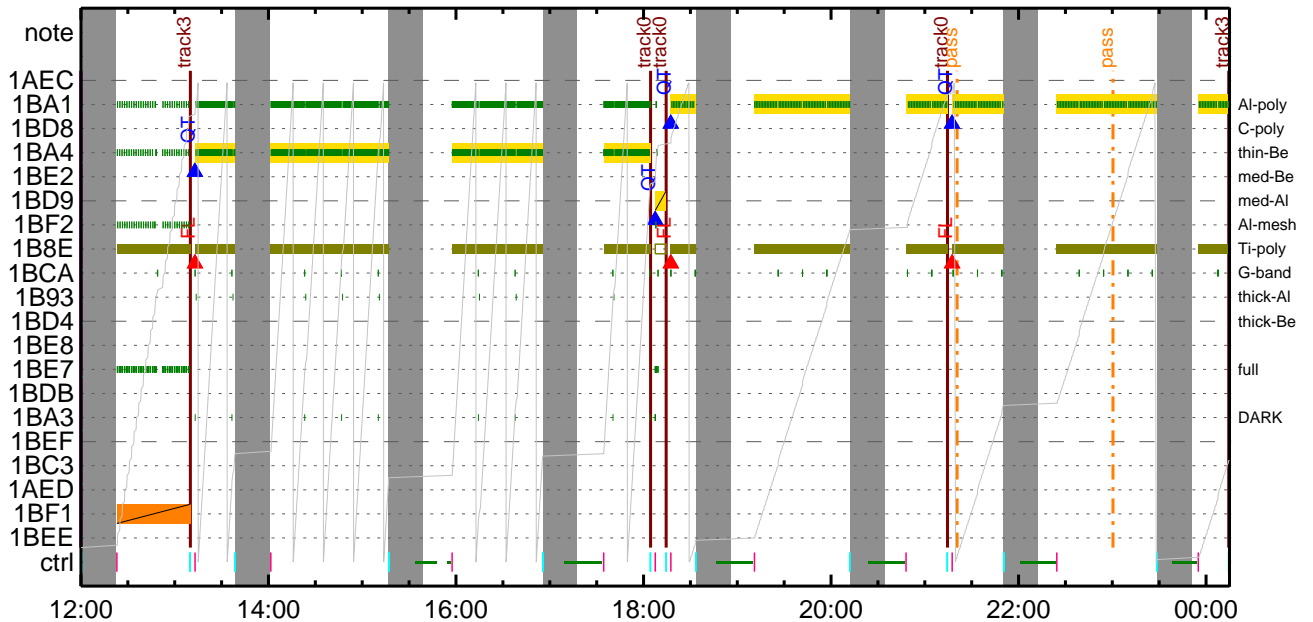
CMDI #0464 2018/04/29



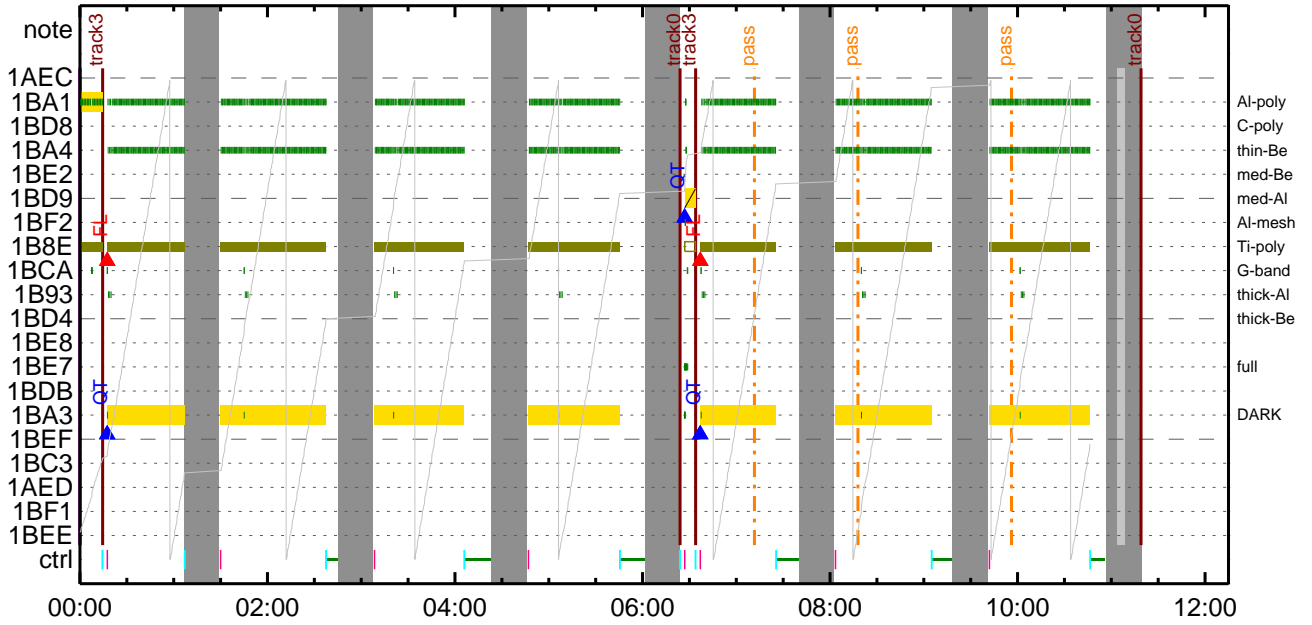
CMDI #0464 2018/04/30



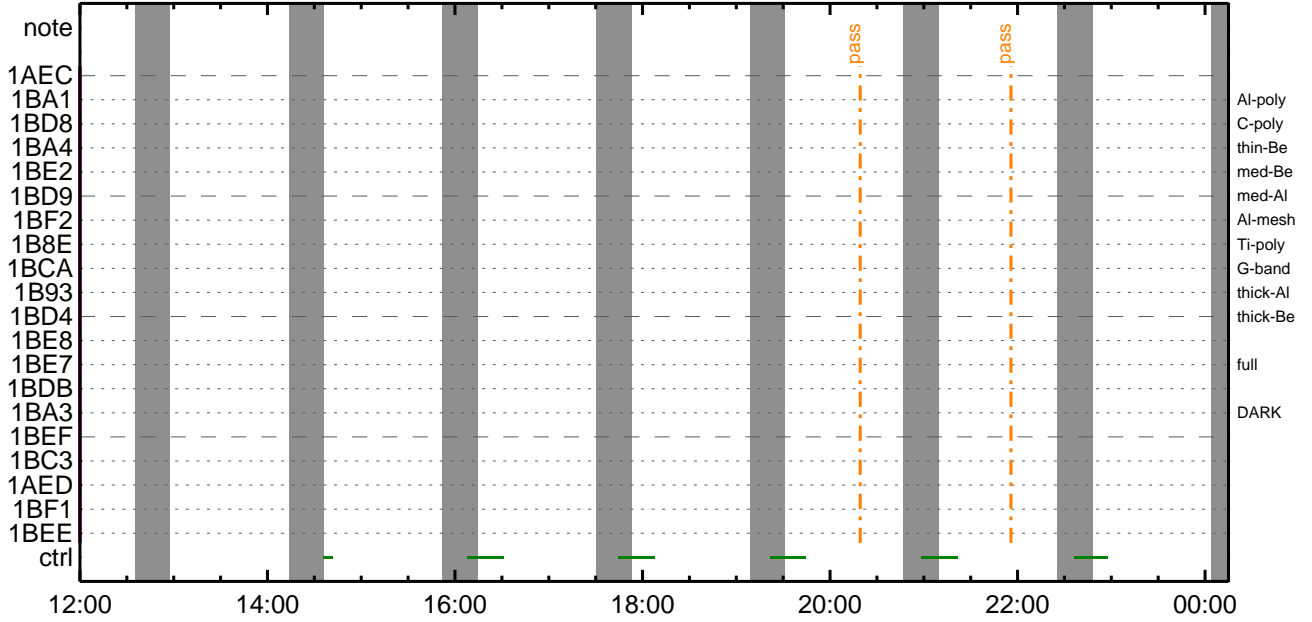
CMDI #0464 2018/04/30



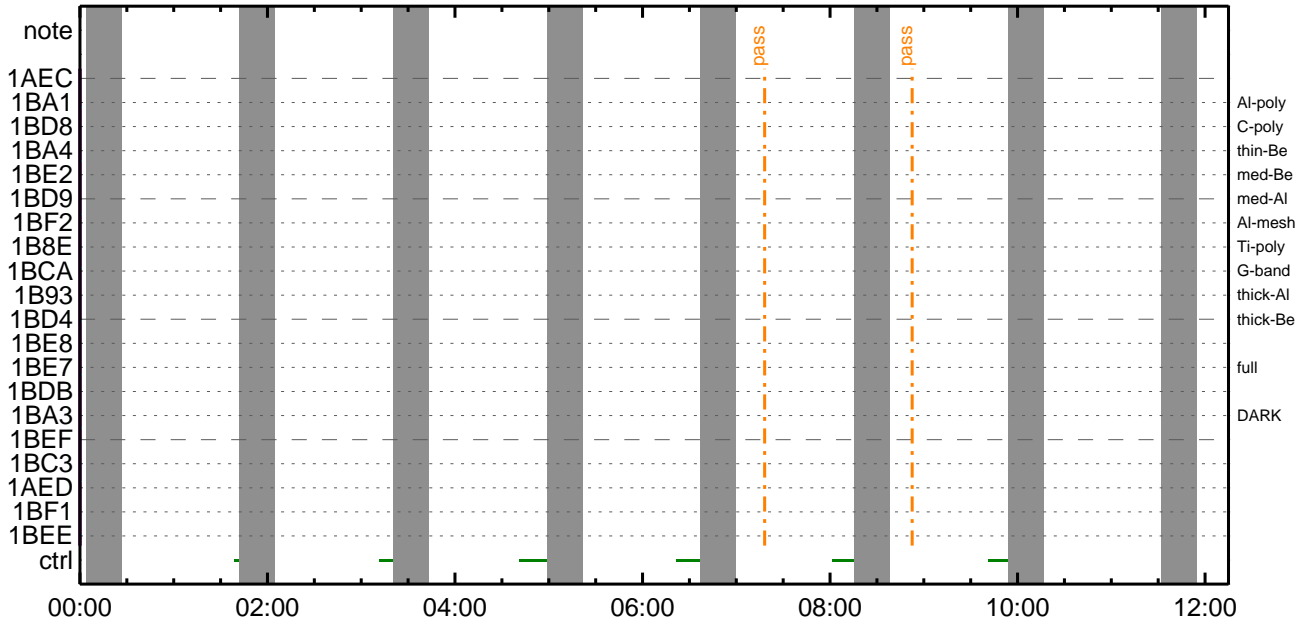
CMDI #0464 2018/05/01



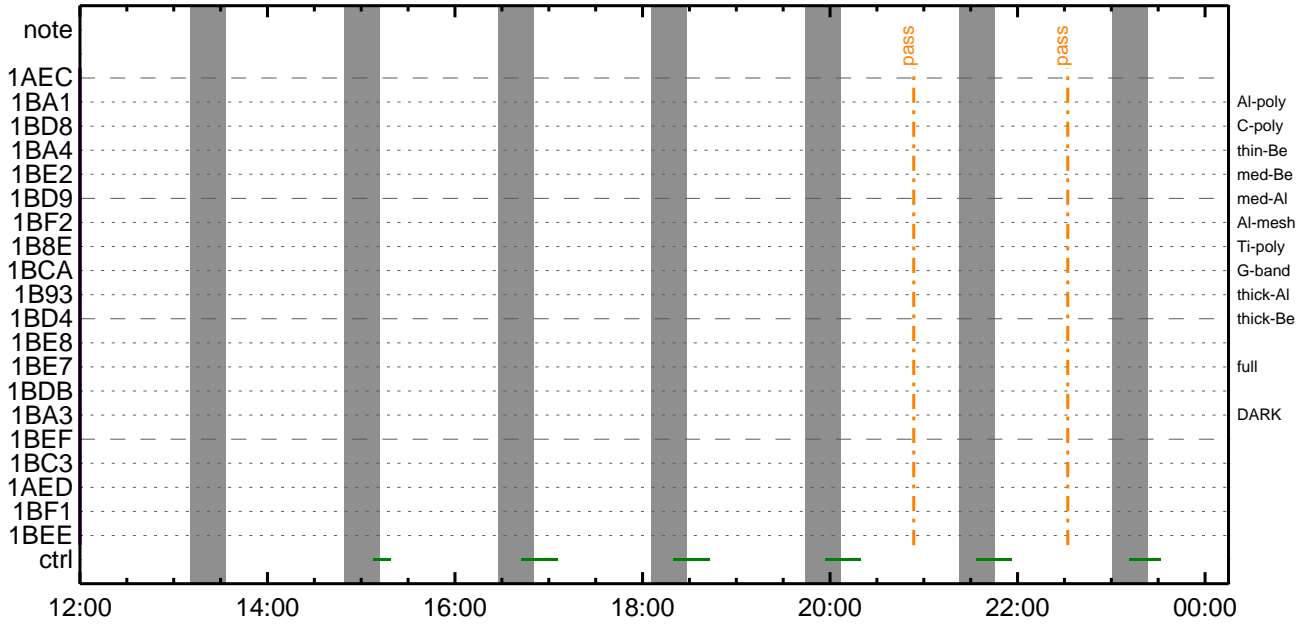
CMDI #0464 2018/05/01



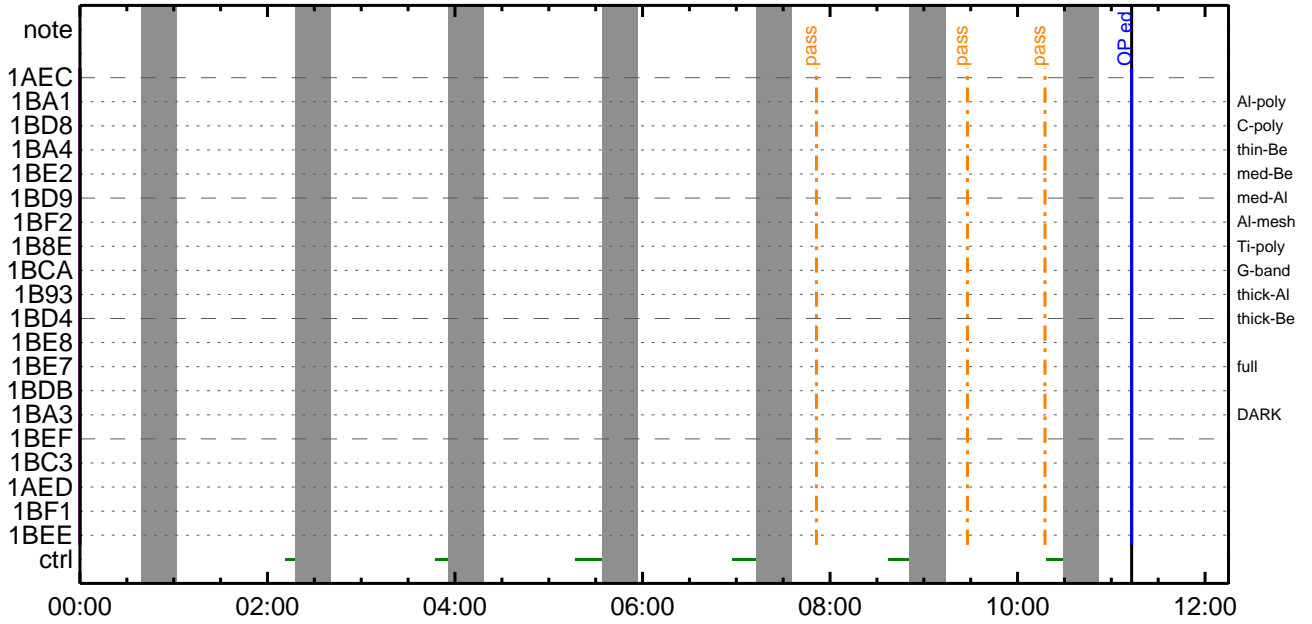
CMDI #0464 2018/05/02



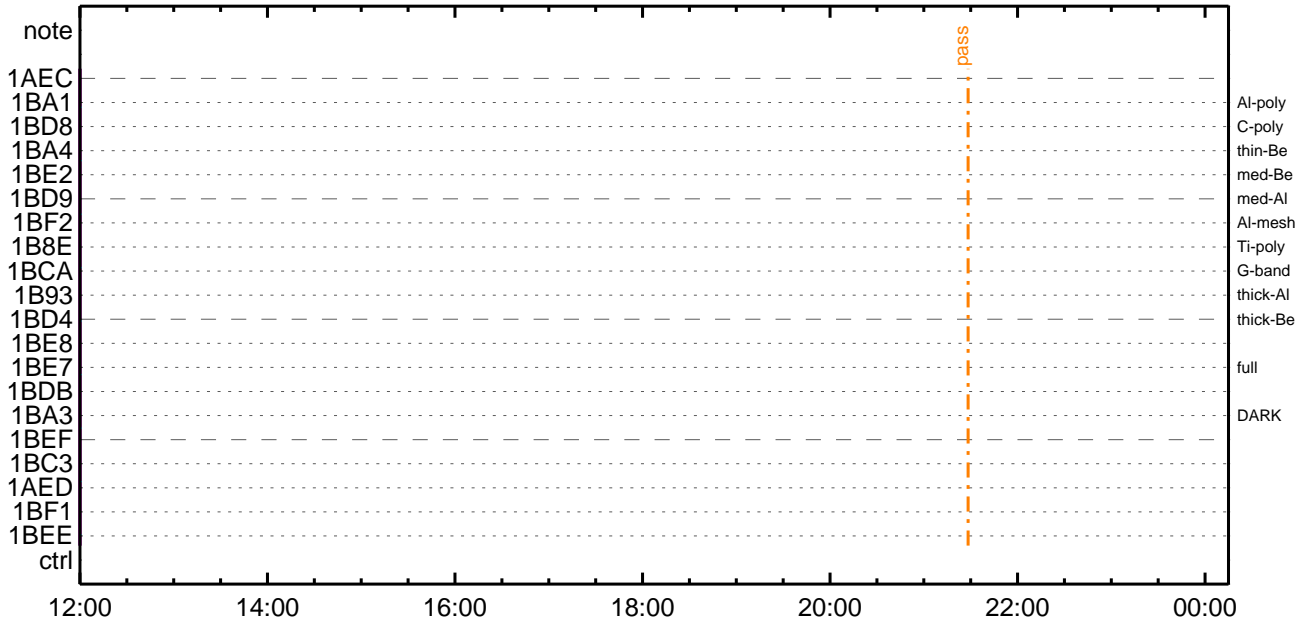
CMDI #0464 2018/05/02



CMDI #0464 2018/05/03



CMDI #0464 2018/05/03







```

0096 C.                0300; SET0EDUMP01E±0iYNY¹0Ç¹00|0³0E;E
0097 C.
0098 C. TIY³YFYOYÉ00ðÁDİ¿(UT)
0099 +. TI 2018-04-28 10:56:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                ÇÇ[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0102 C.
0103 +. TI 2018-04-28 10:56:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                ÇÇ[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0106 C.
0107 +. TI 2018-04-28 10:56:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                ÇÇ[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0110 C.
0111 +. TI 2018-04-28 11:00:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                ÇÇ[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0114 C.
0115 C. °E²¼0İÄè%îÍÑ0İYÁY§YÄY-¹àİÜ
0116 C.                ÇÇ[HK1_TI_CMD_ENA/DIS]                EQ        ENA
0117 C.                ÇÇ[HK1_TI_CMD_NUM]                EQ        4
0118 C.                ÇÇ[HK1_NEXT_EXEC_PIM]                EQ        DHU
0119 C.                ÇÇ[HK1_NEXT_EXEC_DC]                EQ        0xB3
0120 C.
0121 C. *****
0122 C. TIİİ°èYÄYOY×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.                ÇÇ[HK1_DMP_TOP_ADRS_1]                EQ        07
0129 C.                ÇÇ[HK1_DMP_TOP_ADRS_0]                EQ        2B
0130 C.                ÇÇ[HK1_DMP_BLOCK_NUM]                EQ        3
0131 C.                ÇÇ[HK1_DMP_REPEAT_NUM]                EQ        0
0132 C.                ÇÇ[HK1_DMA_DMP_PIM]                EQ        DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.                ÇÇ[HK1_PKT_FORM_NO]                EQ        7
0136 C.                ÇÇ[HK1_PKT_GEN_TIME]                EQ        0.25 s
0137 C.                ÇÇ[HK1_S_TLM_BIT_RATE]                EQ        32k
0138 C.                ÇÇ[HK1_X_TLM_BIT_RATE]                EQ        4M
0139 C.                ÇÇ[HK1_DMP_CHK_FLG]                EQ        EXEC
0140 C.
0141 C. YÄYOY×¼¹İ»0ð³İÇ§
0142 C.                ÇÇ[HK1_DMP_CHK_FLG]                EQ        NON
0143 C.
0144 C. RAM ID=TI_TBL0İ%È¹Ç•è²İOK0ð³İÇ§
0145 C.
0146 C. DHUYâ;¼YÉ;È¼Y¼, Yİ;¼YÈ;È0ðİá0¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.                ÇÇ[HK1_PKT_FORM_NO]                EQ        2
0150 C.                ÇÇ[HK1_PKT_GEN_TIME]                EQ        0.5S
0151 C.                ÇÇ[HK1_S_TLM_BIT_RATE]                EQ        32K
0152 C.                ÇÇ[HK1_X_TLM_BIT_RATE]                EQ        4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2018-04-28 11:00:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC (41)
0161 C. -----
0162 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 C.
0166 C. ***** XRT START *****
0167 C. Execute, after the success of OP upload.
0168 +. TI 2018-04-28 11:00:00.0
0169 DC 07-F0 MDP_XRT_MODE_STBY
0170 BC (c3)
0171 C.                [ ] [HK1_TI_CMD_NUM]                EQ        1COUNTUP
0172 C.
0173 C. ***** XRT END *****
0174 C.
0175 C. ***** MDP `ûÄİ0İ»ò¼Y0ÈÄ0¹0èDCBC•×²è *****
0176 C. (¼á°İYÖYÄYÈYbYÈYáYçYè0E¼¼0¼Ä»Ü0¹0è)
0177 S. DC-BC dcbc-402:DCBC
0178 (MDP_known_event)
0179 C.
0180 C.
0181 C. ***** YDY¹•İ Daily±;İÑ0È`00¹0èDCBC•×²è *****
0182 S. DC-BC dcbc-153:DCBC
0183 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0184 C.
0185 C.
0186 C. ;äLOSYÄY§YÄY-¼Ä»Ü;ä
0187 C.
0188 C. ***** LOS *****
0189 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-341 2018-04-28 12:13:55 144 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSÝÄÝÝÝÄÝ-¼Ä»Ü;ã
0005 C.
0006 C. ÝÄÝß;¼Ý³ÝßÝÖÝÉÄ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOSCS : Reload orbital element (send every contact) *****
0010 C. Äí;ÈççãÄã•µ°Æ»Í×ÄÇãíÝçÝÄÝ×Ýí;¼ÝÉ;ÈÈÈ¼µ•ííÈ;ÈãÈ¼°ÇÖã•çç¼í¹çãí;çÄ®, ùã¹ããßãçÄ÷ç®ã•ãÈããã³ãÈ; ç
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷ç®µ;ON
0016 C. *****
0017 C. ç“ °ÆÄ, Í×ÈÝãããLOSãããããí»í·Öãã¹íí, ã•; çÉÖÍ×ãÈXÄÖONãí¹ÖãÈãíãÈããã³ãÈ; ç
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XÝDÝÖÝÉÝíÝÄÝ-¾ÖÄÖã-ãÄããã•çç; ç°È²¼ãí°ÆÄ, ¼È¼çããã¼Ä¹Öã¹ãÈ; ç
0030 C.
0031 . C. *****
0032 C. DR PT1 Äí¼í°ÆÄ,
0033 C. *****
0034 C. ç“ RESTART;ÈPT1;Èããççã¼¹çãí; ç°È²¼ãí°ÆÄ¹Öãã°; çDCBC-150ãççãÈãã; ç
0035 C.
0036 . C. ;ãPT1°ÆÄ, ³«»í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ü)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ü)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ü)
0045 C.
0046 . C. ;ãÝçÝÖÝÄÝÈÄÜÄÖ;ÈÄ•Ä°²óÈè;È, ããí°ÆÄ, °Æ³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ü)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ü)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ü)
0052 C.
0053 C.
0054 . C. PT1°ÆÄ, ç-¼«Æ°Äã»ßããçç;ã; ç°È²¼ãã¼Ä¹Öã¹ãÈ; ç
0055 C. ÝçÝÖÝÄÝÈÄÜÄÖãÄ•Ä°²óÈèã-¾ãã¼¹çãí°ÆÄ¹Öãããããççãã; ç
0056 C.
0057 . C. *****
0058 C. DR PT2 Äí¼í°ÆÄ,
0059 C. *****
0060 C. ç“ RESTART;ÈPT2;Èããççã¼¹çãí; ç°È²¼ãí°ÆÄ¹Öãã°; çDCBC-151ãççãÈãã; ç
0061 C.
0062 . C. ;ãPT2°ÆÄ, ³«»í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ü)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ü)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ü)
0071 C.
0072 . C. ;ãÝçÝÖÝÄÝÈÄÜÄÖ;ÈÄ•Ä°²óÈè;È, ããí°ÆÄ, °Æ³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ü)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ü)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ü)
0078 C.
0079 . C. *****
0080 C. DR°ÆÄ, Äã»ß; çXÄ÷ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÄ, Äã»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÄ÷ç®µ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
```

```

0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 C.
0130 . C. ***** MDP 'uAÎaî»ô%ÿaEÂDâ¹aèDCBC•x²è *****
0131 C. (%â°îÿÓÿÄÿEÿPÿEÿâÿçÿèaE%¼a¼A»Ûa¹aè)
0132 . S. DC-BC dcbc-402:DCBC
0133 (MDP_known_event)
0134 C.
0135 C.
0136 . C. ***** ÿDÿ¹•Ï Daily±;îÑaE'Øa¹aèDCBC•x²è *****
0137 . S. DC-BC dcbc-153:DCBC
0138 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0139 C.
0140 C.
0141 . C. ;ãLOSÿÁÿSÿÿÿ¼A»Û;ã
0142 C.
0143 . C. ***** LOS *****
0144 C.

```



```

0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop SP table >
0102 +. DC 07-F0 MDP_SP_CTRL_MANU
0103 BC (61)
0104 C. -----
0105 C. MDP_SP_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload SP Observation Table>
0109 . S. RAM ram-285:MDP_OBS_S
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_S >
0113 +. DC 07-F0 MDP_DUMP_SPTBL
0114 BC (83 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_S verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 C. *****
0120 C. SOT TI command set
0121 C. *****
0122 C. Execute, after the success of TBL upload.
0123 +. TI 2018-04-28 11:00:18.0
0124 DC 07-F0 MDP_SOT_MODE_OBSV
0125 BC (40)
0126 . C. -----
0127 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0128 C. -----
0129 C.
0130 C.
0131 C. ***** XRT START *****
0132 C.
0133 +. DC 07-F0 MDP_XRT_CTRL_MANU
0134 BC (c1)
0135 +. DC 07-F0 MDP_XRT_CTRL_MANU
0136 BC (c1)
0137 +. DC 07-F0 MDP_XRT_MODE_STBY
0138 BC (c3)
0139 . C. ----- Success Verify ? OK / NG____
0140 C.
0141 C. XRT Obs. Table Upload
0142 . S. RAM ram-291:MDP_OBS_X
0143 ( )
0144 C.
0145 +. DC 07-F0 MDP_DUMP_XRTTBL
0146 BC (84 07 00 00 00 3a d4)
0147 . C. ----- Comparison Check ? OK / ERR ____
0148 C.
0149 C.
0150 +. DC 07-F0 MDP_XRT_ROI_SET
0151 BC (cd 01 b1 b1 04 04)
0152 +. DC 07-F0 MDP_XRT_ROI_SET
0153 BC (cd 02 b1 b1 08 08)
0154 +. DC 07-F0 MDP_XRT_ROI_SET
0155 BC (cd 03 b1 b1 08 08)
0156 +. DC 07-F0 MDP_XRT_ROI_SET
0157 BC (cd 04 b1 b1 06 06)
0158 +. DC 07-F0 MDP_XRT_ROI_SET
0159 BC (cd 05 85 83 06 06)
0160 +. DC 07-F0 MDP_XRT_ROI_SET
0161 BC (cd 06 85 83 06 06)
0162 +. DC 07-F0 MDP_XRT_ROI_SET
0163 BC (cd 07 85 83 08 08)
0164 +. DC 07-F0 MDP_XRT_ROI_SET
0165 BC (cd 08 80 80 20 20)
0166 +. DC 07-F0 MDP_XRT_ROI_SET
0167 BC (cd 09 80 80 08 08)
0168 +. DC 07-F0 MDP_XRT_ROI_SET
0169 BC (cd 0a 80 80 20 08)
0170 +. DC 07-F0 MDP_XRT_ROI_SET
0171 BC (cd 0b 80 80 08 20)
0172 +. DC 07-F0 MDP_XRT_ROI_SET
0173 BC (cd 0c 80 60 20 18)
0174 +. DC 07-F0 MDP_XRT_ROI_SET
0175 BC (cd 0d a0 80 18 20)
0176 +. DC 07-F0 MDP_XRT_ROI_SET
0177 BC (cd 0f 80 80 06 06)
0178 +. DC 07-F0 MDP_XRT_ROI_SET
0179 BC (cd 10 80 80 08 08)
0180 +. DC 07-F0 MDP_XRT_FLD_ENA
0181 BC (d8)
0182 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0183 BC (c8)
0184 +. DC 07-F0 MDP_XRT_ARS_DIS
0185 BC (d5)
0186 +. DC 07-F0 MDP_XRT_AEC_RESET
0187 BC (d0)
0188 +. DC 07-F0 MDP_XRT_FLD_RESET
0189 BC (da)
0190 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0191 BC (c4 06)
0192 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0193 BC (c5 0d)

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0194 . C. ----- Success Verify ?           OK / NG ____
0195 . C.
0196 . C.
0197 . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0198 . C.
0199 +. DC 07-F0 MDP_XRT_MODE_OBSV
0200     BC          (c2)
0201 +. TI 2018-04-28 11:00:02.0
0202     DC 07-F0 MDP_XRT_MODE_OBSV
0203     BC          (c2)
0204 . C. ----- Success Verify ?           OK / NG ____
0205 . C.
0206 . C. ***** XRT END *****
0207 . C.
0208 . C. ***** MDP `úÃîñî»ö¼ÝðËÂð¹ñèDCBC•x²è *****
0209 . C. (¼ã°îÿÓÿÄÿÈÿÞÿËÿÁÿçÿèñÈ¼ññ¼Ã»Ûñ¹ñè)
0210 . S. DC-BC dcbc-402:DCBC
0211     (MDP_known_event)
0212 . C.
0213 . C.
0214 . C. ***** ÿÐÿ¹•Ï Daily±¿ÎÑñÈ´Øñ¹ñèDCBC•x²è *****
0215 . S. DC-BC dcbc-153:DCBC
0216     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0217 . C.
0218 . C.
0219 . C. ;ãLOSÿÁÿ$ÿÃÿÿ-¼Ã»Û;ã
0220 . C.
0221 . C. ***** LOS *****
0222 . C.

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\*\*\* OP Sequence for XRT \*\*\*

2018/04/28	11:10:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/28	11:10:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/28	11:10:58.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2018/04/28	11:11:00.5	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2018/04/28	11:11:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2018/04/28	11:11:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2018/04/28	11:11:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2018/04/28	11:11:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2018/04/28	11:11:26.0	XRT_FLD_RESET_433_OG [0x1b1]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2018/04/28	11:13:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02		
2018/04/28	11:13:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2018/04/28	11:14:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/04/28	12:28:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/28	12:28:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/28	12:28:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2018/04/28	12:28:36.0	XRT_PREFLR_STRT_406_OG [0x196]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/04/28	12:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/04/28	12:49:00.0	XRT_Custom_430_OG [0x1ae]					
2018/04/28	12:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/04/28	14:06:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/28	14:06:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/28	14:06:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2018/04/28	14:06:36.0	XRT_PREFLR_STRT_406_OG [0x196]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/04/28	14:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/04/28	14:10:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/28	14:10:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/28	14:10:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2018/04/28	14:11:00.0	AOCS_ORe-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	03 00 00 00 00		
2018/04/28	14:11:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2018/04/28	14:11:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2018/04/28	14:11:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2018/04/28	14:11:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2018/04/28	14:11:26.0	XRT_FLD_RESET_433_OG [0x1b1]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2018/04/28	14:13:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11		
2018/04/28	14:13:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2018/04/28	14:32:00.0	XRT_Custom_430_OG [0x1ae]					
2018/04/28	14:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/04/28	15:45:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/28	15:45:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/28	15:45:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2018/04/28	15:45:06.0	XRT_PREFLR_STRT_406_OG [0x196]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/04/28	15:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/04/28	16:24:00.0	XRT_Custom_430_OG [0x1ae]					
2018/04/28	16:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/04/28	17:23:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/28	17:23:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/28	17:23:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		



2018/04/28	17:23:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/04/28	17:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/04/28	18:00:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/28	18:00:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/28	18:00:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2018/04/28	18:00:30.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00	
2018/04/28	18:00:48.0	XRT_FLD_DIS_401_OG [0x191]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2018/04/28	18:00:50.5	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2018/04/28	18:00:52.5	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/04/28	18:03:28.5	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f	
2018/04/28	18:03:30.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/04/28	18:10:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/28	18:10:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/28	18:10:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2018/04/28	18:10:30.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	03 00 00 00 00	
2018/04/28	18:10:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2018/04/28	18:10:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2018/04/28	18:10:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2018/04/28	18:10:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/04/28	18:10:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/04/28	18:13:26.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2018/04/28	18:13:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2018/04/28	18:13:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/04/28	19:01:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/28	19:01:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/28	19:01:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/04/28	19:01:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/04/28	19:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/04/28	19:37:00.0	XRT_Custom_430_OG [0x1ae]					
2018/04/28	19:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/04/28	20:40:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/28	20:40:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/28	20:40:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/04/28	20:40:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/04/28	20:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/04/28	21:14:30.0	XRT_Custom_430_OG [0x1ae]					
2018/04/28	21:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/04/28	22:18:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/28	22:18:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/28	22:18:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/04/28	22:18:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/04/28	22:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/04/28	22:50:00.0	XRT_Custom_430_OG [0x1ae]					
2018/04/28	22:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/04/28	23:56:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/28	23:56:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/28	23:56:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/04/28	23:56:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	

2018/04/28	23:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/04/29	00:18:01.0	XRT_Custom_430_OG [0x1ae]							
2018/04/29	00:19:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/04/29	01:32:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/29	01:32:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/29	01:32:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/04/29	01:32:06.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/04/29	01:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/04/29	01:56:00.0	XRT_Custom_430_OG [0x1ae]							
2018/04/29	01:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/04/29	03:04:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/29	03:04:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/29	03:04:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/04/29	03:04:36.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/04/29	03:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/04/29	03:34:30.0	XRT_Custom_430_OG [0x1ae]							
2018/04/29	03:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/04/29	04:33:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/29	04:33:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/29	04:33:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/04/29	04:33:06.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/04/29	04:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/04/29	05:13:00.0	XRT_Custom_430_OG [0x1ae]							
2018/04/29	05:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/04/29	05:45:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/29	05:45:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/29	05:45:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2018/04/29	05:46:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2018/04/29	05:46:18.0	XRT_FLD_DIS_401_OG [0x191]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2018/04/29	05:46:20.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2018/04/29	05:46:22.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/04/29	05:48:58.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f				
2018/04/29	05:49:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/04/29	05:55:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/29	05:55:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/29	05:55:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2018/04/29	05:56:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2018/04/29	05:56:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2018/04/29	05:56:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2018/04/29	05:56:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2018/04/29	05:56:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/04/29	05:56:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/04/29	05:58:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2018/04/29	05:58:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2018/04/29	05:59:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/04/29	06:13:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/29	06:13:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/04/29	06:13:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/04/29	06:13:36.0	XRT_PREFLR_STRT_406_OG [0x196]							

2018/04/29	06:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/04/29	06:51:30.0	XRT_Custom_430_OG [0x1ae]						
2018/04/29	06:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/04/29	07:53:30.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/29	07:53:32.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/29	07:53:34.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2018/04/29	07:53:36.0	XRT_PREFLR_STRT_406_OG [0x196]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/04/29	07:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/04/29	08:29:30.0	XRT_Custom_430_OG [0x1ae]						
2018/04/29	08:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/04/29	09:33:30.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/29	09:33:32.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/29	09:33:34.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2018/04/29	09:33:36.0	XRT_PREFLR_STRT_406_OG [0x196]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/04/29	09:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/04/29	10:08:00.5	XRT_Custom_430_OG [0x1ae]						
2018/04/29	10:09:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/04/29	10:59:54.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/29	10:59:56.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/29	10:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]						
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2018/04/29	11:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]						
			AOCU_NM	5	02-76	00 00 00 00 00		
2018/04/29	11:00:18.0	XRT_FLD_ENA_411_OG [0x19b]						
			MDP_XRT_FLD_ENA	1	07-F0	d8		
2018/04/29	11:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2018/04/29	11:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]						
			MDP_XRT_AEC_RESET	1	07-F0	d0		
2018/04/29	11:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]						
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2018/04/29	11:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2018/04/29	11:02:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]						
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 02		
2018/04/29	11:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]						
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2018/04/29	11:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/04/29	11:25:00.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/29	11:25:02.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/29	11:25:04.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2018/04/29	11:25:06.0	XRT_PREFLR_STRT_406_OG [0x196]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/04/29	11:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/04/29	11:46:30.0	XRT_Custom_430_OG [0x1ae]						
2018/04/29	11:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/04/29	13:03:30.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/29	13:03:32.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/29	13:03:34.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2018/04/29	13:03:36.0	XRT_PREFLR_STRT_406_OG [0x196]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/04/29	13:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/04/29	13:25:00.0	XRT_Custom_430_OG [0x1ae]						
2018/04/29	13:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/04/29	13:59:54.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/29	13:59:56.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/04/29	13:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2018/04/29	14:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]						
			AOCU_NM	5	02-76	01 02 c0 01 ca		
2018/04/29	14:00:18.0	XRT_FLD_ENA_411_OG [0x19b]						
			MDP_XRT_FLD_ENA	1	07-F0	d8		
2018/04/29	14:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						

2018/04/29	14:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2018/04/29	14:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/04/29	14:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/04/29	14:02:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e
2018/04/29	14:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2018/04/29	14:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/04/29	14:42:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/29	14:42:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/29	14:42:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/04/29	14:42:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/04/29	14:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/04/29	15:11:00.0	XRT_Custom_430_OG [0x1ae]					
2018/04/29	15:12:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/04/29	16:20:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/29	16:20:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/29	16:20:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/04/29	16:20:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/04/29	16:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/04/29	16:58:30.0	XRT_Custom_430_OG [0x1ae]					
2018/04/29	16:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/04/29	17:58:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/29	17:58:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/29	17:58:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/04/29	17:58:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/04/29	18:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/04/29	18:35:00.0	XRT_Custom_430_OG [0x1ae]					
2018/04/29	18:36:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/04/29	19:24:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/29	19:24:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/29	19:24:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2018/04/29	19:25:00.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00	00 00 00 00
2018/04/29	19:25:18.0	XRT_FLD_DIS_401_OG [0x191]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2018/04/29	19:25:20.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2018/04/29	19:25:22.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/04/29	19:27:58.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f
2018/04/29	19:28:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/04/29	19:34:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/29	19:34:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/04/29	19:34:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2018/04/29	19:35:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	03	00 00 00 00
2018/04/29	19:35:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2018/04/29	19:35:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2018/04/29	19:35:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2018/04/29	19:35:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/04/29	19:35:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/04/29	19:37:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11
2018/04/29	19:37:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2018/04/29	20:12:00.0	XRT_Custom_430_OG [0x1ae]					

2018/04/29	20:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/04/29	21:15:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/29	21:15:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/29	21:15:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/04/29	21:15:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/04/29	21:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/04/29	21:49:00.0	XRT_Custom_430_OG [0x1ae]				
2018/04/29	21:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/04/29	22:53:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/29	22:53:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/29	22:53:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/04/29	22:53:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/04/29	22:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/04/29	23:24:00.0	XRT_Custom_430_OG [0x1ae]				
2018/04/29	23:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/04/30	00:32:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/30	00:32:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/30	00:32:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/04/30	00:32:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/04/30	00:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/04/30	00:53:30.0	XRT_Custom_430_OG [0x1ae]				
2018/04/30	00:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/04/30	02:04:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/30	02:04:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/30	02:04:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/04/30	02:04:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/04/30	02:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/04/30	02:32:00.0	XRT_Custom_430_OG [0x1ae]				
2018/04/30	02:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/04/30	03:39:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/30	03:39:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/30	03:39:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/04/30	03:39:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/04/30	03:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/04/30	04:10:00.0	XRT_Custom_430_OG [0x1ae]				
2018/04/30	04:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/04/30	05:09:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/30	05:09:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/30	05:09:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/04/30	05:09:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/04/30	05:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/04/30	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/30	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/04/30	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2018/04/30	06:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00
2018/04/30	06:00:18.0	XRT_FLD_DIS_425_OG [0x1a9]	MDP_XRT_FLD_DIS	1	07-F0	d9
2018/04/30	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2018/04/30	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2018/04/30	06:02:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12

2018/04/30	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2018/04/30	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/04/30	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/04/30	06:09:58.0	XRT_FOCUS_POSITION_439_OG [0x1b7]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2018/04/30	06:10:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 ad 59 00 00			
2018/04/30	06:10:18.0	XRT_FLD_DIS_427_OG [0x1ab]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2018/04/30	06:24:54.0	XRT_FLRCTRL_DIS_435_OG [0x1b3]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2018/04/30	06:24:56.0	XRT_ARS_DIS_445_OG [0x1bd]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2018/04/30	06:24:58.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14			
2018/04/30	06:25:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2018/04/30	08:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/04/30	08:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/04/30	08:09:58.0	XRT_FOCUS_POSITION_439_OG [0x1b7]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2018/04/30	08:10:00.0	AOCS_Ore-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00 00 00 56 35			
2018/04/30	08:10:18.0	XRT_FLD_DIS_442_OG [0x1ba]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2018/04/30	08:24:49.0	XRT_ROI_A_432_OG [0x1b0]	MDP_XRT_ROI_SET	6	07-F0	cd 05 85 83 06 06			
			MDP_XRT_ROI_SET	6	07-F0	cd 06 85 83 06 06			
			MDP_XRT_ROI_SET	6	07-F0	cd 07 85 83 08 08			
			MDP_XRT_ROI_SET	6	07-F0	cd 08 80 80 20 20			
			MDP_XRT_ROI_SET	6	07-F0	cd 09 80 80 08 08			
			MDP_XRT_ROI_SET	6	07-F0	cd 0a 80 80 20 08			
			MDP_XRT_ROI_SET	6	07-F0	cd 0b 80 80 08 20			
			MDP_XRT_ROI_SET	6	07-F0	cd 0d a0 80 18 20			
2018/04/30	08:24:49.5	XRT_ROI_B_407_OG [0x197]	MDP_XRT_ROI_SET	6	07-F0	cd 0d a0 80 18 20			
			MDP_XRT_ROI_SET	6	07-F0	cd 0f 80 80 06 06			
			MDP_XRT_ROI_SET	6	07-F0	cd 10 80 80 08 08			
2018/04/30	08:24:54.5	XRT_FLRCTRL_DIS_435_OG [0x1b3]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2018/04/30	08:24:56.5	XRT_ARS_DIS_445_OG [0x1bd]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2018/04/30	08:24:58.5	XRT_QT_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03			
2018/04/30	08:25:00.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2018/04/30	10:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/04/30	10:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/04/30	10:09:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2018/04/30	10:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00			
2018/04/30	10:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2018/04/30	10:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2018/04/30	10:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2018/04/30	10:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2018/04/30	10:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da			
2018/04/30	10:12:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02			
2018/04/30	10:12:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d			
2018/04/30	10:43:30.0	XRT_Custom_430_OG [0x1ae]							
2018/04/30	10:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2018/04/30	12:00:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/04/30	12:00:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/04/30	12:00:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2018/04/30	12:00:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2018/04/30	12:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2018/04/30	12:22:00.0	XRT_Custom_430_OG [0x1ae]							
2018/04/30	12:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2018/04/30	13:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/04/30	13:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			

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2018/04/30	13:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2018/04/30	13:10:00.0	AOCs_OrE-point_Start_2_OG [0x098] AOCU_NM	5	02-76	03	00	00	00
2018/04/30	13:10:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0		d8		
2018/04/30	13:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0		c8		
2018/04/30	13:10:22.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0		d0		
2018/04/30	13:10:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0		d5		
2018/04/30	13:10:26.0	XRT_FLD_RESET_433_OG [0x1b1] MDP_XRT_FLD_RESET	1	07-F0		da		
2018/04/30	13:12:56.0	XRT_QT_PROG_SET_436_OG [0x1b4] MDP_XRT_QT_PROG_SET	2	07-F0	c4		11	
2018/04/30	13:12:58.0	XRT_FL_PROG_SET_440_OG [0x1b8] MDP_XRT_FL_PROG_SET	2	07-F0	c5		0d	
2018/04/30	13:13:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2018/04/30	13:38:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2018/04/30	13:38:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2018/04/30	13:38:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0		da		
2018/04/30	13:38:36.0	XRT_PREFLR_STRT_406_OG [0x196] MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2018/04/30	13:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2018/04/30	14:00:30.0	XRT_Custom_430_OG [0x1ae]						
2018/04/30	14:01:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2018/04/30	15:17:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2018/04/30	15:17:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2018/04/30	15:17:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0		da		
2018/04/30	15:17:06.0	XRT_PREFLR_STRT_406_OG [0x196] MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2018/04/30	15:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2018/04/30	15:56:30.0	XRT_Custom_430_OG [0x1ae]						
2018/04/30	15:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2018/04/30	16:55:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2018/04/30	16:55:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2018/04/30	16:55:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0		da		
2018/04/30	16:55:36.0	XRT_PREFLR_STRT_406_OG [0x196] MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2018/04/30	16:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2018/04/30	17:33:30.0	XRT_Custom_430_OG [0x1ae]						
2018/04/30	17:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2018/04/30	18:04:24.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2018/04/30	18:04:26.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2018/04/30	18:04:28.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2018/04/30	18:04:30.0	AOCs_OrE-point_Start_1_OG [0x097] AOCU_NM	5	02-76	00	00	00	00
2018/04/30	18:04:48.0	XRT_FLD_DIS_401_OG [0x191] MDP_XRT_FLD_DIS	1	07-F0		d9		
2018/04/30	18:04:50.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0		c9		
2018/04/30	18:04:52.0	XRT_ARS_DIS_422_OG [0x1a6] MDP_XRT_ARS_DIS	1	07-F0		d5		
2018/04/30	18:07:28.0	XRT_QT_PROG_SET_404_OG [0x194] MDP_XRT_QT_PROG_SET	2	07-F0	c4		0f	
2018/04/30	18:07:30.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2018/04/30	18:14:24.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2018/04/30	18:14:26.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2018/04/30	18:14:28.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2018/04/30	18:14:30.0	AOCs_OrE-point_Start_6_OG [0x09c] AOCU_NM	5	02-76	00	c4	8c	01
2018/04/30	18:14:48.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0		d8		
2018/04/30	18:14:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0		c8		
2018/04/30	18:14:52.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0		d0		
2018/04/30	18:14:54.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0		d5		

2018/04/30	18:14:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da			
2018/04/30	18:17:26.0	XRT_QT_PROG_SET_409_OG [0x199]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	13		
2018/04/30	18:17:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d		
2018/04/30	18:17:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2018/04/30	18:33:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/04/30	18:33:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/04/30	18:33:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2018/04/30	18:33:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2018/04/30	18:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2018/04/30	19:10:00.0	XRT_Custom_430_OG [0x1ae]							
2018/04/30	19:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2018/04/30	20:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/04/30	20:12:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/04/30	20:12:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2018/04/30	20:12:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2018/04/30	20:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2018/04/30	20:47:00.0	XRT_Custom_430_OG [0x1ae]							
2018/04/30	20:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2018/04/30	21:14:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/04/30	21:14:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/04/30	21:14:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00		
2018/04/30	21:14:30.0	AOCS_ORe-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 37	e7 01 ca		
2018/04/30	21:14:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2018/04/30	21:14:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2018/04/30	21:14:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2018/04/30	21:14:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2018/04/30	21:14:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da			
2018/04/30	21:17:26.0	XRT_QT_PROG_SET_409_OG [0x199]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	13		
2018/04/30	21:17:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d		
2018/04/30	21:17:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2018/04/30	21:50:30.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/04/30	21:50:32.5	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/04/30	21:50:34.5	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2018/04/30	21:50:36.5	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2018/04/30	21:53:44.5	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2018/04/30	22:23:30.0	XRT_Custom_430_OG [0x1ae]							
2018/04/30	22:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2018/04/30	23:28:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/04/30	23:28:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/04/30	23:28:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2018/04/30	23:28:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2018/04/30	23:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2018/04/30	23:54:00.0	XRT_Custom_430_OG [0x1ae]							
2018/04/30	23:55:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2018/05/01	00:14:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/05/01	00:14:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/05/01	00:14:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00		
2018/05/01	00:14:30.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	03 00 00 00 00			
2018/05/01	00:14:48.0	XRT_FLD_ENA_411_OG [0x19b]							



2018/05/01	00:14:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2018/05/01	00:14:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2018/05/01	00:14:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/05/01	00:14:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/05/01	00:17:26.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	06
2018/05/01	00:17:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2018/05/01	00:17:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/05/01	01:07:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/05/01	01:07:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/05/01	01:07:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/05/01	01:07:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/05/01	01:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/05/01	01:29:00.0	XRT_Custom_430_OG [0x1ae]					
2018/05/01	01:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/05/01	02:37:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/05/01	02:37:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/05/01	02:37:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/05/01	02:37:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/05/01	02:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/05/01	03:07:30.0	XRT_Custom_430_OG [0x1ae]					
2018/05/01	03:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/05/01	04:06:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/05/01	04:06:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/05/01	04:06:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/05/01	04:06:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/05/01	04:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/05/01	04:46:00.0	XRT_Custom_430_OG [0x1ae]					
2018/05/01	04:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/05/01	05:45:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/05/01	05:45:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/05/01	05:45:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/05/01	05:45:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/05/01	05:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/05/01	06:23:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/05/01	06:23:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/05/01	06:23:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2018/05/01	06:24:00.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00	00 00 00 00
2018/05/01	06:24:18.0	XRT_FLD_DIS_401_OG [0x191]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2018/05/01	06:24:20.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2018/05/01	06:24:22.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/05/01	06:26:58.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f
2018/05/01	06:27:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/05/01	06:33:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/05/01	06:33:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/05/01	06:33:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2018/05/01	06:34:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	03	00 00 00 00
2018/05/01	06:34:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2018/05/01	06:34:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					

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2018/05/01	06:34:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2018/05/01	06:34:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2018/05/01	06:34:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2018/05/01	06:36:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/05/01	06:36:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2018/05/01	06:37:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2018/05/01	07:25:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/05/01	07:25:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/05/01	07:25:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/05/01	07:25:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/05/01	07:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/05/01	08:02:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/05/01	08:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2018/05/01	09:05:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/05/01	09:05:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/05/01	09:05:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/05/01	09:05:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/05/01	09:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/05/01	09:41:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/05/01	09:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2018/05/01	10:46:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/05/01	10:46:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/05/01	10:46:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/05/01	10:46:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/05/01	10:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/05/01	11:19:00.0	AOCS_OrE-point_Start_1_OG [0x097]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
		AOCU_NM		5	02-76	00 00 00 00 00