

XRT Timeline to be uploaded on 2018/05/03

Period: 2018/05/03 11:13:00 - 2018/05/08 10:22:00

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Normal mode

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XOB #1BF1: HOP349 - 3-filter Synoptics (Al-mesh[512/2048/4096], Al-poly[512/4096/8192], thin-Be[3897/16384/32768] with 512x512 G-band+Leak - 45 min cad

Term	Pointing (x, y)	Comment
05/03 11:26:00 - 05/03 13:46:00	Fixed (0.0, 0.0)	# OP start + 10min. HOP 349 near disk center.
05/04 06:45:30 - 05/04 10:55:00	Fixed (-450.0, 0.0)	# XRT disk center.
05/04 11:29:00 - 05/04 13:59:54	Fixed (0.0, 0.0)	# HOP 349

PROG= 02 Inf.-time(s)

Subr= 1	1-time(s)	300.0sec														
Seqn= 12 1-time(s) 2.0sec			Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh			Open/Al-mesh	close	Safe	Norm	2.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec	
Open/Al-mesh			Open/Al-mesh	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 82 1-time(s) 2.0sec			Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open			Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec	
Al-poly/Open			Al-poly/Open	close	Safe	Norm	8.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 52 1-time(s) 2.0sec			thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open			thin-Be/Open	close	Safe	Norm	16.0s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec	
thin-Be/Open			thin-Be/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 30 1-time(s) 2.0sec			Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512	(1024, 1024)	Q=90	0	0	2.0sec
Open/G-band			Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512	(1024, 1024)	Q=95	0	0	2.0sec	
Subr= 2			30-time(s)		90.0sec											
Seqn= 8 1-time(s) 2.0sec			thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048	(1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open			med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048	(1024, 1024)	DPCM	2	0	2.0sec	
Seqn= 6 1-time(s) 2.0sec			Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open			Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048	(1024, 1024)	DPCM	2	0	2.0sec	
Seqn= 29 1-time(s) 2.0sec			Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	3	0	2.0sec
Open/Al-mesh			Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	2	0	2.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval					

XOB #1BF3: QS (Filter-Ratio with Al/poly and Al/mesh), 384FOV at 1064, 1048 with G-band (1ms/1ms leak) 180s cad - AEC0- HOP328

Term	Pointing (x, y)	Comment
05/03 14:10:00 - 05/03 19:59:54	Fixed (-930.0, 223.0)	# ALMA coordination HOP 328.

PROG= 11 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec														
Seqn= 92 1-time(s) 2.0sec			Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384	(1064, 1048)	DPCM	0	0	2.0sec
Open/G-band			Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384	(1064, 1048)	DPCM	0	0	2.0sec	
Open/Ti-poly			Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384	(1064, 1048)	Q=98	0	0	2.0sec	
Subr= 2			1-time(s)		2.0sec											
Seqn= 76 20-time(s) 180.0sec			Open/Al-mesh	Open/thick-Al	close	Safe	Norm	5.66s	Obs	1x1	384x384	(1064, 1048)	Q=95	0	0	2.0sec
Al-poly/Open			Al-poly/thick-Al	close	Safe	Norm	8.00s	Obs	1x1	384x384	(1064, 1048)	Q=95	0	0	2.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval					

XOB #1BD8: Synoptic 7 Filter w/ Al-mesh(64/512/2897), Al-poly(45/512/4096), Thin-Be(1024/11571/23142) - Thick-Be(65536), Al-poly+Ti-poly(512/8192), Med

Term	Pointing (x, y)	Comment
05/03 20:03:00 - 05/03 20:09:54	Fixed (0.0, 0.0)	synoptic, shifted.

PROG= 18 1-time(s)

Subr= 1	1-time(s)	2.0sec														
Seqn= 5 1-time(s) 2.0sec			Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly			Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec	
Open/Ti-poly			Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec	
Open/Ti-poly			Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512	(1024, 1024)	DPCM	0	0	2.0sec	
Open/Ti-poly			Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048	(1024, 1024)	DPCM	0	0	2.0sec	
Seqn= 36 1-time(s) 2.0sec			Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	63ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh			Open/Al-mesh	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec	
Open/Al-mesh			Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 99 1-time(s) 2.0sec			Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open			Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec	
Al-poly/Open			Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 33 1-time(s) 2.0sec			thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open			thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec	
thin-Be/Open			thin-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec	

Seqn= 23	1-time(s)	4.0sec															
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec					
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec					
Subr= 2	1-time(s)	2.0sec															
Seqn= 46	1-time(s)	2.0sec															
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec					
Seqn= 17	1-time(s)	2.0sec															
med-Al/Open	med-Al/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec					
med-Al/Open	med-Al/Open	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec					
Seqn= 25	1-time(s)	2.0sec															
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec					
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec					
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval					

XOB #1BCC: CME watch - 4x4 - AEC 1/2 - Al-poly - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 360s cad (G-band/Leak first)

Term	Pointing (x, y)	Comment
05/03 20:55:30 - 05/04 05:53:00	Track (-50.0, 502.2) @ 05/03 20:10:00	# HOP 312.
05/04 14:03:00 - 05/04 19:59:54	Track (-38.6, 687.1) @ 05/04 14:00:00	# HOP 312.

PROG= 12 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec															
Seqn= 30	1-time(s)	2.0sec															
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec					
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec					
Subr= 2	20-time(s)	360.0sec															
Seqn= 7	1-time(s)	2.0sec															
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	1	0	2.0sec					
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec					
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval					

XOB #1BE7: Synoptic Q95 2x2 - Al/mesh(512/2048/4096) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(512/4096/8192)

Term	Pointing (x, y)	Comment
05/04 06:35:30 - 05/04 06:42:24	Fixed (0.0, 0.0)	synoptic, shifted 32.5 min
05/04 20:03:00 - 05/04 20:09:54	Fixed (0.0, 0.0)	synoptic, shifted.
05/05 06:03:00 - 05/05 06:09:54	Fixed (0.0, 0.0)	synoptic

PROG= 08 1-time(s)

Subr= 1	1-time(s)	2.0sec															
Seqn= 5	1-time(s)	2.0sec															
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec					
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec					
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec					
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec					
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec					
Seqn= 12	1-time(s)	2.0sec															
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec					
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec					
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec					
Seqn= 82	1-time(s)	2.0sec															
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec					
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec					
Al-poly/Open	Al-poly/Open	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec					
Seqn= 52	1-time(s)	2.0sec															
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec					
thin-Be/Open	thin-Be/Open	close	Safe	Norm	16.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec					
thin-Be/Open	thin-Be/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec					
Seqn= 23	1-time(s)	2.0sec															
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec					
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec					
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval					

XOB #1BE8: HOP349 - 3-filter Synoptics (Al-mesh[512/2048/4096], Al-poly[512/4096/8192], thin-Be[3897/16384/32768] with 512x512 G-band+Leak - 45 min cad)

Term	Pointing (x, y)	Comment
05/04 20:13:00 - 05/05 05:59:54	Fixed (-450.0, 0.0)	# XRT disc center.
05/05 06:13:00 - 05/05 09:49:00	Fixed (-450.0, 0.0)	# XRT disk center.

PROG= 01 Inf.-time(s)

Subr= 1	1-time(s)	300.0sec															
Seqn= 12	1-time(s)	2.0sec															
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec					
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec					
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec					
Seqn= 82	1-time(s)	2.0sec															
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec					
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec					
Al-poly/Open	Al-poly/Open	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec					
Seqn= 52	1-time(s)	2.0sec															
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec					
thin-Be/Open	thin-Be/Open	close	Safe	Norm	16.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec					
thin-Be/Open	thin-Be/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec					

Seqn= 30	1-time(s)	2.0sec																		
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec								
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec								
Subr= 2	18-time(s)	150.0sec																		
Seqn= 8	1-time(s)	2.0sec																		
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec								
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec								
Seqn= 6	1-time(s)	2.0sec																		
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec								
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec								
Seqn= 29	1-time(s)	2.0sec																		
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec								
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec								
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval									

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Flare mode

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XOB #1B8E: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512		
Term	Pointing (x, y)	Comment
05/03 11:26:00 - 05/03 13:46:00	Fixed (0.0, 0.0)	# OP start + 10min. HOP 349 near disk center.
05/03 14:10:00 - 05/03 19:59:54	Fixed (-930.0, 223.0)	# ALMA coordination HOP 328.
05/03 20:55:30 - 05/04 05:53:00	Track (-50.0, 502.2) @ 05/03 20:10:00	# HOP 312.
05/04 06:45:30 - 05/04 10:55:00	Fixed (-450.0, 0.0)	# XRT disk center.
05/04 11:29:00 - 05/04 13:59:54	Fixed (0.0, 0.0)	# HOP 349
05/04 14:03:00 - 05/04 19:59:54	Track (-38.6, 687.1) @ 05/04 14:00:00	# HOP 312.
05/04 20:13:00 - 05/05 05:59:54	Fixed (-450.0, 0.0)	# XRT disc center.
05/05 06:13:00 - 05/05 09:49:00	Fixed (-450.0, 0.0)	# XRT disk center.

PROG= 13 30-time(s)												
Subr= 1	20-time(s)	2.0sec										
Seqn= 11	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn=100	1-time(s)	10.0sec										
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2	1-time(s)	2.0sec										
Seqn= 10	1-time(s)	2.0sec										
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 87	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

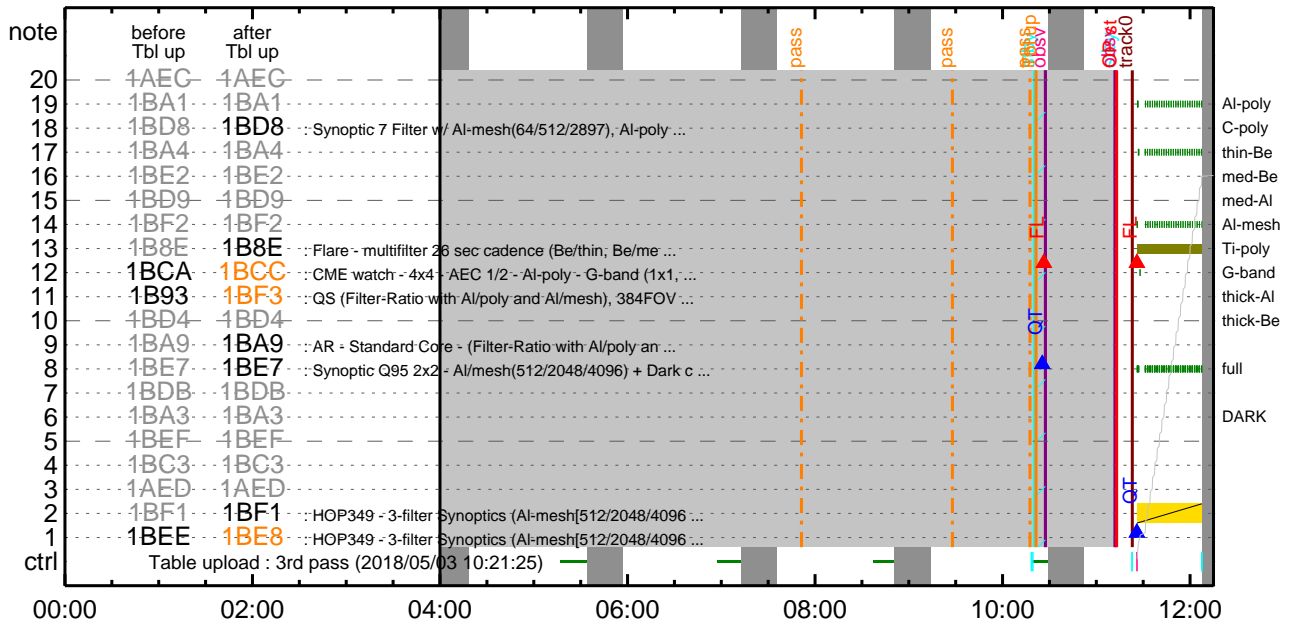
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Flare Detection

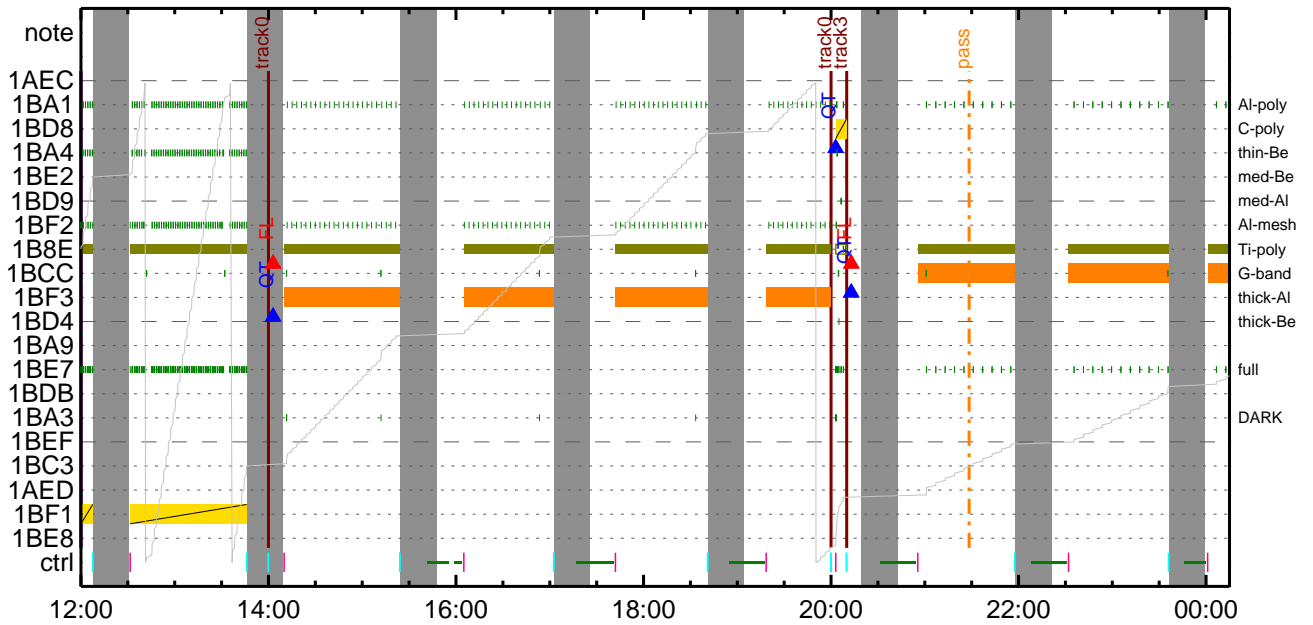
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FLD Patrol											
Term	Pointing (x, y)	Comment									
05/03 20:10:18 - 05/04 06:32:48	Track (-50.0, 502.2) @ 05/03 20:10:00	# HOP 312.									
05/04 06:42:48 - 05/04 20:00:18	Fixed (-450.0, 0.0)	# XRT disk center.									
05/04 20:10:18 - 05/05 06:00:18	Fixed (-450.0, 0.0)	# XRT disc center.									
05/05 06:10:18 - 05/08 10:22:00	Fixed (-450.0, 0.0)	# XRT disk center.									
Al-poly/Open	Al-poly/Open	close	Safe	Norm	8ms	Obs	8x8		Q=50		80sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

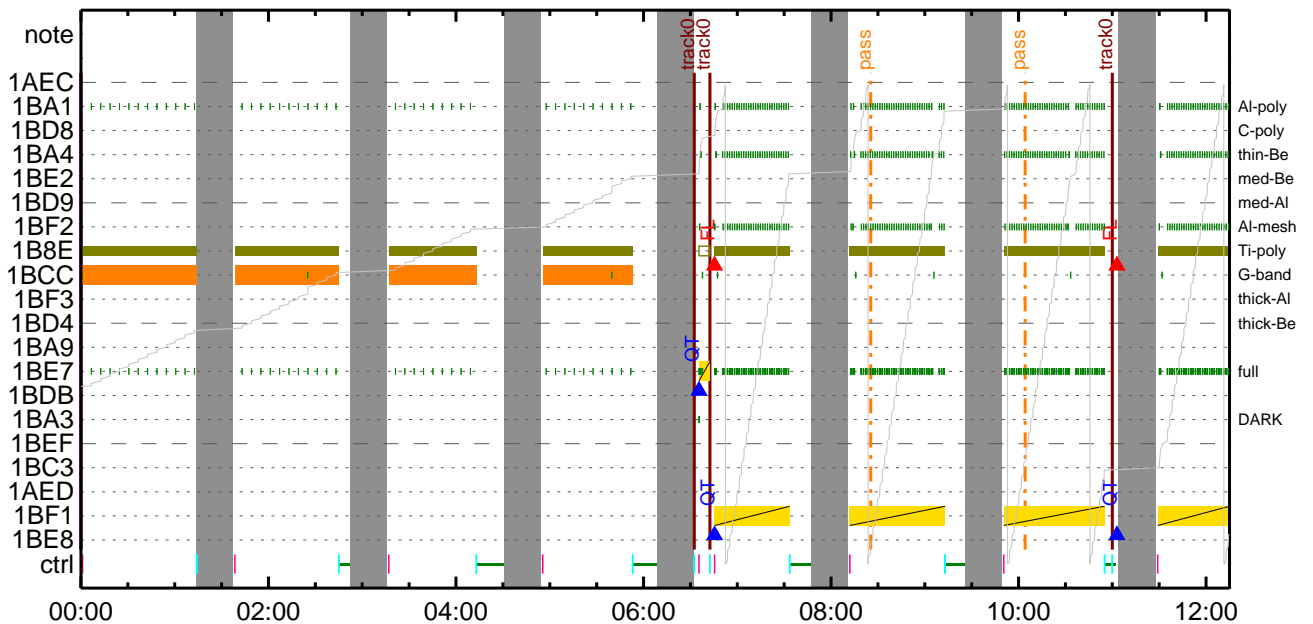
CMDI #0472 2018/05/03



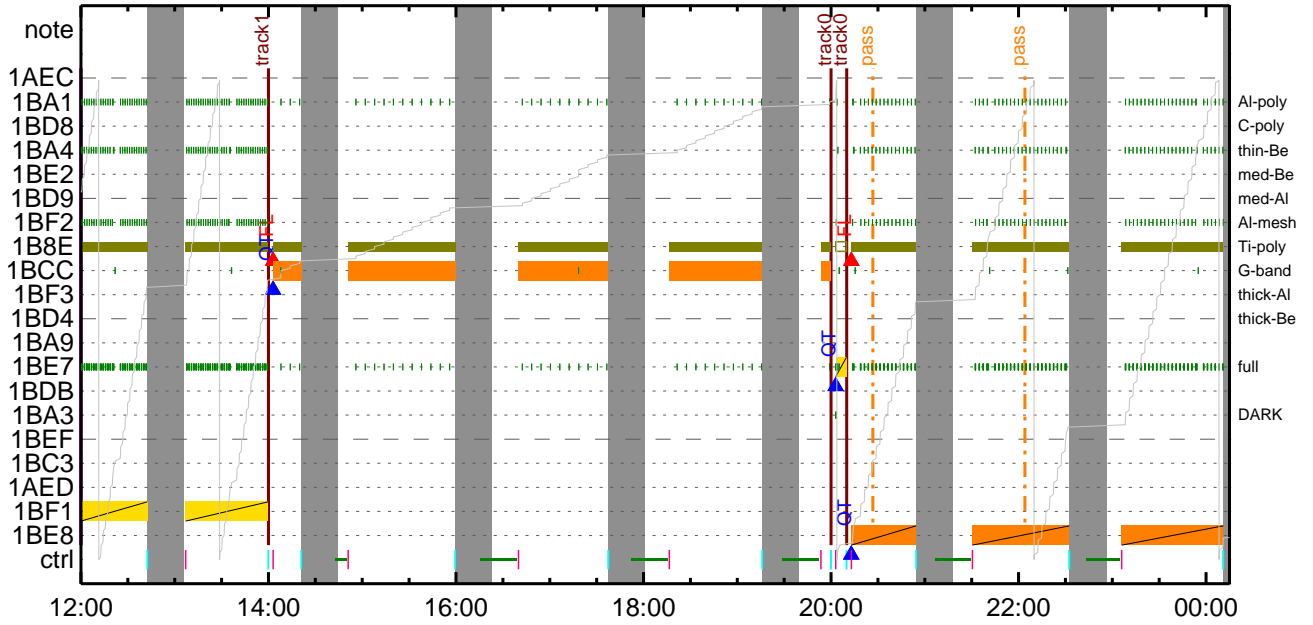
CMDI #0472 2018/05/03



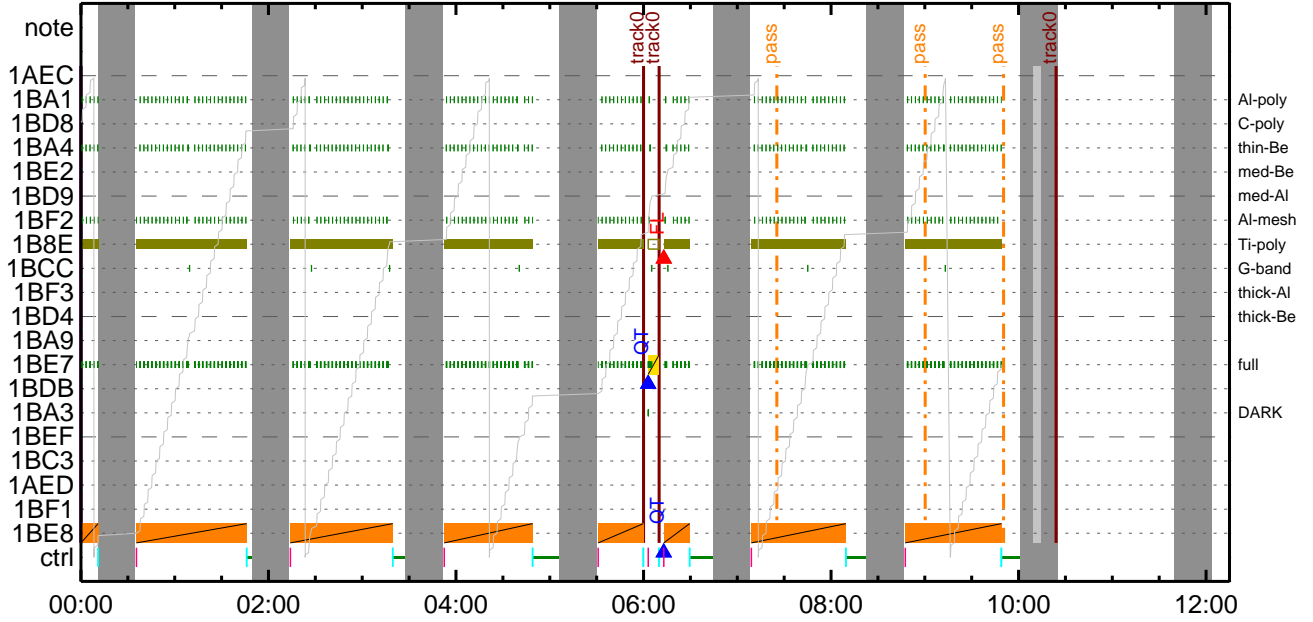
CMDI #0472 2018/05/04



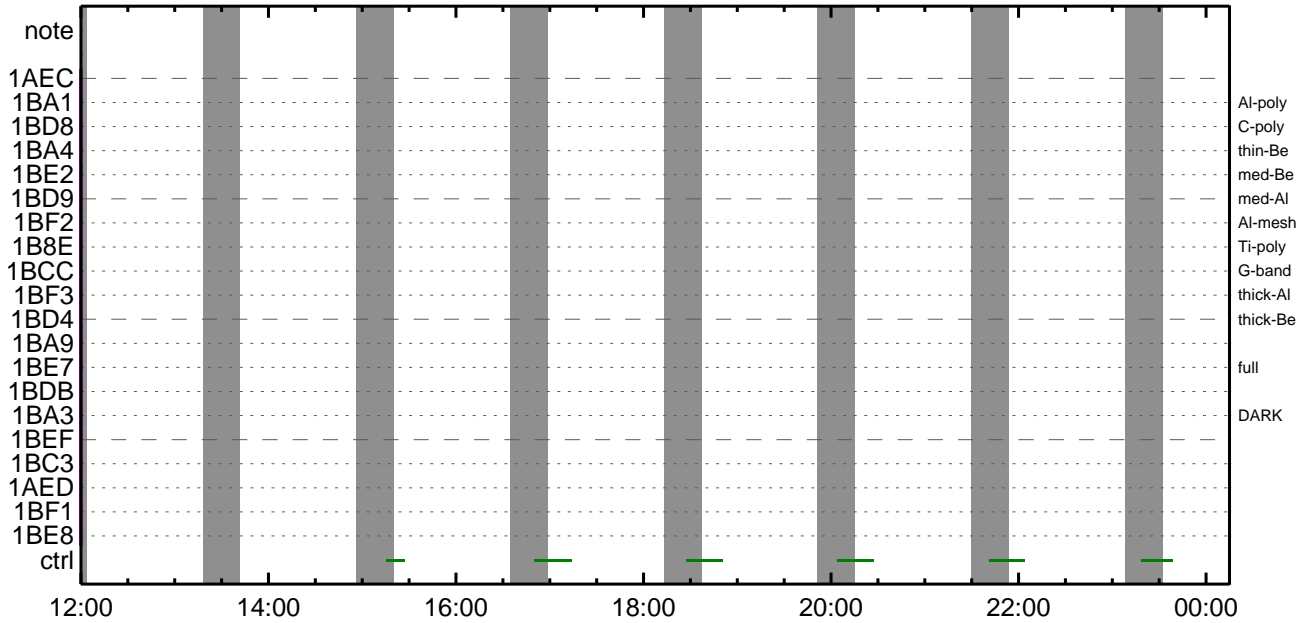
CMDI #0472 2018/05/04



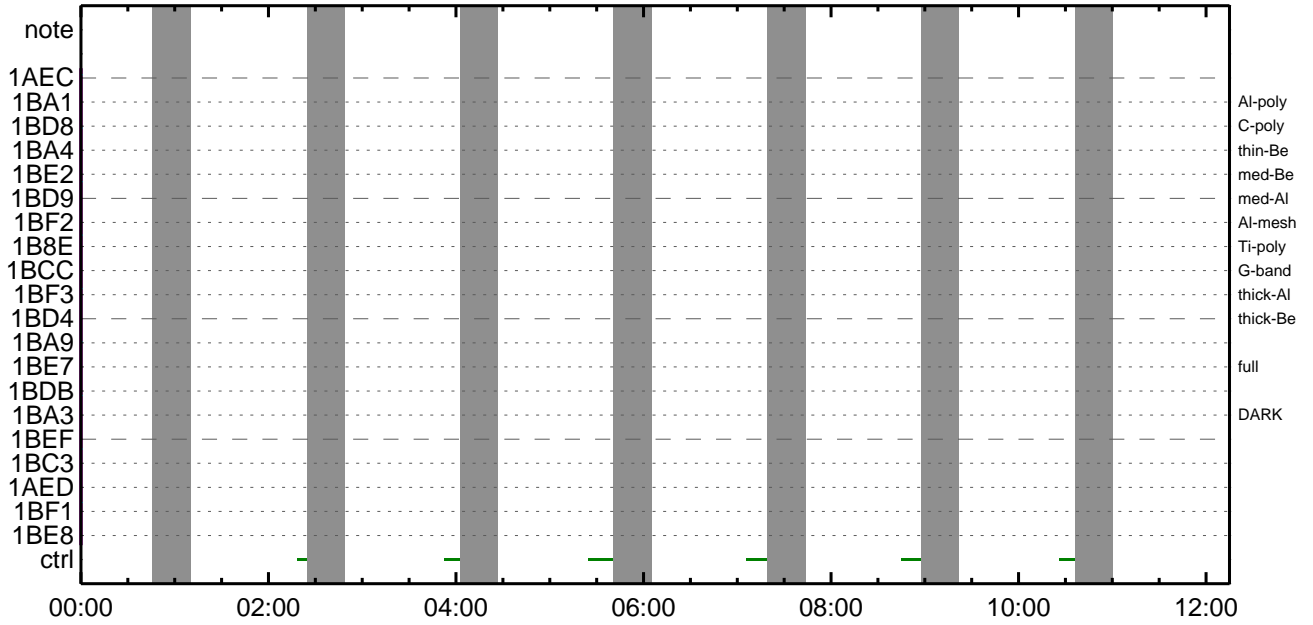
CMDI #0472 2018/05/05



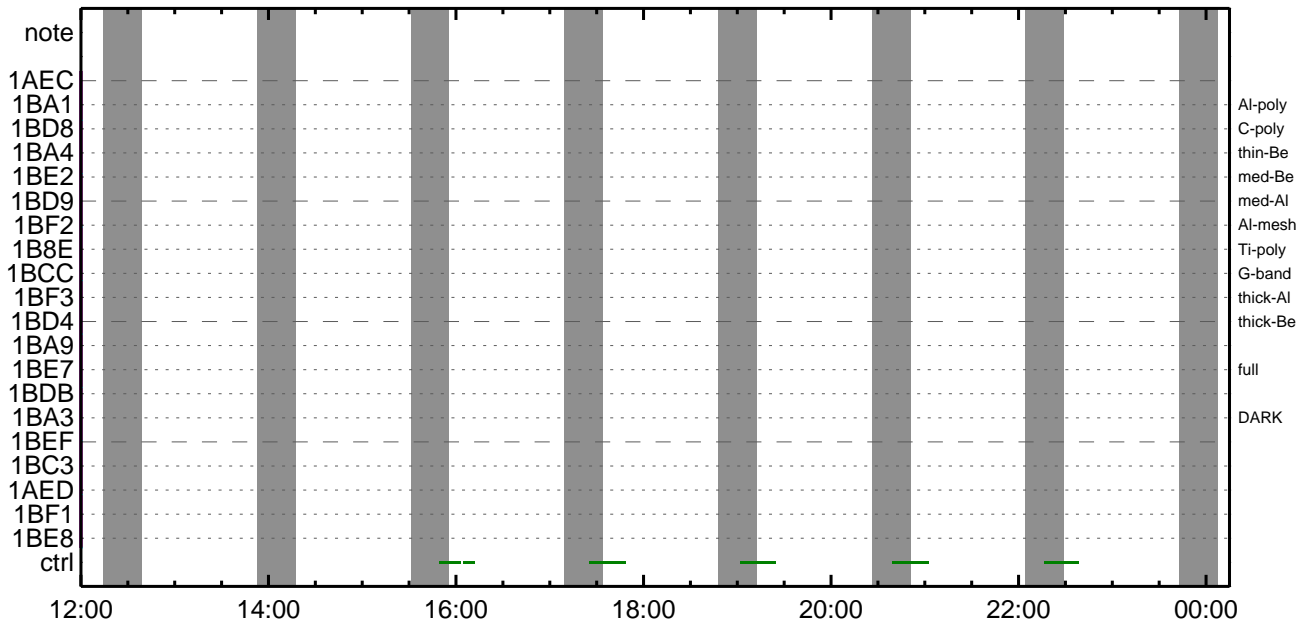
CMDI #0472 2018/05/05



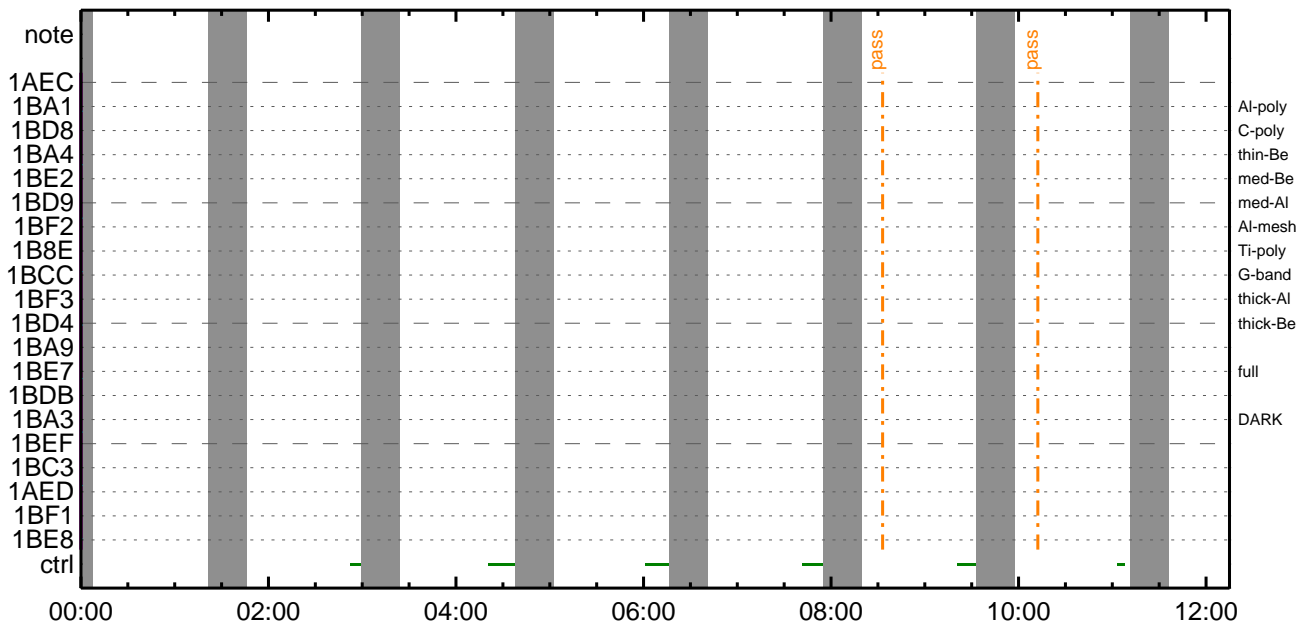
CMDI #0472 2018/05/06



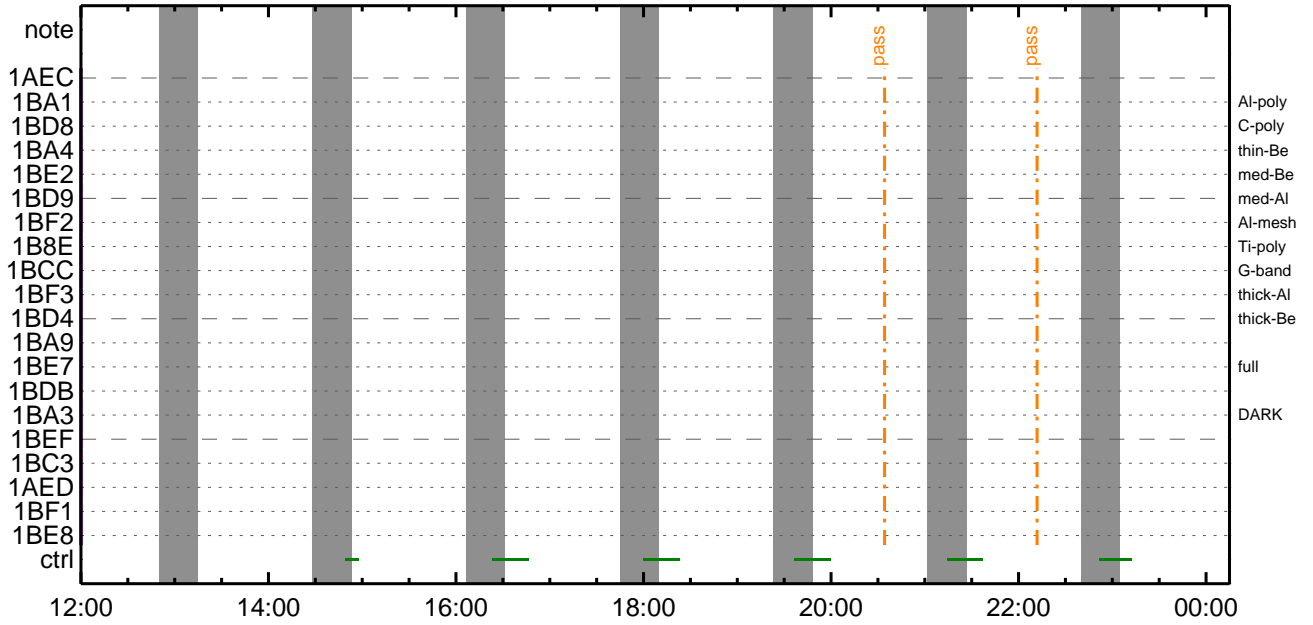
CMDI #0472 2018/05/06



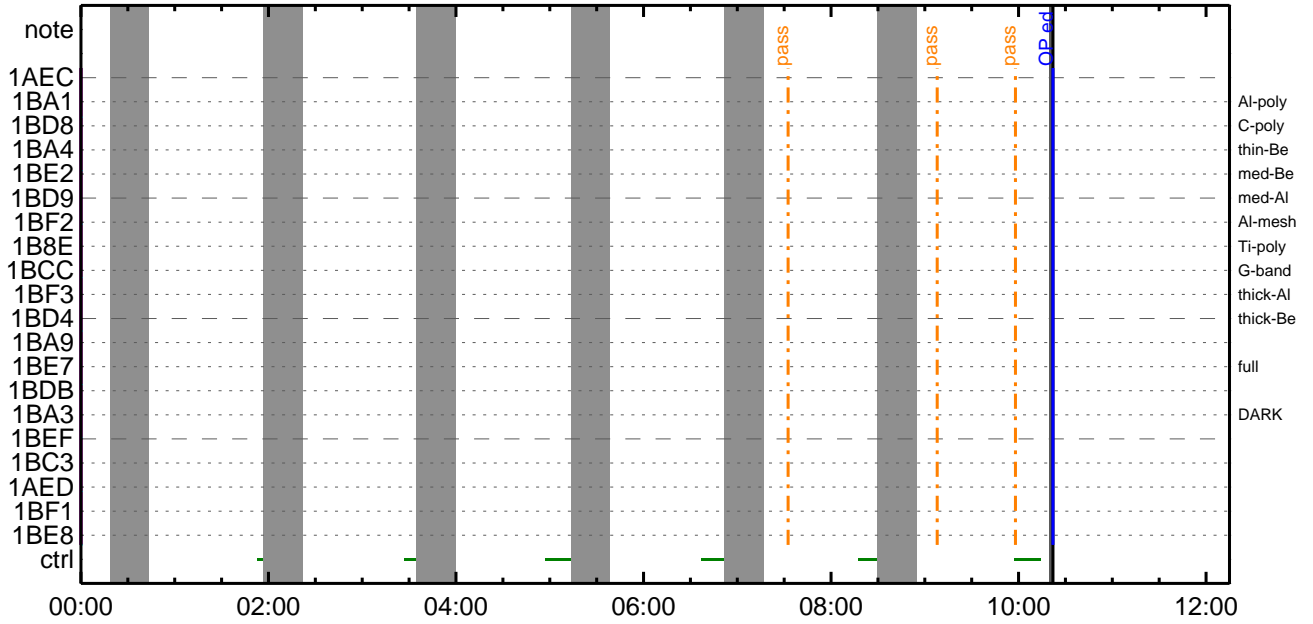
CMDI #0472 2018/05/07



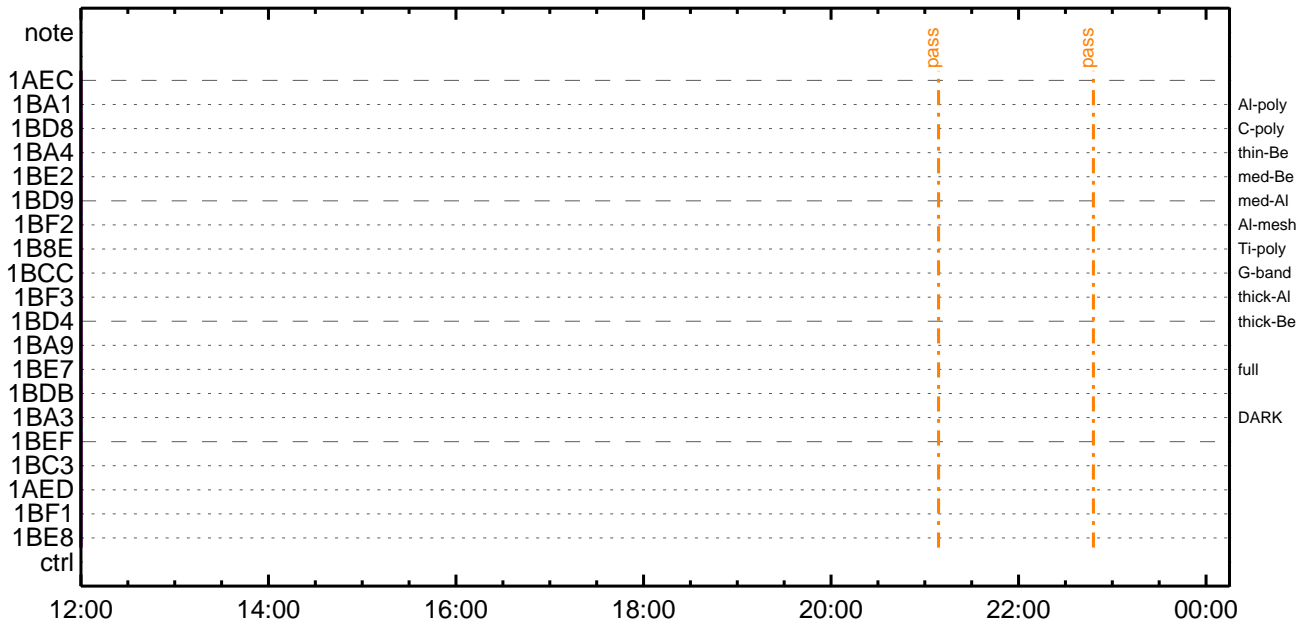
CMDI #0472 2018/05/07



CMDI #0472 2018/05/08



CMDI #0472 2018/05/08




```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-349:OP
0104 ( )
0105 S. OG og-349:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ª î»ò³ îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGãî½E¹ç•è² îOKò³ îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ª î»ò³ îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGãî½E¹ç•è² îOKò³ îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ª î»ò³ îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPãî½E¹ç•è² îOKò³ îÇ§
0165 C.
0166 C. ***** òE²¼ò î½Ã´ ¶Ã°òEÈ¬ò Æ÷¿@ (½ãµ-YAYOXx½E½çò ÆÃ°æç½ª° òE½¿i¹çòçã) *****
0167 C. DHUYã;½YE;E½Y½, Y;½YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD¬-Ã÷¿@NGUî½i¹ç;ç°E²¼ò îTI-CMDÃ÷¿@ Æ½Ã¹Ò° òEòòò³ òE;f
0180 C. òEò¿;çSETòEDUMPAîÆ±° îYÑY¹ ç¹Ò° |ò³ òE;f
0181 C.
0182 C. TIY³Y½Y½Eòð Æî¿¿ (UT)
0183 +. TI 2018-05-03 11:08:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2018-05-03 11:08:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2018-05-03 11:08:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```


(a) Spacecraft Operation Procedure (real-commands)

```
main-350 2018-05-03 12:12:55 144 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÄY-¼Ä»Û;ã
0005 C.
0006 C. YÄYß;¼Y³YÞYÓYÉÄ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;ËççãÄ•µ°Æ»Í×ÁÇçÍYçYÄY×Yí;¼YÉ;ËÈÈµ•íÍË;Ë°È¼°ÇÖã•çç¼í¹ççí;çÄ®, ùã¹ãèãßãÇÄ+ç®ã•ãèãçãè;ç
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+ççµ;ON
0016 C. *****
0017 C. ç“ °ÆÀ, Í×ËYçãäLOSãßçãí»ß´Öãð¹íí, ç. ; çÉÖÍ×ãÈXÄÓONãí¹ÖãÈíãèãçãè;ç
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDÝÓYÉYíYÄY-¼ÖÄÖã-°ÄÄêã•ççç; ç°È²¼ççí°ÆÀ, ¼ê¼ççð¼Ä¹Öççç;ç
0030 C.
0031 . C. *****
0032 C. DR PT1 Äí¼í°ÆÀ,
0033 C. *****
0034 C. ç“ RESTART;ÈPT1;Ëã•ççç¼í¹ççí; ç°È²¼ççí°ÆÀ¹Öççç; çDCBC-150çççÈççç;ç
0035 C.
0036 . C. ;ãPT1°ÆÀ, ³«»Í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYçYÓYÉYÈÄÜÄØ;ÈÄ•Ä°²óÈð;Ë, áãí°ÆÀ, °Æ³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÀ, ç-¼«Æ°Äã»ßã•ççç, á; ç°È²¼ççð¼Ä¹Öççç;ç
0055 C. YçYÓYÉYÈÄÜÄØãÄ•Ä°²óÈðã-¼áçç¼í¹ççí°ÆÀ¹Öçççãßççç;ç
0056 C.
0057 . C. *****
0058 C. DR PT2 Äí¼í°ÆÀ,
0059 C. *****
0060 C. ç“ RESTART;ÈPT2;Ëã•ççç¼í¹ççí; ç°È²¼ççí°ÆÀ¹Öççç; çDCBC-151çççÈççç;ç
0061 C.
0062 . C. ;ãPT2°ÆÀ, ³«»Í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYçYÓYÉYÈÄÜÄØ;ÈÄ•Ä°²óÈð;Ë, áãí°ÆÀ, °Æ³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÀ, Äã»ß; çXÄ+ççµ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÀ, Äã»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÄ+ççµ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
```

```

0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 C.
0130 . C. ***** MDP 'ûÃîñî»ò¼ÿñÊÂðñ¹ñèDCBC•x²è *****
0131 C. (¼â°îÿÓÿÃÿÊÿPÿËÿâÿçÿèñ¼¼¼¼»Ûñ¹ñè)
0132 . S. DC-BC dcbc-402:DCBC
0133 (MDP_known_event)
0134 C.
0135 C.
0136 . C. ***** ÿDÿ¹•Ï Daily±;îÑñÊ'Øñ¹ñèDCBC•x²è *****
0137 . S. DC-BC dcbc-153:DCBC
0138 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0139 C.
0140 C.
0141 . C. ;ãLOSÿÃÿSÿÿÿ¼Ã»Û;ã
0142 C.
0143 . C. ***** LOS *****
0144 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-351 2018-05-03 12:12:55 130 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È□¿□Á□•µ°È»Í×ÁÇ□íYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;È□È¼°ÇÖ□•□¿¼í¹ç□í;çÀ®, ù□¹□è□È□çÁ+¿®□•□È□□□³□È;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 +. DC 07-F0 MDP_XRT_CTRL_MANU
0020 BC (c1)
0021 +. DC 07-F0 MDP_XRT_MODE_STBY
0022 BC (c3)
0023 . C. ----- Success Verify ? OK / NG____
0024 C.
0025 C. XRT Obs. Table Upload
0026 . S. RAM ram-291:MDP_OBS_X
0027 ( )
0028 C.
0029 +. DC 07-F0 MDP_DUMP_XRTTBL
0030 BC (84 07 00 00 00 3a d4)
0031 . C. ----- Comparison Check ? OK / ERR ____
0032 C.
0033 C.
0034 +. DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 01 b1 b1 04 04)
0036 +. DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 02 b1 b1 08 08)
0038 +. DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 03 b1 b1 08 08)
0040 +. DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 04 b1 b1 06 06)
0042 +. DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 06 06)
0044 +. DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 80 80 20 20)
0046 +. DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 08 08)
0048 +. DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 20 08)
0050 +. DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 80 80 08 20)
0052 +. DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0f 80 80 06 06)
0054 +. DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 10 80 80 08 08)
0056 +. DC 07-F0 MDP_XRT_FLD_ENA
0057 BC (d8)
0058 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0059 BC (c8)
0060 +. DC 07-F0 MDP_XRT_ARS_DIS
0061 BC (d5)
0062 +. DC 07-F0 MDP_XRT_AEC_RESET
0063 BC (d0)
0064 +. DC 07-F0 MDP_XRT_FLD_RESET
0065 BC (da)
0066 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0067 BC (c4 09)
0068 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0069 BC (c5 0d)
0070 . C. ----- Success Verify ? OK / NG ____
0071 C.
0072 C.
0073 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0074 C.
0075 +. DC 07-F0 MDP_XRT_MODE_OBSV
0076 BC (c2)
0077 +. TI 2018-05-03 11:12:02.0
0078 DC 07-F0 MDP_XRT_MODE_OBSV
0079 BC (c2)
0080 . C. ----- Success Verify ? OK / NG ____
0081 C.
0082 C. ***** XRT END *****
0083 . C. *****
0084 C. SOT table upload
0085 C. *****
0086 . C. < Stop SP table >
0087 +. DC 07-F0 MDP_SP_CTRL_MANU
0088 BC (61)
0089 C. -----
0090 C. MDP_SP_CTRL_MODE = MANU [ ]
0091 C. -----
0092 C.
0093 . C. <Upload SP Observation Table>
0094 . S. RAM ram-287:MDP_OBS_S
0095 ( )
```


May 03, 18 12:12

XRT_OGLIST_0472.chk

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*** OP Sequence for XRT ***

2018/05/03	11:22:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/05/03	11:22:56.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/05/03	11:22:58.0	XRT_FOCUS_POSITION_403_OG [0x193]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2018/05/03	11:23:00.5	AOCS_Ore-point_Start_1_OG [0x097]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2018/05/03	11:23:18.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2018/05/03	11:23:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2018/05/03	11:23:22.0	XRT_AEC_RESET_448_OG [0x1c0]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2018/05/03	11:23:24.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2018/05/03	11:23:26.0	XRT_FLD_RESET_433_OG [0x1b1]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2018/05/03	11:25:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02			
2018/05/03	11:25:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d			
2018/05/03	11:26:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2018/05/03	12:07:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/05/03	12:07:32.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/05/03	12:07:34.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2018/05/03	12:07:36.0	XRT_PREFLR_STRT_406_OG [0x196]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2018/05/03	12:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2018/05/03	12:30:30.0	XRT_Custom_430_OG [0x1ae]						
2018/05/03	12:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2018/05/03	13:46:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/05/03	13:46:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/05/03	13:46:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2018/05/03	13:46:06.0	XRT_PREFLR_STRT_406_OG [0x196]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2018/05/03	13:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2018/05/03	13:59:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/05/03	13:59:56.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/05/03	13:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2018/05/03	14:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	00 ec 32 52 a7			
2018/05/03	14:00:18.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2018/05/03	14:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2018/05/03	14:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2018/05/03	14:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2018/05/03	14:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2018/05/03	14:02:56.0	XRT_QT_PROG_SET_428_OG [0x1ac]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b			
2018/05/03	14:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d			
2018/05/03	14:09:00.0	XRT_Custom_430_OG [0x1ae]						
2018/05/03	14:10:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2018/05/03	15:24:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/05/03	15:24:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/05/03	15:24:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2018/05/03	15:24:06.0	XRT_PREFLR_STRT_406_OG [0x196]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2018/05/03	15:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2018/05/03	16:04:00.0	XRT_Custom_430_OG [0x1ae]						
2018/05/03	16:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2018/05/03	17:02:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/05/03	17:02:32.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/05/03	17:02:34.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			

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2018/05/03	17:02:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/05/03	17:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/05/03	17:41:00.0	XRT_Custom_430_OG [0x1ae]						
2018/05/03	17:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/05/03	18:41:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/05/03	18:41:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/05/03	18:41:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/05/03	18:41:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/05/03	18:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/05/03	19:17:30.0	XRT_Custom_430_OG [0x1ae]						
2018/05/03	19:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/05/03	19:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/05/03	19:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/05/03	19:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2018/05/03	20:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00		
2018/05/03	20:00:18.0	XRT_FLD_DIS_425_OG [0x1a9]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2018/05/03	20:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2018/05/03	20:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2018/05/03	20:02:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12		
2018/05/03	20:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/05/03	20:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/05/03	20:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/05/03	20:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2018/05/03	20:10:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	03 00 00 00 00		
2018/05/03	20:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2018/05/03	20:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2018/05/03	20:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2018/05/03	20:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2018/05/03	20:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/05/03	20:12:56.0	XRT_QT_PROG_SET_418_OG [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c		
2018/05/03	20:12:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2018/05/03	20:54:30.0	XRT_Custom_430_OG [0x1ae]						
2018/05/03	20:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/05/03	21:57:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/05/03	21:57:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/05/03	21:57:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/05/03	21:57:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/05/03	22:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/05/03	22:31:00.0	XRT_Custom_430_OG [0x1ae]						
2018/05/03	22:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/05/03	23:36:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/05/03	23:36:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/05/03	23:36:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/05/03	23:36:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/05/03	23:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/05/04	00:00:01.0	XRT_Custom_430_OG [0x1ae]						
2018/05/04	00:01:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/05/04	01:14:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/05/04	01:14:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		

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2018/05/04	01:14:04.0	XRT_FLD_RESET_415_OG [0x19f]	1 07-F0 da
		MDP_XRT_FLD_RESET	
2018/05/04	01:14:06.0	XRT_PREFLR_STRT_406_OG [0x196]	1 07-F0 e8
		MDP_XRT_PREFLR_STRT	
2018/05/04	01:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1 07-F0 e9
		MDP_XRT_PREFLR_STOP	
2018/05/04	01:37:30.0	XRT_Custom_430_OG [0x1ae]	
2018/05/04	01:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1 07-F0 c0
		MDP_XRT_CTRL_AUTO	
2018/05/04	02:45:00.0	XRT_CTRL_MANU_400_OG [0x190]	1 07-F0 c1
		MDP_XRT_CTRL_MANU	
2018/05/04	02:45:02.0	XRT_CTRL_MANU_402_OG [0x192]	1 07-F0 c1
		MDP_XRT_CTRL_MANU	
2018/05/04	02:45:04.0	XRT_FLD_RESET_415_OG [0x19f]	1 07-F0 da
		MDP_XRT_FLD_RESET	
2018/05/04	02:45:06.0	XRT_PREFLR_STRT_406_OG [0x196]	1 07-F0 e8
		MDP_XRT_PREFLR_STRT	
2018/05/04	02:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1 07-F0 e9
		MDP_XRT_PREFLR_STOP	
2018/05/04	03:16:00.0	XRT_Custom_430_OG [0x1ae]	
2018/05/04	03:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1 07-F0 c0
		MDP_XRT_CTRL_AUTO	
2018/05/04	04:13:00.0	XRT_CTRL_MANU_400_OG [0x190]	1 07-F0 c1
		MDP_XRT_CTRL_MANU	
2018/05/04	04:13:02.0	XRT_CTRL_MANU_402_OG [0x192]	1 07-F0 c1
		MDP_XRT_CTRL_MANU	
2018/05/04	04:13:04.0	XRT_FLD_RESET_415_OG [0x19f]	1 07-F0 da
		MDP_XRT_FLD_RESET	
2018/05/04	04:13:06.0	XRT_PREFLR_STRT_406_OG [0x196]	1 07-F0 e8
		MDP_XRT_PREFLR_STRT	
2018/05/04	04:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1 07-F0 e9
		MDP_XRT_PREFLR_STOP	
2018/05/04	04:54:30.0	XRT_Custom_430_OG [0x1ae]	
2018/05/04	04:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1 07-F0 c0
		MDP_XRT_CTRL_AUTO	
2018/05/04	05:53:00.0	XRT_CTRL_MANU_400_OG [0x190]	1 07-F0 c1
		MDP_XRT_CTRL_MANU	
2018/05/04	05:53:02.0	XRT_CTRL_MANU_402_OG [0x192]	1 07-F0 c1
		MDP_XRT_CTRL_MANU	
2018/05/04	05:53:04.0	XRT_FLD_RESET_415_OG [0x19f]	1 07-F0 da
		MDP_XRT_FLD_RESET	
2018/05/04	05:53:06.0	XRT_PREFLR_STRT_406_OG [0x196]	1 07-F0 e8
		MDP_XRT_PREFLR_STRT	
2018/05/04	05:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1 07-F0 e9
		MDP_XRT_PREFLR_STOP	
2018/05/04	06:32:24.0	XRT_CTRL_MANU_402_OG [0x192]	1 07-F0 c1
		MDP_XRT_CTRL_MANU	
2018/05/04	06:32:26.0	XRT_CTRL_MANU_402_OG [0x192]	1 07-F0 c1
		MDP_XRT_CTRL_MANU	
2018/05/04	06:32:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	4 07-F8 22 ff aa 00
		XRT_FOCUS_POSITION	
2018/05/04	06:32:30.0	AOCs_Or-e-point_Start_1_OG [0x097]	5 02-76 00 00 00 00 00
		AOCU_NM	
2018/05/04	06:32:48.0	XRT_FLD_DIS_401_OG [0x191]	1 07-F0 d9
		MDP_XRT_FLD_DIS	
2018/05/04	06:32:50.0	XRT_FLRCTRL_DIS_405_OG [0x195]	1 07-F0 c9
		MDP_XRT_FLRCTRL_DIS	
2018/05/04	06:32:52.0	XRT_ARS_DIS_422_OG [0x1a6]	1 07-F0 d5
		MDP_XRT_ARS_DIS	
2018/05/04	06:35:28.0	XRT_QT_PROG_SET_443_OG [0x1bb]	2 07-F0 c4 08
		MDP_XRT_QT_PROG_SET	
2018/05/04	06:35:30.0	XRT_CTRL_AUTO_408_OG [0x198]	1 07-F0 c0
		MDP_XRT_CTRL_AUTO	
2018/05/04	06:42:24.0	XRT_CTRL_MANU_402_OG [0x192]	1 07-F0 c1
		MDP_XRT_CTRL_MANU	
2018/05/04	06:42:26.0	XRT_CTRL_MANU_402_OG [0x192]	1 07-F0 c1
		MDP_XRT_CTRL_MANU	
2018/05/04	06:42:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	4 07-F8 22 ff aa 00
		XRT_FOCUS_POSITION	
2018/05/04	06:42:30.0	AOCs_Or-e-point_Start_4_OG [0x09a]	5 02-76 00 00 00 28 00
		AOCU_NM	
2018/05/04	06:42:48.0	XRT_FLD_ENA_411_OG [0x19b]	1 07-F0 d8
		MDP_XRT_FLD_ENA	
2018/05/04	06:42:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	1 07-F0 c8
		MDP_XRT_FLRCTRL_ENA	
2018/05/04	06:42:52.0	XRT_AEC_RESET_448_OG [0x1c0]	1 07-F0 d0
		MDP_XRT_AEC_RESET	
2018/05/04	06:42:54.0	XRT_ARS_DIS_423_OG [0x1a7]	1 07-F0 d5
		MDP_XRT_ARS_DIS	
2018/05/04	06:42:56.0	XRT_FLD_RESET_433_OG [0x1b1]	1 07-F0 da
		MDP_XRT_FLD_RESET	
2018/05/04	06:45:26.0	XRT_QT_PROG_SET_437_OG [0x1b5]	2 07-F0 c4 02
		MDP_XRT_QT_PROG_SET	
2018/05/04	06:45:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]	2 07-F0 c5 0d
		MDP_XRT_FL_PROG_SET	
2018/05/04	06:45:30.0	XRT_CTRL_AUTO_408_OG [0x198]	1 07-F0 c0
		MDP_XRT_CTRL_AUTO	
2018/05/04	07:33:30.0	XRT_CTRL_MANU_400_OG [0x190]	1 07-F0 c1
		MDP_XRT_CTRL_MANU	
2018/05/04	07:33:32.0	XRT_CTRL_MANU_402_OG [0x192]	1 07-F0 c1
		MDP_XRT_CTRL_MANU	
2018/05/04	07:33:34.0	XRT_FLD_RESET_415_OG [0x19f]	1 07-F0 da
		MDP_XRT_FLD_RESET	

2018/05/04	07:33:36.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/05/04	07:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/05/04	08:11:00.0	XRT_Custom_430_OG [0x1ae]							
2018/05/04	08:12:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/05/04	09:13:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/04	09:13:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/04	09:13:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/05/04	09:13:06.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/05/04	09:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/05/04	09:49:30.0	XRT_Custom_430_OG [0x1ae]							
2018/05/04	09:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/05/04	10:55:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/04	10:55:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/04	10:55:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/05/04	10:55:06.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/05/04	10:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/05/04	10:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/04	10:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/04	10:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2018/05/04	11:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2018/05/04	11:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2018/05/04	11:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2018/05/04	11:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2018/05/04	11:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/05/04	11:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/05/04	11:02:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02				
2018/05/04	11:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2018/05/04	11:28:00.0	XRT_Custom_430_OG [0x1ae]							
2018/05/04	11:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/05/04	12:42:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/04	12:42:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/04	12:42:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/05/04	12:42:36.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/05/04	12:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/05/04	13:06:00.0	XRT_Custom_430_OG [0x1ae]							
2018/05/04	13:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/05/04	13:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/04	13:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/04	13:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2018/05/04	14:00:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2018/05/04	14:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2018/05/04	14:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2018/05/04	14:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2018/05/04	14:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/05/04	14:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/05/04	14:02:56.0	XRT_QT_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2018/05/04	14:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2018/05/04	14:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/05/04	14:21:00.0	XRT_CTRL_MANU_400_OG [0x190]							

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2018/05/04	14:21:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/04	14:21:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/04	14:21:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da				
2018/05/04	14:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/05/04	14:50:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/05/04	14:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/05/04	15:59:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/04	15:59:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/04	15:59:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/04	15:59:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da				
2018/05/04	16:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/05/04	16:39:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/05/04	16:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/05/04	17:37:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/04	17:37:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/04	17:37:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/04	17:37:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da				
2018/05/04	17:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/05/04	18:15:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/05/04	18:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/05/04	19:16:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/04	19:16:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/04	19:16:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/04	19:16:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da				
2018/05/04	19:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/05/04	19:52:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/05/04	19:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/05/04	19:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/04	19:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/04	19:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/04	20:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2018/05/04	20:00:18.0	XRT_FLD_DIS_401_OG [0x191]	AOCU_NM	5	02-76	00 00 00 00 00				
2018/05/04	20:00:20.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2018/05/04	20:00:22.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2018/05/04	20:02:58.0	XRT_QT_PROG_SET_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/05/04	20:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2018/05/04	20:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/05/04	20:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/04	20:09:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/04	20:10:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2018/05/04	20:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 00 00 28 00				
2018/05/04	20:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2018/05/04	20:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2018/05/04	20:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2018/05/04	20:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/05/04	20:12:56.0	XRT_QT_PROG_SET_413_OG [0x19d]	MDP_XRT_FLD_RESET	1	07-F0	da				
2018/05/04	20:12:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2018/05/04	20:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				

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2018/05/04	20:54:30.5	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/04	20:54:32.5	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/04	20:54:34.5	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/05/04	20:54:36.5	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/05/04	20:57:44.5	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/05/04	21:29:30.0	XRT_Custom_430_OG [0x1ae]							
2018/05/04	21:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/05/04	22:32:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/04	22:32:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/04	22:32:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/05/04	22:32:36.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/05/04	22:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/05/04	23:05:00.0	XRT_Custom_430_OG [0x1ae]							
2018/05/04	23:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/05/05	00:11:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/05	00:11:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/05	00:11:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/05/05	00:11:06.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/05/05	00:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/05/05	00:34:30.0	XRT_Custom_430_OG [0x1ae]							
2018/05/05	00:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/05/05	01:46:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/05	01:46:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/05	01:46:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/05/05	01:46:06.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/05/05	01:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/05/05	02:13:00.0	XRT_Custom_430_OG [0x1ae]							
2018/05/05	02:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/05/05	03:19:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/05	03:19:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/05	03:19:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/05/05	03:19:36.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/05/05	03:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/05/05	03:51:30.0	XRT_Custom_430_OG [0x1ae]							
2018/05/05	03:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/05/05	04:49:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/05	04:49:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/05	04:49:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/05/05	04:49:06.0	XRT_PREFLR_STRT_406_OG [0x196]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/05/05	04:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/05/05	05:30:00.0	XRT_Custom_430_OG [0x1ae]							
2018/05/05	05:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/05/05	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/05	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/05/05	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2018/05/05	06:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2018/05/05	06:00:18.0	XRT_FLD_DIS_401_OG [0x191]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2018/05/05	06:00:20.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2018/05/05	06:00:22.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/05/05	06:02:58.0	XRT_QT_PROG_SET_443_OG [0x1bb]							

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2018/05/05	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	08
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/05/05	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/05/05	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/05/05	06:09:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2018/05/05	06:10:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 00 00 28	00
2018/05/05	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2018/05/05	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2018/05/05	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2018/05/05	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/05/05	06:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/05/05	06:12:56.0	XRT_QT_PROG_SET_413_OG [0x19d]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
2018/05/05	06:12:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2018/05/05	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/05/05	06:29:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/05/05	06:29:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/05/05	06:29:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/05/05	06:29:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/05/05	06:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/05/05	07:08:00.0	XRT_Custom_430_OG [0x1ae]					
2018/05/05	07:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/05/05	08:09:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/05/05	08:09:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/05/05	08:09:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/05/05	08:09:36.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/05/05	08:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/05/05	08:46:30.0	XRT_Custom_430_OG [0x1ae]					
2018/05/05	08:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/05/05	09:49:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/05/05	09:49:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/05/05	09:49:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/05/05	09:49:06.0	XRT_PREFLR_STRT_406_OG [0x196]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/05/05	09:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/05/05	10:24:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00	00