

PROG= 07 1-time(s)												
└ Subr= 1 1-time(s) 12.0sec												
└ Seqn= 8 1-time(s) 12.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #15AB: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant

Term	Pointing (x, y)	Comment
08/29 18:43:00 - 08/30 05:45:24	Fixed (-528.4, 528.4)	* 4 of 4 -- End of four-quadrant pointing.
PROG= 19 1-time(s)		
└ Subr= 1 1-time(s) 12.0sec		
└ Seqn= 40 1-time(s) 12.0sec		
Open/G-band	Open/G-band	open Safe Norm 44ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec
Open/G-band	Open/G-band	open Safe Norm 44ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec
Open/thick-Be	Open/thick-Be	close Safe Dark 44ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec
Open/thick-Be	Open/thick-Be	close Safe Dark 44ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #15A4: Synoptic Q95 2x2 - Al/mesh(512/5795) + Dark cal(512 Q98) + Ti-poly(723/11571) + G-band(16)

Term	Pointing (x, y)	Comment
08/30 05:47:30 - 08/30 05:55:24	Fixed (0.0, 0.0)	synoptic, shifted -14.5 min
08/31 06:02:00 - 08/31 06:09:54	Fixed (0.0, 0.0)	synoptic
PROG= 12 1-time(s)		
└ Subr= 1 1-time(s) 12.0sec		
└ Seqn= 87 1-time(s) 4.0sec		
Open/Al-mesh	Open/Ti-poly	close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Ti-poly	close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└ Seqn= 72 1-time(s) 2.0sec		
Open/Al-mesh	Open/Al-mesh	close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└ Seqn= 76 1-time(s) 4.0sec		
Open/Ti-poly	Open/thick-Al	close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└ Seqn= 92 1-time(s) 2.0sec		
Open/G-band	Open/G-band	open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

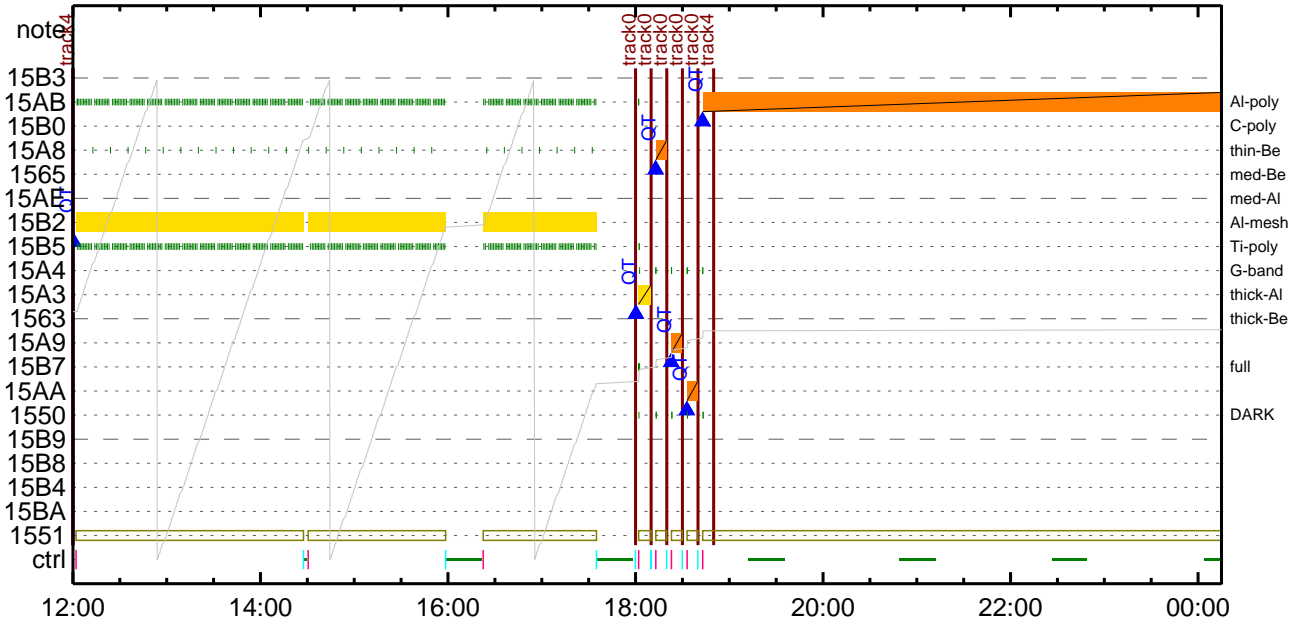
```

* * * * *           Flare mode           * * * * *
                                NOT USED

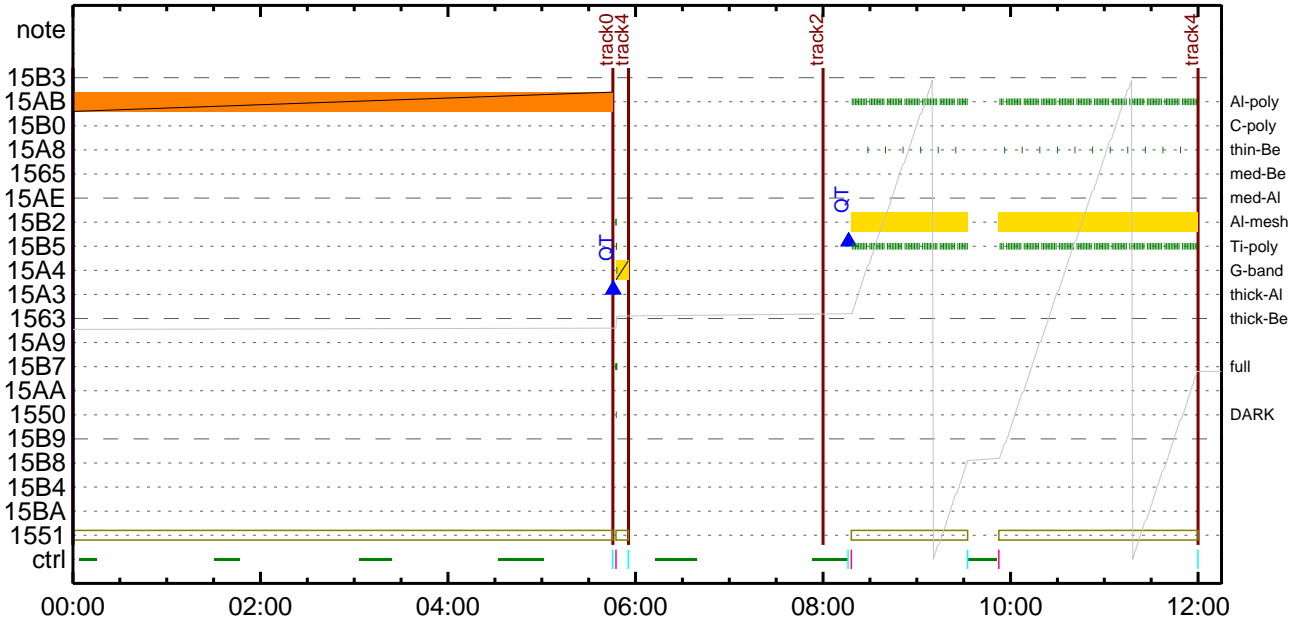
* * * * *           Active Region Search   * * * * *
                                NOT USED

* * * * *           Flare Detection       * * * * *
                                NOT USED
    
```

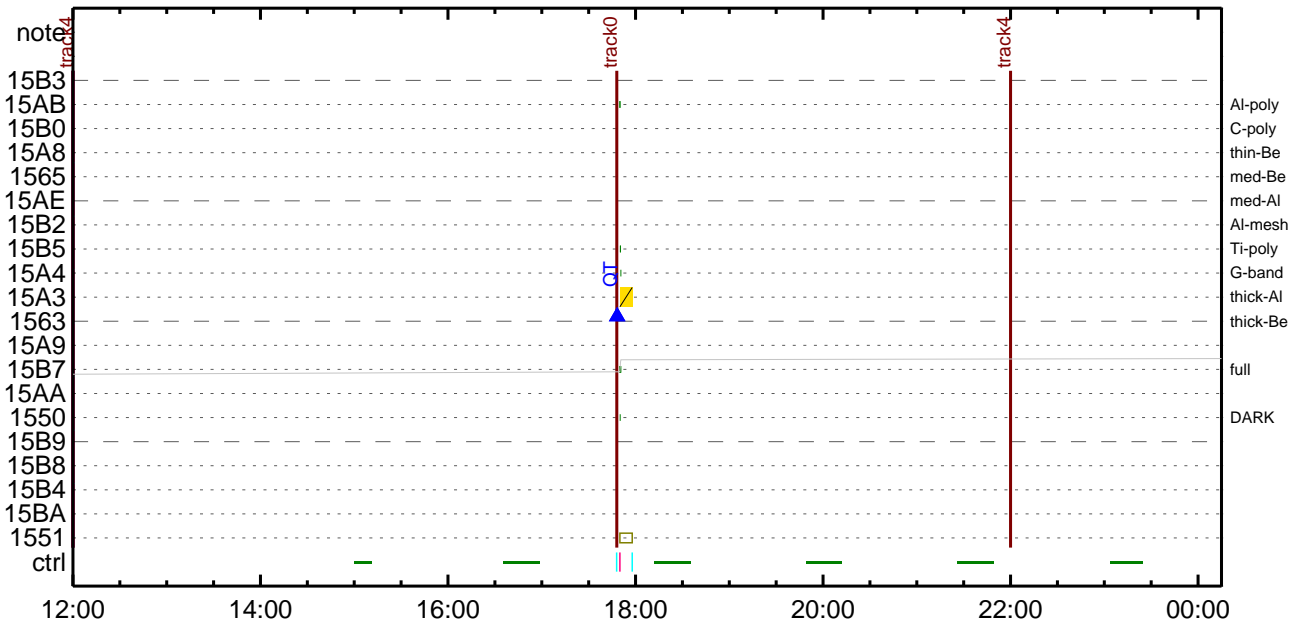

CMDI #0106 2008/08/29



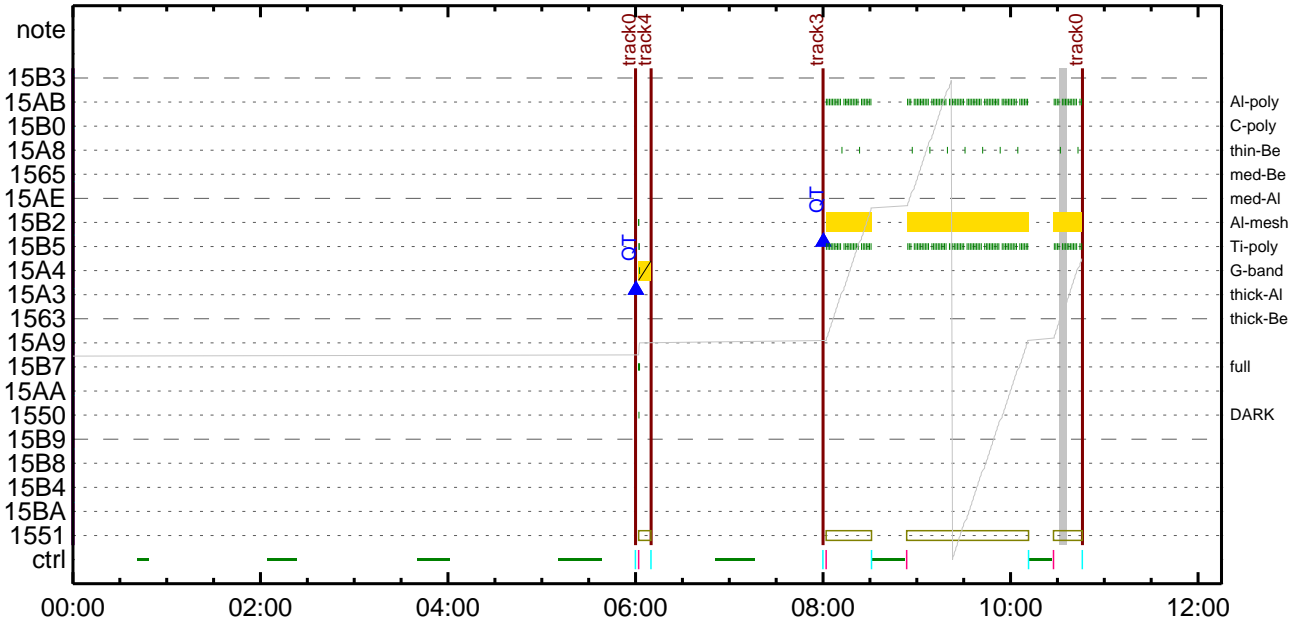
CMDI #0106 2008/08/30



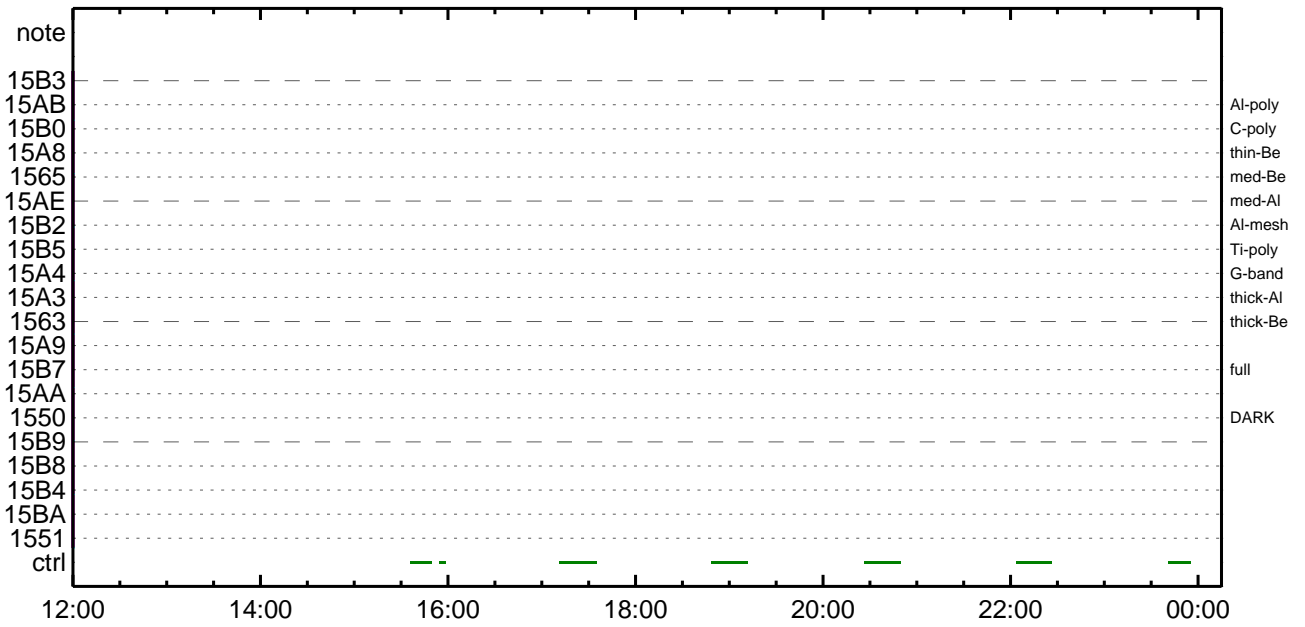
CMDI #0106 2008/08/30



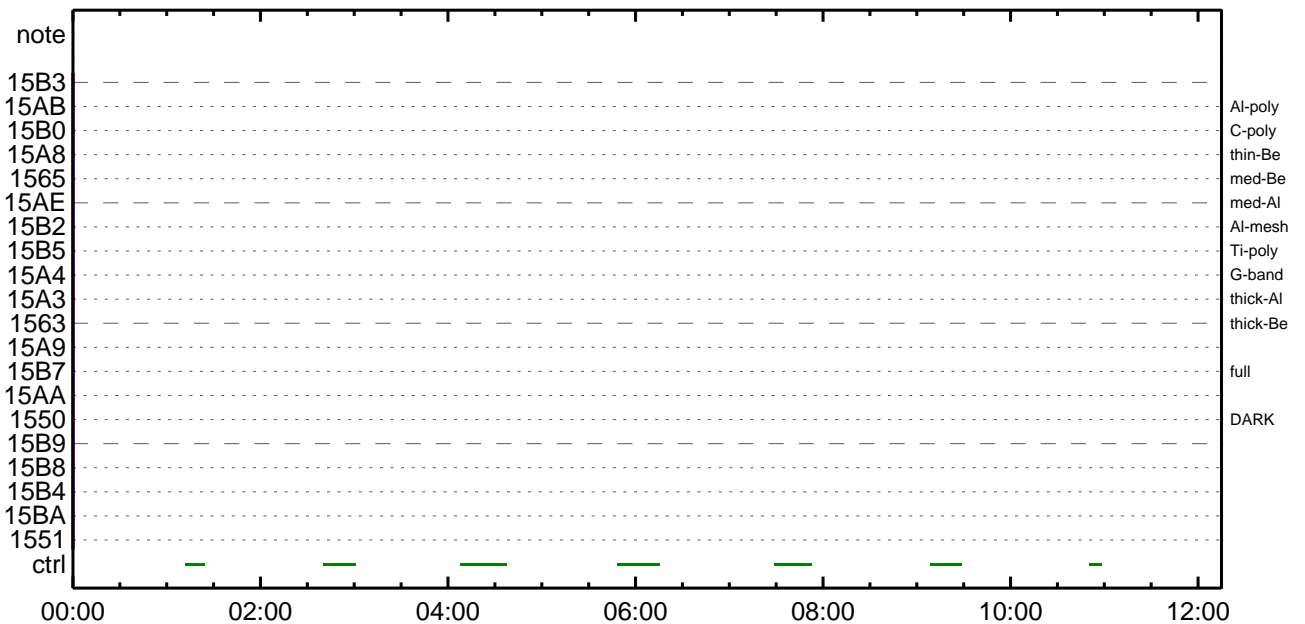
CMDI #0106 2008/08/31



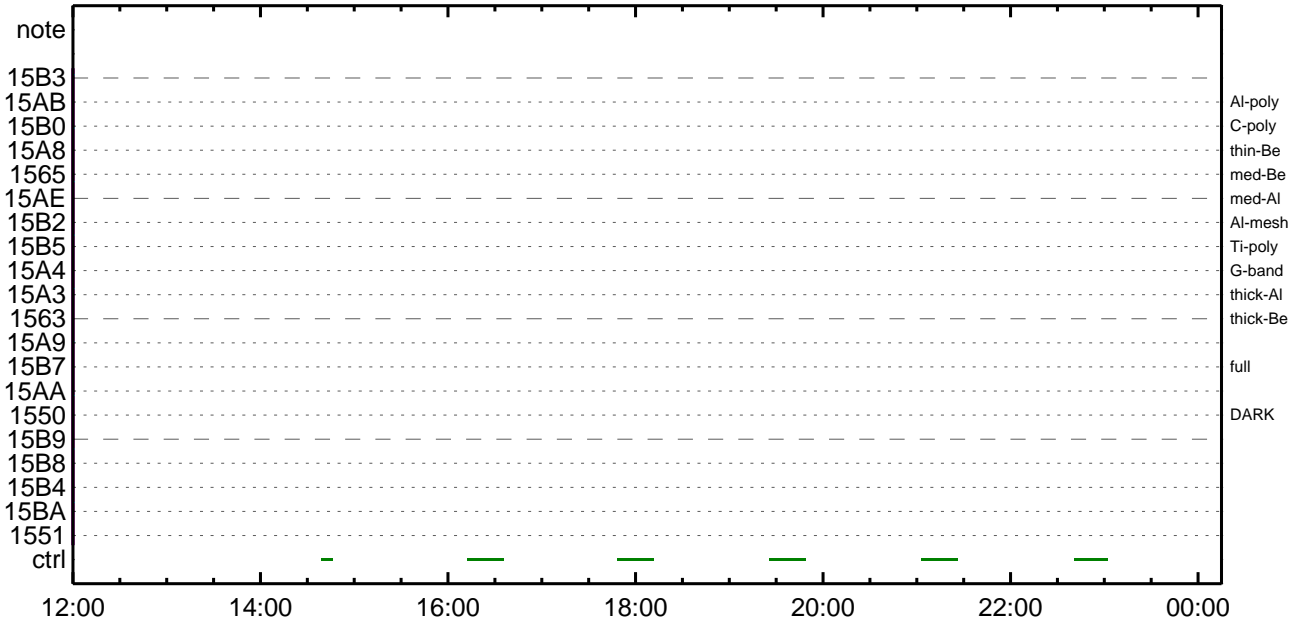
CMDI #0106 2008/08/31



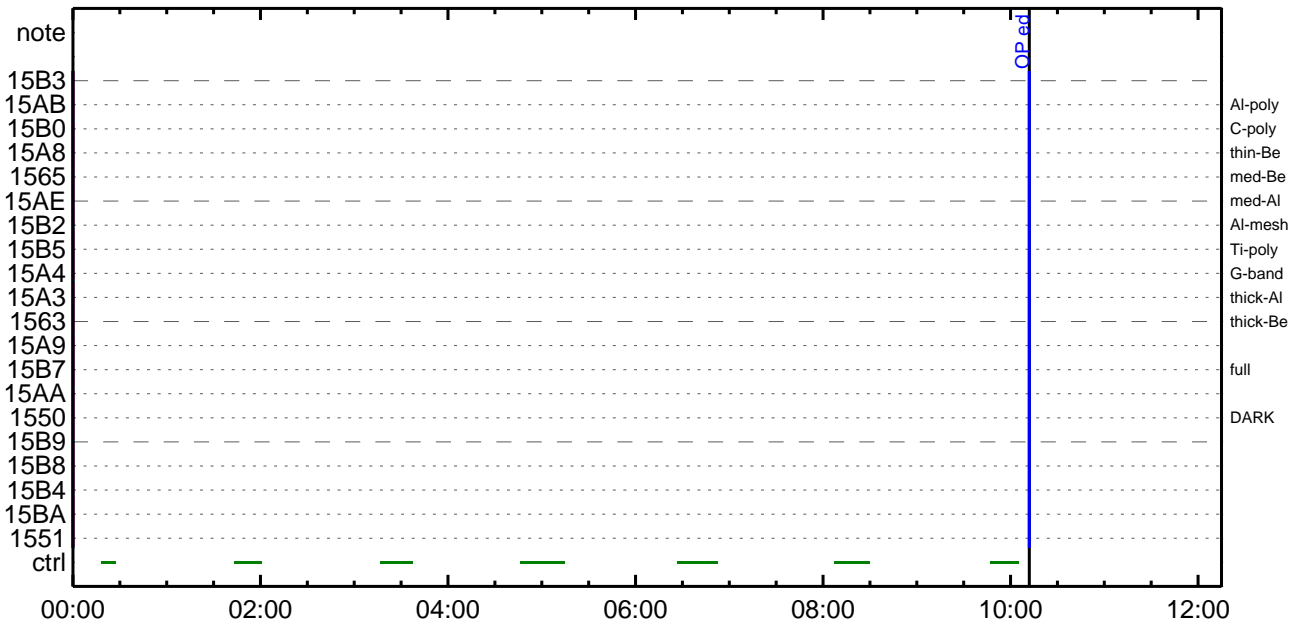
CMDI #0106 2008/09/01



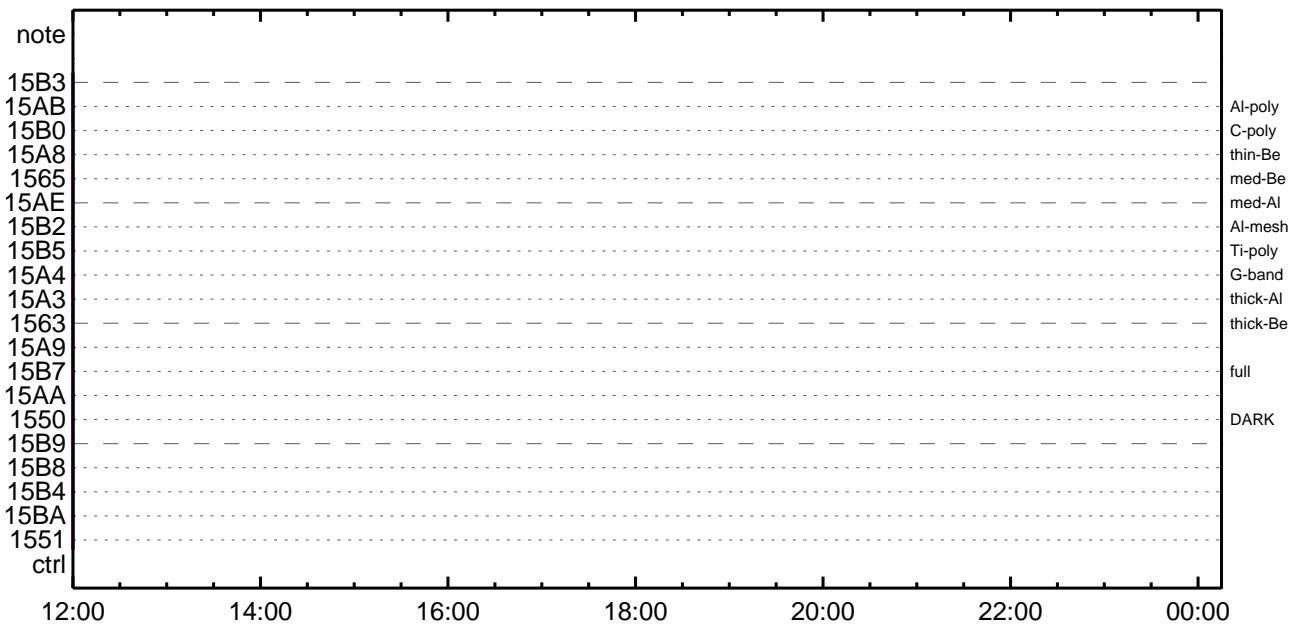
CMDI #0106 2008/09/01



CMDI #0106 2008/09/02



CMDI #0106 2008/09/02




```

0194 C.
0195 +. TI 2008-08-28 10:21:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          꺾꺾[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0198 C.
0199 C.  °Ê²¼ªîÄë%îîñªîŷÄŷ§ŷÄŷ¹àîü
0200 C.          꺾꺾[HK1_TI_CMD_ENA/DIS]          EQ          ENA
0201 C.          꺾꺾[HK1_TI_CMD_NUM]          EQ          4
0202 C.          꺾꺾[HK1_NEXT_EXEC_PIM]          EQ          DHU
0203 C.          꺾꺾[HK1_NEXT_EXEC_DC]          EQ          0xB3
0204 C.
0205 C.  *****
0206 C.  TIîî°èŷÄŷÖŷ×
0207 C.  *****
0208 C.
0209 C.  TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC          (03 ab 03 01 02)
0212 C.          꺾꺾[HK1_DMP_TOP_ADRS_1]          EQ          07
0213 C.          꺾꺾[HK1_DMP_TOP_ADRS_0]          EQ          2B
0214 C.          꺾꺾[HK1_DMP_BLOCK_NUM]          EQ          3
0215 C.          꺾꺾[HK1_DMP_REPEAT_NUM]          EQ          0
0216 C.          꺾꺾[HK1_DMA_DMP_PIM]          EQ          DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC          (07 0b f8)
0219 C.          꺾꺾[HK1_PKT_FORM_NO]          EQ          7
0220 C.          꺾꺾[HK1_PKT_GEN_TIME]          EQ          0.25 s
0221 C.          꺾꺾[HK1_S_TLM_BIT_RATE]          EQ          32k
0222 C.          꺾꺾[HK1_X_TLM_BIT_RATE]          EQ          4M
0223 C.          꺾꺾[HK1_DMP_CHK_FLG]          EQ          EXEC
0224 C.
0225 C.  ŷÄŷÖŷ×½ªî»ªò³îÇ§
0226 C.          꺾꺾[HK1_DMP_CHK_FLG]          EQ          NON
0227 C.
0228 C.  RAM ID=TI_TBLªîî½È¹Ç·è²îOKªò³îÇ§
0229 C.
0230 C.  DHUŷªî;¼ŷÈ;È¼ŷ½. ŷî;¼ŷÈ;Èªòîªª¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC          (02 0a f8)
0233 C.          꺾꺾[HK1_PKT_FORM_NO]          EQ          2
0234 C.          꺾꺾[HK1_PKT_GEN_TIME]          EQ          0.5S
0235 C.          꺾꺾[HK1_S_TLM_BIT_RATE]          EQ          32K
0236 C.          꺾꺾[HK1_X_TLM_BIT_RATE]          EQ          4M
0237 C.
0238 C.  *****
0239 C.  SOT TI command set
0240 C.  *****
0241 C.  Execute, after the success of OP upload.
0242 +. TI 2008-08-28 10:21:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC          (41)
0245 C.  -----
0246 C.  HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C.  -----
0248 C.  ***** SOT END *****
0249 C.  Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C.  ***** Start EIS operation (TI set) *****
0253 C.  Execute, after the success of OP upload.
0254 C.  Set EIS TI-commands
0255 +. TI 2008-08-28 10:21:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC          (21 02)
0258 +. TI 2008-08-28 10:21:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC          (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]          EQ          2 COUNTUP
0262 C.  ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C.  ***** XRT START *****
0267 C.  Execute, after the success of OP upload.
0268 +. TI 2008-08-28 10:21:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC          (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]          EQ          1COUNTUP
0272 C.
0273 C.  ***** XRT END *****
0274 C.
0275 C.  ***** MDP ´ûÄîªî»ö¼ŷªîÈÄªª¹ªèDCBC·x²è *****
0276 C.  (¼ªªîîŷÄŷÈŷŷŷÄŷÇŷèªÈ¼ªªªÄ»Ûªªè)
0277 C.  DC-BC dcbc-402:DCBC
0278 C.  (MDP_known_event)
0279 C.
0280 C.
0281 C.  ***** ŷÄŷ¹·î Daily±çîîñªîÈ´ªª¹ªèDCBC·x²è *****
0282 C.  DC-BC dcbc-153:DCBC
0283 C.  (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C.  ;ãLOSŷÄŷ§ŷÄŷ¹¼Ä»Û;ã
0287 C.
0288 C.  ***** LOS *****
0289 C.

```



```

0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_MANU
0131 BC (21 02)
0132 . C. Verify EIS in MANUAL mode
0133 . C. Estimated OBSTBL upload time is 16s
0134 C. *****
0135 C. EIS START OBSTBL LOAD
0136 C. *****
0137 . S. RAM ram-820:EIS_OBSTBL
0138 ( )
0139 +. DC 07-FC EIS_DUMP_OBSTBL
0140 BC (07 07 07 00 00 70 00)
0141 C.
0142 C. Execute, after the success of OBSTBL upload.
0143 C. Set EIS TI-commands
0144 +. TI 2008-08-28 10:21:50.0
0145 DC 07-FC EIS_MODE_CHG_ENA
0146 BC (20)
0147 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0148 C. *****
0149 C. EIS END OBSTBL LOAD
0150 C. *****
0151 C. *****
0152 C. * AOCs EÛ%È/Eù±çÈ%ÄêAUT0ÄSÄê *
0153 C. *****
0154 C.
0155 . C. ***** EÛ%ÈEù±çÈ%ÄêöAUT0ÄEÖ¹ë *****
0156 +. DC 02-50 AOCU_N/D_AUTO
0157 BC (1a)
0158 . C. çç <ORB>[S/W TIMER JDG] EQ AUTO
0159 C.
0160 C.
0161 C.
0162 C. ***** End of AOCs Special CMD *****
0163 . C.
0164 C.
0165 C. ***** MDP `ûÃîï»ö%ýöEÄð¹èDCBC•x²è *****
0166 C. (%ä°îÿÓYÄÿEÿPÿEÿÄÿçÿèöE%¼ö¼Ä»Û¹ë)
0167 . S. DC-BC dcbc-402:DCBC
0168 (MDP_known_event)
0169 C.
0170 C.
0171 . C. ***** ¥Dÿ¹•İ Daily±¿ÍñöÉ´Ø¹èDCBC•x²è *****
0172 . S. DC-BC dcbc-153:DCBC
0173 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0174 C.
0175 C.
0176 . C. ;ãLOS¥Á¥$ÿÄÿ-¼Ä»Û;ä
0177 C.
0178 . C. ***** LOS *****
0179 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-100 2008-08-28 12:55:00 179 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿òÁø•µ°È»Í×ÁÇøÍYçYÁY×Yí;¼YÉ;ÈÈè%µ•íÉ;ÈÈÈ¼°ÇÓø•ø¿¼í¹çøÍ;çÀ®, ùø¹øÈøÈøÇÁ+¿®ø•øÈøøø³øÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-266:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Resume FG table (auto mode) >
0036 +. DC 07-F0 MDP_FG_CTRL_AUTO
0037 BC (50)
0038 . C. -----
0039 C. MDP_FG_CTRL_MODE = AUTO [ ]
0040 C. -----
0041 C.
0042 C. *****
0043 C. SOT TI command set
0044 C. *****
0045 C. Execute, after the success of TBL upload.
0046 +. TI 2008-08-28 10:21:18.0
0047 DC 07-F0 MDP_SOT_MODE_OBSV
0048 BC (40)
0049 . C. -----
0050 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0051 C. -----
0052 C.
0053 C. Only when FG_CTRL_AUTO is used in RT.
0054 +. TI 2008-08-28 10:21:20.0
0055 DC 07-F0 MDP_FG_CTRL_AUTO
0056 BC (50)
0057 . C. -----
0058 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0059 C. -----
0060 C. ***** SOT END *****
0061 C.
0062 C. ***** XRT START *****
0063 C.
0064 +. DC 07-F0 MDP_XRT_CTRL_MANU
0065 BC (c1)
0066 + DC 07-F0 MDP_XRT_MODE_STBY
0067 BC (c3)
0068 . C. ----- Success Verify ? OK / NG_____
0069 C.
0070 C. XRT Obs. Table Upload
0071 . S. RAM ram-291:MDP_OBS_X
0072 ( )
0073 C.
0074 +. DC 07-F0 MDP_DUMP_XRTTBL
0075 BC (84 07 00 00 00 3a d4)
0076 . C. ----- Comparison Check ? OK / ERR _____
0077 C.
0078 C.
0079 +. DC 07-F0 MDP_XRT_ROI_SET
0080 BC (cd 01 b1 b1 04 04)
0081 + DC 07-F0 MDP_XRT_ROI_SET
0082 BC (cd 02 b1 b1 08 08)
0083 + DC 07-F0 MDP_XRT_ROI_SET
0084 BC (cd 03 b1 b1 08 08)
0085 + DC 07-F0 MDP_XRT_ROI_SET
0086 BC (cd 04 b1 b1 06 06)
0087 + DC 07-F0 MDP_XRT_ROI_SET
0088 BC (cd 06 80 80 20 04)
0089 + DC 07-F0 MDP_XRT_ROI_SET
0090 BC (cd 07 80 80 06 06)
0091 + DC 07-F0 MDP_XRT_ROI_SET
0092 BC (cd 08 80 80 20 20)
0093 + DC 07-F0 MDP_XRT_ROI_SET
0094 BC (cd 09 c0 c0 10 10)
0095 + DC 07-F0 MDP_XRT_ROI_SET
```

```
0096 BC (cd 0a 40 c0 10 10)
0097 + DC 07-F0 MDP_XRT_ROI_SET
0098 BC (cd 0b 40 40 10 10)
0099 + DC 07-F0 MDP_XRT_ROI_SET
0100 BC (cd 0c c0 40 10 10)
0101 + DC 07-F0 MDP_XRT_ROI_SET
0102 BC (cd 0f 80 80 06 06)
0103 + DC 07-F0 MDP_XRT_ROI_SET
0104 BC (cd 10 80 80 04 04)
0105 + DC 07-F0 MDP_XRT_QT_PROG_SET
0106 BC (c4 0a)
0107 + DC 07-F0 MDP_XRT_ARS_DIS
0108 BC (d5)
0109 + DC 07-F0 MDP_XRT_FLD_DIS
0110 BC (d9)
0111 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0112 BC (c9)
0113 . C. ----- Success Verify ? OK / NG ____
0114 C.
0115 C.
0116 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0117 C.
0118 + DC 07-F0 MDP_XRT_MODE_OBSV
0119 BC (c2)
0120 + TI 2008-08-28 10:21:02.0
0121 DC 07-F0 MDP_XRT_MODE_OBSV
0122 BC (c2)
0123 + TI 2008-08-28 10:21:04.0
0124 DC 07-F0 MDP_XRT_CTRL_AUTO
0125 BC (c0)
0126 . C. ----- Success Verify ? OK / NG ____
0127 C.
0128 C. ***** XRT END *****
0129 C. *****
0130 C. START of XRT_CCD_HEATER_ON operation
0131 C. *****
0132 C.
0133 + DC 07-F0 MDP_XRT_CTRL_MANU
0134 BC (c1)
0135 C. ----- Success Verify ? OK / NG;
0136 C.
0137 + DC 04-BC TCIB_XRT_S_HTR_A_ENA
0138 C. ----- Success Verify ? OK / NG;
0139 C.
0140 C. -----
0141 C. If anomalous situation appeared, execute TCIB_XRT_S_HTR_A_DIS using DCBC-442 (line 24)
0142 C. -----
0143 C.
0144 + DC 07-F0 MDP_XRT_QT_PROG_SET
0145 BC (c4 0a)
0146 + DC 07-F0 MDP_XRT_FLD_DIS
0147 BC (d9)
0148 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0149 BC (c9)
0150 + DC 07-F0 MDP_XRT_ARS_DIS
0151 BC (d5)
0152 C. ----- Success Verify ? OK / NG ____
0153 C.
0154 C.
0155 C. All OK? Yes--> Please Proceed. / No --> Stop here.
0156 C.
0157 + DC 07-F0 MDP_XRT_CTRL_AUTO
0158 BC (c0)
0159 C. *****
0160 C. END of XRT_CCD_HEATER_ON operation
0161 C. *****
0162 C.
0163 C.
0164 C.
0165 . C. ***** MDP `uãîï»ö%ÝðËÄð¹ñèDCBC•x²è *****
0166 C. (%ã°îÿÓYÁYBYBYEYáYçYèè%¼ð¼Á»Û¹è)
0167 . S. DC-BC dcbc-402:DCBC
0168 (MDP_known_event)
0169 C.
0170 C.
0171 . C. ***** ¥D¥¹·İ Daily±zİÑðË'Ø¹ñèDCBC•x²è *****
0172 . S. DC-BC dcbc-153:DCBC
0173 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0174 C.
0175 C.
0176 . C. ;ãLOS¥Á¥$¥Ã¥¹¼Á»Û;ã
0177 C.
0178 . C. ***** LOS *****
0179 C.
```

Aug 28, 08 12:55

XRT_OGLIST_0106.chk

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*** OP Sequence for XRT ***

```

2008/08/28 14:00:00.0 XRT_CTRL_MANU_428_OG [0x1ac]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2008/08/28 14:00:02.0 XRT_QT_PROG_SET_420_OG [0x1a4]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 10
2008/08/28 14:00:04.0 XRT_ARS_DIS_422_OG [0x1a6]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2008/08/28 14:00:06.0 XRT_FLD_DIS_445_OG [0x1bd]
                        MDP_XRT_FLD_DIS 1 07-F0 d9
2008/08/28 14:00:08.0 XRT_FLRCTRL_DIS_416_OG [0x1a0]
                        MDP_XRT_FLRCTRL_DIS 1 07-F0 c9
2008/08/28 14:00:10.0 XRT_CTRL_AUTO_403_OG [0x193]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2008/08/29 04:00:00.0 XRT_CTRL_MANU_428_OG [0x1ac]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2008/08/29 04:00:02.0 XRT_TCIB_XRT_S_HTR_A_DIS_433_OG [0x1b1]
                        TCIB_XRT_S_HTR_A_DIS 0 04-C0
2008/08/29 04:10:00.0 XRT_CTRL_MANU_428_OG [0x1ac]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2008/08/29 08:00:00.0 AOCs_Ore-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 01 00 00 00 00
2008/08/29 11:59:54.0 XRT_CTRL_MANU_428_OG [0x1ac]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2008/08/29 11:59:56.0 XRT_FOCUS_POSITION_441_OG [0x1b9]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2008/08/29 12:00:00.0 AOCs_Ore-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 04 00 00 00 00
2008/08/29 12:00:16.0 XRT_QT_PROG_SET_411_OG [0x19b]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0e
2008/08/29 12:01:54.0 XRT_ARS_DIS_422_OG [0x1a6]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2008/08/29 12:01:56.0 XRT_FLD_DIS_445_OG [0x1bd]
                        MDP_XRT_FLD_DIS 1 07-F0 d9
2008/08/29 12:01:58.0 XRT_FLRCTRL_DIS_416_OG [0x1a0]
                        MDP_XRT_FLRCTRL_DIS 1 07-F0 c9
2008/08/29 12:02:00.0 XRT_CTRL_AUTO_403_OG [0x193]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2008/08/29 14:27:30.0 XRT_CTRL_MANU_435_OG [0x1b3]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2008/08/29 14:29:30.0 XRT_Custom_430_OG [0x1ae]
2008/08/29 14:30:30.0 XRT_CTRL_AUTO_432_OG [0x1b0]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2008/08/29 15:58:30.0 XRT_CTRL_MANU_435_OG [0x1b3]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2008/08/29 16:21:30.0 XRT_Custom_430_OG [0x1ae]
2008/08/29 16:22:30.0 XRT_CTRL_AUTO_432_OG [0x1b0]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2008/08/29 17:35:00.0 XRT_CTRL_MANU_435_OG [0x1b3]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2008/08/29 17:59:54.0 XRT_CTRL_MANU_428_OG [0x1ac]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2008/08/29 17:59:56.0 XRT_FOCUS_POSITION_442_OG [0x1ba]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2008/08/29 18:00:00.0 AOCs_Ore-point_Start_3_OG [0x099]
                        AOCU_NM 5 02-76 00 00 00 00 00
2008/08/29 18:00:16.0 XRT_QT_PROG_SET_409_OG [0x199]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0b
2008/08/29 18:00:18.0 XRT_FLD_DIS_419_OG [0x1a3]
                        MDP_XRT_FLD_DIS 1 07-F0 d9
2008/08/29 18:00:20.0 XRT_FLRCTRL_DIS_447_OG [0x1bf]
                        MDP_XRT_FLRCTRL_DIS 1 07-F0 c9
2008/08/29 18:00:22.0 XRT_ARS_DIS_427_OG [0x1ab]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2008/08/29 18:02:00.0 XRT_CTRL_AUTO_444_OG [0x1bc]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2008/08/29 18:09:54.0 XRT_CTRL_MANU_428_OG [0x1ac]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2008/08/29 18:09:54.5 XRT_CTRL_MANU_405_OG [0x195]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2008/08/29 18:10:00.0 AOCs_Ore-point_Start_4_OG [0x09a]
                        AOCU_NM 5 02-76 00 2e f9 2e f9
2008/08/29 18:12:32.5 XRT_FOCUS_POSITION_442_OG [0x1ba]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2008/08/29 18:12:52.5 XRT_QT_PROG_SET_423_OG [0x1a7]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 11
2008/08/29 18:12:54.5 XRT_FLD_DIS_419_OG [0x1a3]
                        MDP_XRT_FLD_DIS 1 07-F0 d9
2008/08/29 18:12:56.5 XRT_FLRCTRL_DIS_447_OG [0x1bf]
                        MDP_XRT_FLRCTRL_DIS 1 07-F0 c9
2008/08/29 18:12:58.5 XRT_ARS_DIS_418_OG [0x1a2]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2008/08/29 18:13:00.5 XRT_CTRL_AUTO_432_OG [0x1b0]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2008/08/29 18:19:54.0 XRT_CTRL_MANU_405_OG [0x195]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2008/08/29 18:20:00.0 AOCs_Ore-point_Start_5_OG [0x09b]
                        AOCU_NM 5 02-76 00 2e f9 d1 07
2008/08/29 18:22:32.0 XRT_FOCUS_POSITION_442_OG [0x1ba]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2008/08/29 18:22:52.0 XRT_QT_PROG_SET_407_OG [0x197]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 09
2008/08/29 18:22:54.0 XRT_FLD_DIS_419_OG [0x1a3]

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2008/08/29	18:22:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLD_DIS	1	07-F0	d9				
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/08/29	18:22:58.0	XRT_ARS_DIS_418_OG [0x1a2]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/08/29	18:23:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/08/29	18:29:54.0	XRT_CTRL_MANU_405_OG [0x195]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/29	18:30:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00 d1 07 d1 07				
2008/08/29	18:32:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2008/08/29	18:32:52.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2008/08/29	18:32:54.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/08/29	18:32:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/08/29	18:32:58.0	XRT_ARS_DIS_418_OG [0x1a2]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/08/29	18:33:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/08/29	18:39:54.0	XRT_CTRL_MANU_405_OG [0x195]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/29	18:40:00.0	AOCS_ORe-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 d1 07 2e f9				
2008/08/29	18:42:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2008/08/29	18:42:52.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2008/08/29	18:42:54.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/08/29	18:42:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/08/29	18:42:58.0	XRT_ARS_DIS_418_OG [0x1a2]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/08/29	18:43:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/08/29	18:50:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	04 00 00 00 00				
2008/08/30	05:45:24.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/30	05:45:26.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2008/08/30	05:45:30.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00				
2008/08/30	05:45:46.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2008/08/30	05:45:48.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/08/30	05:45:50.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/08/30	05:45:52.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/08/30	05:47:30.0	XRT_CTRL_AUTO_444_OG [0x1bc]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/08/30	05:55:24.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/30	05:55:30.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	04 00 00 00 00				
2008/08/30	08:00:00.0	AOCS_ORe-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	02 00 00 00 00				
2008/08/30	08:16:00.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/30	08:16:02.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2008/08/30	08:16:22.0	XRT_QT_PROG_SET_411_OG [0x19b]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2008/08/30	08:18:00.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/08/30	08:18:02.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/08/30	08:18:04.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/08/30	08:18:06.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/08/30	09:32:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/30	09:51:30.0	XRT_Custom_430_OG [0x1ae]								
2008/08/30	09:52:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/08/30	11:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/30	12:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	04 00 00 00 00				
2008/08/30	17:47:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/30	17:47:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2008/08/30	17:48:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00				
2008/08/30	17:48:16.0	XRT_QT_PROG_SET_409_OG [0x199]								

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2008/08/30	17:48:18.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2008/08/30	17:48:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2008/08/30	17:48:22.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2008/08/30	17:50:00.0	XRT_CTRL_AUTO_444_OG [0x1bc]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2008/08/30	17:57:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2008/08/30	22:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	04 00 00 00 00	
2008/08/31	05:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2008/08/31	05:59:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2008/08/31	06:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00	
2008/08/31	06:00:16.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c	
2008/08/31	06:00:18.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2008/08/31	06:00:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2008/08/31	06:00:22.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2008/08/31	06:02:00.0	XRT_CTRL_AUTO_444_OG [0x1bc]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2008/08/31	06:09:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2008/08/31	06:10:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	04 00 00 00 00	
2008/08/31	07:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2008/08/31	07:59:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2008/08/31	08:00:00.0	AOCS_ORe-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	03 00 00 00 00	
2008/08/31	08:00:16.0	XRT_QT_PROG_SET_411_OG [0x19b]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e	
2008/08/31	08:01:54.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2008/08/31	08:01:56.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2008/08/31	08:01:58.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2008/08/31	08:02:00.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2008/08/31	08:31:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2008/08/31	08:52:30.0	XRT_Custom_430_OG [0x1ae]					
2008/08/31	08:53:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2008/08/31	10:11:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2008/08/31	10:26:30.0	XRT_Custom_430_OG [0x1ae]					
2008/08/31	10:27:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2008/08/31	10:45:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2008/08/31	10:46:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00	