

XRT Timeline to be uploaded on 2009/06/25

Period: 2009/06/25 10:57:00 - 2009/06/30 10:48:00

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Normal mode

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XOB #1563: CCD Monitor During Bakeout - G-band + dark - wide FOV												
Term		Pointing (x, y)						Comment				
06/25 11:09:30 - 06/25 11:30:00		Fixed (,)										
06/25 21:12:30 - 06/26 03:01:30		Track (125.8, -439.9) @ 06/25 18:13:30 AR11023										
PROG= 04 Inf.-time(s)												
└ Subr= 1 1-time(s) 600.0sec												
└─ Seqn= 98 1-time(s) 4.0sec												
└─┬ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 2048x256 (1024, 1024) DPCM 0 0 2.0sec												
└─┴ Open/thick-Be Open/thick-Be close Safe Dark 44ms Obs 1x1 2048x256 (1024, 1024) DPCM 0 0 2.0sec												
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #1565: CCD Monitor During Bakeout - G-band + dark - wide FOV - lower cadence (30min)												
Term		Pointing (x, y)						Comment				
06/25 11:30:10 - 06/25 12:18:30		Fixed (0.0, 0.0)						# OP start + 10min				
06/25 12:48:00 - 06/25 18:52:30		Track (73.5, -439.7) @ 06/25 11:40:00 AR11023										
06/25 19:35:00 - 06/25 20:30:00		Track (125.8, -439.9) @ 06/25 18:13:30 AR11023										
PROG= 20 Inf.-time(s)												
└ Subr= 1 1-time(s) 1800.0sec												
└─ Seqn= 98 1-time(s) 4.0sec												
└─┬ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 2048x256 (1024, 1024) DPCM 0 0 2.0sec												
└─┴ Open/thick-Be Open/thick-Be close Safe Dark 44ms Obs 1x1 2048x256 (1024, 1024) DPCM 0 0 2.0sec												
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #15A8: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant												
Term		Pointing (x, y)						Comment				
06/26 05:18:00 - 06/26 05:19:54		Fixed (-528.4, -528.4)						Four quadrant #1				
PROG= 10 1-time(s)												
└ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 28 1-time(s) 12.0sec												
└─┬ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec												
└─┬ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec												
└─┬ Open/thick-Be Open/thick-Be close Safe Dark 44ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec												
└─┴ Open/thick-Be Open/thick-Be close Safe Dark 44ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec												
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #15A9: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant												
Term		Pointing (x, y)						Comment				
06/26 05:23:00 - 06/26 05:24:54		Fixed (528.4, -528.4)						Four quadrant #2				
PROG= 12 1-time(s)												
└ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 41 1-time(s) 12.0sec												
└─┬ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec												
└─┬ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec												
└─┬ Open/thick-Be Open/thick-Be close Safe Dark 44ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec												
└─┴ Open/thick-Be Open/thick-Be close Safe Dark 44ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec												
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #15AA: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant												
Term		Pointing (x, y)						Comment				
06/26 05:28:00 - 06/26 05:29:54		Fixed (528.4, 528.4)						Four quadrant #3				
PROG= 02 1-time(s)												
└ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 8 1-time(s) 12.0sec												
└─┬ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec												
└─┬ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec												
└─┬ Open/thick-Be Open/thick-Be close Safe Dark 44ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec												
└─┴ Open/thick-Be Open/thick-Be close Safe Dark 44ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec												
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #15AB: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant												
Term		Pointing (x, y)						Comment				
06/26 05:33:00 - 06/26 05:44:54		Fixed (-528.4, 528.4)						Four quadrant #4				
PROG= 15 1-time(s)												
└ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 40 1-time(s) 12.0sec												
└─┬ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec												
└─┬ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec												
└─┬ Open/thick-Be Open/thick-Be close Safe Dark 44ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec												
└─┴ Open/thick-Be Open/thick-Be close Safe Dark 44ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec												
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #16BA: Synoptic Q95 2x2 - Al/mesh(256/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Ti-poly(181/2048) + G-band(16)											
Term		Pointing (x, y)				Comment					
06/26 05:48:00 - 06/26 05:59:54		Fixed (0.0, 0.0)				stnoptic, shifted -16.0 min					
06/27 06:03:00 - 06/27 06:14:54		Fixed (0.0, 0.0)				synoptic					
PROG= 01 1-time(s)											
└─ Subr= 1 1-time(s) 12.0sec											
└─ Seqn= 18 1-time(s) 4.0sec											
└─ Open/Al-mesh		Open/Al-mesh		close	Safe	Norm	250ms	Obs 2x2	2048x2048 (1024, 1024)		Q=95 0 0 2.0sec
└─ Open/Al-mesh		Open/Al-mesh		close	Safe	Norm	2.83s	Obs 2x2	2048x2048 (1024, 1024)		Q=98 0 0 2.0sec
└─ Seqn= 7 1-time(s) 2.0sec											
└─ Open/Ti-poly		Open/thick-Al		close	Safe	Dark	500ms	Obs 2x2	2048x2048 (1024, 1024)		Q=98 0 0 2.0sec
└─ Open/Ti-poly		Open/thick-Al		close	Safe	Dark	500ms	Obs 4x4	2048x2048 (1024, 1024)		Q=98 0 0 2.0sec
└─ Open/Ti-poly		Open/thick-Al		close	Safe	Dark	500ms	Obs 8x8	2048x2048 (1024, 1024)		Q=98 0 0 2.0sec
└─ Seqn= 68 1-time(s) 4.0sec											
└─ Open/Ti-poly		Open/thick-Al		close	Safe	Norm	1.00s	Obs 2x2	2048x2048 (1024, 1024)		Q=95 0 0 2.0sec
└─ Open/Ti-poly		Open/thick-Al		close	Safe	Norm	8.00s	Obs 2x2	2048x2048 (1024, 1024)		Q=95 0 0 2.0sec
└─ Seqn= 92 1-time(s) 2.0sec											
└─ Open/G-band		Open/G-band		open	Safe	Norm	16ms	Obs 2x2	2048x2048 (1024, 1024)		Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #16CF: AR-dynamics -384x384 - C/poly -Thin-Be -AEC 1 -1 min - Q95											
Term		Pointing (x, y)				Comment					
06/26 06:03:02 - 06/26 17:36:24		Track (218.3, -439.8) ^{Ⓜ 06/26 06:00:00}				AR11023					
06/26 18:37:02 - 06/27 05:59:54		Track (308.7, -439.3) ^{Ⓜ 06/26 17:51:30}				AR11023					
06/27 06:18:02 - 06/27 10:10:00		Track (399.1, -438.3) ^{Ⓜ 06/27 06:15:00}				AR11023					
PROG= 18 Inf.-time(s)											
└─ Subr= 1 1-time(s) 60.0sec											
└─ Seqn= 83 1-time(s) 2.0sec											
└─ C-poly/Open		C-poly/thick-Al		close	Safe	Norm	4.00s	Obs 1x1	384x384 (1024, 1024)		Q=95 1 0 2.0sec
└─ thin-Be/Open		thin-Be/thick-Al		close	Safe	Norm	16.0s	Obs 1x1	384x384 (1024, 1024)		Q=95 1 0 2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

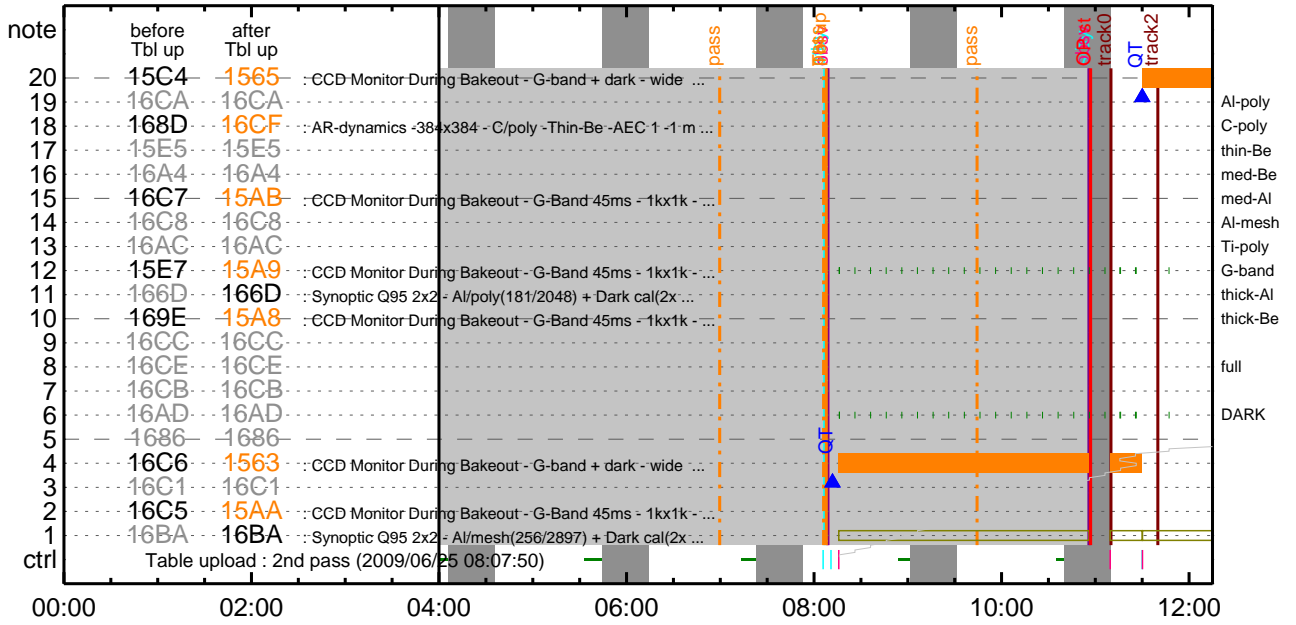
XOB #166D: Synoptic Q95 2x2 - Al/poly(181/2048) + Dark cal(2x2 4x4 8x8 512 Q98) + Ti-poly(256/4096) + G-band(16)											
Term		Pointing (x, y)				Comment					
06/26 17:39:30 - 06/26 17:51:00		Fixed (0.0, 0.0)				synoptic, shifted -23.5 min					
PROG= 11 1-time(s)											
└─ Subr= 1 1-time(s) 12.0sec											
└─ Seqn= 14 1-time(s) 4.0sec											
└─ Al-poly/Open		Al-poly/Open		close	Safe	Norm	177ms	Obs 2x2	2048x2048 (1024, 1024)		Q=95 0 0 2.0sec
└─ Al-poly/Open		Al-poly/Open		close	Safe	Norm	2.00s	Obs 2x2	2048x2048 (1024, 1024)		Q=98 0 0 2.0sec
└─ Seqn= 7 1-time(s) 2.0sec											
└─ Open/Ti-poly		Open/thick-Al		close	Safe	Dark	500ms	Obs 2x2	2048x2048 (1024, 1024)		Q=98 0 0 2.0sec
└─ Open/Ti-poly		Open/thick-Al		close	Safe	Dark	500ms	Obs 4x4	2048x2048 (1024, 1024)		Q=98 0 0 2.0sec
└─ Open/Ti-poly		Open/thick-Al		close	Safe	Dark	500ms	Obs 8x8	2048x2048 (1024, 1024)		Q=98 0 0 2.0sec
└─ Seqn= 21 1-time(s) 4.0sec											
└─ Open/Ti-poly		Open/Ti-poly		close	Safe	Norm	250ms	Obs 2x2	2048x2048 (1024, 1024)		Q=95 0 0 2.0sec
└─ Open/Ti-poly		Open/Ti-poly		close	Safe	Norm	4.00s	Obs 2x2	2048x2048 (1024, 1024)		Q=98 0 0 2.0sec
└─ Seqn= 92 1-time(s) 2.0sec											
└─ Open/G-band		Open/G-band		open	Safe	Norm	16ms	Obs 2x2	2048x2048 (1024, 1024)		Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

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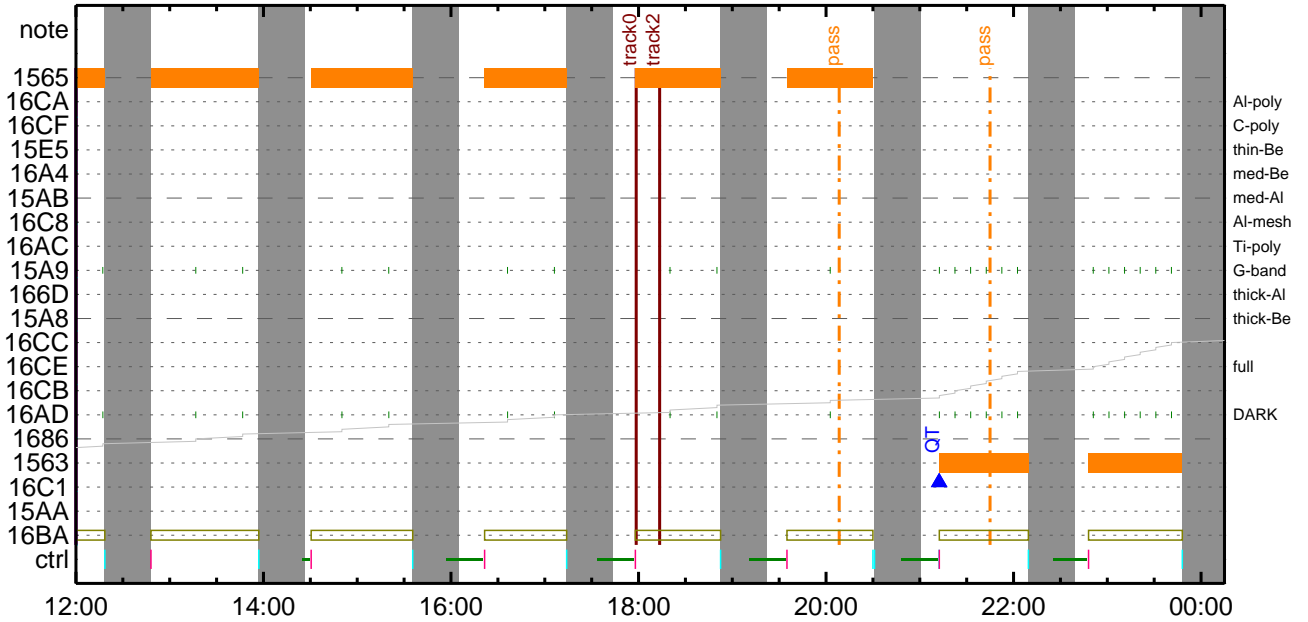
* * * * *
                                Flare mode                                * * * * *
                                NOT USED
* * * * *
                                Active Region Search                        * * * * *
                                NOT USED
* * * * *
                                Flare Detection                            * * * * *
                                NOT USED

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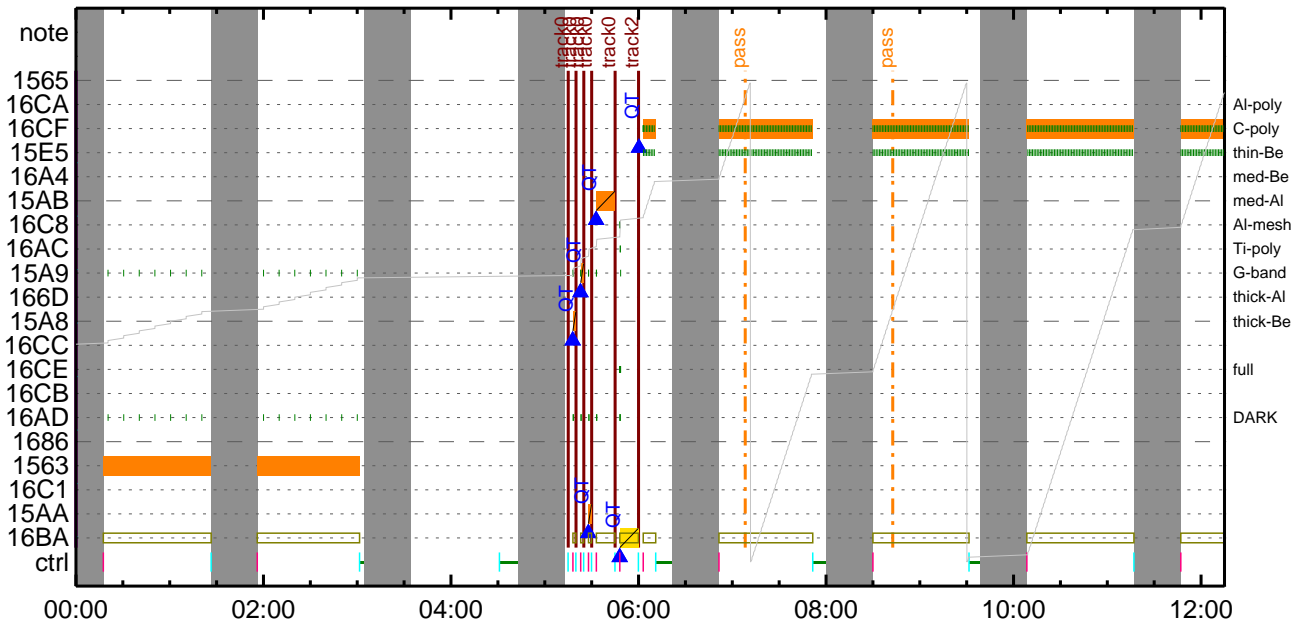
CMDI #0633 2009/06/25



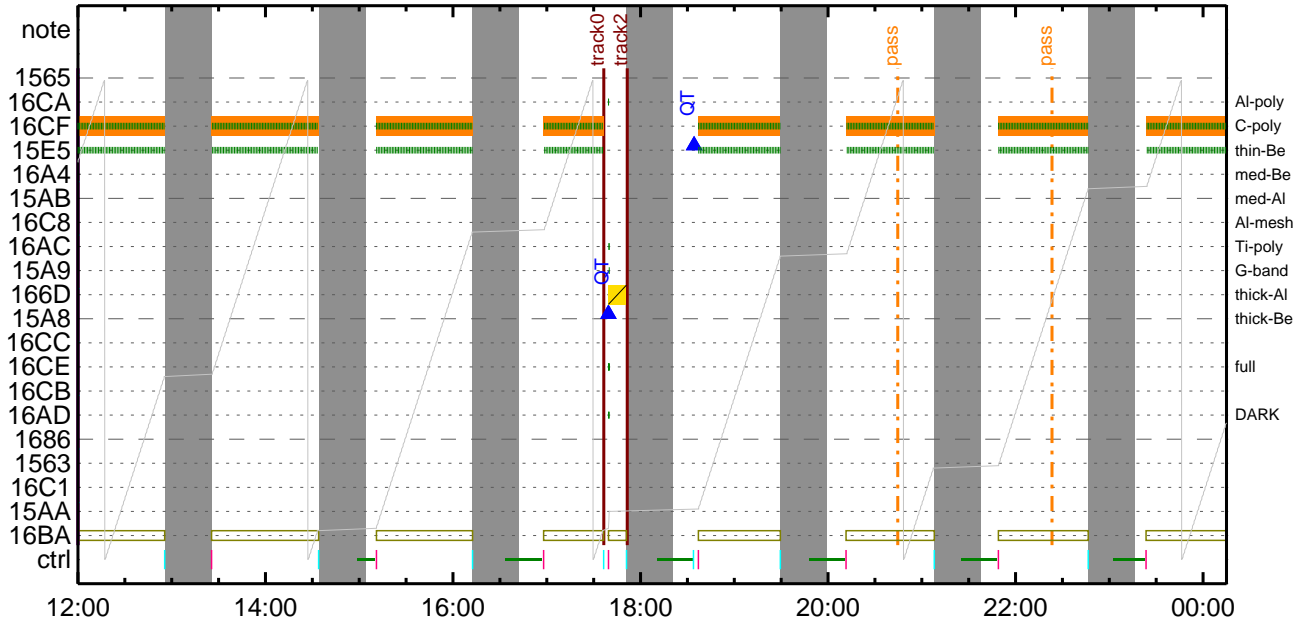
CMDI #0633 2009/06/25



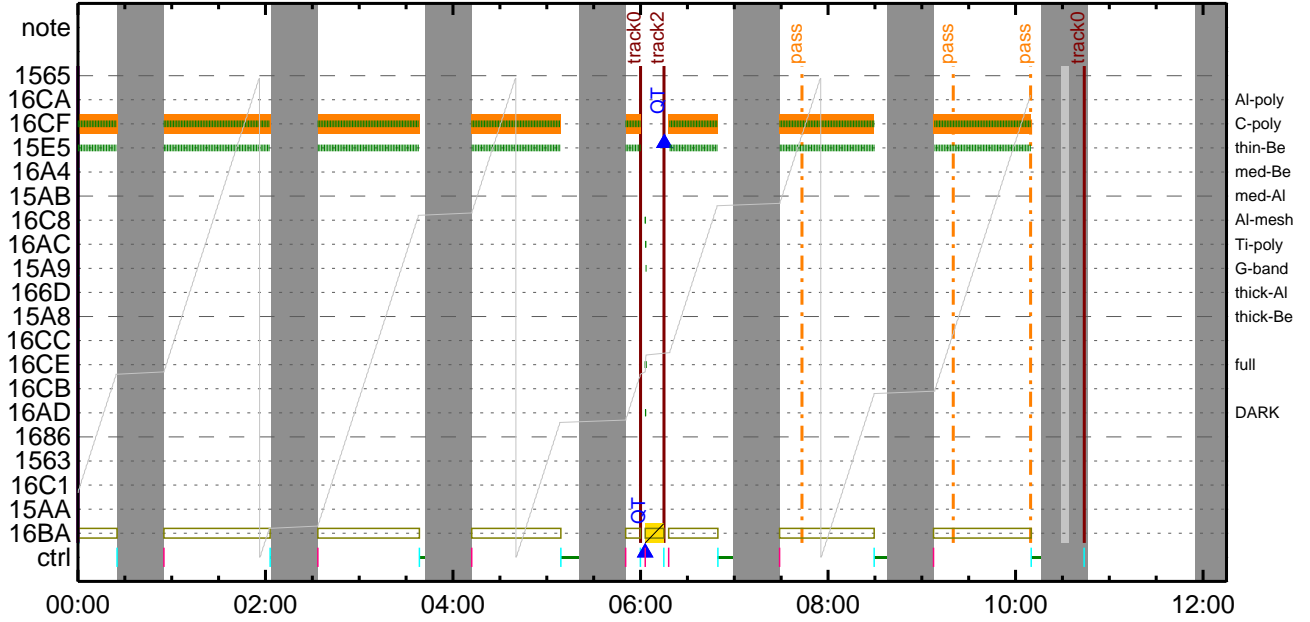
CMDI #0633 2009/06/26



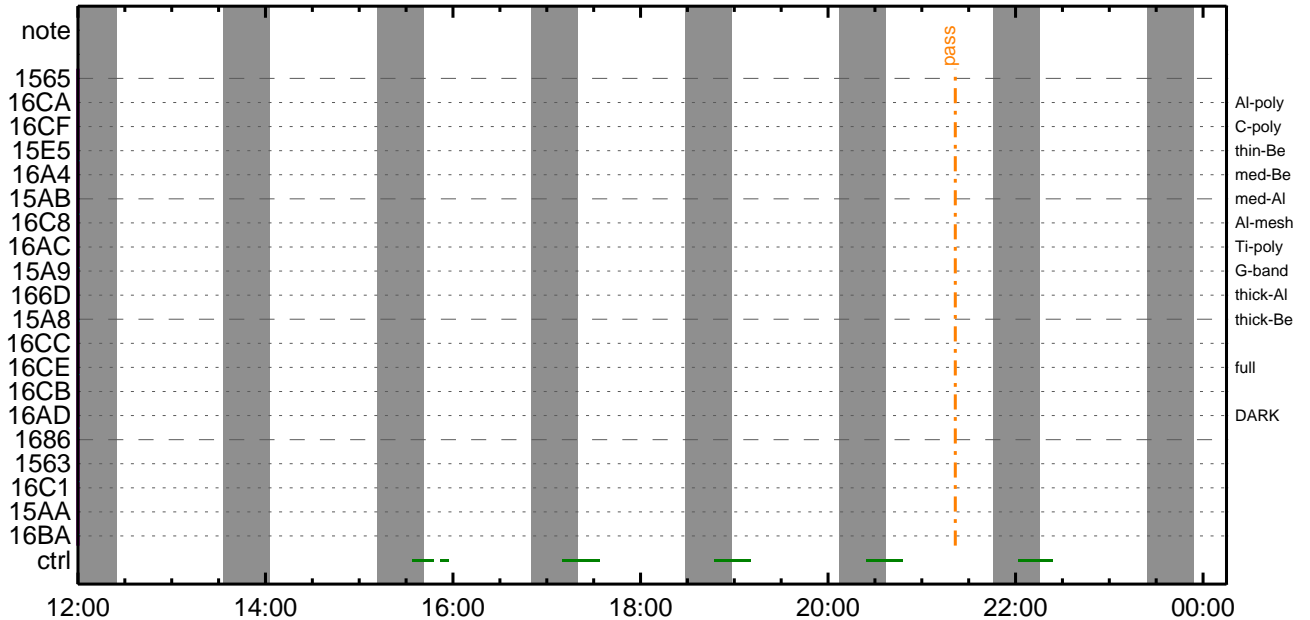
CMDI #0633 2009/06/26



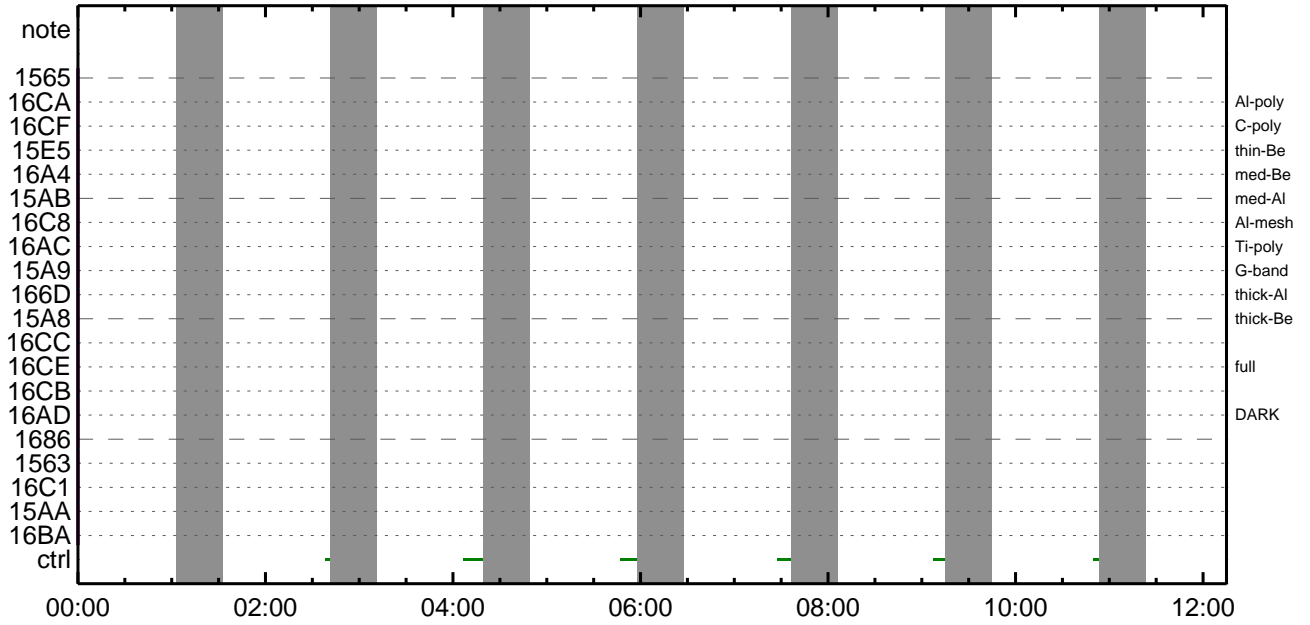
CMDI #0633 2009/06/27



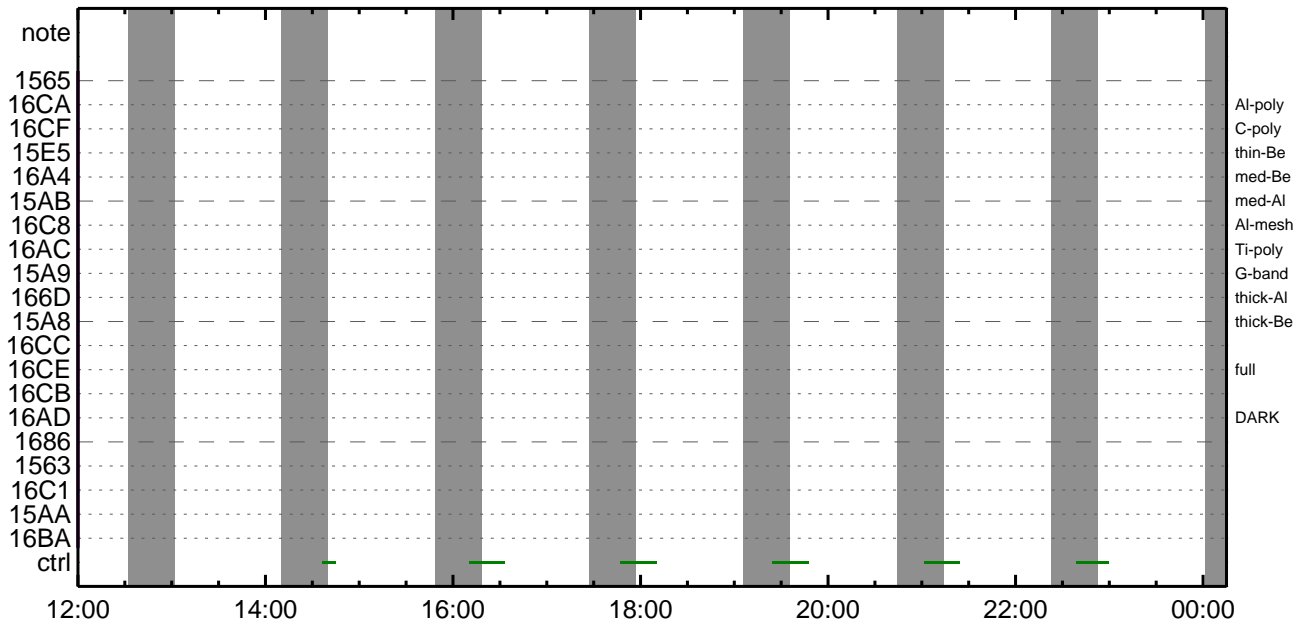
CMDI #0633 2009/06/27



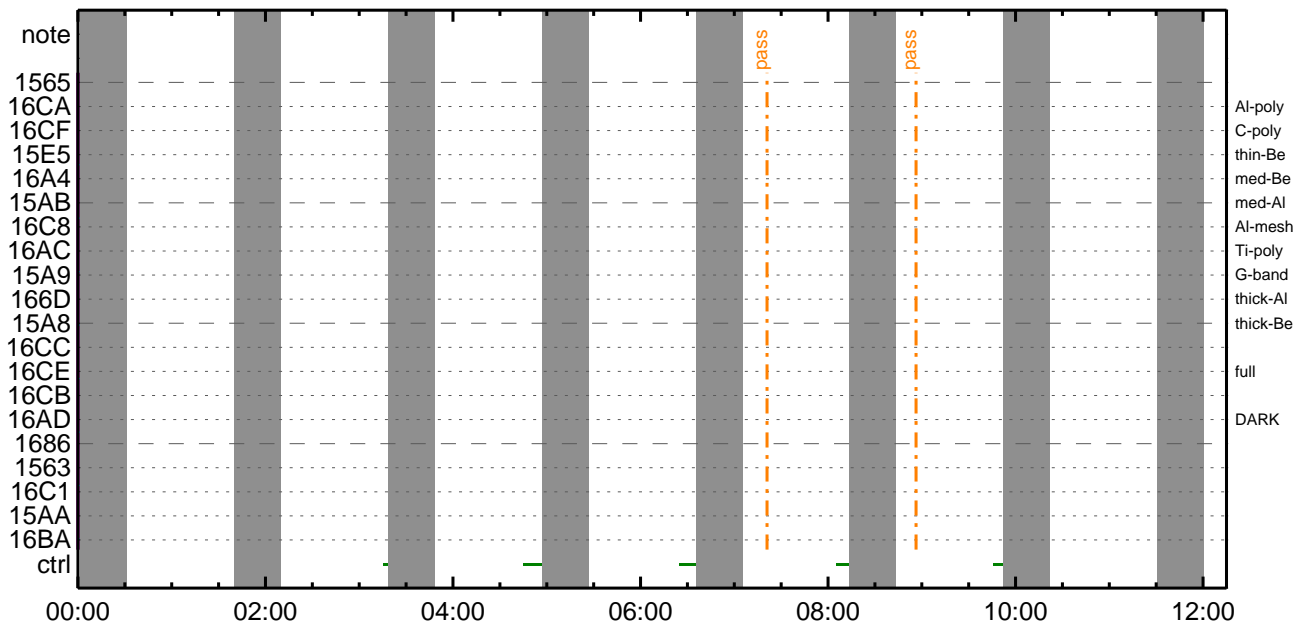
CMDI #0633 2009/06/28



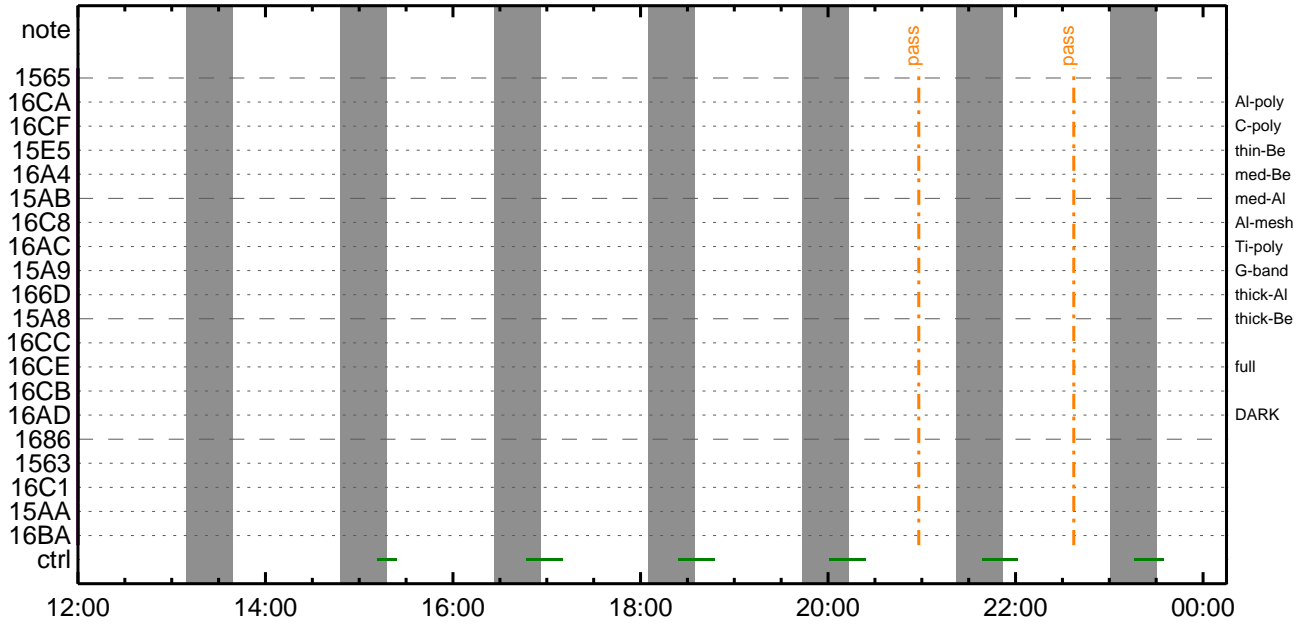
CMDI #0633 2009/06/28



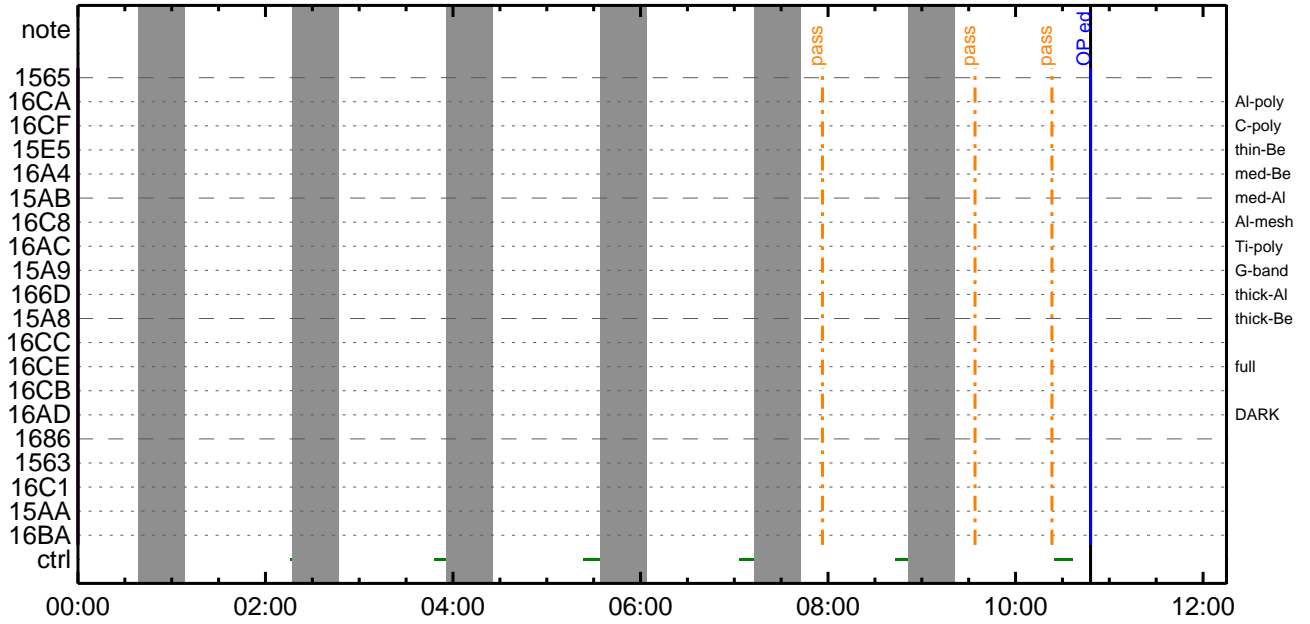
CMDI #0633 2009/06/29



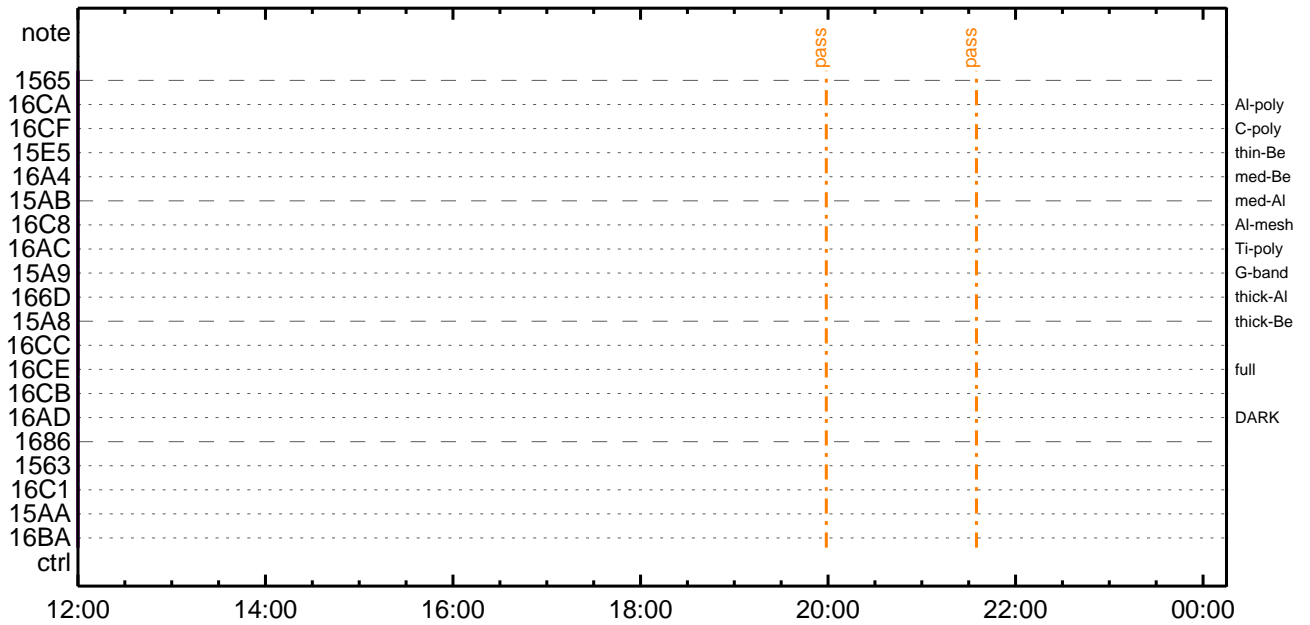
CMDI #0633 2009/06/29



CMDI #0633 2009/06/30



CMDI #0633 2009/06/30




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0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øα¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 C.
0130 C. ***** XRT START *****
0131 C.
0132 +. DC 07-F0 MDP_XRT_CTRL_MANU
0133 BC (c1)
0134 + DC 07-F0 MDP_XRT_MODE_STBY
0135 BC (c3)
0136 . C. ----- Success Verify ? OK / NG____
0137 C.
0138 C. XRT Obs. Table Upload
0139 . S. RAM ram-291:MDP_OBS_X
0140 ( )
0141 C.
0142 +. DC 07-F0 MDP_DUMP_XRTTBL
0143 BC (84 07 00 00 00 3a d4)
0144 . C. ----- Comparison Check ? OK / ERR ____
0145 C.
0146 C.
0147 +. DC 07-F0 MDP_XRT_ROI_SET
0148 BC (cd 01 b1 b1 04 04)
0149 + DC 07-F0 MDP_XRT_ROI_SET
0150 BC (cd 02 b1 b1 08 08)
0151 + DC 07-F0 MDP_XRT_ROI_SET
0152 BC (cd 03 b1 b1 08 08)
0153 + DC 07-F0 MDP_XRT_ROI_SET
0154 BC (cd 04 b1 b1 06 06)
0155 + DC 07-F0 MDP_XRT_ROI_SET
0156 BC (cd 06 80 80 20 04)
0157 + DC 07-F0 MDP_XRT_ROI_SET
0158 BC (cd 07 c0 c0 10 10)
0159 + DC 07-F0 MDP_XRT_ROI_SET
0160 BC (cd 08 40 c0 10 10)
0161 + DC 07-F0 MDP_XRT_ROI_SET
0162 BC (cd 09 40 40 10 10)
0163 + DC 07-F0 MDP_XRT_ROI_SET
0164 BC (cd 0a c0 40 10 10)
0165 + DC 07-F0 MDP_XRT_ROI_SET
0166 BC (cd 0b 80 80 20 20)
0167 + DC 07-F0 MDP_XRT_ROI_SET
0168 BC (cd 0c 80 80 06 06)
0169 + DC 07-F0 MDP_XRT_ROI_SET
0170 BC (cd 0f 80 80 04 04)
0171 + DC 07-F0 MDP_XRT_ROI_SET
0172 BC (cd 10 80 80 10 10)
0173 . C. ----- Success Verify ? OK / NG ____
0174 C.
0175 C.
0176 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0177 C.
0178 +. DC 07-F0 MDP_XRT_MODE_OBSV
0179 BC (c2)
0180 +. TI 2009-06-25 10:56:02.0
0181 DC 07-F0 MDP_XRT_MODE_OBSV
0182 BC (c2)
0183 . C. ----- Success Verify ? OK / NG ____
0184 C.
0185 C. ***** XRT END *****
0186 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0187 +. DC 07-FC EIS_MODE_MANU
0188 BC (21 02)
0189 . C. Verify EIS in MANUAL mode
0190 . C. Estimated OBSTBL upload time is 58s
0191 C. *****
0192 C. EIS START OBSTBL LOAD
0193 C. *****

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0194 . S. RAM ram-820:EIS_OBSTBL
0195 ( )
0196 +. DC 07-FC EIS_DUMP_OBSTBL
0197 BC (07 07 07 00 00 70 00)
0198 C.
0199 C. Execute, after the success of OBSTBL upload.
0200 C. Set EIS TI-commands
0201 +. TI 2009-06-25 10:56:50.0
0202 DC 07-FC EIS_MODE_CHG_ENA
0203 BC (20)
0204 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0205 C. *****
0206 C. EIS END OBSTBL LOAD
0207 C. *****
0208 C. *****
0209 C. START of XRT_CCD_HEATER_ON operation
0210 C. *****
0211 C.
0212 +. DC 07-F0 MDP_XRT_CTRL_MANU
0213 BC (c1)
0214 C. ----- Success Verify ? OK / NG;
0215 C.
0216 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0217 BC (c4 04)
0218 + DC 07-F0 MDP_XRT_FLD_DIS
0219 BC (d9)
0220 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0221 BC (c9)
0222 + DC 07-F0 MDP_XRT_ARS_DIS
0223 BC (d5)
0224 C. ----- Success Verify ? OK / NG ____
0225 C.
0226 C.
0227 C. All OK? Yes--> Please Proceed. / No --> Stop here.
0228 C.
0229 +. DC 07-F0 MDP_XRT_CTRL_AUTO
0230 BC (c0)
0231 C. ----- Success Verify ? OK / NG;
0232 C.
0233 +. DC 04-BC TCIB_XRT_S_HTR_A_ENA
0234 C. ----- Success Verify ? OK / NG;
0235 C.
0236 C. -----
0237 C. If anomalous situation appeared, execute TCIB_XRT_S_HTR_A_DIS using DCBC-441 (line 24)
0238 C. -----
0239 C. *****
0240 C. END of XRT_CCD_HEATER_ON operation
0241 C. *****
0242 C.
0243 C.
0244 C.
0245 . C. ***** MDP `úãîï»ö%ýðÉÄð¹ðèDCBC•x²è *****
0246 C. (%á°îÿÓÿÄÿÈÿÞÿÿÿáÿçÿèðÈ%¼ð¼Ä»Û¹ðé)
0247 . S. DC-BC dcbc-402:DCBC
0248 (MDP_known_event)
0249 C.
0250 C.
0251 . C. ***** ¥Ðÿ¹•İ Daily±¿İÑðÈ´Ø¹ðèDCBC•x²è *****
0252 . S. DC-BC dcbc-153:DCBC
0253 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0254 C.
0255 C.
0256 . C. ;ãLOS¥Á¥§¥Ã¥´¼Ä»Û;ä
0257 C.
0258 . C. ***** LOS *****
0259 C.

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0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-261:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 . C. < Stop SP table >
0120 +. DC 07-F0 MDP_SP_CTRL_MANU
0121 BC (61)
0122 C. -----
0123 C. MDP_SP_CTRL_MODE = MANU [ ]
0124 C. -----
0125 C.
0126 . C. <Upload SP Observation Table>
0127 . S. RAM ram-285:MDP_OBS_S
0128 ( )
0129 C.
0130 . C. < Dump RAMID=MDP_OBS_S >
0131 +. DC 07-F0 MDP_DUMP_SPTBL
0132 BC (83 07 00 00 00 38 b8)
0133 C. -----
0134 C. MDP_OBS_S verify = OK/NG [ ]
0135 C. -----
0136 C.
0137 C. *****
0138 C. SOT TI command set
0139 C. *****
0140 C. Execute, after the success of TBL upload.
0141 +. TI 2009-06-25 10:56:18.0
0142 DC 07-F0 MDP_SOT_MODE_OBSV
0143 BC (40)
0144 . C. -----
0145 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0146 C. -----
0147 C.
0148 C.
0149 . C. ***** MDP 'úÃîâî»ö¼ÝðÈÄð¹ñèDCBC•x²è *****
0150 C. (%ã°îÿÓÿÄÿËÿÏÿËÿÄÿÇÿÈ±È¼¼¼¼»Û¹æè)
0151 . S. DC-BC dcbc-402:DCBC
0152 (MDP_known_event)
0153 C.
0154 C.
0155 . C. ***** ÿÐÿ¹•İ Daily±¿İÑ±È´Ø¹±æDCBC•x²è *****
0156 . S. DC-BC dcbc-153:DCBC
0157 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0158 C.
0159 C.
0160 . C. ;ãLOSÿÁÿÏÿÄÿ¼Ä»Û;ã
0161 C.
0162 . C. ***** LOS *****
0163 C.
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*** OP Sequence for XRT ***

2009/06/25	11:09:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/25	11:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2009/06/25	11:30:00.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/25	11:30:02.0	XRT_QT_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2009/06/25	11:30:04.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/06/25	11:30:06.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/06/25	11:30:08.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/06/25	11:30:10.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/25	11:40:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2009/06/25	12:18:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/25	12:48:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/25	13:57:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/25	14:29:30.0	XRT_Custom_430_OG [0x1ae]							
2009/06/25	14:30:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/25	15:35:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/25	16:20:30.0	XRT_Custom_430_OG [0x1ae]							
2009/06/25	16:21:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/25	17:14:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/25	17:57:00.0	XRT_Custom_430_OG [0x1ae]							
2009/06/25	17:58:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/25	17:58:30.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2009/06/25	18:13:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2009/06/25	18:52:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/25	19:34:00.0	XRT_Custom_430_OG [0x1ae]							
2009/06/25	19:35:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/25	20:30:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/25	20:30:02.0	XRT_TCIB_XRT_S_HTR_A_DIS_417_OG [0x1a1]							
		TCIB_XRT_S_HTR_A_DIS	0	04-C0					
2009/06/25	20:31:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/25	21:12:20.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/25	21:12:22.0	XRT_QT_PROG_SET_413_OG [0x19d]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2009/06/25	21:12:24.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/06/25	21:12:26.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/06/25	21:12:28.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/06/25	21:12:30.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/25	22:09:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/25	22:47:00.5	XRT_Custom_430_OG [0x1ae]							
2009/06/25	22:48:00.5	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/25	23:48:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/26	00:17:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/26	01:26:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/26	01:56:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/26	03:01:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/26	04:31:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/26	05:14:54.0	XRT_CTRL_MANU_448_OG [0x1c0]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/26	05:15:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 2e f9 2e f9				
2009/06/26	05:17:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2009/06/26	05:17:52.0	XRT_QT_PROG_SET_406_OG [0x196]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2009/06/26	05:17:54.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				

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2009/06/26	05:17:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/06/26	05:17:58.0	XRT_ARS_DIS_446_OG [0x1be]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/06/26	05:18:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/26	05:19:54.0	XRT_CTRL_MANU_448_OG [0x1c0]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/26	05:20:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 2e f9 d1 07				
2009/06/26	05:22:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2009/06/26	05:22:52.0	XRT_QT_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2009/06/26	05:22:54.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/06/26	05:22:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/06/26	05:22:58.0	XRT_ARS_DIS_446_OG [0x1be]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/06/26	05:23:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/26	05:24:54.0	XRT_CTRL_MANU_448_OG [0x1c0]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/26	05:25:00.0	AOCS_Ore-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00 d1 07 d1 07				
2009/06/26	05:27:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2009/06/26	05:27:52.0	XRT_QT_PROG_SET_414_OG [0x19e]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02				
2009/06/26	05:27:54.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/06/26	05:27:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/06/26	05:27:58.0	XRT_ARS_DIS_446_OG [0x1be]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/06/26	05:28:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/26	05:29:54.0	XRT_CTRL_MANU_448_OG [0x1c0]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/26	05:30:00.0	AOCS_Ore-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00 d1 07 2e f9				
2009/06/26	05:32:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2009/06/26	05:32:52.0	XRT_QT_PROG_SET_411_OG [0x19b]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f				
2009/06/26	05:32:54.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/06/26	05:32:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/06/26	05:32:58.0	XRT_ARS_DIS_446_OG [0x1be]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/06/26	05:33:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/26	05:44:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/26	05:44:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2009/06/26	05:45:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2009/06/26	05:45:16.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/06/26	05:45:18.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/06/26	05:45:20.0	XRT_ARS_DIS_410_OG [0x19a]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/06/26	05:47:58.0	XRT_QT_PROG_SET_400_OG [0x190]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2009/06/26	05:48:00.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/26	05:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/26	05:59:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2009/06/26	06:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2009/06/26	06:00:16.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2009/06/26	06:00:18.0	XRT_AEC_RESET_415_OG [0x19f]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2009/06/26	06:00:20.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/06/26	06:00:22.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/06/26	06:00:24.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/06/26	06:03:02.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/26	06:11:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/26	06:51:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/26	07:51:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							

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2009/06/26	08:30:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/26	09:31:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/26	10:08:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/26	11:17:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/26	11:47:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/26	12:55:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/26	13:25:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/26	14:34:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/26	15:10:00.0	XRT_Custom_430_OG [0x1ae]								
2009/06/26	15:11:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/26	16:12:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/26	16:57:00.0	XRT_Custom_430_OG [0x1ae]								
2009/06/26	16:58:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/26	17:36:24.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/26	17:36:26.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2009/06/26	17:36:30.0	AOCS_Orе-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00				
2009/06/26	17:36:46.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/06/26	17:36:48.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/06/26	17:36:50.0	XRT_ARS_DIS_410_OG [0x19a]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/06/26	17:39:28.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2009/06/26	17:39:30.0	XRT_CTRL_AUTO_440_OG [0x1b8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/26	17:51:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/26	17:51:30.0	AOCS_Orе-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	02 00 00 00 00				
2009/06/26	18:33:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/26	18:33:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2009/06/26	18:34:16.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2009/06/26	18:34:18.0	XRT_AEC_RESET_415_OG [0x19f]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2009/06/26	18:34:20.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/06/26	18:34:22.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/06/26	18:34:24.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/06/26	18:37:02.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/26	19:29:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/26	20:10:30.0	XRT_Custom_430_OG [0x1ae]								
2009/06/26	20:11:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/26	21:08:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/26	21:48:00.0	XRT_Custom_430_OG [0x1ae]								
2009/06/26	21:49:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/26	22:46:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/26	23:22:30.0	XRT_Custom_430_OG [0x1ae]								
2009/06/26	23:23:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/27	00:25:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/27	00:55:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/27	02:03:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/27	02:33:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/27	03:38:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/27	04:12:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/27	05:09:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/27	05:50:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/27	05:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]								

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2009/06/27	05:59:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		XRT_FOCUS_POSITION		4	07-F8	22 ff aa 00
2009/06/27	06:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00
2009/06/27	06:00:16.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9
2009/06/27	06:00:18.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/06/27	06:00:20.0	XRT_ARS_DIS_410_OG [0x19a]	MDP_XRT_ARS_DIS	1	07-F0	d5
2009/06/27	06:02:58.0	XRT_QT_PROG_SET_400_OG [0x190]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2009/06/27	06:03:00.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/06/27	06:14:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/06/27	06:14:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2009/06/27	06:15:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	02 00 00 00 00
2009/06/27	06:15:16.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2009/06/27	06:15:18.0	XRT_AEC_RESET_415_OG [0x19f]	MDP_XRT_AEC_RESET	1	07-F0	d0
2009/06/27	06:15:20.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2009/06/27	06:15:22.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_FLD_DIS	1	07-F0	d9
2009/06/27	06:15:24.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/06/27	06:18:02.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/06/27	06:49:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/06/27	07:29:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/06/27	08:29:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/06/27	09:07:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/06/27	10:10:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/06/27	10:43:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/06/27	10:44:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00