

XRT Timeline to be uploaded on 2011/09/01

Period: 2011/09/01 10:00:00 - 2011/09/06 10:55:00

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Normal mode

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XOB #189F: AR Standard-A(Filter-Ratio) with PFB, thin-Be, thick Al and Al/Poly context, 384x384 at 1064 1048, 60s cad												
Term	Pointing (x, y)						Comment					
09/01 10:50:00 - 09/01 16:06:00	Track (-354.9, -449.9) ^{Ⓢ 09/01 10:40:00}						* Track AR 11281.					
09/01 20:03:00 - 09/02 05:52:54	Track (-300.1, -452.7) ^{Ⓢ 09/01 17:42:00}						* Track AR 11281.					
09/02 10:33:00 - 09/02 17:55:54	Track (-164.2, -457.7) ^{Ⓢ 09/02 10:30:00}						* AR 11281.					
09/02 20:21:00 - 09/03 05:18:30	Track (-101.1, -459.0) ^{Ⓢ 09/02 18:06:00}						# Cont.					
PROG= 18 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 19 1-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 76 4-time(s) 2.0sec												
└─ Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512 (1024, 1024)	Q=95	3	0	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1024, 1024)	Q=95	3	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 62 20-time(s) 2.0sec												
└─ thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec
└─ thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
└─ thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
└─ thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #187C: Synoptic Q95 2x2 - Al/mesh(45/2048) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(88/2897) + Thin-Be(36)											
Term	Pointing (x, y)						Comment				
09/01 17:35:00 - 09/01 19:59:54	Fixed (0.0, 0.0)						# Synoptic, shifted manually.				
09/02 05:56:00 - 09/02 10:29:54	Fixed (0.0, 0.0)						synoptic, shifted -7.0 min				
09/02 17:59:00 - 09/02 20:05:54	Fixed (0.0, 0.0)						synoptic, shifted -4.0 min				
09/03 06:03:00 - 09/03 10:52:54	Fixed (0.0, 0.0)						synoptic, shifted manually, and SOT flat fields.				

PROG= 07 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 86 1-time(s) 4.0sec												
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5 1-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 37 1-time(s) 4.0sec												
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 32 1-time(s) 2.0sec												
└─ thin-Be/Open	thin-Be/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 4 1-time(s) 2.0sec												
└─ Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1869: Flare standard obs. multifilter (thin-Be,med-Al,thick-Be 384x384 - Al-poly 512x512 2x2)											
Term	Pointing (x, y)						Comment				
09/01 10:50:00 - 09/01 16:06:00	Track (-354.9, -449.9) ^{Ⓢ 09/01 10:40:00}						* Track AR 11281.				
09/01 20:03:00 - 09/02 05:52:54	Track (-300.1, -452.7) ^{Ⓢ 09/01 17:42:00}						* Track AR 11281.				
09/02 10:33:00 - 09/02 17:55:54	Track (-164.2, -457.7) ^{Ⓢ 09/02 10:30:00}						* AR 11281.				
09/02 20:21:00 - 09/03 05:18:30	Track (-101.1, -459.0) ^{Ⓢ 09/02 18:06:00}						# Cont.				

PROG= 13 1-time(s)												
└─ Subr= 1 4-time(s) 2.0sec												
└─ Seqn= 55 45-time(s) 20.0sec												
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ Seqn= 90 1-time(s) 2.0sec												
└─ Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec

Subr= 2	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs 1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs 2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 89	1-time(s)		600.0sec									
	1-time(s)		2.0sec									
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs 1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs 1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs 1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs 1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

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Active Region Search

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NOT USED

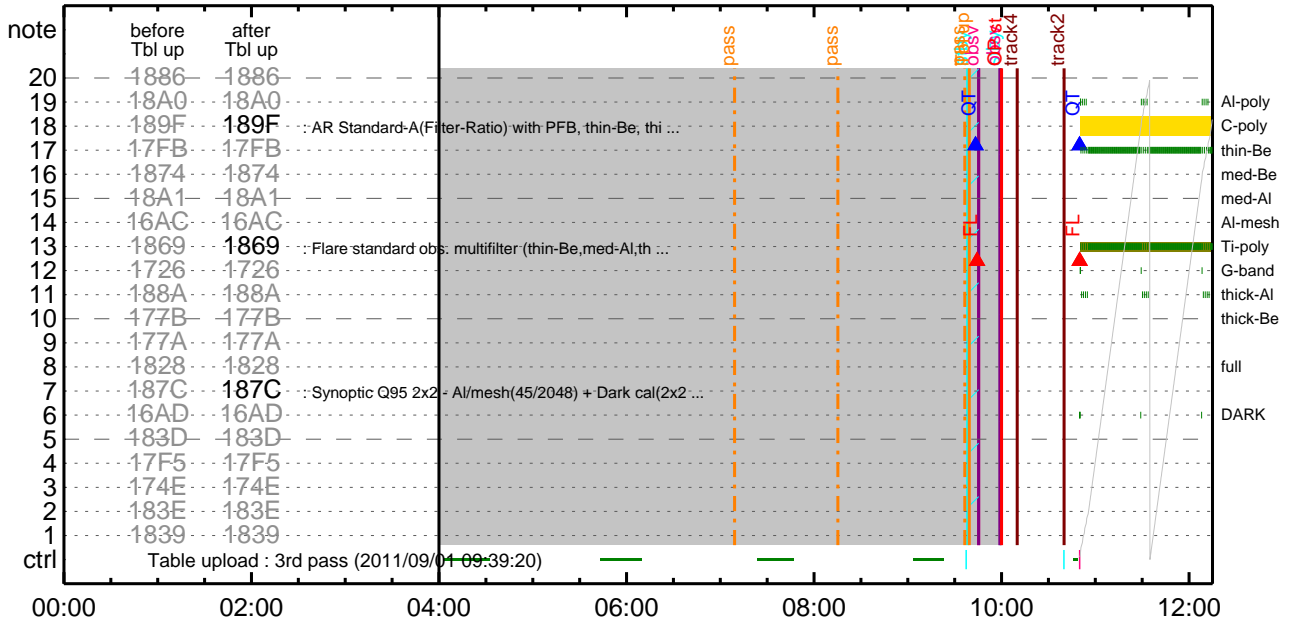
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Flare Detection

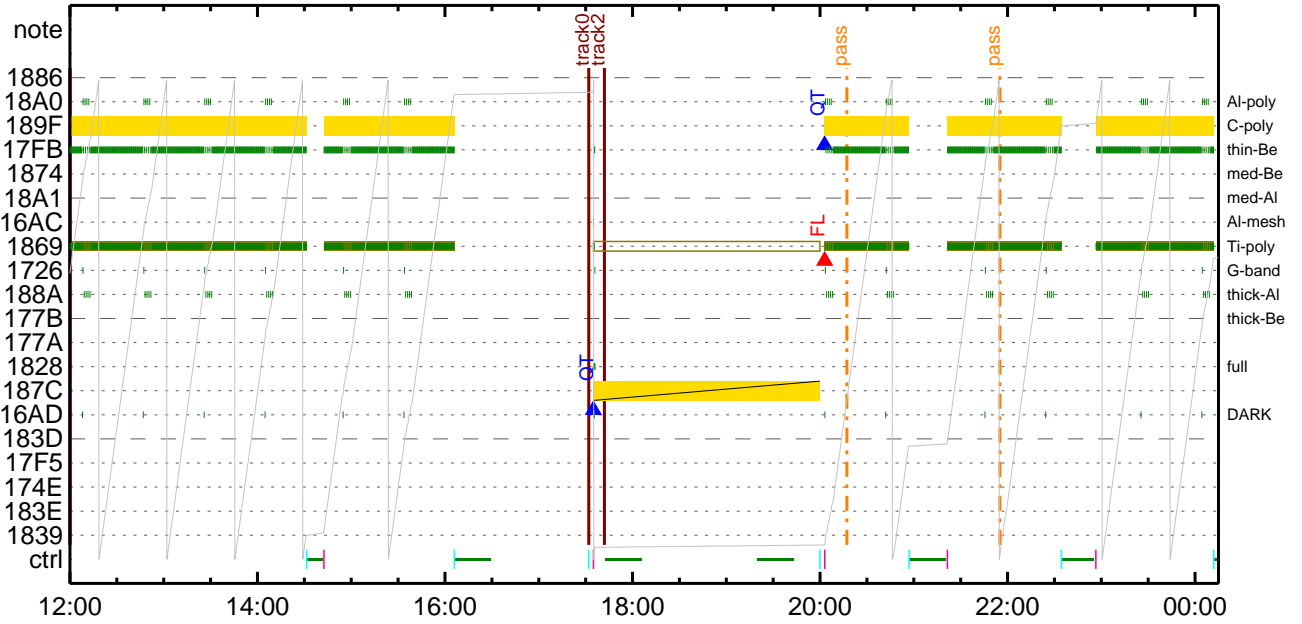
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FLD Patrol												
Term	Pointing (x, y)						Comment					
09/01 20:02:46 - 09/02 05:53:16	Track (-300.1, -452.7)	[Ⓢ] 09/01 17:42:00	* Track AR 11281.									
09/02 10:32:46 - 09/02 17:56:16	Track (-164.2, -457.7)	[Ⓢ] 09/02 10:30:00	* AR 11281.									
09/02 20:20:46 - 09/03 06:00:16	Track (-101.1, -459.0)	[Ⓢ] 09/02 18:06:00	# Cont.									
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs 8x8	Q=50				30sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

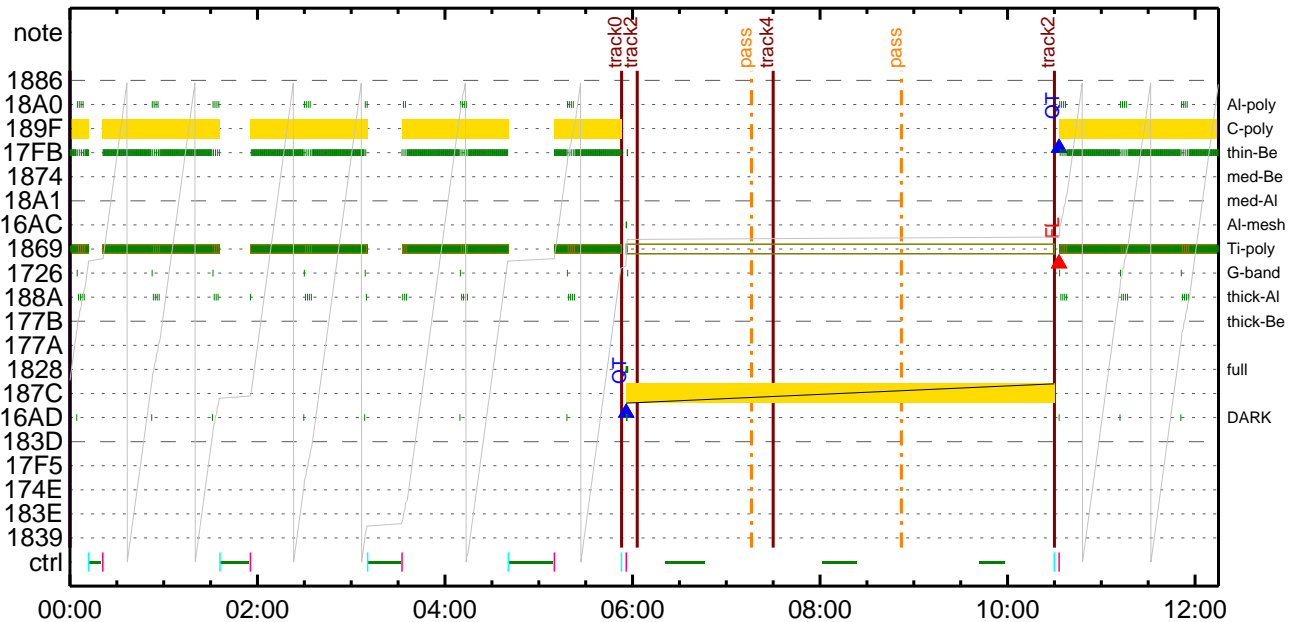
CMDI #0120 2011/09/01



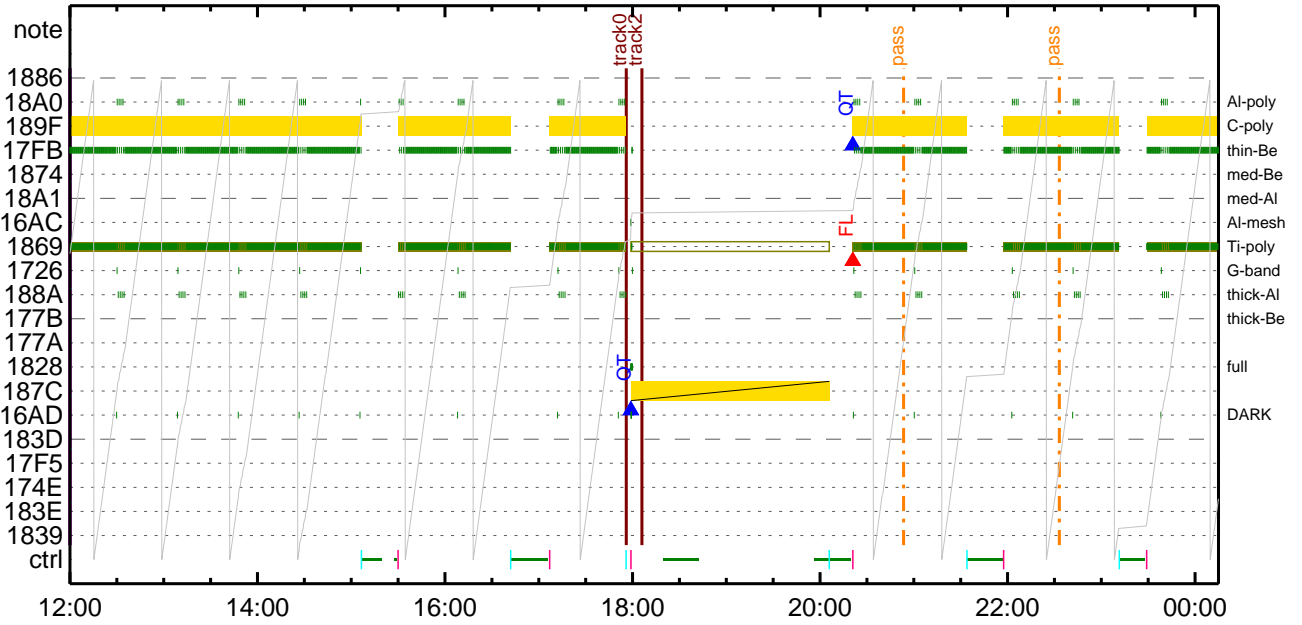
CMDI #0120 2011/09/01



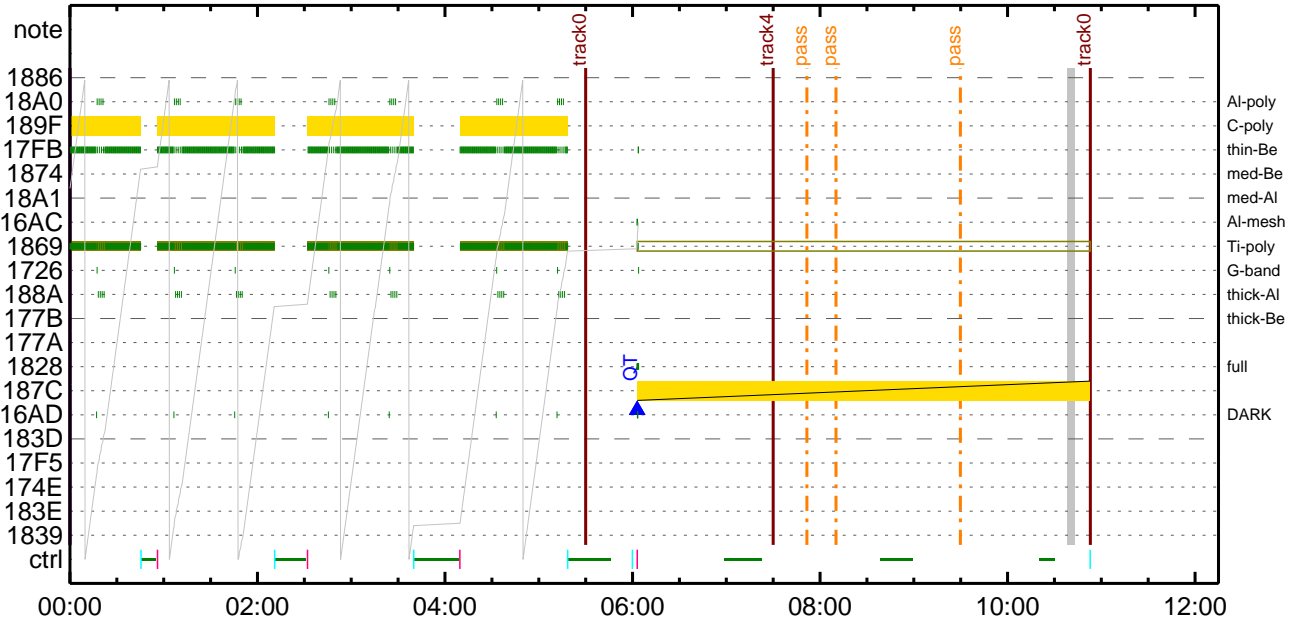
CMDI #0120 2011/09/02



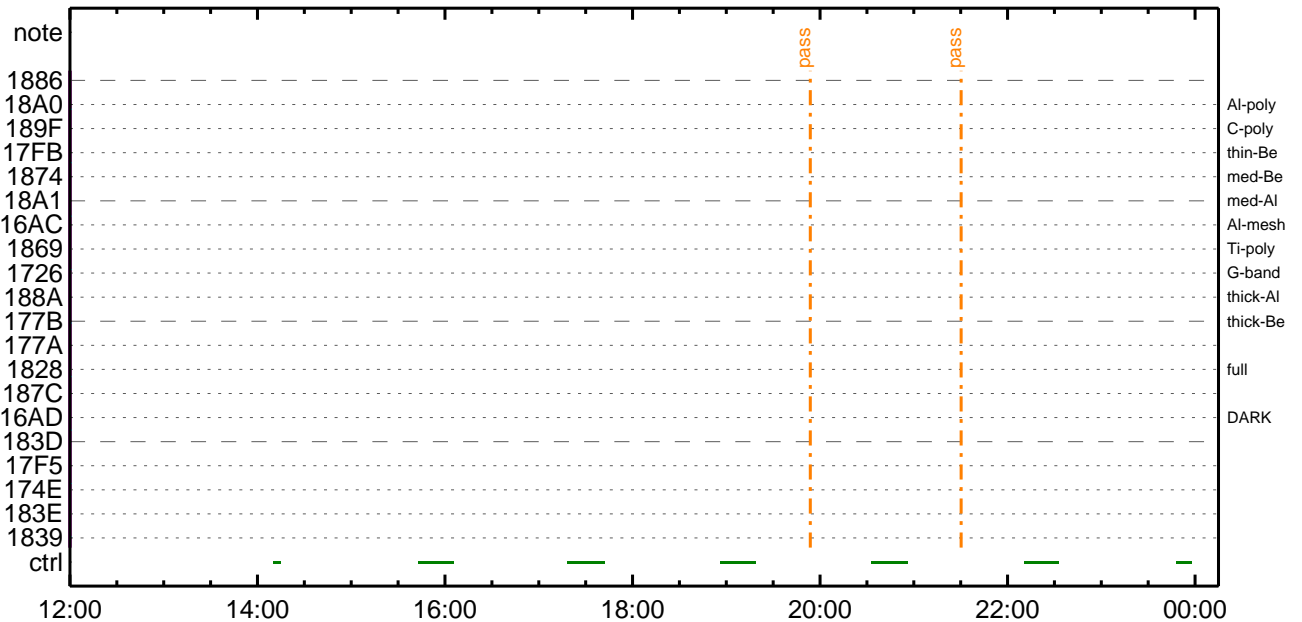
CMDI #0120 2011/09/02



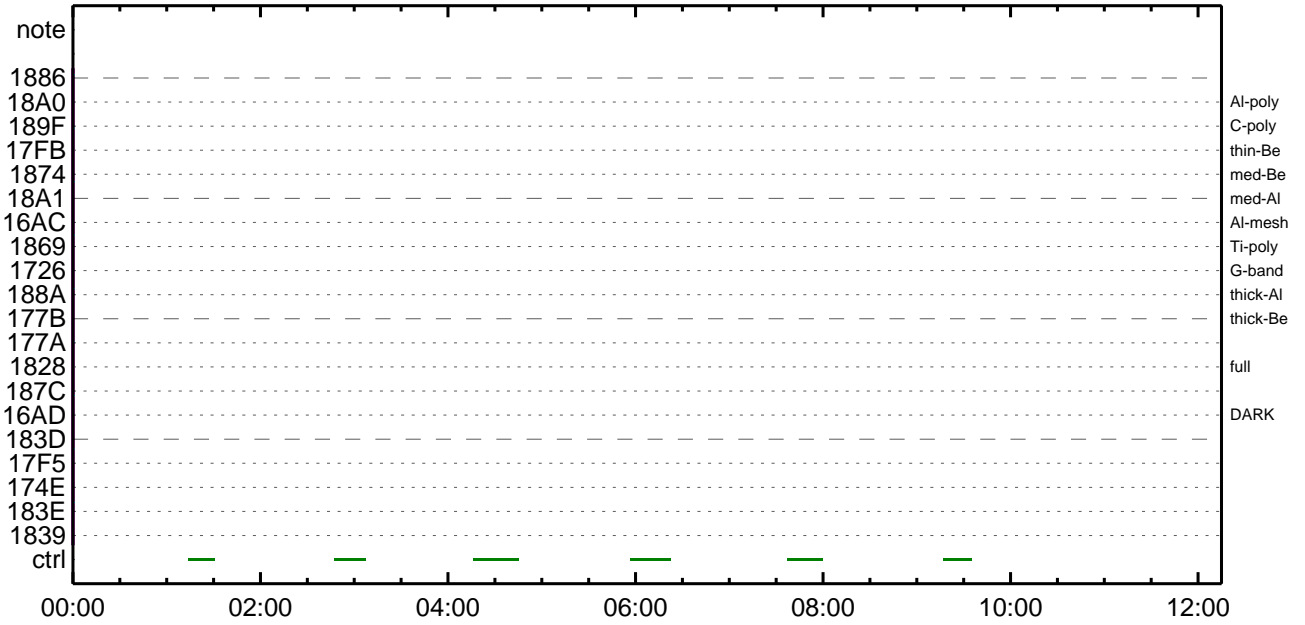
CMDI #0120 2011/09/03



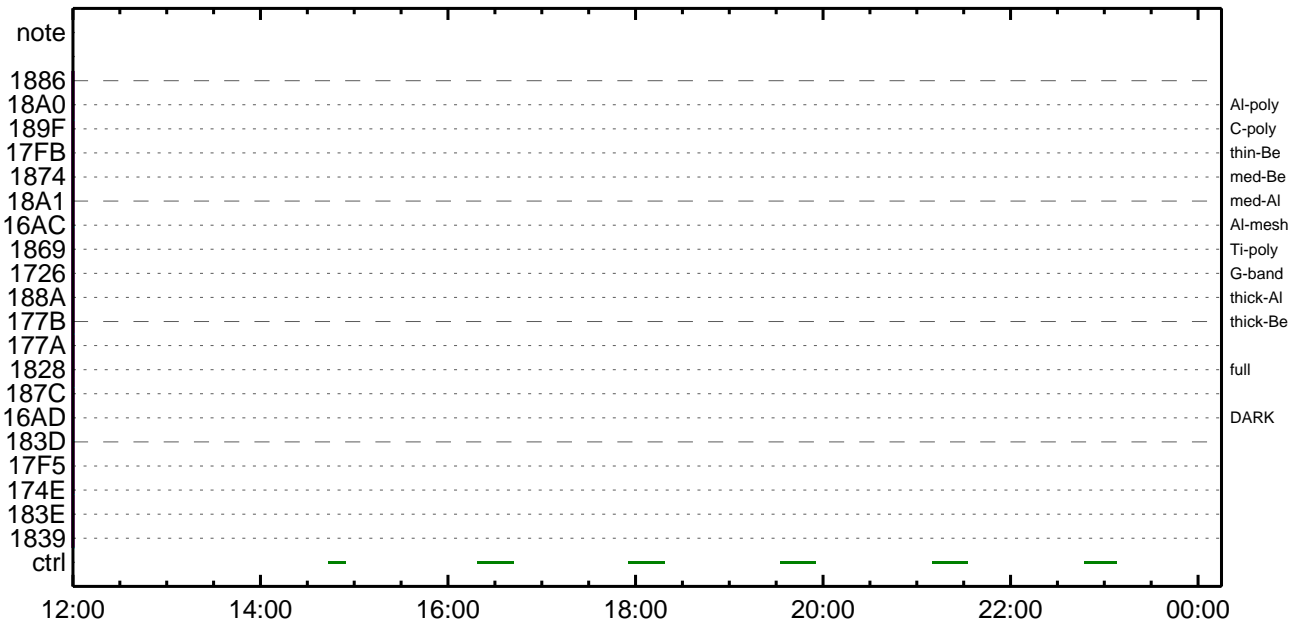
CMDI #0120 2011/09/03



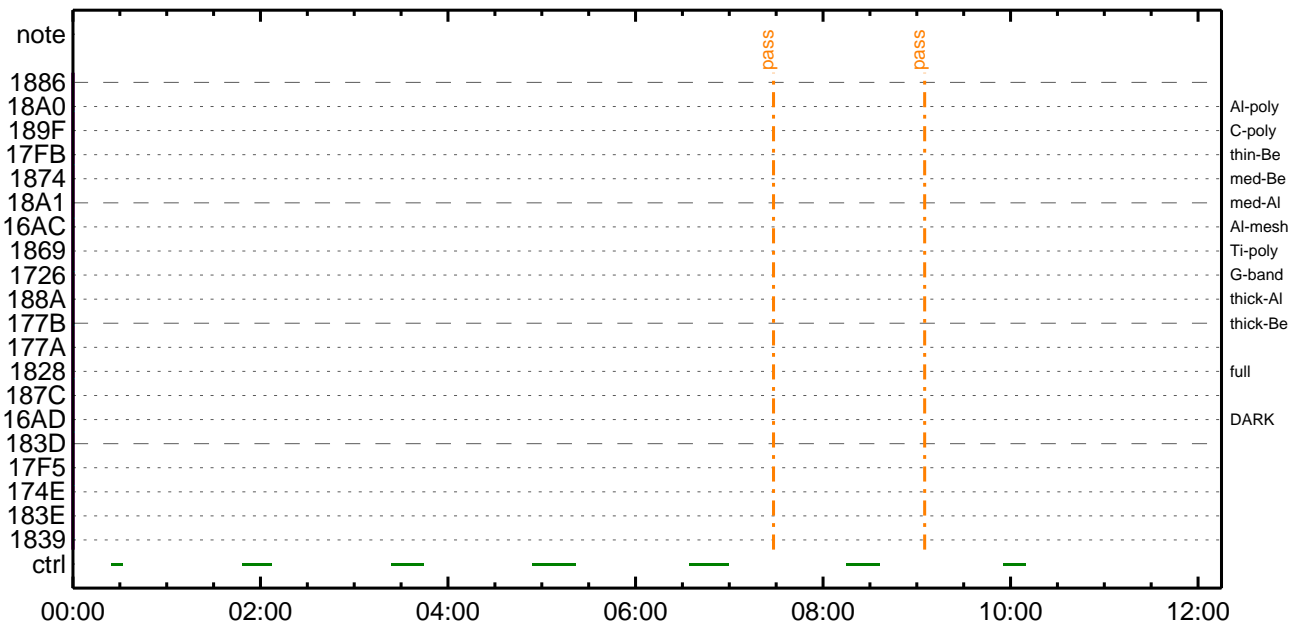
CMDI #0120 2011/09/04



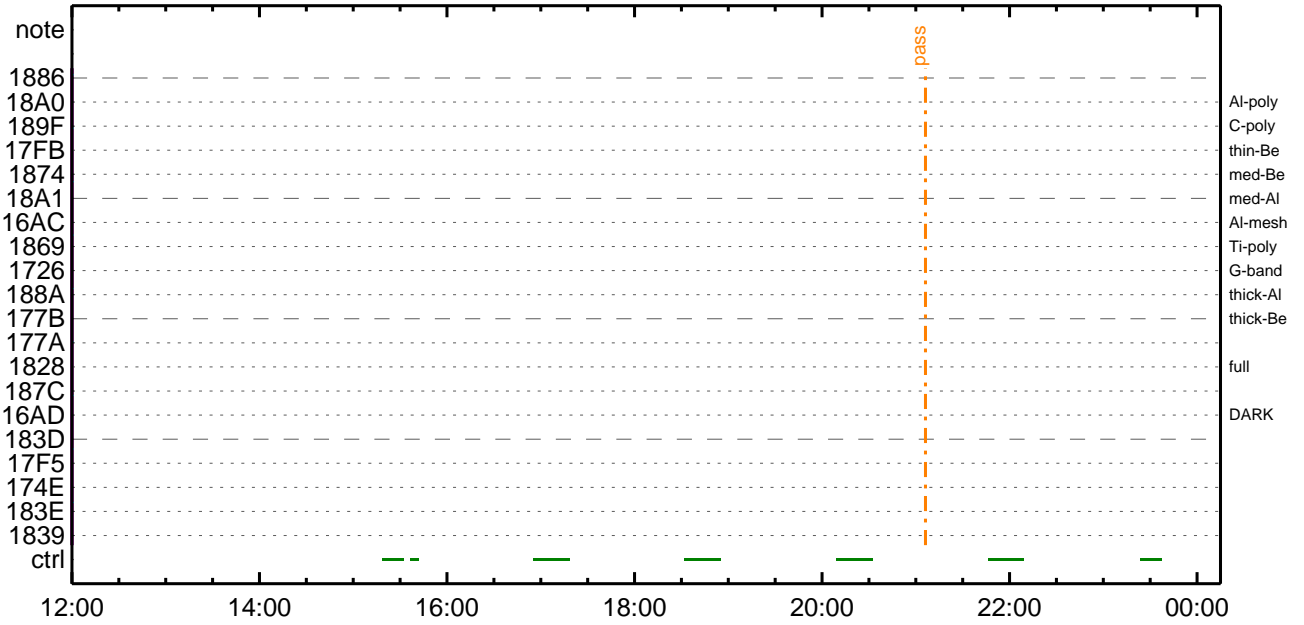
CMDI #0120 2011/09/04



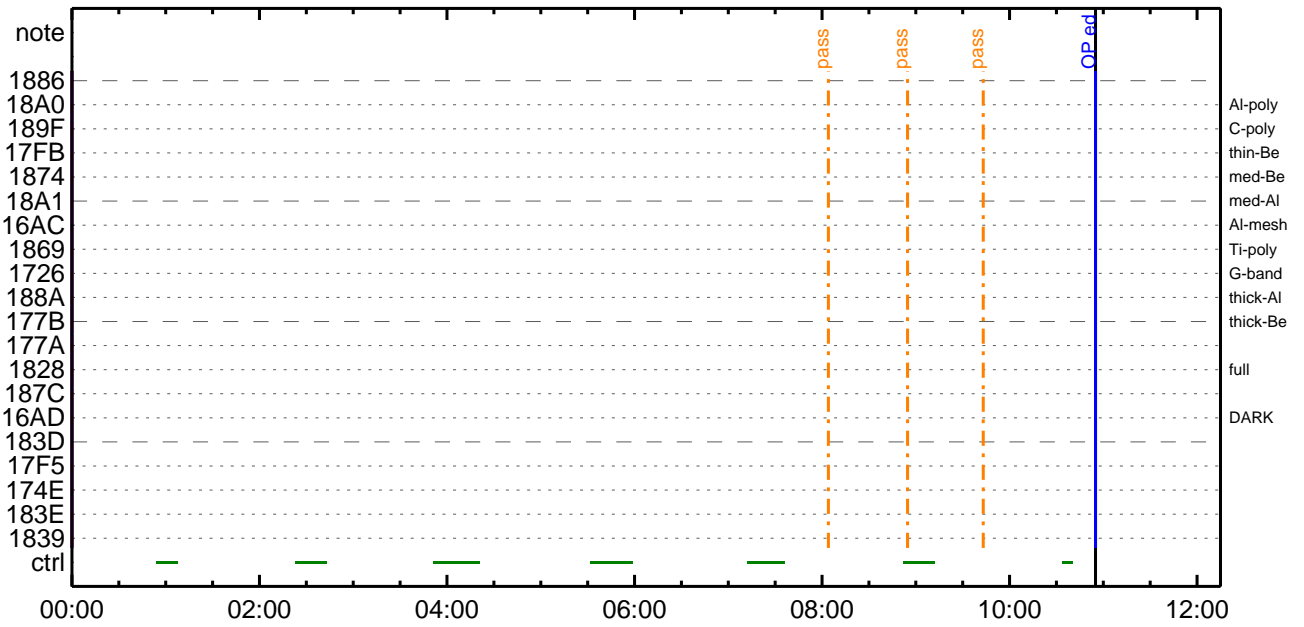
CMDI #0120 2011/09/05



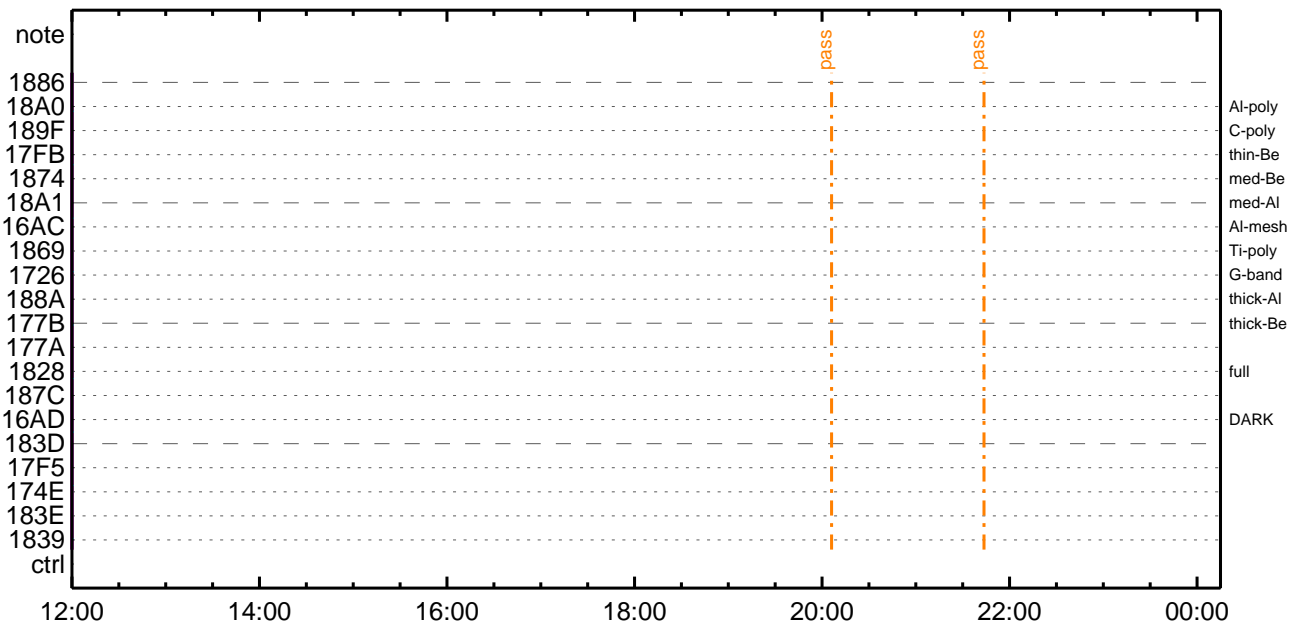
CMDI #0120 2011/09/05



CMDI #0120 2011/09/06



CMDI #0120 2011/09/06




```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Û;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
```


(a) Spacecraft Operation Procedure (real-commands)

```
main-297 2011-09-01 11:58:52 145 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YFYOYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿òÁø•µ°È»Í×ÁÇøÍYçYÁY×Yí;¼YÉ;ÈÈèµ•íÉ;ÈÈÈ¼°ÇÔø•ø¿¼í¹çøÍ;çÀ®, ùø¹øÈøÈøÇÁ+¿®ø•øÈøøøø³øÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-262:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Resume FG table (auto mode) >
0036 +. DC 07-F0 MDP_FG_CTRL_AUTO
0037 BC (50)
0038 . C. -----
0039 C. MDP_FG_CTRL_MODE = AUTO [ ]
0040 C. -----
0041 C.
0042 C. *****
0043 C. SOT TI command set
0044 C. *****
0045 C. Execute, after the success of TBL upload.
0046 +. TI 2011-09-01 09:59:18.0
0047 DC 07-F0 MDP_SOT_MODE_OBSV
0048 BC (40)
0049 . C. -----
0050 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0051 C. -----
0052 C.
0053 C. Only when FG_CTRL_AUTO is used in RT.
0054 +. TI 2011-09-01 09:59:20.0
0055 DC 07-F0 MDP_FG_CTRL_AUTO
0056 BC (50)
0057 . C. -----
0058 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0059 C. -----
0060 C. ***** SOT END *****
0061 C.
0062 C. ***** XRT START *****
0063 C.
0064 +. DC 07-F0 MDP_XRT_CTRL_MANU
0065 BC (c1)
0066 + DC 07-F0 MDP_XRT_MODE_STBY
0067 BC (c3)
0068 . C. ----- Success Verify ? OK / NG_____
0069 C.
0070 C. XRT Obs. Table Upload
0071 . S. RAM ram-291:MDP_OBS_X
0072 ( )
0073 C.
0074 +. DC 07-F0 MDP_DUMP_XRTTBL
0075 BC (84 07 00 00 00 3a d4)
0076 . C. ----- Comparison Check ? OK / ERR _____
0077 C.
0078 C.
0079 +. DC 07-F0 MDP_XRT_ROI_SET
0080 BC (cd 01 b1 b1 04 04)
0081 + DC 07-F0 MDP_XRT_ROI_SET
0082 BC (cd 02 b1 b1 08 08)
0083 + DC 07-F0 MDP_XRT_ROI_SET
0084 BC (cd 03 b1 b1 08 08)
0085 + DC 07-F0 MDP_XRT_ROI_SET
0086 BC (cd 04 b1 b1 06 06)
0087 + DC 07-F0 MDP_XRT_ROI_SET
0088 BC (cd 05 85 83 06 06)
0089 + DC 07-F0 MDP_XRT_ROI_SET
0090 BC (cd 06 85 83 06 06)
0091 + DC 07-F0 MDP_XRT_ROI_SET
0092 BC (cd 07 80 80 08 08)
0093 + DC 07-F0 MDP_XRT_ROI_SET
0094 BC (cd 08 80 80 20 20)
0095 + DC 07-F0 MDP_XRT_ROI_SET
```

```
0096 BC (cd 09 80 80 20 08)
0097 + DC 07-F0 MDP_XRT_ROI_SET
0098 BC (cd 0a 80 80 08 20)
0099 + DC 07-F0 MDP_XRT_ROI_SET
0100 BC (cd 0f 80 80 06 06)
0101 + DC 07-F0 MDP_XRT_ROI_SET
0102 BC (cd 10 80 80 08 08)
0103 + DC 07-F0 MDP_XRT_FLD_ENA
0104 BC (d8)
0105 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0106 BC (c8)
0107 + DC 07-F0 MDP_XRT_AEC_RESET
0108 BC (d0)
0109 + DC 07-F0 MDP_XRT_ARS_DIS
0110 BC (d5)
0111 + DC 07-F0 MDP_XRT_FLD_RESET
0112 BC (da)
0113 + DC 07-F0 MDP_XRT_QT_PROG_SET
0114 BC (c4 12)
0115 + DC 07-F0 MDP_XRT_FL_PROG_SET
0116 BC (c5 0d)
0117 . C. ----- Success Verify ? OK / NG ____
0118 C.
0119 C.
0120 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0121 C.
0122 + DC 07-F0 MDP_XRT_MODE_OBSV
0123 BC (c2)
0124 + TI 2011-09-01 09:59:02.0
0125 DC 07-F0 MDP_XRT_MODE_OBSV
0126 BC (c2)
0127 . C. ----- Success Verify ? OK / NG ____
0128 C.
0129 C. ***** XRT END *****
0130 C.
0131 . C. ***** MDP 'úÃîâî»ö¼ýðËÄð¹æDCBC•x²è *****
0132 C. (%ã°îÿÓÿÄÿÈÿßÿËÿáÿçÿÈæ%¼æ¼Ä»Û¹æè)
0133 . S. DC-BC dcbc-402:DCBC
0134 (MDP_known_event)
0135 C.
0136 C.
0137 . C. ***** ÿDÿ¹•Ï Daily±¿ÎÑæÉ´Ø¹æèDCBC•x²è *****
0138 . S. DC-BC dcbc-153:DCBC
0139 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0140 C.
0141 C.
0142 . C. ;ãLOSÿÄÿ§ÿÄÿÿ¼Ä»Û;ä
0143 C.
0144 . C. ***** LOS *****
0145 C.
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Sep 01, 11 11:59

XRT_OGLIST_0120.chk

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*** OP Sequence for XRT ***

2011/09/01	10:10:00.0	AOCS_Orе-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04	00	00	00	00
2011/09/01	10:39:54.0	XRT_CTRL_MANU_421_OG [0x1a5]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/01	10:40:00.0	AOCS_Orе-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	02	00	00	00	00
2011/09/01	10:49:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2011/09/01	10:49:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/09/01	10:49:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/09/01	10:49:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/09/01	10:49:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/09/01	10:49:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/09/01	10:49:56.0	XRT_QT_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2011/09/01	10:49:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2011/09/01	10:50:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/09/01	14:31:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/01	14:31:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/09/01	14:31:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/09/01	14:34:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/09/01	14:41:30.0	XRT_Custom_418_OG [0x1a2]							
2011/09/01	14:42:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/09/01	16:06:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/01	16:06:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/09/01	16:06:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/09/01	16:09:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/09/01	17:31:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/01	17:31:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2011/09/01	17:32:00.0	AOCS_Orе-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	00	00
2011/09/01	17:32:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/09/01	17:32:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/09/01	17:32:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/09/01	17:34:58.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	07			
2011/09/01	17:35:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/09/01	17:42:00.0	AOCS_Orе-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	02	00	00	00	00
2011/09/01	19:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/01	20:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2011/09/01	20:02:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/09/01	20:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/09/01	20:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/09/01	20:02:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/09/01	20:02:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/09/01	20:02:56.0	XRT_QT_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2011/09/01	20:02:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2011/09/01	20:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/09/01	20:57:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/01	20:57:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/09/01	20:57:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/09/01	21:00:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/09/01	21:20:30.0	XRT_Custom_418_OG [0x1a2]							
2011/09/01	21:21:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							

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2011/09/01	22:34:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/01	22:34:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/09/01	22:34:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/09/01	22:37:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/09/01	22:55:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/09/01	22:56:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/02	00:12:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/02	00:12:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/09/02	00:12:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/09/02	00:15:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/09/02	00:20:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/09/02	00:21:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/02	01:36:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/02	01:36:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/09/02	01:36:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/09/02	01:36:04.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/09/02	01:39:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/09/02	01:54:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/09/02	01:55:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/02	03:10:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/02	03:10:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/09/02	03:10:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/09/02	03:13:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/09/02	03:31:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/09/02	03:32:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/02	04:40:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/02	04:40:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/09/02	04:40:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/09/02	04:40:34.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/09/02	04:43:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/09/02	05:09:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/09/02	05:10:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/02	05:52:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/02	05:52:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/09/02	05:53:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00				
2011/09/02	05:53:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/09/02	05:53:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/09/02	05:53:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/09/02	05:55:58.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2011/09/02	05:56:00.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/09/02	06:03:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	02 00 00 00 00				
2011/09/02	07:30:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04 00 00 00 00				
2011/09/02	10:29:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/02	10:30:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	02 00 00 00 00				
2011/09/02	10:32:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/09/02	10:32:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/09/02	10:32:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/09/02	10:32:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/09/02	10:32:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/09/02	10:32:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2011/09/02	10:32:56.0	XRT_QT_PROG_SET_448_OG [0x1c0]								

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2011/09/02	10:32:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2011/09/02	10:33:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/09/02	15:06:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/09/02	15:06:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/09/02	15:06:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/09/02	15:09:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/09/02	15:29:00.0	XRT_Custom_418_OG [0x1a2]					
2011/09/02	15:30:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/09/02	16:42:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/09/02	16:42:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/09/02	16:42:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/09/02	16:45:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/09/02	17:06:00.0	XRT_Custom_418_OG [0x1a2]					
2011/09/02	17:07:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/09/02	17:55:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/09/02	17:55:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2011/09/02	17:56:00.0	AOCS_Or-e-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00	00
2011/09/02	17:56:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2011/09/02	17:56:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2011/09/02	17:56:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/09/02	17:58:58.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07
2011/09/02	17:59:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/09/02	18:06:00.0	AOCS_Or-e-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	02 00 00 00	00
2011/09/02	20:05:54.0	XRT_CTRL_MANU_426_OG [0x1aa]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/09/02	20:20:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00
2011/09/02	20:20:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/09/02	20:20:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/09/02	20:20:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/09/02	20:20:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/09/02	20:20:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/09/02	20:20:56.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12
2011/09/02	20:20:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2011/09/02	20:21:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/09/02	21:34:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/09/02	21:34:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/09/02	21:34:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/09/02	21:37:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/09/02	21:56:30.0	XRT_Custom_418_OG [0x1a2]					
2011/09/02	21:57:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/09/02	23:11:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/09/02	23:11:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/09/02	23:11:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/09/02	23:14:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/09/02	23:28:00.0	XRT_Custom_418_OG [0x1a2]					
2011/09/02	23:29:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/09/03	00:45:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/09/03	00:45:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/09/03	00:45:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	

2011/09/03	00:48:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/09/03	00:55:00.0	XRT_Custom_418_OG [0x1a2]							
2011/09/03	00:56:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/09/03	02:11:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/03	02:11:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/09/03	02:11:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/09/03	02:14:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/09/03	02:31:00.0	XRT_Custom_418_OG [0x1a2]							
2011/09/03	02:32:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/09/03	03:40:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/03	03:40:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/09/03	03:40:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/09/03	03:43:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/09/03	04:08:30.0	XRT_Custom_418_OG [0x1a2]							
2011/09/03	04:09:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/09/03	05:18:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/03	05:18:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/09/03	05:18:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/09/03	05:21:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/09/03	05:30:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/09/03	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/03	05:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/09/03	06:00:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/09/03	06:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/09/03	06:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/09/03	06:02:58.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2011/09/03	06:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/09/03	07:30:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2011/09/03	10:52:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/09/03	10:53:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				