

# XRT Timeline to be uploaded on 2011/10/11

Period: 2011/10/11 10:07:00 - 2011/10/15 10:25:00

\* \* \* \* \*

Normal mode

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## XOB #18AE: AR Standard-A(Filter-Ratio) with PFB, shorter thin-Be, thick Al and Al/Poly context, 384x384 at 1064 1048 (all), 150s cad

Term	Pointing (x, y)	Comment
10/11 10:20:00 - 10/11 11:16:54	Track ( 600.0, 296.0) @ 10/11 10:17:00	# OP start + 10min, HOP204
10/11 21:33:00 - 10/11 22:29:54	Track ( 666.1, 303.5) @ 10/11 21:30:00	HOP204
10/12 08:33:00 - 10/12 09:29:54	Track ( 723.6, 311.4) @ 10/12 08:30:00	HOP204
10/12 22:06:00 - 10/12 23:02:54	Track ( 783.6, 321.9) @ 10/12 22:03:00	HOP204
<b>PROG= 10 Inf.-time(s)</b>		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 19 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al close Safe Dark 16.0s	Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Open/G-band	Open/G-band open Safe Norm 63ms	Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
└─ Seqn= 96 4-time(s) 2.0sec		
Al-poly/Open	thin-Be/Open close Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/Ti-poly	Open/thick-Be close Safe Norm 1.00s	Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open close Safe Norm 5.66s	Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Al close Safe Norm 16.0s	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 62 15-time(s) 150.0sec		
thin-Be/Open	Open/thick-Al close Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/Ti-poly	Open/thick-Be close Safe Norm 1.00s	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 15.0sec
thin-Be/Open	Open/thick-Al close Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
Open/Ti-poly	Open/thick-Be close Safe Norm 1.00s	Obs 1x1 384x384 (1064, 1048) Q=95 3 1 15.0sec
thin-Be/Open	Open/thick-Al close Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
Open/Ti-poly	Open/thick-Be close Safe Norm 1.00s	Obs 1x1 384x384 (1064, 1048) Q=95 3 2 15.0sec
thin-Be/Open	Open/thick-Al close Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec
Open/Ti-poly	Open/thick-Be close Safe Norm 1.00s	Obs 1x1 384x384 (1064, 1048) Q=95 3 3 15.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center)	Comp. AEC Buffer Interval

## XOB #1892: CME watch - 4x4 - AEC 2 - Ti/poly Al/Mesh

Term	Pointing (x, y)	Comment
10/11 11:20:00 - 10/11 11:32:54	Fixed ( 936.0, 0.0)	HOP130 Full Disc SLot Scans (1/15)
10/11 11:36:00 - 10/11 11:48:54	Fixed ( 468.0, 0.0)	(2/15)
10/11 11:52:00 - 10/11 12:04:54	Fixed ( 0.0, 0.0)	(3/15)
10/11 12:08:00 - 10/11 12:20:54	Fixed ( -468.0, 0.0)	(4/15)
10/11 12:24:00 - 10/11 12:36:54	Fixed ( -936.0, 0.0)	(5/15)
10/11 12:40:00 - 10/11 12:46:54	Fixed ( 815.0, 470.0)	(6/15)
10/11 12:50:00 - 10/11 13:02:54	Fixed ( 411.0, 847.0)	(7/15)
10/11 13:06:00 - 10/11 13:18:54	Fixed ( 0.0, 941.0)	(8/15)
10/11 13:22:00 - 10/11 13:34:54	Fixed ( -411.0, 847.0)	(9/15)
10/11 13:38:00 - 10/11 13:44:54	Fixed ( -815.0, 470.0)	(10/15)
10/11 13:48:00 - 10/11 13:54:54	Fixed ( 815.0, -470.0)	(11/15)
10/11 13:58:00 - 10/11 14:10:54	Fixed ( 411.0, -847.0)	(12/15)
10/11 14:14:00 - 10/11 14:53:54	Fixed ( 0.0, -941.0)	(13/15)
10/11 14:57:00 - 10/11 15:09:54	Fixed ( -411.0, -847.0)	(14/15)
10/11 15:13:00 - 10/11 15:18:54	Fixed ( -815.0, -471.0)	(15/15)

## PROG= 05 Inf.-time(s)

└─ Subr= 1 1-time(s) 120.0sec		
└─ Seqn= 66 1-time(s) 4.0sec		
Open/Ti-poly	Open/thick-Al close Safe Norm 125ms	Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec
Open/Al-mesh	Open/thick-Al close Safe Norm 250ms	Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center)	Comp. AEC Buffer Interval

## XOB #18AF: AR Standard-A(Filter-Ratio) with PFB, shorter thin-Be, thick Al and Al/Poly context, 384x384 at 1064 1048 (all), 4min cad

Term	Pointing (x, y)	Comment
10/11 15:22:00 - 10/11 17:49:00	Track ( -745.4, 342.4) @ 10/11 15:19:00	AR11314 obs.
10/11 18:25:30 - 10/11 21:29:54	Track ( -731.8, 340.3) @ 10/11 18:22:30	# AR11314 Obs.
10/11 22:33:00 - 10/12 05:59:54	Track ( -712.5, 337.5) @ 10/11 22:30:00	AR11314 obs.
10/12 06:13:00 - 10/12 08:08:00	Track ( -673.8, 332.5) @ 10/12 06:10:00	#AR11314 obs.
10/12 09:33:00 - 10/12 17:59:54	Track ( -655.9, 330.4) @ 10/12 09:30:00	AR11314 obs.
10/12 18:13:00 - 10/12 21:40:00	Track ( -606.3, 325.3) @ 10/12 18:10:00	#AR11314 obs.
10/12 23:06:00 - 10/13 06:01:54	Track ( -576.5, 322.7) @ 10/12 23:03:00	AR11314 obs.
10/13 06:15:00 - 10/13 10:27:00	Track ( -530.7, 319.1) @ 10/13 06:12:00	#AR11314 obs.

## PROG= 20 Inf.-time(s)

└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 19 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al close Safe Dark 16.0s	Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Open/G-band	Open/G-band open Safe Norm 63ms	Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
└─ Seqn= 96 4-time(s) 2.0sec		
Al-poly/Open	thin-Be/Open close Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/Ti-poly	Open/thick-Be close Safe Norm 1.00s	Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open close Safe Norm 5.66s	Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Al close Safe Norm 16.0s	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec

Subr= 2		1-time(s)		2.0sec											
Seqn= 62		11-time(s)		240.0sec											
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec			
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec			
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec			
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec			
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec			
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec			
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec			
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval				

**XOB #187C: Synoptic Q95 2x2 - Al/mesh(45/2048) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(88/2897) + Thin-Be(36)**

Term	Pointing (x, y)	Comment
10/11 18:15:30 - 10/11 18:22:24	Fixed ( 0.0, 0.0)	synoptic, shifted 12.5 min
10/12 06:03:00 - 10/12 06:09:54	Fixed ( 0.0, 0.0)	synoptic
10/12 18:03:00 - 10/12 18:09:54	Fixed ( 0.0, 0.0)	synoptic
10/13 06:05:00 - 10/13 06:11:54	Fixed ( 0.0, 0.0)	synoptic, shifted 2.0 min

Subr= 1		1-time(s)		12.0sec											
Seqn= 86		1-time(s)		4.0sec											
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec			
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec			
Seqn= 5		1-time(s)		2.0sec											
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec			
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec			
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec			
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec			
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec			
Seqn= 37		1-time(s)		4.0sec											
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec			
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec			
Seqn= 32		1-time(s)		2.0sec											
thin-Be/Open	thin-Be/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec			
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec			
Seqn= 4		1-time(s)		2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval				

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**Flare mode**

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**XOB #1869: Flare standard obs. multifilter (thin-Be,med-Al,thick-Be 384x384 - Al-poly 512x512 2x2)**

Term	Pointing (x, y)	Comment
10/11 10:20:00 - 10/11 11:16:54	Track ( 600.0, 296.0) @ 10/11 10:17:00	# OP start + 10min, HOP204
10/11 11:20:00 - 10/11 11:32:54	Fixed ( 936.0, 0.0)	HOP130 Full Disc SLOt Scans (1/15)
10/11 11:36:00 - 10/11 11:48:54	Fixed ( 468.0, 0.0)	(2/15)
10/11 11:52:00 - 10/11 12:04:54	Fixed ( 0.0, 0.0)	(3/15)
10/11 12:08:00 - 10/11 12:20:54	Fixed ( -468.0, 0.0)	(4/15)
10/11 12:24:00 - 10/11 12:36:54	Fixed ( -936.0, 0.0)	(5/15)
10/11 12:40:00 - 10/11 12:46:54	Fixed ( 815.0, 470.0)	(6/15)
10/11 12:50:00 - 10/11 13:02:54	Fixed ( 411.0, 847.0)	(7/15)
10/11 13:06:00 - 10/11 13:18:54	Fixed ( 0.0, 941.0)	(8/15)
10/11 13:22:00 - 10/11 13:34:54	Fixed ( -411.0, 847.0)	(9/15)
10/11 13:38:00 - 10/11 13:44:54	Fixed ( -815.0, 470.0)	(10/15)
10/11 13:48:00 - 10/11 13:54:54	Fixed ( 815.0, -470.0)	(11/15)
10/11 13:58:00 - 10/11 14:10:54	Fixed ( 411.0, -847.0)	(12/15)
10/11 14:14:00 - 10/11 14:53:54	Fixed ( 0.0, -941.0)	(13/15)
10/11 14:57:00 - 10/11 15:09:54	Fixed ( -411.0, -847.0)	(14/15)
10/11 15:13:00 - 10/11 15:18:54	Fixed ( -815.0, -471.0)	(15/15)
10/11 15:22:00 - 10/11 17:49:00	Track ( -745.4, 342.4) @ 10/11 15:19:00	AR11314 obs.
10/11 18:25:30 - 10/11 21:29:54	Track ( -731.8, 340.3) @ 10/11 18:22:30	# AR11314 Obs.
10/11 21:33:00 - 10/11 22:29:54	Track ( 666.1, 303.5) @ 10/11 21:30:00	HOP204
10/11 22:33:00 - 10/12 05:59:54	Track ( -712.5, 337.5) @ 10/11 22:30:00	AR11314 obs.
10/12 06:13:00 - 10/12 08:08:00	Track ( -673.8, 332.5) @ 10/12 06:10:00	#AR11314 obs.
10/12 08:33:00 - 10/12 09:29:54	Track ( 723.6, 311.4) @ 10/12 08:30:00	HOP204
10/12 09:33:00 - 10/12 17:59:54	Track ( -655.9, 330.4) @ 10/12 09:30:00	AR11314 obs.
10/12 18:13:00 - 10/12 21:40:00	Track ( -606.3, 325.3) @ 10/12 18:10:00	#AR11314 obs.
10/12 22:06:00 - 10/12 23:02:54	Track ( 783.6, 321.9) @ 10/12 22:03:00	HOP204
10/12 23:06:00 - 10/13 06:01:54	Track ( -576.5, 322.7) @ 10/12 23:03:00	AR11314 obs.
10/13 06:15:00 - 10/13 10:27:00	Track ( -530.7, 319.1) @ 10/13 06:12:00	#AR11314 obs.

Subr= 1		4-time(s)		2.0sec											
Seqn= 55		45-time(s)		20.0sec											
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec			
Seqn= 90		1-time(s)		2.0sec											

Subr= 2	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	1-time(s)		600.0sec										
Seqn= 89	Open/AI-mesh	Open/thick-AI	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/AI-mesh	Open/AI-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
1-time(s)		2.0sec											
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

\* \* \* \* \*

### Active Region Search

\* \* \* \* \*

NOT USED

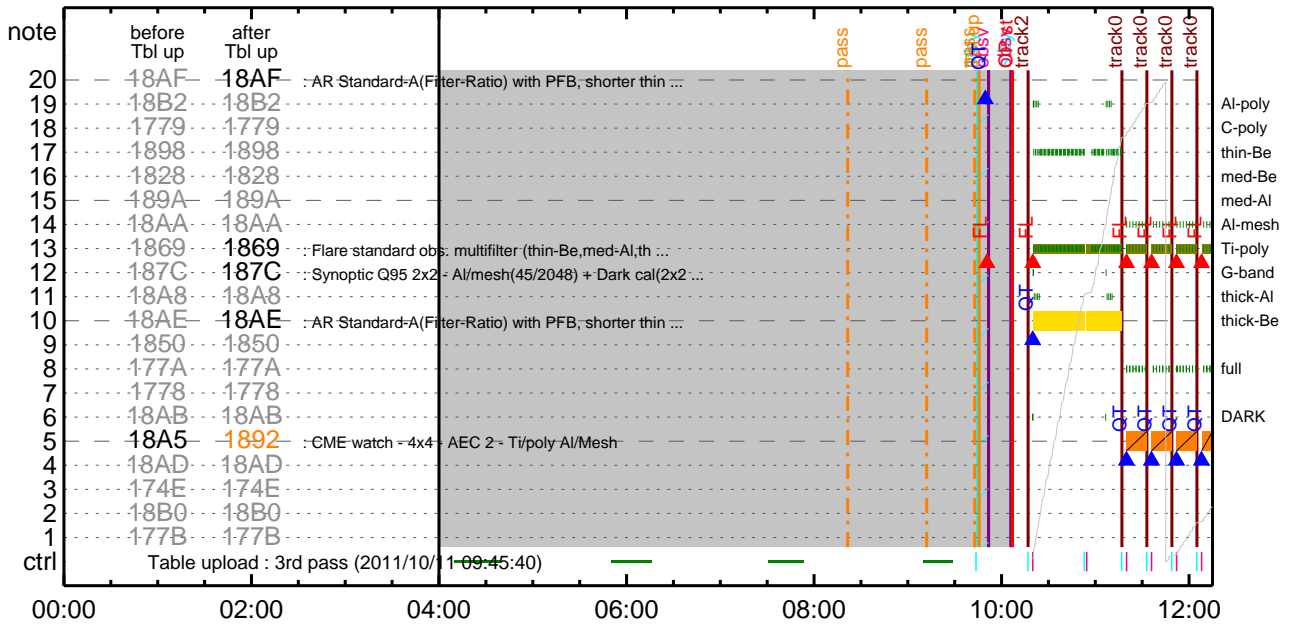
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### Flare Detection

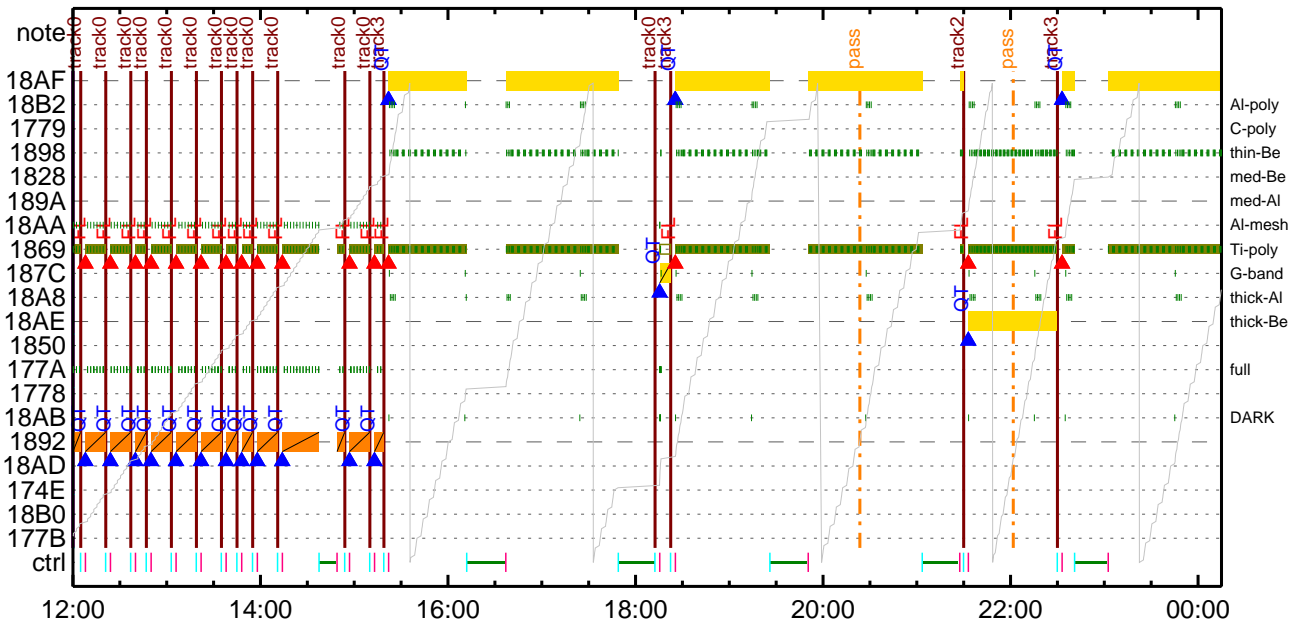
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FLD Patrol													
Term		Pointing (x, y)						Comment					
10/11 18:22:46 - 10/12 06:00:16	Track ( -731.8, 340.3)	⑩ 10/11 18:22:30 # AR11314 Obs.											
10/12 06:10:16 - 10/12 18:00:16	Track ( -673.8, 332.5)	⑩ 10/12 06:10:00 #AR11314 obs.											
10/12 18:10:16 - 10/13 06:02:16	Track ( -606.3, 325.3)	⑩ 10/12 18:10:00 #AR11314 obs.											
10/13 06:12:16 - 10/15 10:25:00	Track ( -530.7, 319.1)	⑩ 10/13 06:12:00 #AR11314 obs.											
Open/Ti-poly	Open/thick-AI	close	Safe	Norm	8ms	Obs	8x8	Q=50		30sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

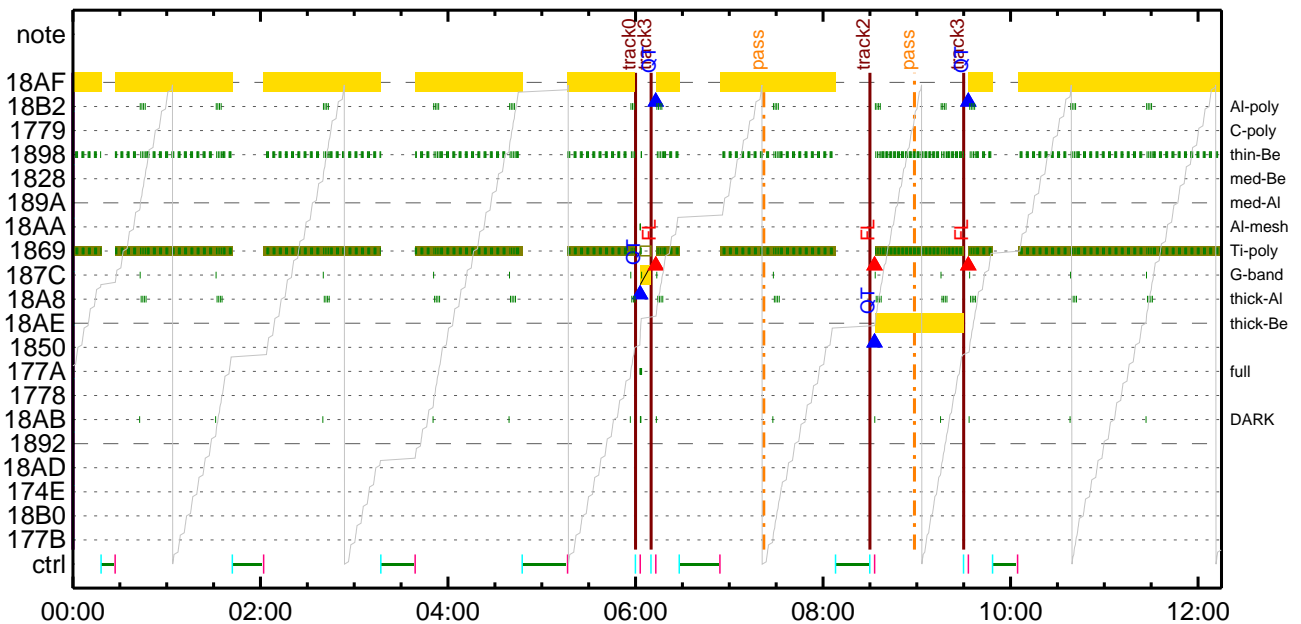
### CMDI #0203 2011/10/11



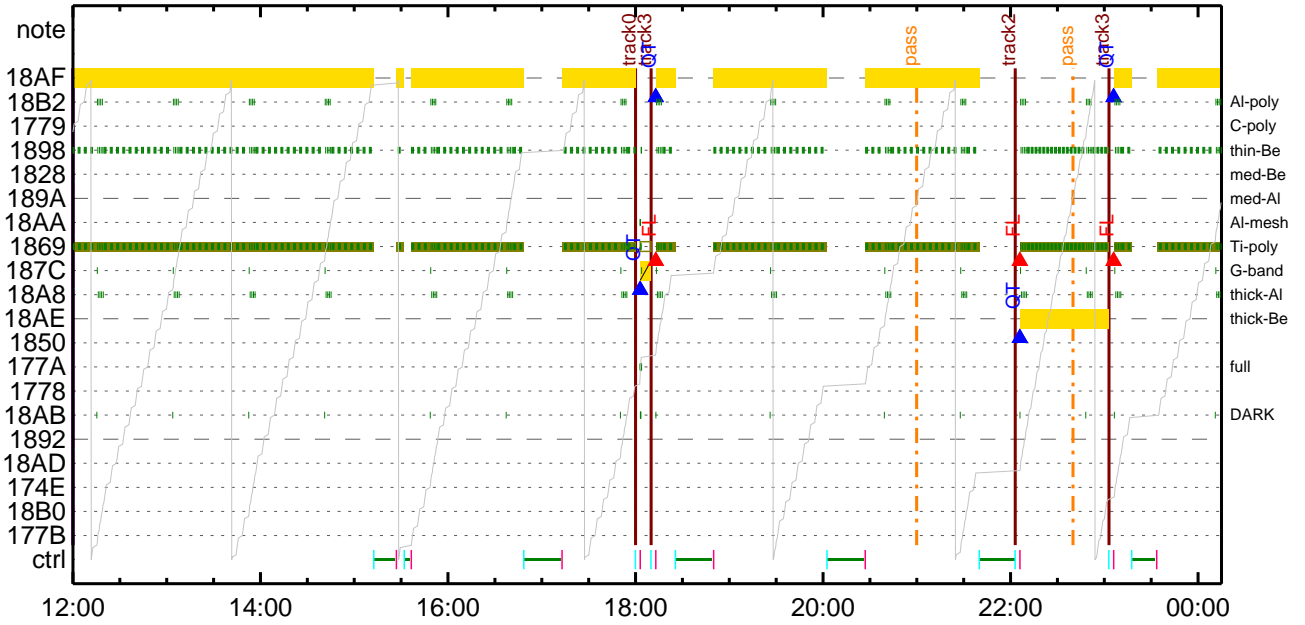
### CMDI #0203 2011/10/11



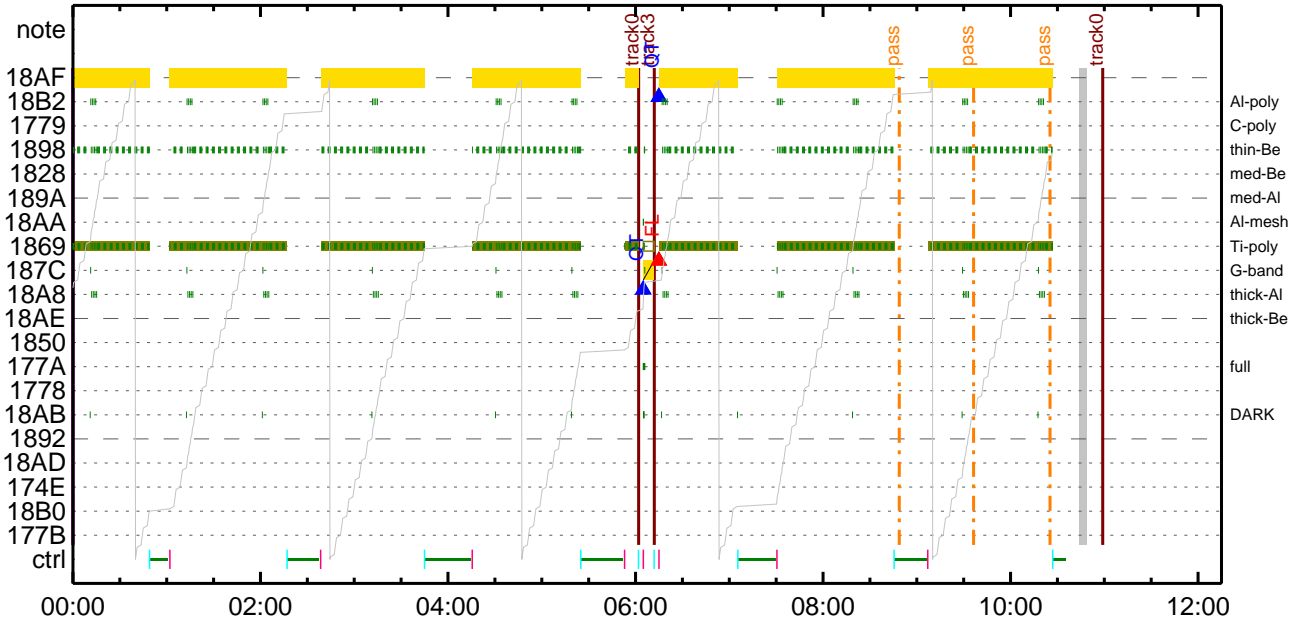
### CMDI #0203 2011/10/12



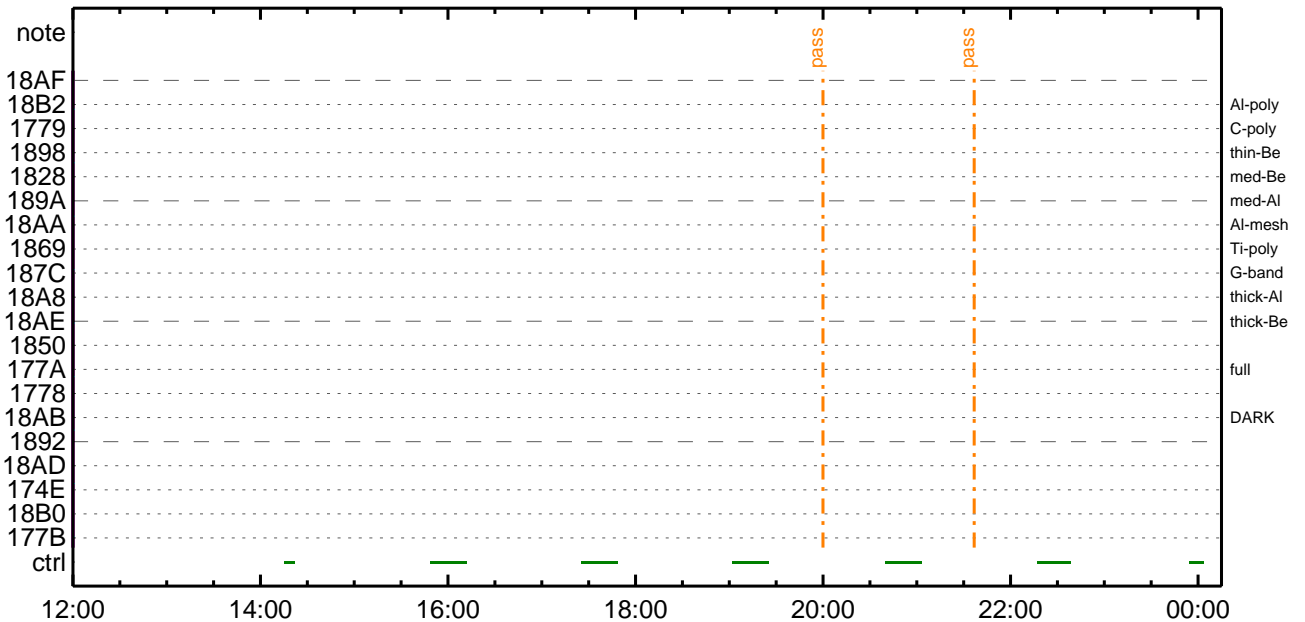
### CMDI #0203 2011/10/12



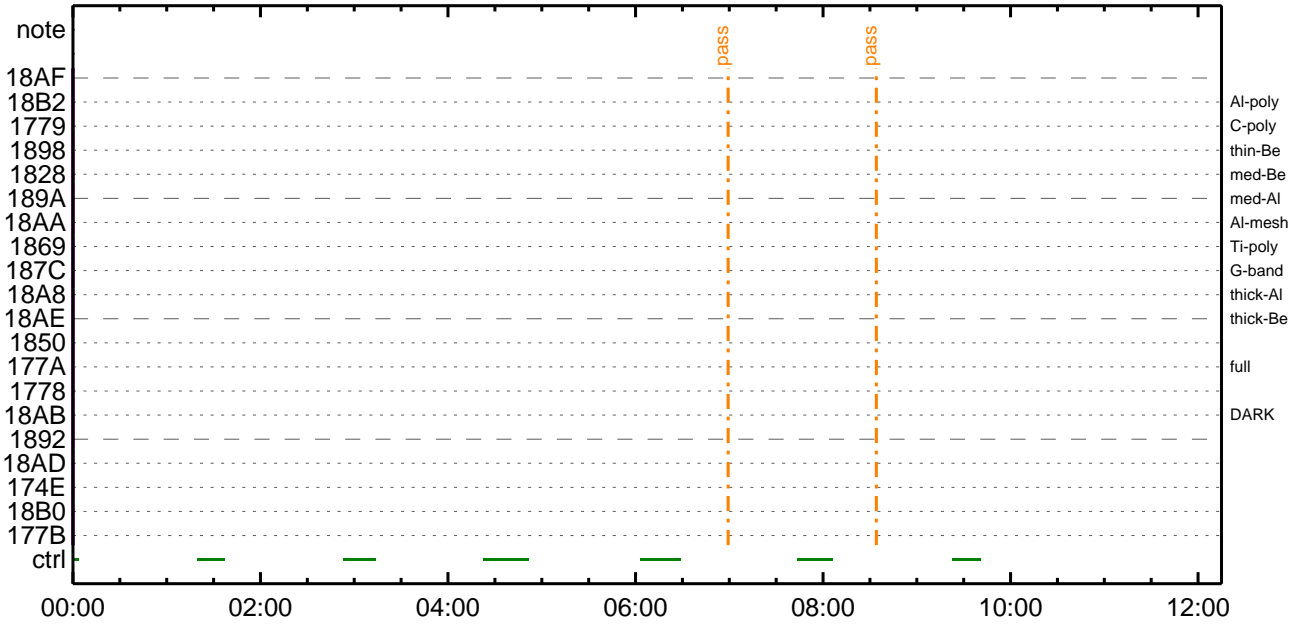
### CMDI #0203 2011/10/13



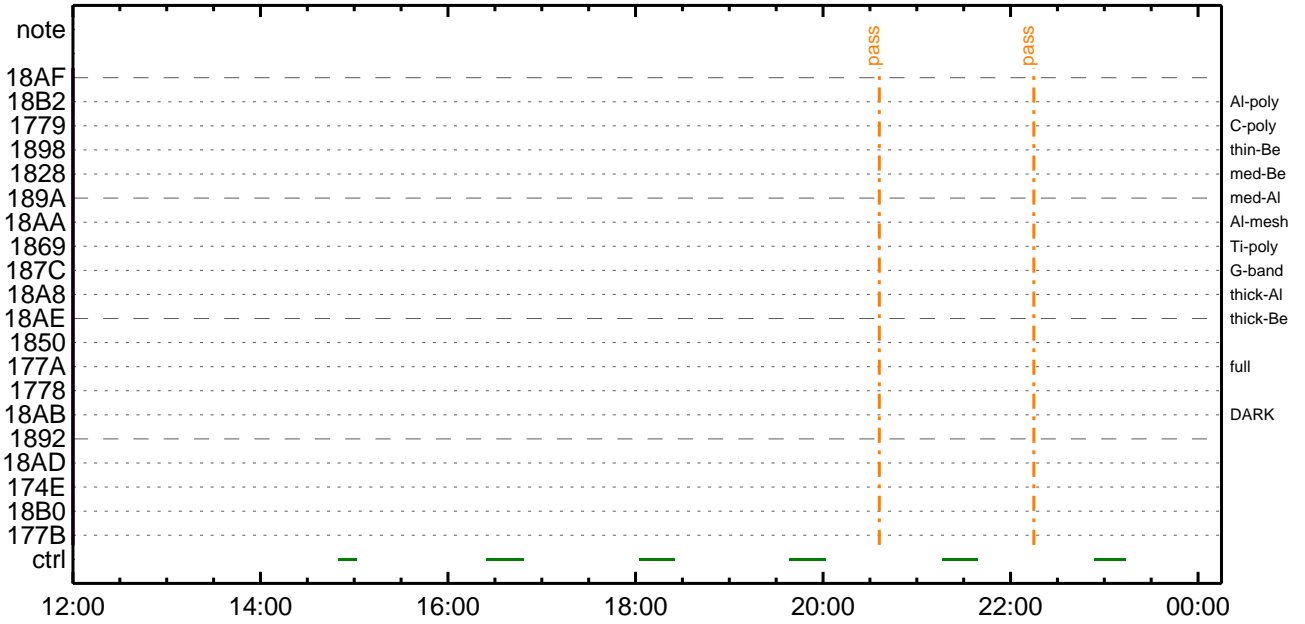
### CMDI #0203 2011/10/13



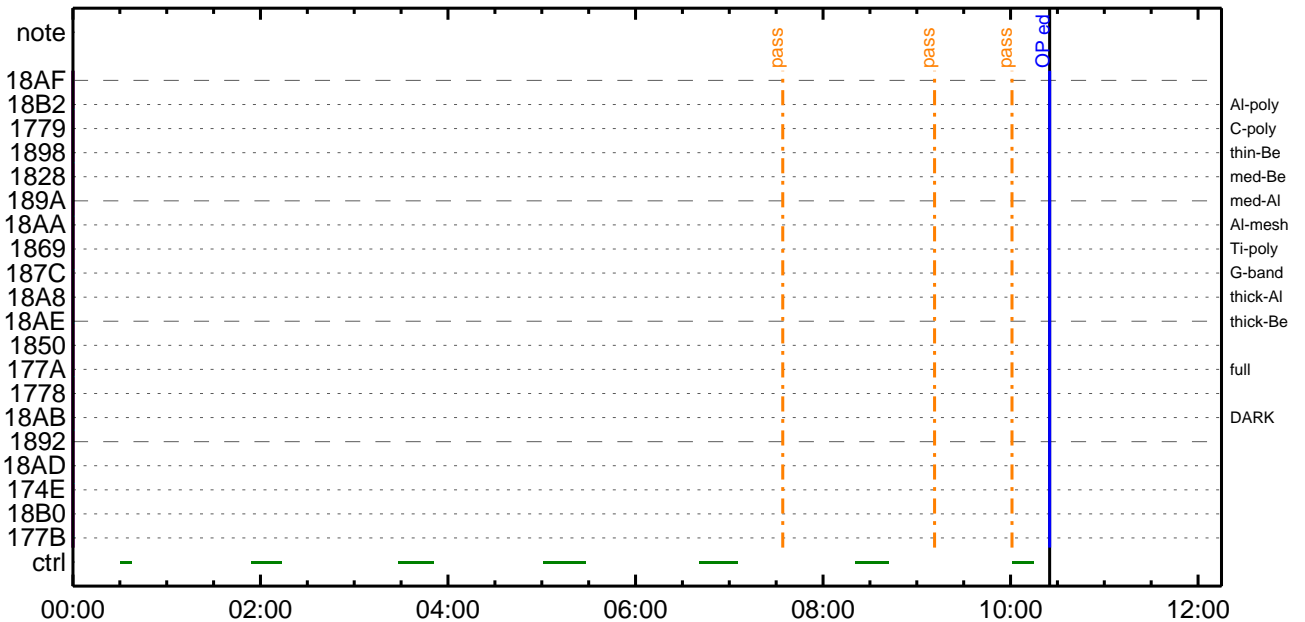
### CMDI #0203 2011/10/14



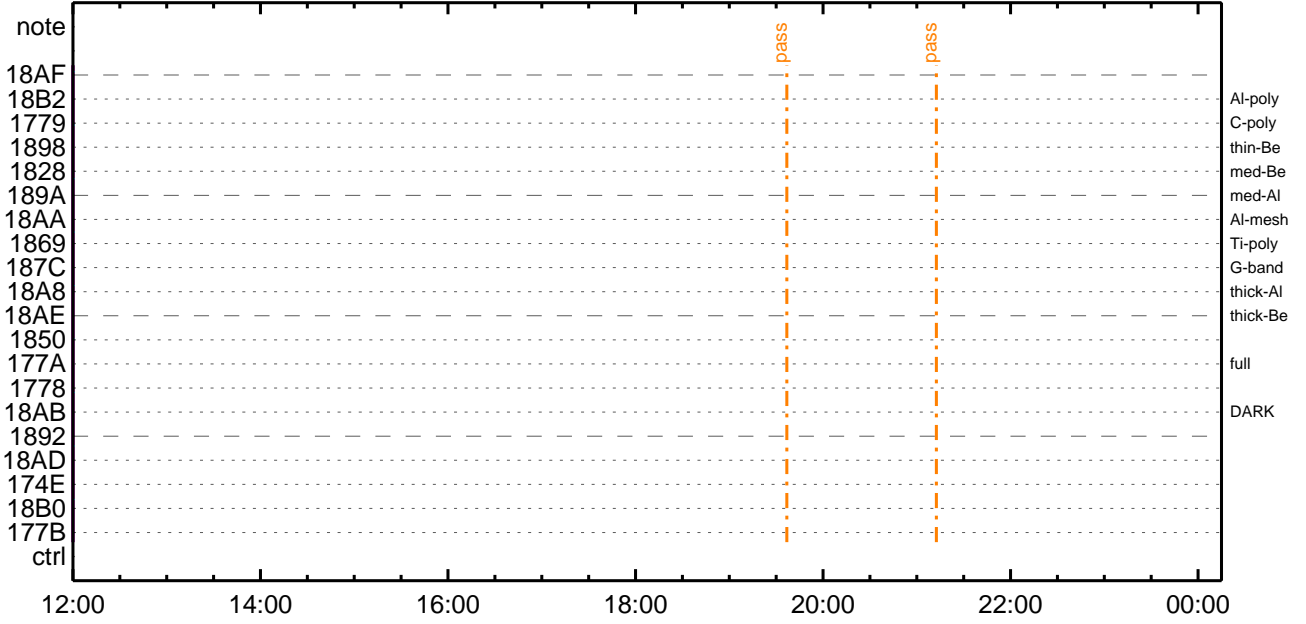
### CMDI #0203 2011/10/14



### CMDI #0203 2011/10/15



CMDI #0203 2011/10/15







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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-390:OP
0104 ( )
0105 S. OG og-390:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ª î»ò³ îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è² îOKò³ îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ª î»ò³ îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è² îOKò³ îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ª î»ò³ îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è² îOKò³ îÇ§
0165 C.
0166 C. ***** °E²¼ò î½Ã´ ¶Á°òEÈ¬ò°Á÷¿® (¼âµ-YAYOXx½ª ê½çòðÁÔæòÇ¼ª°¬òE¼i¹çòçðâ) *****
0167 C. DHUYâ;4YE;E½Y½, Y;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE | § OPOG UPLOAD²-Á÷¿®NG²î½i¹ç; ç°E²¼ò îTI-CMDÁ÷¿®²î½Á¹Ô²•²E²²²³²E; f
0180 C. ²²²²; çSET²E²DUMP²î½±²î½Y¹²ç¹Ô²²²³²E; f
0181 C.
0182 C. TIY³Y²Y²Y²E²òðÁDî¿¿(UT)
0183 +. TI 2011-10-11 10:02:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2011-10-11 10:02:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2011-10-11 10:02:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```

```

0194  C.
0195  +. TI 2011-10-11 10:06:59.5
0196  DC 01-B2 DHU_OP_START
0197  C.          çç[HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0198  C.
0199  C.   °È²¼ºïÄë%íñºîŷÄŷ§ŷÄŷ¹âîŧ
0200  C.          çç[HK1_TI_CMD_ENA/DIS]                    EQ      ENA
0201  C.          çç[HK1_TI_CMD_NUM]                        EQ      4
0202  C.          çç[HK1_NEXT_EXEC_PIM]                     EQ      DHU
0203  C.          çç[HK1_NEXT_EXEC_DC]                      EQ      0xB3
0204  C.
0205  C.   *****
0206  C.   TIîŧ°èŷÄŷôŷx
0207  C.   *****
0208  C.
0209  C.   TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210  +. DC 01-23 DHU_DMA_DMP_PRМ_SET
0211  BC      (03 ab 03 01 02)
0212  C.          çç[HK1_DMP_TOP_ADRS_1]                    EQ      07
0213  C.          çç[HK1_DMP_TOP_ADRS_0]                    EQ      2B
0214  C.          çç[HK1_DMP_BLOCK_NUM]                     EQ      3
0215  C.          çç[HK1_DMP_REPEAT_NUM]                    EQ      0
0216  C.          çç[HK1_DMA_DMP_PIM]                       EQ      DHU
0217  +. DC 01-22 DHU_MODE_CHNG
0218  BC      (07 0b f8)
0219  C.          çç[HK1_PKT_FORM_NO]                       EQ      7
0220  C.          çç[HK1_PKT_GEN_TIME]                       EQ      0.25 s
0221  C.          çç[HK1_S_TLM_BIT_RATE]                     EQ      32k
0222  C.          çç[HK1_X_TLM_BIT_RATE]                     EQ      4M
0223  C.          çç[HK1_DMP_CHK_FLG]                       EQ      EXEC
0224  C.
0225  C.   ŷÄŷôŷx½ªî»º³îç§
0226  C.          çç[HK1_DMP_CHK_FLG]                       EQ      NON
0227  C.
0228  C.   RAM ID=TI_TBLîŧ¾È¹ç•è²îOKº³îç§
0229  C.
0230  C.   DHUŷâ;¼ŷÈ;È¼ŷ¾.ŷî;¼ŷÈ;Èºðîáª¹
0231  +. DC 01-22 DHU_MODE_CHNG
0232  BC      (02 0a f8)
0233  C.          çç[HK1_PKT_FORM_NO]                       EQ      2
0234  C.          çç[HK1_PKT_GEN_TIME]                       EQ      0.5S
0235  C.          çç[HK1_S_TLM_BIT_RATE]                     EQ      32K
0236  C.          çç[HK1_X_TLM_BIT_RATE]                     EQ      4M
0237  C.
0238  C.   Stop EIS observation and temporarily disable EIS mode changes
0239  C.
0240  C.
0241  C.   ***** Start EIS operation (TI set) *****
0242  C.   Execute, after the success of OP upload.
0243  C.   Set EIS TI-commands
0244  +. TI 2011-10-11 10:06:30.0
0245  DC 07-FC EIS_MODE_MANU
0246  BC      (21 02)
0247  +. TI 2011-10-11 10:06:40.0
0248  DC 07-FC EIS_MODE_CHG_DIS
0249  BC      (22)
0250  C.          [ ] [HK1_TI_CMD_NUM]                      EQ      2 COUNTUP
0251  C.   ***** End EIS operation (TI set) *****
0252  C.
0253  C.
0254  C.   *****
0255  C.   SOT TI command set
0256  C.   *****
0257  C.   Execute, after the success of OP upload.
0258  +. TI 2011-10-11 10:06:16.0
0259  DC 07-F0 MDP_SOT_MODE_STBY
0260  BC      (41)
0261  C.   -----
0262  C.   HK1_TI_CMD_NUM      = 1 CNTUP [ ]
0263  C.   -----
0264  C.   ***** SOT END *****
0265  C.
0266  C.   ***** XRT START *****
0267  C.   Execute, after the success of OP upload.
0268  +. TI 2011-10-11 10:06:00.0
0269  DC 07-F0 MDP_XRT_MODE_STBY
0270  BC      (c3)
0271  C.          [ ] [HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0272  C.
0273  C.   ***** XRT END *****
0274  C.
0275  C.   ***** MDP `úÃîñî»ö¼ŷºÈÃº¹ºèDCBC•x²è *****
0276  C.   (¼ª°îŷÄŷÈŷŷŷÄŷçŷèÈ¼ºª¼ª»ŧº¹è)
0277  C.   S. DC-BC dcbc-402:DCBC
0278  C.   (MDP_known_event)
0279  C.
0280  C.
0281  C.   ***** ŷDŷ¹.î Daily±çîñºè'Øº¹ºèDCBC•x²è *****
0282  C.   S. DC-BC dcbc-153:DCBC
0283  C.   (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284  C.
0285  C.
0286  C.   ;ãLOSŷÄŷ§ŷÄŷ¹¼ª»ŧ;ã
0287  C.
0288  C.   ***** LOS *****
0289  C.

```



(a) Spacecraft Operation Procedure (real-commands)

```
main-392 2011-10-11 11:35:30 141 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YFÝÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. ÁÍ;Ëð¿ðÁð•µ°È»Í×ÁÇçÁÍYçYÁY×YÍ;¼YÉ;ËÈÈµ•íÍË;ËðÈ¼°ÇÓð•ð¿¼Í¹çðÍ;çÁ®, ùð¹ðÈðBðÇÁ+¿®ð•ðÈððð³ðÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG ____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 06 06)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 85 83 08 08)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 20 20)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 20 08)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 80 80 08 20)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0f 80 80 06 06)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 10 80 80 08 08)
0056 + DC 07-F0 MDP_XRT_FLD_ENA
0057 BC (d8)
0058 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0059 BC (c8)
0060 +. DC 07-F0 MDP_XRT_AEC_RESET
0061 BC (d0)
0062 + DC 07-F0 MDP_XRT_ARS_DIS
0063 BC (d5)
0064 +. DC 07-F0 MDP_XRT_FLD_RESET
0065 BC (da)
0066 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0067 BC (c4 14)
0068 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0069 BC (c5 0d)
0070 . C. ----- Success Verify ? OK / NG ____
0071 C.
0072 C.
0073 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0074 C.
0075 +. DC 07-F0 MDP_XRT_MODE_OBSV
0076 BC (c2)
0077 +. TI 2011-10-11 10:06:02.0
0078 DC 07-F0 MDP_XRT_MODE_OBSV
0079 BC (c2)
0080 . C. ----- Success Verify ? OK / NG ____
0081 C.
0082 C. ***** XRT END *****
0083 C. *****
0084 C. SOT table upload
0085 C. *****
0086 . C. < Upload DPL table >
0087 +. DC 07-F0 MDP_FG_CTRL_MANU
0088 BC (51)
0089 . C. -----
0090 C. MDP_FG_CTRL_MODE = MANU [ ]
0091 C. -----
0092 C.
0093 C. YçYÁY×YÍ;¼YÉðîÁ°ðÈSTS_CHKððOFFðÈð¹ðÈ
0094 C.
0095 . S. RAM ram-271:MDP_DPL
```

```

0096      ( )
0097      C.
0098      . C. < Dump RAMID=MDP_DPL >
0099 +. DC 07-F0 MDP_DUMP_FGTBL
0100      BC      (82 07 00 38 b8 00 40)
0101      C. -----
0102      C. MDP_DPL verify          = OK      [ ]
0103      C. -----
0104      C.
0105      C. STS_CHKαδONαÈα¹αè
0106      C.
0107      . C. < Update MDP DSC PAR1 >
0108 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0109      BC      (4c)
0110      C. MDP_CMD_CODE          = F04C0700[ ]
0111      C. MDP_CMD_CNT          (count-up 1) [ ]
0112      C. -----
0113      C.
0114      . C.
0115      C. *****
0116      C. SOT TI command set
0117      C. *****
0118      C. Execute, after the success of TBL upload.
0119 +. TI 2011-10-11 10:06:18.0
0120      DC 07-F0 MDP_SOT_MODE_OBSV
0121      BC      (40)
0122      C. -----
0123      C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0124      C. -----
0125      C.
0126      C.
0127      . C. ***** MDP `ûÃÎαî»ò¼ÝαÈÃDα¹αèDCBC•x²è *****
0128      C. (¼ã°îÿÓÿÃÿÈÿPÿËÿãÿçÿèαÈ¼αα¼Ã»Ûα¹αè)
0129      . S. DC-BC dcbc-402:DCBC
0130      (MDP_known_event)
0131      C.
0132      C.
0133      . C. ***** ¼Dÿ¹•Ï Daily±;îÑαÈ´Øα¹αèDCBC•x²è *****
0134      . S. DC-BC dcbc-153:DCBC
0135      (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0136      C.
0137      C.
0138      . C. ;ãLOSÿÃÿSÿÿÿ¼Ã»Û;ã
0139      C.
0140      . C. ***** LOS *****
0141      C.

```

\*\*\* OP Sequence for XRT \*\*\*

```

2011/10/11 10:16:54.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2011/10/11 10:16:56.0 XRT_FOCUS_POSITION_409_OG [0x199]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2011/10/11 10:17:00.0 AOCS_Ore-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 02 00 00 00 00
2011/10/11 10:17:16.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2011/10/11 10:17:18.0 XRT_FLRCTRL_ENA_413_OG [0x19d]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2011/10/11 10:17:20.0 XRT_AEC_RESET_443_OG [0x1bb]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2011/10/11 10:17:22.0 XRT_ARS_DIS_427_OG [0x1ab]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2011/10/11 10:19:54.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2011/10/11 10:19:56.0 XRT_QT_PROG_SET_404_OG [0x194]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0a
2011/10/11 10:19:58.0 XRT_FL_PROG_SET_420_OG [0x1a4]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 0d
2011/10/11 10:20:00.0 XRT_CTRL_AUTO_406_OG [0x196]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2011/10/11 10:53:00.0 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2011/10/11 10:53:02.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2011/10/11 10:53:04.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2011/10/11 10:53:30.0 XRT_Custom_418_OG [0x1a2]
2011/10/11 10:54:30.0 XRT_CTRL_AUTO_419_OG [0x1a3]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2011/10/11 10:56:14.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2011/10/11 11:16:54.0 XRT_CTRL_MANU_439_OG [0x1b7]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2011/10/11 11:17:00.0 AOCS_Ore-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 00 00 00 ac cd
2011/10/11 11:19:26.0 XRT_FOCUS_POSITION_401_OG [0x191]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2011/10/11 11:19:46.0 XRT_FLD_ENA_435_OG [0x1b3]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2011/10/11 11:19:48.0 XRT_FLRCTRL_ENA_403_OG [0x193]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2011/10/11 11:19:50.0 XRT_AEC_RESET_443_OG [0x1bb]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2011/10/11 11:19:52.0 XRT_ARS_DIS_431_OG [0x1af]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2011/10/11 11:19:54.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2011/10/11 11:19:56.0 XRT_QT_PROG_SET_444_OG [0x1bc]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 05
2011/10/11 11:19:58.0 XRT_FL_PROG_SET_420_OG [0x1a4]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 0d
2011/10/11 11:20:00.0 XRT_CTRL_AUTO_406_OG [0x196]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2011/10/11 11:32:54.0 XRT_CTRL_MANU_439_OG [0x1b7]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2011/10/11 11:33:00.0 AOCS_Ore-point_Start_3_OG [0x099]
                        AOCU_NM 5 02-76 00 00 00 d6 67
2011/10/11 11:35:26.0 XRT_FOCUS_POSITION_401_OG [0x191]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2011/10/11 11:35:46.0 XRT_FLD_ENA_435_OG [0x1b3]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2011/10/11 11:35:48.0 XRT_FLRCTRL_ENA_403_OG [0x193]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2011/10/11 11:35:50.5 XRT_AEC_RESET_443_OG [0x1bb]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2011/10/11 11:35:52.5 XRT_ARS_DIS_431_OG [0x1af]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2011/10/11 11:35:54.5 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2011/10/11 11:35:56.5 XRT_QT_PROG_SET_444_OG [0x1bc]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 05
2011/10/11 11:35:58.5 XRT_FL_PROG_SET_420_OG [0x1a4]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 0d
2011/10/11 11:36:00.5 XRT_CTRL_AUTO_406_OG [0x196]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2011/10/11 11:48:54.0 XRT_CTRL_MANU_439_OG [0x1b7]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2011/10/11 11:49:00.0 AOCS_Ore-point_Start_4_OG [0x09a]
                        AOCU_NM 5 02-76 00 00 00 00 00
2011/10/11 11:51:26.0 XRT_FOCUS_POSITION_401_OG [0x191]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2011/10/11 11:51:46.0 XRT_FLD_ENA_435_OG [0x1b3]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2011/10/11 11:51:48.0 XRT_FLRCTRL_ENA_403_OG [0x193]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2011/10/11 11:51:50.0 XRT_AEC_RESET_443_OG [0x1bb]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2011/10/11 11:51:52.0 XRT_ARS_DIS_431_OG [0x1af]
                        MDP_XRT_ARS_DIS 1 07-F0 d5

```

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## XRT\_OGLIST\_0203.chk

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2011/10/11	11:51:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/11	11:51:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			
2011/10/11	11:51:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2011/10/11	11:52:00.5	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/11	12:04:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/11	12:05:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	00	00	29	99
2011/10/11	12:07:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2011/10/11	12:07:46.0	XRT_FLD_ENA_435_OG [0x1b3]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/10/11	12:07:48.0	XRT_FLRCTRL_ENA_403_OG [0x193]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/10/11	12:07:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/10/11	12:07:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/10/11	12:07:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/11	12:07:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			
2011/10/11	12:07:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2011/10/11	12:08:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/11	12:20:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/11	12:21:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	00	00	53	33
2011/10/11	12:23:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2011/10/11	12:23:46.0	XRT_FLD_ENA_435_OG [0x1b3]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/10/11	12:23:48.0	XRT_FLRCTRL_ENA_403_OG [0x193]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/10/11	12:23:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/10/11	12:23:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/10/11	12:23:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/11	12:23:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			
2011/10/11	12:23:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2011/10/11	12:24:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/11	12:36:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/11	12:37:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	d6	36	b7	8e
2011/10/11	12:39:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2011/10/11	12:39:46.0	XRT_FLD_ENA_435_OG [0x1b3]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/10/11	12:39:48.0	XRT_FLRCTRL_ENA_403_OG [0x193]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/10/11	12:39:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/10/11	12:39:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/10/11	12:39:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/11	12:39:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			
2011/10/11	12:39:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2011/10/11	12:40:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/11	12:46:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/11	12:47:00.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00	b4	b5	db	75
2011/10/11	12:49:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2011/10/11	12:49:46.0	XRT_FLD_ENA_435_OG [0x1b3]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/10/11	12:49:48.0	XRT_FLRCTRL_ENA_403_OG [0x193]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/10/11	12:49:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/10/11	12:49:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/10/11	12:49:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/11	12:49:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			
2011/10/11	12:49:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]							

2011/10/11	12:50:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d	
2011/10/11	13:02:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/10/11	13:03:00.0	AOCS_ORe-point_Start_9_OG [0x09f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/11	13:05:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	AOCU_NM	5	02-76	00 ac	5b 00 00	
2011/10/11	13:05:46.0	XRT_FLD_ENA_435_OG [0x1b3]	XRT_FOCUS_POSITION	4	07-F8	22 ff	aa 00	
2011/10/11	13:05:48.0	XRT_FLRCTRL_ENA_403_OG [0x193]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/10/11	13:05:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/10/11	13:05:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/10/11	13:05:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/10/11	13:05:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/11	13:05:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05	
2011/10/11	13:06:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d	
2011/10/11	13:18:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/10/11	13:19:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/11	13:21:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	AOCU_NM	5	02-76	00 b4	b5 24 8b	
2011/10/11	13:21:46.0	XRT_FLD_ENA_435_OG [0x1b3]	XRT_FOCUS_POSITION	4	07-F8	22 ff	aa 00	
2011/10/11	13:21:48.0	XRT_FLRCTRL_ENA_403_OG [0x193]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/10/11	13:21:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/10/11	13:21:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/10/11	13:21:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/10/11	13:21:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/11	13:21:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05	
2011/10/11	13:22:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d	
2011/10/11	13:34:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/10/11	13:35:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/11	13:37:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	AOCU_NM	5	02-76	00 d6	36 48 72	
2011/10/11	13:37:46.0	XRT_FLD_ENA_435_OG [0x1b3]	XRT_FOCUS_POSITION	4	07-F8	22 ff	aa 00	
2011/10/11	13:37:48.0	XRT_FLRCTRL_ENA_403_OG [0x193]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/10/11	13:37:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/10/11	13:37:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/10/11	13:37:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/10/11	13:37:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/11	13:37:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05	
2011/10/11	13:38:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d	
2011/10/11	13:44:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/10/11	13:45:00.0	AOCS_ORe-point_Start_12_OG [0x0a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/11	13:47:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	AOCU_NM	5	02-76	00 29	ca b7 8e	
2011/10/11	13:47:46.0	XRT_FLD_ENA_435_OG [0x1b3]	XRT_FOCUS_POSITION	4	07-F8	22 ff	aa 00	
2011/10/11	13:47:48.0	XRT_FLRCTRL_ENA_403_OG [0x193]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/10/11	13:47:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/10/11	13:47:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/10/11	13:47:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/10/11	13:47:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/11	13:47:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05	
2011/10/11	13:48:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d	
2011/10/11	13:54:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		



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2011/10/11	13:55:00.0	AOCS_OrE-point_Start_13_OG [0x0a3] AOCU_NM	5	02-76	00	4b	4b	db	75
2011/10/11	13:57:26.0	XRT_FOCUS_POSITION_401_OG [0x191] XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2011/10/11	13:57:46.0	XRT_FLD_ENA_435_OG [0x1b3] MDP_XRT_FLD_ENA	1	07-F0		d8			
2011/10/11	13:57:48.0	XRT_FLRCTRL_ENA_403_OG [0x193] MDP_XRT_FLRCTRL_ENA	1	07-F0		c8			
2011/10/11	13:57:50.0	XRT_AEC_RESET_443_OG [0x1bb] MDP_XRT_AEC_RESET	1	07-F0		d0			
2011/10/11	13:57:52.0	XRT_ARS_DIS_431_OG [0x1af] MDP_XRT_ARS_DIS	1	07-F0		d5			
2011/10/11	13:57:54.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0		da			
2011/10/11	13:57:56.0	XRT_QT_PROG_SET_444_OG [0x1bc] MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			
2011/10/11	13:57:58.0	XRT_FL_PROG_SET_420_OG [0x1a4] MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2011/10/11	13:58:00.0	XRT_CTRL_AUTO_406_OG [0x196] MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2011/10/11	14:10:54.0	XRT_CTRL_MANU_439_OG [0x1b7] MDP_XRT_CTRL_MANU	1	07-F0		c1			
2011/10/11	14:11:00.0	AOCS_OrE-point_Start_14_OG [0x0a4] AOCU_NM	5	02-76	00	53	a5	00	00
2011/10/11	14:13:26.0	XRT_FOCUS_POSITION_401_OG [0x191] XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2011/10/11	14:13:46.0	XRT_FLD_ENA_435_OG [0x1b3] MDP_XRT_FLD_ENA	1	07-F0		d8			
2011/10/11	14:13:48.0	XRT_FLRCTRL_ENA_403_OG [0x193] MDP_XRT_FLRCTRL_ENA	1	07-F0		c8			
2011/10/11	14:13:50.0	XRT_AEC_RESET_443_OG [0x1bb] MDP_XRT_AEC_RESET	1	07-F0		d0			
2011/10/11	14:13:52.0	XRT_ARS_DIS_431_OG [0x1af] MDP_XRT_ARS_DIS	1	07-F0		d5			
2011/10/11	14:13:54.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0		da			
2011/10/11	14:13:56.0	XRT_QT_PROG_SET_444_OG [0x1bc] MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			
2011/10/11	14:13:58.0	XRT_FL_PROG_SET_420_OG [0x1a4] MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2011/10/11	14:14:00.0	XRT_CTRL_AUTO_406_OG [0x196] MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2011/10/11	14:37:30.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0		c1			
2011/10/11	14:37:32.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0		da			
2011/10/11	14:37:34.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2011/10/11	14:40:44.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2011/10/11	14:48:00.0	XRT_Custom_418_OG [0x1a2]							
2011/10/11	14:49:00.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2011/10/11	14:53:54.0	XRT_CTRL_MANU_439_OG [0x1b7] MDP_XRT_CTRL_MANU	1	07-F0		c1			
2011/10/11	14:54:00.0	AOCS_OrE-point_Start_15_OG [0x0a5] AOCU_NM	5	02-76	00	4b	4b	24	8b
2011/10/11	14:56:26.0	XRT_FOCUS_POSITION_401_OG [0x191] XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2011/10/11	14:56:46.0	XRT_FLD_ENA_435_OG [0x1b3] MDP_XRT_FLD_ENA	1	07-F0		d8			
2011/10/11	14:56:48.0	XRT_FLRCTRL_ENA_403_OG [0x193] MDP_XRT_FLRCTRL_ENA	1	07-F0		c8			
2011/10/11	14:56:50.0	XRT_AEC_RESET_443_OG [0x1bb] MDP_XRT_AEC_RESET	1	07-F0		d0			
2011/10/11	14:56:52.0	XRT_ARS_DIS_431_OG [0x1af] MDP_XRT_ARS_DIS	1	07-F0		d5			
2011/10/11	14:56:54.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0		da			
2011/10/11	14:56:56.0	XRT_QT_PROG_SET_444_OG [0x1bc] MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			
2011/10/11	14:56:58.0	XRT_FL_PROG_SET_420_OG [0x1a4] MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2011/10/11	14:57:00.0	XRT_CTRL_AUTO_406_OG [0x196] MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2011/10/11	15:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7] MDP_XRT_CTRL_MANU	1	07-F0		c1			
2011/10/11	15:10:00.0	AOCS_OrE-point_Start_16_OG [0x0a6] AOCU_NM	5	02-76	00	29	db	48	72
2011/10/11	15:12:26.0	XRT_FOCUS_POSITION_401_OG [0x191] XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2011/10/11	15:12:46.0	XRT_FLD_ENA_435_OG [0x1b3] MDP_XRT_FLD_ENA	1	07-F0		d8			
2011/10/11	15:12:48.0	XRT_FLRCTRL_ENA_403_OG [0x193] MDP_XRT_FLRCTRL_ENA	1	07-F0		c8			
2011/10/11	15:12:50.0	XRT_AEC_RESET_443_OG [0x1bb] MDP_XRT_AEC_RESET	1	07-F0		d0			
2011/10/11	15:12:52.0	XRT_ARS_DIS_431_OG [0x1af] MDP_XRT_ARS_DIS	1	07-F0		d5			
2011/10/11	15:12:54.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0		da			
2011/10/11	15:12:56.0	XRT_QT_PROG_SET_444_OG [0x1bc] MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			

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2011/10/11	15:12:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2011/10/11	15:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/10/11	15:18:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/10/11	15:18:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2011/10/11	15:19:00.0	AOCS_OrE-point_Start_17_OG [0x0a7]	AOCU_NM	5	02-76	03	00 00 00 00
2011/10/11	15:19:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/10/11	15:19:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/10/11	15:19:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/10/11	15:19:22.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/10/11	15:21:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/10/11	15:21:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	14
2011/10/11	15:21:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2011/10/11	15:22:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/10/11	16:12:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/10/11	16:12:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/10/11	16:12:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/10/11	16:15:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/10/11	16:36:00.0	XRT_Custom_418_OG [0x1a2]					
2011/10/11	16:37:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/10/11	17:49:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/10/11	17:49:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/10/11	17:49:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/10/11	17:52:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/10/11	18:12:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/10/11	18:12:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2011/10/11	18:12:30.0	AOCS_OrE-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00	00 00 00 00
2011/10/11	18:12:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2011/10/11	18:12:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2011/10/11	18:12:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/10/11	18:15:28.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c
2011/10/11	18:15:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/10/11	18:22:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/10/11	18:22:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2011/10/11	18:22:30.0	AOCS_OrE-point_Start_17_OG [0x0a7]	AOCU_NM	5	02-76	03	00 00 00 00
2011/10/11	18:22:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2011/10/11	18:22:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2011/10/11	18:22:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2011/10/11	18:22:52.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2011/10/11	18:25:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/10/11	18:25:26.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	14
2011/10/11	18:25:28.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2011/10/11	18:25:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2011/10/11	19:26:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2011/10/11	19:26:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2011/10/11	19:26:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2011/10/11	19:29:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2011/10/11	19:49:30.0	XRT_Custom_418_OG [0x1a2]					
2011/10/11	19:50:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					

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2011/10/11	21:03:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/10/11	21:03:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	da			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2011/10/11	21:03:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/10/11	21:06:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/10/11	21:26:30.0	XRT_Custom_418_OG [0x1a2]	XRT_CTRL_AUTO_419_OG [0x1a3]						
2011/10/11	21:27:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/10/11	21:29:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/10/11	21:29:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2011/10/11	21:30:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 00 00 00 00			
			AOCU_NM	5	02-76	02 00 00 00 00			
2011/10/11	21:30:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
			MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/10/11	21:30:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/10/11	21:30:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
			MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/10/11	21:30:22.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5			
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/10/11	21:32:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2011/10/11	21:32:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a			
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a			
2011/10/11	21:32:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d			
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d			
2011/10/11	21:33:00.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/10/11	22:29:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/10/11	22:29:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2011/10/11	22:30:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]	AOCU_NM	5	02-76	03 00 00 00 00			
			AOCU_NM	5	02-76	03 00 00 00 00			
2011/10/11	22:30:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
			MDP_XRT_FLD_ENA	1	07-F0	d8			
2011/10/11	22:30:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2011/10/11	22:30:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
			MDP_XRT_AEC_RESET	1	07-F0	d0			
2011/10/11	22:30:22.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5			
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2011/10/11	22:32:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2011/10/11	22:32:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14			
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 14			
2011/10/11	22:32:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d			
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d			
2011/10/11	22:33:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2011/10/11	22:41:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/10/11	22:41:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2011/10/11	22:41:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/10/11	22:44:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/10/11	23:01:30.0	XRT_Custom_418_OG [0x1a2]	XRT_CTRL_AUTO_419_OG [0x1a3]						
2011/10/11	23:02:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/10/12	00:18:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/10/12	00:18:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2011/10/12	00:18:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/10/12	00:21:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/10/12	00:26:00.0	XRT_Custom_418_OG [0x1a2]	XRT_CTRL_AUTO_419_OG [0x1a3]						
2011/10/12	00:27:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/10/12	01:42:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/10/12	01:42:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2011/10/12	01:42:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/10/12	01:45:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2011/10/12	02:01:00.0	XRT_Custom_418_OG [0x1a2]	XRT_CTRL_AUTO_419_OG [0x1a3]						
2011/10/12	02:02:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/10/12	03:17:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2011/10/12	03:17:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
			MDP_XRT_FLD_RESET	1	07-F0	da			

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2011/10/12	03:17:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/12	03:20:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/12	03:38:00.0	XRT_Custom_418_OG [0x1a2]							
2011/10/12	03:39:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/12	04:47:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/12	04:47:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/12	04:47:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/12	04:50:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/12	05:15:30.0	XRT_Custom_418_OG [0x1a2]							
2011/10/12	05:16:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/12	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/12	05:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/10/12	06:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/10/12	06:00:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/10/12	06:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/10/12	06:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/10/12	06:02:58.0	XRT_QT_PROG_SET_432_OG [0x1b0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2011/10/12	06:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/12	06:09:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/12	06:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/10/12	06:10:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2011/10/12	06:10:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/10/12	06:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/10/12	06:10:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/10/12	06:10:22.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/10/12	06:12:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/12	06:12:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2011/10/12	06:12:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2011/10/12	06:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/12	06:28:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/12	06:28:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/12	06:28:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/12	06:31:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/12	06:53:00.0	XRT_Custom_418_OG [0x1a2]							
2011/10/12	06:54:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/12	08:08:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/12	08:08:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/12	08:08:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/12	08:11:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/12	08:29:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/12	08:29:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/10/12	08:30:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2011/10/12	08:30:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/10/12	08:30:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/10/12	08:30:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/10/12	08:30:22.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/10/12	08:32:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/12	08:32:56.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				

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2011/10/12	08:32:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2011/10/12	08:33:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/12	09:29:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/12	09:29:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00			
2011/10/12	09:30:00.0	AOCS_Ore-point_Start_17_OG [0x0a7]							
		AOCU_NM	5	02-76	03	00 00 00 00			
2011/10/12	09:30:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/10/12	09:30:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/10/12	09:30:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/10/12	09:30:22.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/10/12	09:32:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/12	09:32:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	14			
2011/10/12	09:32:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2011/10/12	09:33:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/12	09:48:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/12	09:48:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/12	09:48:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/12	09:51:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/12	10:03:30.0	XRT_Custom_418_OG [0x1a2]							
2011/10/12	10:04:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/12	15:12:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/12	15:12:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/12	15:12:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/12	15:15:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/12	15:26:00.0	XRT_Custom_418_OG [0x1a2]							
2011/10/12	15:27:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/12	15:32:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/12	15:32:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/12	15:32:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/12	15:35:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/12	15:35:30.0	XRT_Custom_418_OG [0x1a2]							
2011/10/12	15:36:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/12	16:48:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/12	16:48:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/12	16:48:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/12	16:51:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/12	17:12:00.0	XRT_Custom_418_OG [0x1a2]							
2011/10/12	17:13:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/12	17:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/12	17:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00			
2011/10/12	18:00:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	00 00 00 00			
2011/10/12	18:00:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/10/12	18:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/10/12	18:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/10/12	18:02:58.0	XRT_QT_PROG_SET_432_OG [0x1b0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2011/10/12	18:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/12	18:09:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/12	18:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00			
2011/10/12	18:10:00.0	AOCS_Ore-point_Start_17_OG [0x0a7]							
		AOCU_NM	5	02-76	03	00 00 00 00			
2011/10/12	18:10:16.0	XRT_FLD_ENA_411_OG [0x19b]							

2011/10/12	18:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8		
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/10/12	18:10:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/10/12	18:10:22.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/10/12	18:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/12	18:12:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	14	
2011/10/12	18:12:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d	
2011/10/12	18:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/10/12	18:25:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/12	18:25:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/12	18:25:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/10/12	18:28:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/10/12	18:49:00.0	XRT_Custom_418_OG [0x1a2]						
2011/10/12	18:50:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/10/12	20:02:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/12	20:02:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/12	20:02:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/10/12	20:05:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/10/12	20:26:00.0	XRT_Custom_418_OG [0x1a2]						
2011/10/12	20:27:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/10/12	21:40:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/12	21:40:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/12	21:40:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2011/10/12	21:43:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2011/10/12	22:02:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/12	22:02:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2011/10/12	22:03:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02	00 00 00 00	
2011/10/12	22:03:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/10/12	22:03:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/10/12	22:03:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/10/12	22:03:22.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/10/12	22:05:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/12	22:05:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a	
2011/10/12	22:05:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d	
2011/10/12	22:06:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/10/12	23:02:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/12	23:02:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2011/10/12	23:03:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]	AOCU_NM	5	02-76	03	00 00 00 00	
2011/10/12	23:03:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2011/10/12	23:03:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2011/10/12	23:03:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2011/10/12	23:03:22.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2011/10/12	23:05:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		
2011/10/12	23:05:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	14	
2011/10/12	23:05:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d	
2011/10/12	23:06:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2011/10/12	23:17:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2011/10/12	23:17:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da		

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2011/10/12	23:17:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/12	23:20:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/12	23:32:30.0	XRT_Custom_418_OG [0x1a2]							
2011/10/12	23:33:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/13	00:49:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/13	00:49:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/13	00:49:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/13	00:52:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/13	01:01:00.0	XRT_Custom_418_OG [0x1a2]							
2011/10/13	01:02:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/13	02:17:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/13	02:17:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/13	02:17:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/13	02:20:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/13	02:37:30.0	XRT_Custom_418_OG [0x1a2]							
2011/10/13	02:38:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/13	03:45:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/13	03:45:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/13	03:45:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/13	03:48:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/13	04:14:30.5	XRT_Custom_418_OG [0x1a2]							
2011/10/13	04:15:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/13	05:25:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/13	05:25:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/13	05:25:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/13	05:28:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/13	05:52:00.0	XRT_Custom_418_OG [0x1a2]							
2011/10/13	05:53:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/13	06:01:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/13	06:01:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2011/10/13	06:02:00.0	AOCS_OrE-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2011/10/13	06:02:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2011/10/13	06:02:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2011/10/13	06:02:20.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/10/13	06:04:58.0	XRT_QT_PROG_SET_432_OG [0x1b0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2011/10/13	06:05:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/13	06:11:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/13	06:11:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2011/10/13	06:12:00.0	AOCS_OrE-point_Start_17_OG [0x0a7]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2011/10/13	06:12:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2011/10/13	06:12:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2011/10/13	06:12:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2011/10/13	06:12:22.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2011/10/13	06:14:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/13	06:14:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2011/10/13	06:14:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2011/10/13	06:15:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/13	07:05:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/13	07:05:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				

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2011/10/13	07:05:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/13	07:08:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/13	07:29:30.0	XRT_Custom_418_OG [0x1a2]							
2011/10/13	07:30:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/13	08:45:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/13	08:45:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/13	08:45:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/13	08:48:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/13	09:06:00.0	XRT_Custom_418_OG [0x1a2]							
2011/10/13	09:07:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2011/10/13	10:27:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2011/10/13	10:27:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2011/10/13	10:27:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2011/10/13	10:30:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2011/10/13	10:59:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 00 00 00 00				