

XRT Timeline to be uploaded on 2012/02/25

Period: 2012/02/25 10:16:00 - 2012/03/01 10:04:00

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Normal mode

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XOB #174E: HOP81 2-filter - Ti/poly 8s, Al/mesh 4s, G-band - 384x384													
Term	Pointing (x, y)	Comment											
02/25 10:29:00 - 02/25 17:59:54	Fixed (-14.0, -935.0)	# OP start + 10min, HOP-81 at the South Pole.											
PROG= 07 Inf.-time(s)													
┌ Subr= 1 1-time(s) 2.0sec													
└ Seqn= 51 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
┌ Subr= 2 30-time(s) 2.0sec													
└ Seqn= 58 2-time(s) 30.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	8.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #18A8: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + Thin-Be(18												
Term	Pointing (x, y)	Comment										
02/25 18:03:00 - 02/25 18:09:54	Fixed (0.0, 0.0)	synoptic										
02/26 06:08:00 - 02/26 06:14:54	Fixed (0.0, 0.0)	synoptic, shifted 5.0 min										
02/26 17:56:30 - 02/26 18:03:24	Fixed (0.0, 0.0)	synoptic, shifted -6.5 min										
02/28 06:22:00 - 02/28 06:24:54	Fixed (0.0, 0.0)	synoptic, shifted about 15 mins.										

PROG= 05 1-time(s)													
┌ Subr= 1 1-time(s) 12.0sec													
└ Seqn= 7 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└ Seqn= 5 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└ Seqn= 8 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└ Seqn= 3 1-time(s) 2.0sec													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└ Seqn= 4 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #18C1: AR Standard-A(Filter-Ratio) with PFB, shorter thin-Be, thick Al and Al/Poly context, 384x384 at 1064 1048 (all), 60s cad												
Term	Pointing (x, y)	Comment										
02/25 18:13:00 - 02/26 06:04:54	Track (445.9, 348.8) ^{Ⓜ 02/25 18:10:00}	# Track AR 11421.										
02/26 06:18:00 - 02/26 17:28:00	Track (538.5, 341.6) ^{Ⓜ 02/26 06:15:00}	# Continue tracking AR 11421										
02/26 18:06:30 - 02/26 23:30:00	Track (621.9, 333.4) ^{Ⓜ 02/26 18:03:30}	# AR cont.										

PROG= 15 Inf.-time(s)													
┌ Subr= 1 1-time(s) 2.0sec													
└ Seqn= 19 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└ Seqn= 96 4-time(s) 2.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
┌ Subr= 2 1-time(s) 2.0sec													
└ Seqn= 62 15-time(s) 2.0sec													
	thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec
	thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
	thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
	thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1778: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh, Ti/Poly-long												
Term	Pointing (x, y)	Comment										
02/28 05:38:00 - 02/28 05:44:54	Fixed (-528.4, -528.4)	# XRT post bakeout quadrant pointings 1/4										
PROG= 01 1-time(s)												

Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 38 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 93 2-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1779: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh,Ti/Poly -long

Term		Pointing (x, y)						Comment					
02/28 05:48:00 - 02/28 05:54:54		Fixed (528.4, -528.4)						# 2/4					
PROG= 12 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 36 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 93 2-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #177A: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant- Al/mesh, Ti/Poly-long

Term		Pointing (x, y)						Comment					
02/28 05:58:00 - 02/28 06:04:54		Fixed (528.4, 528.4)						# 3/4					
PROG= 19 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 39 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 93 2-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #177B: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh, Ti/Poly-long

Term		Pointing (x, y)						Comment					
02/28 06:08:00 - 02/28 06:14:54		Fixed (-528.4, 528.4)						# 4/4					
PROG= 06 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 40 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 93 2-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #18D7: Coronal cavity (6 filters)-HOP114 with ARs-DPCM 2x2-768FOV-Thin-Be_16s-AEC0-3min cad

Term		Pointing (x, y)						Comment					
02/28 06:28:00 - 02/28 10:04:30		Track (733.8, -370.4) @ 02/28 06:25:00						# Filament at limb observations for SOT.					
PROG= 14 Inf.-time(s)													
└─ Subr= 1 5-time(s) 180.0sec													
└─ Seqn= 10 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	2x2	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	2.83s	Obs	2x2	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	C-poly/Open	C-poly/Open	close	Safe	Norm	4.00s	Obs	2x2	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	5.66s	Obs	2x2	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	2x2	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 14 1-time(s) 4.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #18C2: Flare standard obs. multifilter - thin-Be + (med-Al,thick-Be) 384x384 + (Al-poly 512x512 2x2)-no interval context-12 loops

Term	Pointing (x, y)	Comment
02/25 10:29:00 - 02/25 17:59:54	Fixed (-14.0, -935.0)	# OP start + 10min, HOP-81 at the South Pole.
02/25 18:13:00 - 02/26 06:04:54	Track (445.9, 348.8) ^{Ⓜ 02/25 18:10:00}	# Track AR 11421.
02/26 06:18:00 - 02/26 17:28:00	Track (538.5, 341.6) ^{Ⓜ 02/26 06:15:00}	# Continue tracking AR 11421
02/26 18:06:30 - 02/26 23:30:00	Track (621.9, 333.4) ^{Ⓜ 02/26 18:03:30}	# AR cont.
02/28 06:28:00 - 02/28 10:04:30	Track (733.8, -370.4) ^{Ⓜ 02/28 06:25:00}	# Filament at limb observations for SOT.
PROG= 03 12-time(s)		
└─ Subr= 1 45-time(s) 10.0sec		
└─ Seqn= 20 1-time(s) 2.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
└─ Seqn= 63 1-time(s) 2.0sec		
med-Al/Open	med-Al/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
└─ Seqn= 77 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
└─ Subr= 2 1-time(s) 10.0sec		
└─ Seqn= 90 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Active Region Search

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NOT USED

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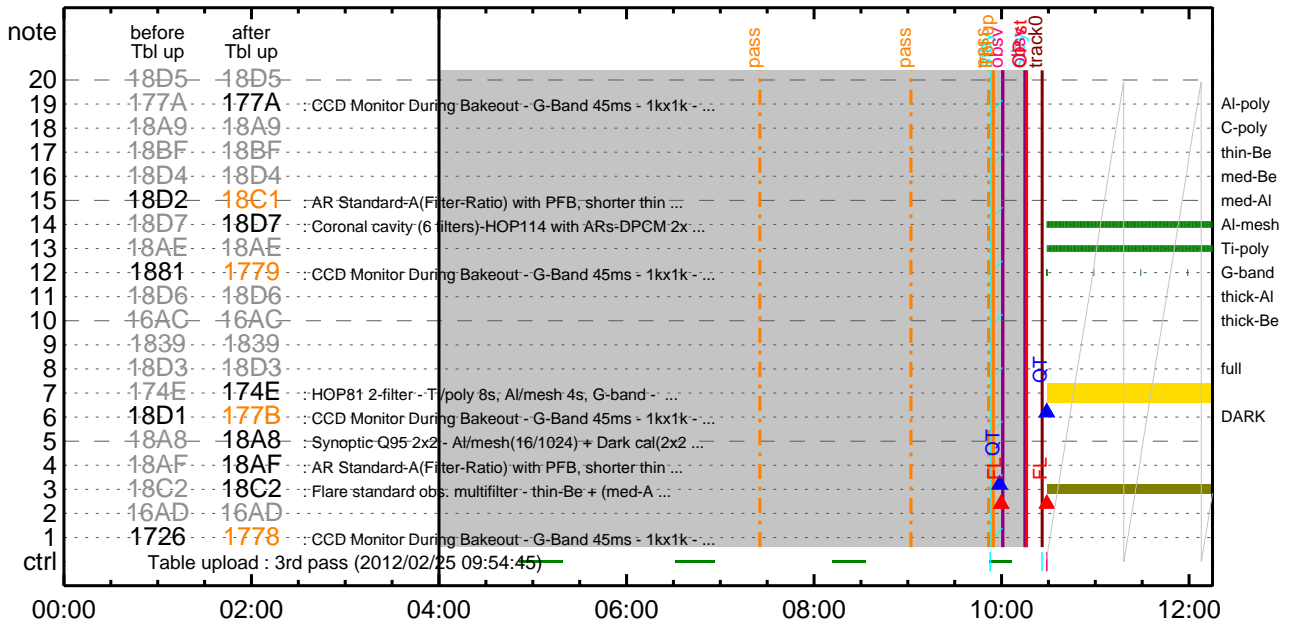
Flare Detection

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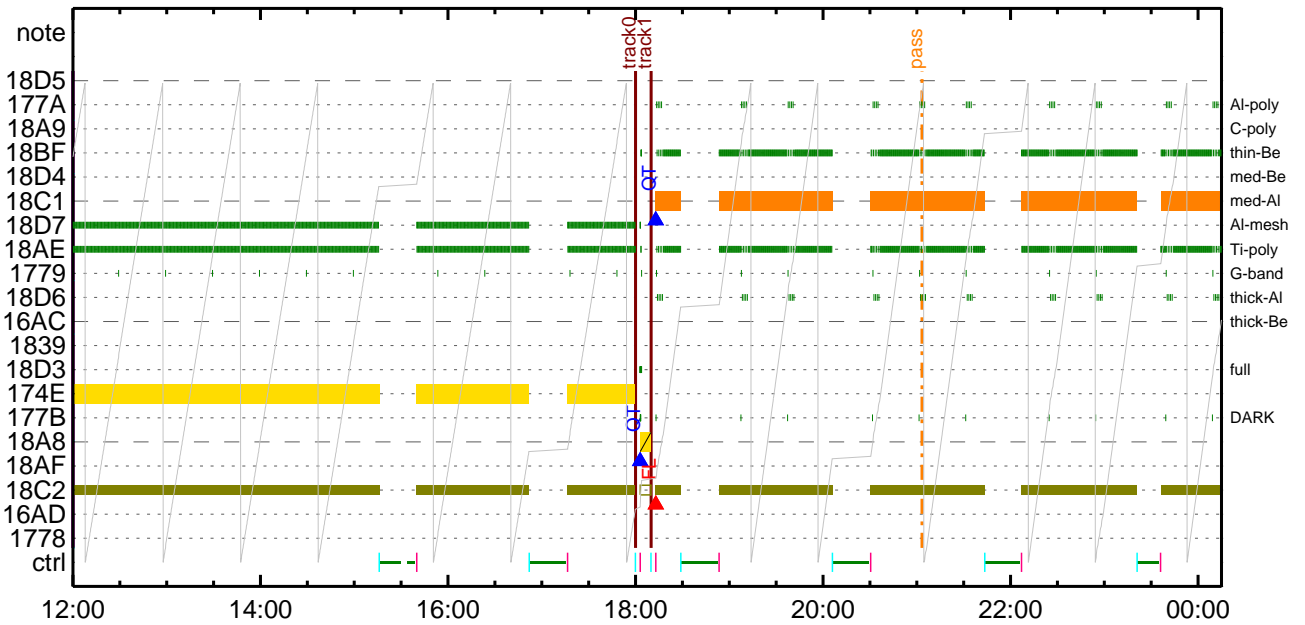
FLD Patrol

Term	Pointing (x, y)	Comment
02/25 18:12:46 - 02/26 06:05:16	Track (445.9, 348.8) ^{Ⓜ 02/25 18:10:00}	# Track AR 11421.
02/26 06:17:46 - 02/26 17:53:46	Track (538.5, 341.6) ^{Ⓜ 02/26 06:15:00}	# Continue tracking AR 11421
02/26 18:06:16 - 02/28 05:37:54	Track (621.9, 333.4) ^{Ⓜ 02/26 18:03:30}	# AR cont.
02/28 06:27:46 - 03/01 10:04:00	Track (733.8, -370.4) ^{Ⓜ 02/28 06:25:00}	# Filament at limb observations for SOT.
Open/Ti-poly	Open/thick-Al close	Safe Norm 8ms Obs 8x8 Q=50 30sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

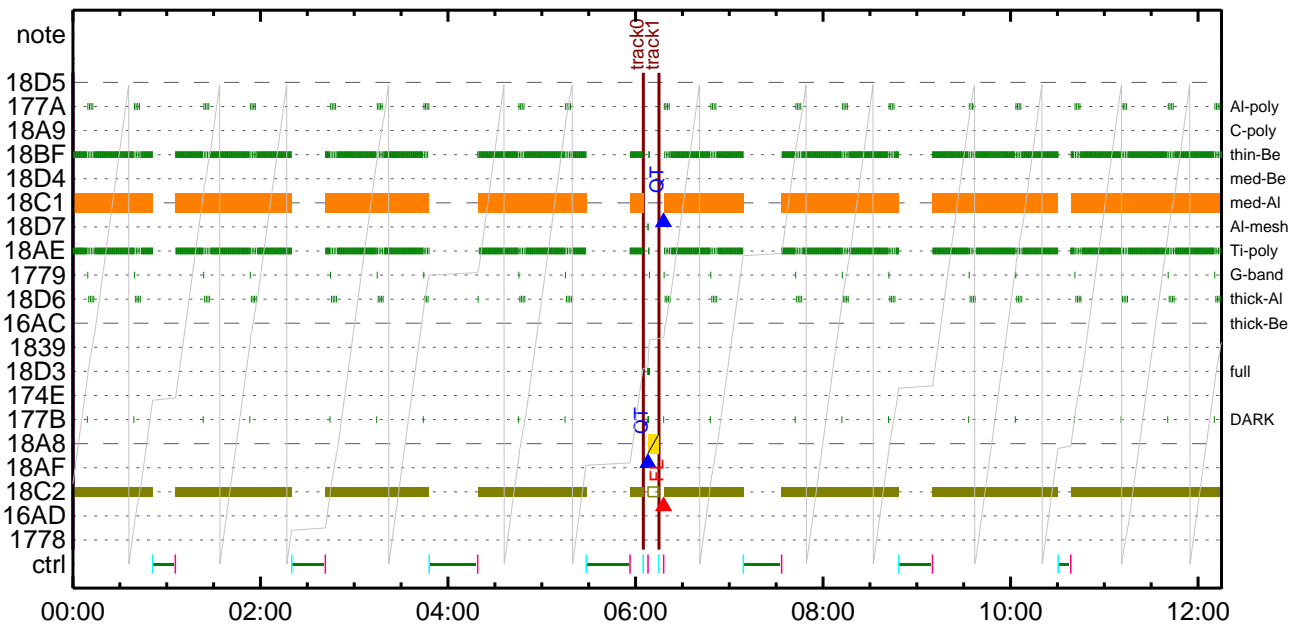
CMDI #0447 2012/02/25



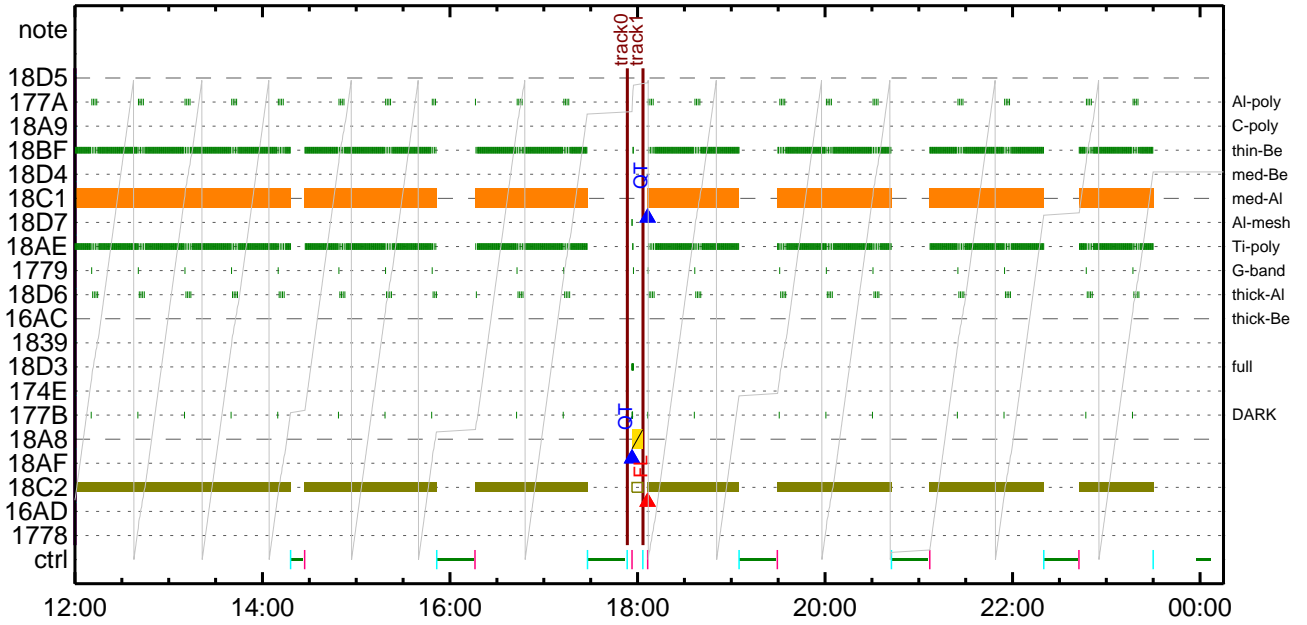
CMDI #0447 2012/02/25



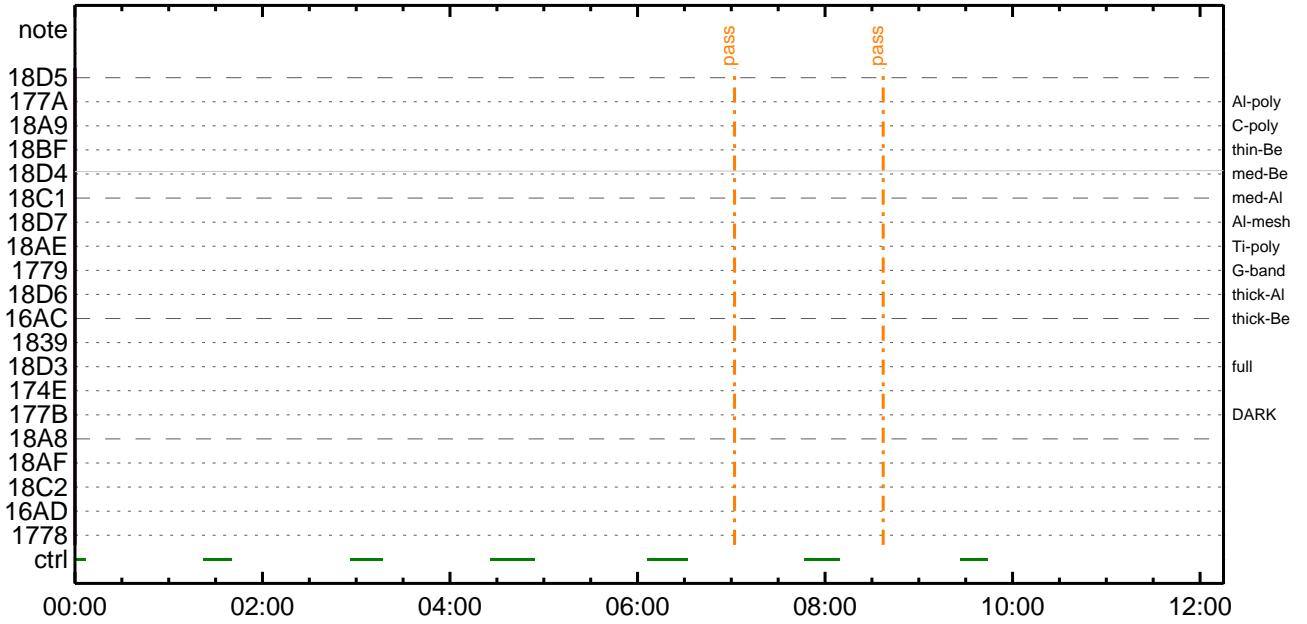
CMDI #0447 2012/02/26



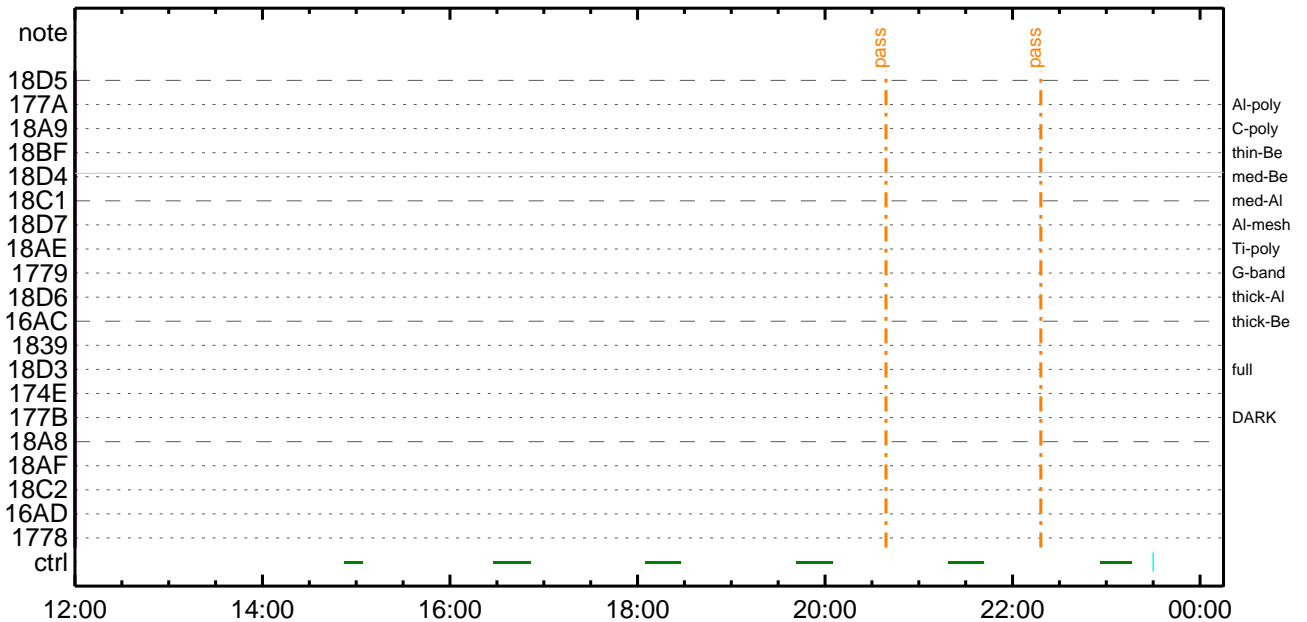
CMDI #0447 2012/02/26



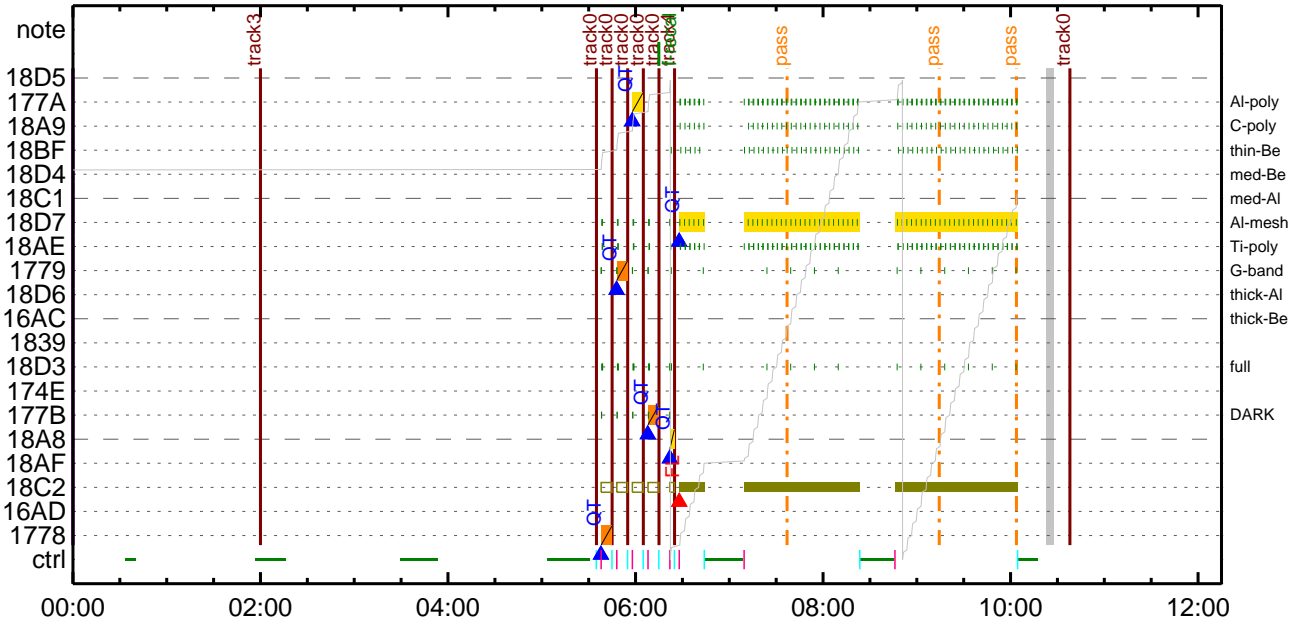
CMDI #0447 2012/02/27



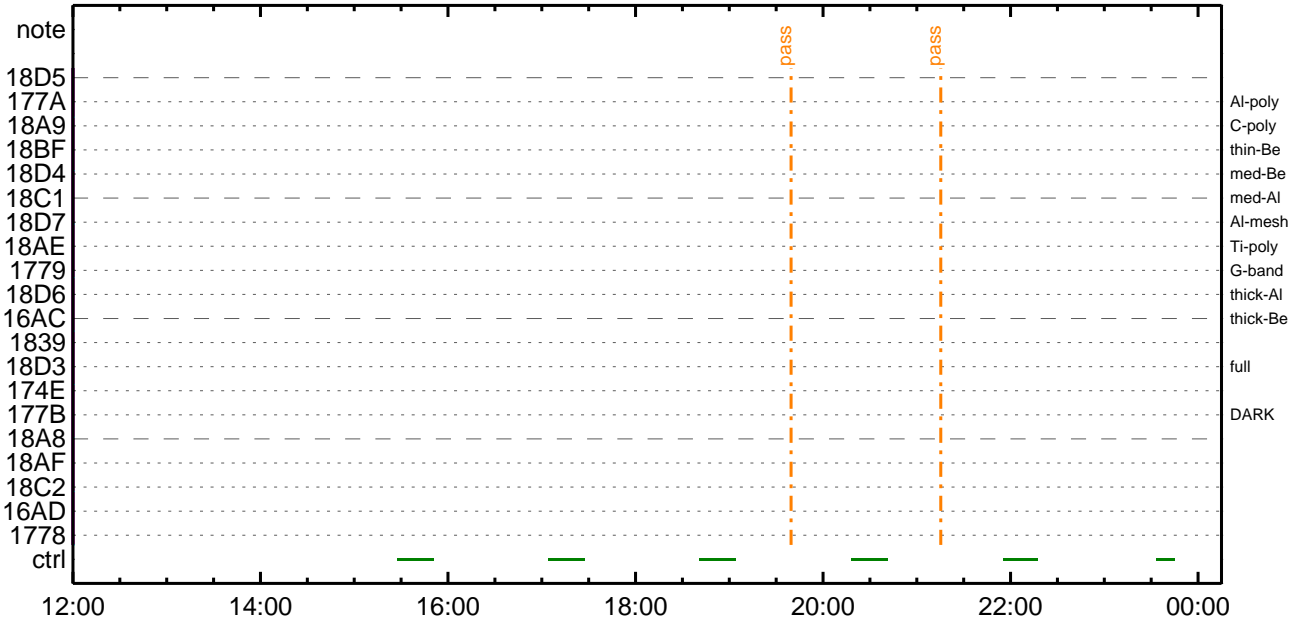
CMDI #0447 2012/02/27



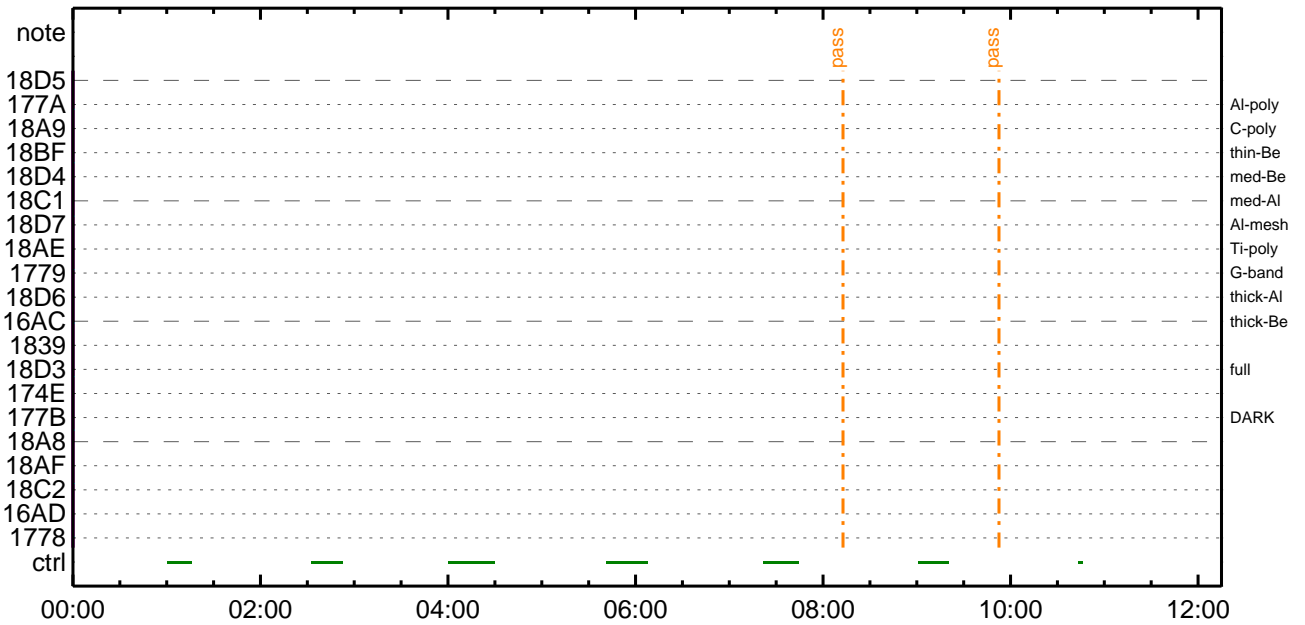
CMDI #0447 2012/02/28



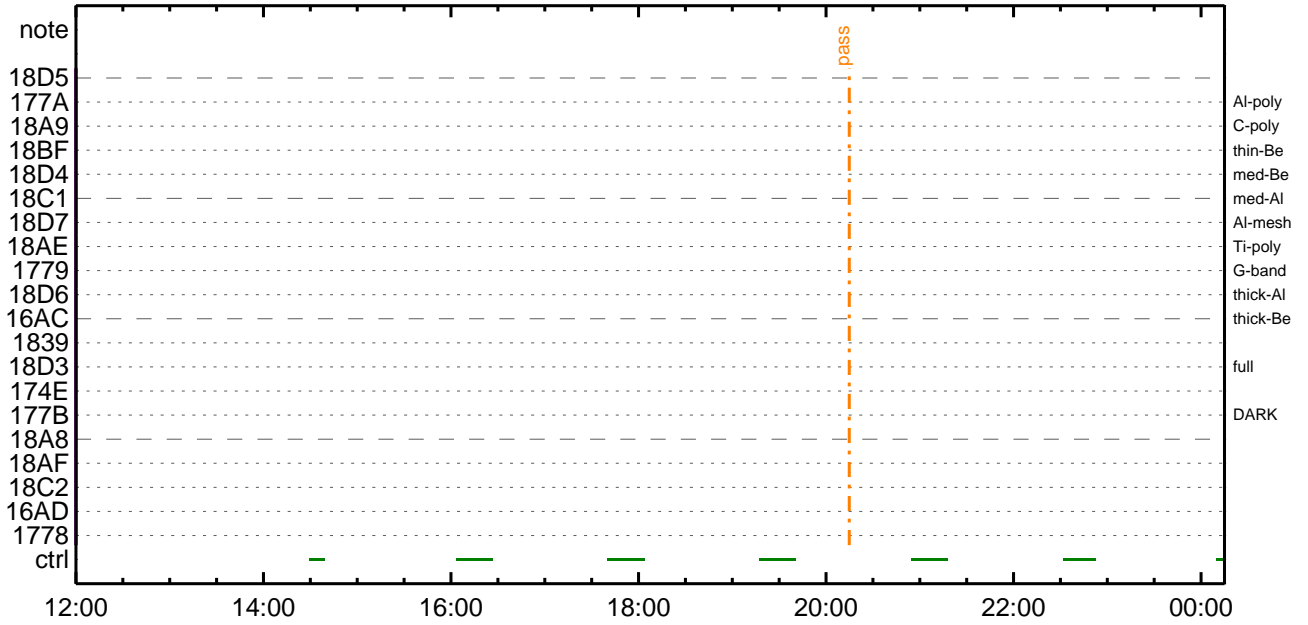
CMDI #0447 2012/02/28



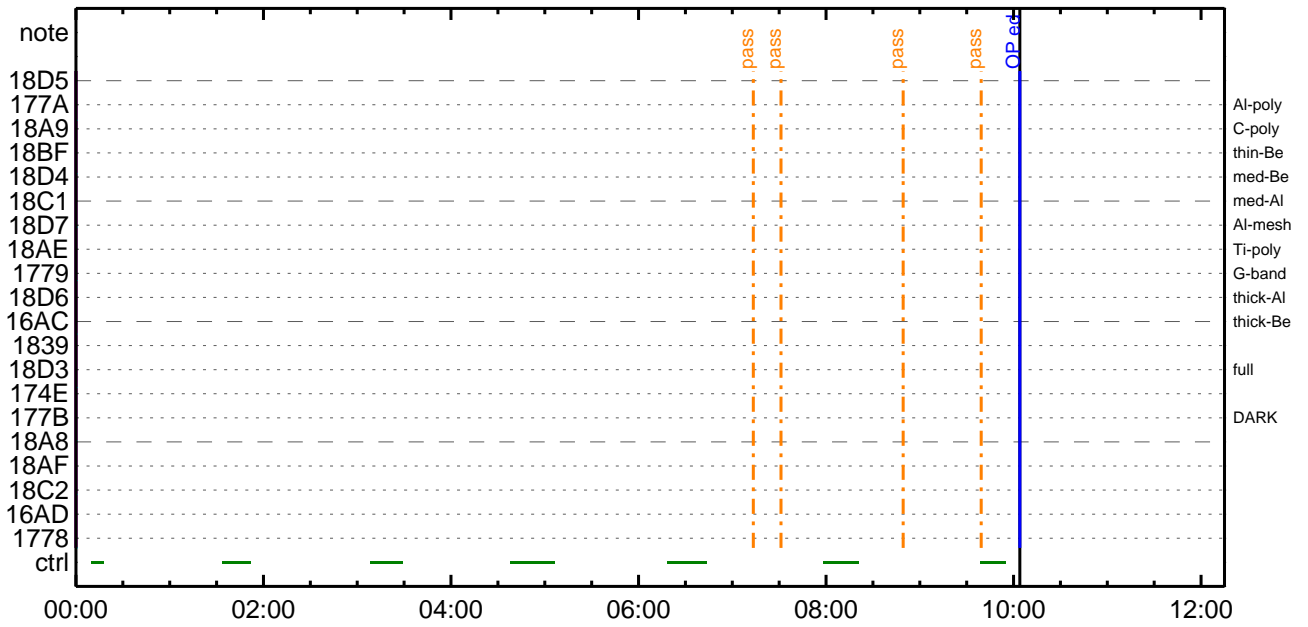
CMDI #0447 2012/02/29



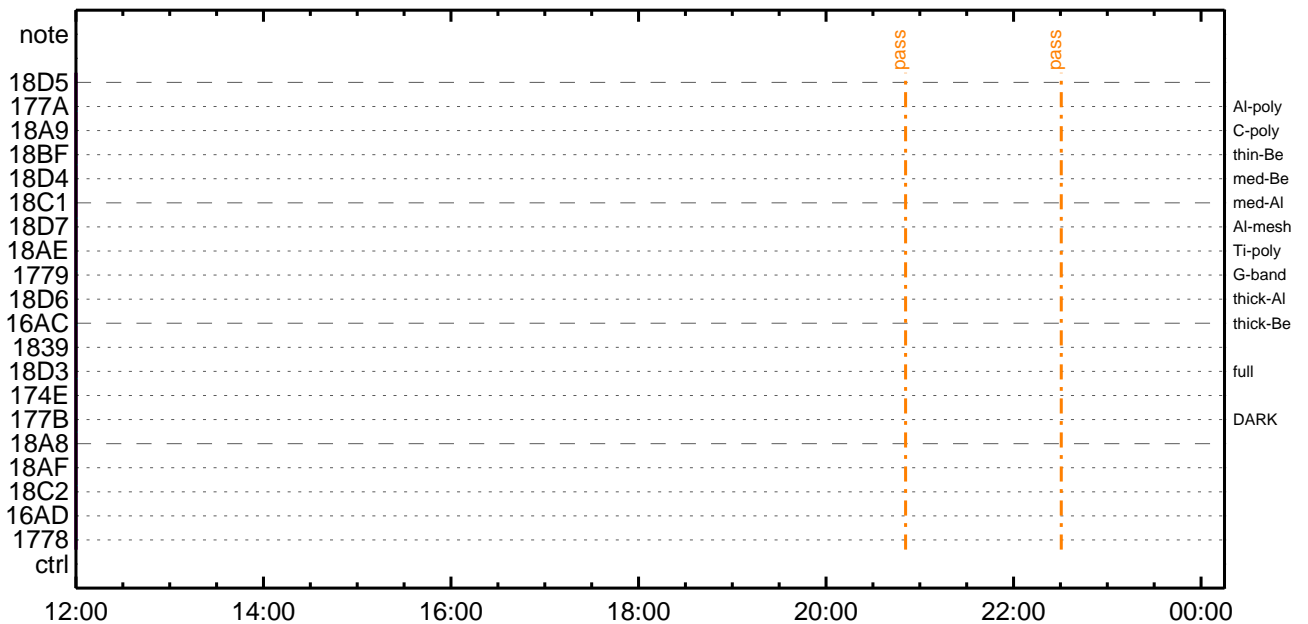
CMDI #0447 2012/02/29



CMDI #0447 2012/03/01



CMDI #0447 2012/03/01




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;1;YAY6Yx
0100 C. *****
0101 C.
0102 . C. ;ãOP/OGY1;4YE;ã
0103 . S. OP op-678:OP
0104 ( )
0105 . S. OG og-678:OG
0106 ( )
0107 C.
0108 . C. ;ãNMOG&OPîî°èYAY6Yx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 . C. YAY6Yx½ªî»òð³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 . C. RAM ID=NMOG²î½Ê¹Ç.è²îOKòð³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 . C. YAY6Yx½ªî»òð³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 . C. RAM ID=NMOG²î½Ê¹Ç.è²îOKòð³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 . C. YAY6Yx½ªî»òð³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 . C. RAM ID=NMOG, RAM ID=OP²î½Ê¹Ç.è²îOKòð³îÇ§
0165 C.
0166 . C. ***** °Ê²¼òî½Ã·¶Á°òÊÊ¬ò°Á÷¿@ (½âµ-YAY6Yx½ê½çòðÁÔÆòÇ½ª°²òè¼i¹çòçòâ) *****
0167 C. DHUYâ;4YE;Ê½Y½;Yi;4YE;Êòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 . C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 . C. NOTICE |§ OPOG UPLOAD²-Á÷¿@NG²î½î¹Ç;ç°Ê²¼òîTI-CMDÁ÷¿@²î½î¹Ç°²òè¼i¹çòçòâ³òÊ;f
0180 C. çç[HK1_PKT_FORM_NO] EQ 2
0181 C.
0182 . C. TIY³YpY6YÊòðÁî¿¿(UT)
0183 +. TI 2012-02-25 10:11:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2012-02-25 10:11:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2012-02-25 10:11:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
```

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0194 C.
0195 +. TI 2012-02-25 10:15:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          ÷÷[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0198 C.
0199 C. °È²¼ñïÄè%ííññîîŷÄŷ§ŷÄŷ-¹àîŷ
0200 C.          ÷÷[HK1_TI_CMD_ENA/DIS]      EQ          ENA
0201 C.          ÷÷[HK1_TI_CMD_NUM]          EQ          4
0202 C.          ÷÷[HK1_NEXT_EXEC_PIM]       EQ          DHU
0203 C.          ÷÷[HK1_NEXT_EXEC_DC]       EQ          0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          ÷÷[HK1_DMP_TOP_ADRS_1]     EQ          07
0213 C.          ÷÷[HK1_DMP_TOP_ADRS_0]     EQ          2B
0214 C.          ÷÷[HK1_DMP_BLOCK_NUM]      EQ          3
0215 C.          ÷÷[HK1_DMP_REPEAT_NUM]     EQ          0
0216 C.          ÷÷[HK1_DMA_DMP_PIM]       EQ          DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          ÷÷[HK1_PKT_FORM_NO]        EQ          7
0220 C.          ÷÷[HK1_PKT_GEN_TIME]       EQ          0.25 s
0221 C.          ÷÷[HK1_S_TLM_BIT_RATE]     EQ          32k
0222 C.          ÷÷[HK1_X_TLM_BIT_RATE]     EQ          4M
0223 C.          ÷÷[HK1_DMP_CHK_FLG]       EQ          EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»ñ³îÇ§
0226 C.          ÷÷[HK1_DMP_CHK_FLG]       EQ          NON
0227 C.
0228 C. RAM ID=TI_TBLñîî¾È¹ç•è²îOKñ³îÇ§
0229 C.
0230 C. DHUŷâ;¼ŷÈ;È¼ŷ¼. ŷî;¼ŷÈ;Èññîñ¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          ÷÷[HK1_PKT_FORM_NO]        EQ          2
0234 C.          ÷÷[HK1_PKT_GEN_TIME]       EQ          0.5S
0235 C.          ÷÷[HK1_S_TLM_BIT_RATE]     EQ          32K
0236 C.          ÷÷[HK1_X_TLM_BIT_RATE]     EQ          4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2012-02-25 10:15:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2012-02-25 10:15:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.          [ ] [HK1_TI_CMD_NUM]      EQ          2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C. *****
0255 C. SOT TI command set
0256 C. *****
0257 C. Execute, after the success of OP upload.
0258 +. TI 2012-02-25 10:15:16.0
0259 DC 07-F0 MDP_SOT_MODE_STBY
0260 BC      (41)
0261 C. -----
0262 C.      HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0263 C. -----
0264 C. ***** SOT END *****
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2012-02-25 10:15:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]      EQ          1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃîñî»ö¼ŷñÈÄñ¹ñèDCBC•x²è *****
0276 C. (¼ª°îŷÖŷÄŷÈŷŷŷÄŷçŷèÈ¼ññ¼ª»ŷñ¹ñè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷĐŷ¹•î Daily±çîññÈ'Øñ¹ñèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼ª»ŷ;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

```


(a) Spacecraft Operation Procedure (real-commands)

```
main-680 2012-02-25 12:27:00 138 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Ü;ã
0005 C.
0006 C. YÁYB;¼Y³YFYóYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;Èò¿òÁò•µ°È»Í×ÁÇòÍYçYÁY×YÍ;¼YÉ;ÈÈè%µ•íÉ;ÈòÈ¼°ÇÒò•ò¿¼ì¹çòÍ;çÀ®, ùò¹òèòòòçÁ+¿®ò•òÈòòò³òÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-261:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2012-02-25 10:15:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 . C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 + DC 07-F0 MDP_XRT_MODE_STBY
0052 BC (c3)
0053 . C. ----- Success Verify ? OK / NG_____
0054 C.
0055 C. XRT Obs. Table Upload
0056 . S. RAM ram-291:MDP_OBS_X
0057 ( )
0058 C.
0059 +. DC 07-F0 MDP_DUMP_XRTTBL
0060 BC (84 07 00 00 00 3a d4)
0061 . C. ----- Comparison Check ? OK / ERR _____
0062 C.
0063 C.
0064 +. DC 07-F0 MDP_XRT_ROI_SET
0065 BC (cd 01 b1 b1 04 04)
0066 + DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 02 b1 b1 08 08)
0068 + DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 03 b1 b1 08 08)
0070 + DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 04 b1 b1 06 06)
0072 + DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 05 85 83 06 06)
0074 + DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 06 85 83 06 06)
0076 + DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 07 85 83 08 08)
0078 + DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 08 80 80 20 20)
0080 + DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 09 80 80 20 08)
0082 + DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 0a 80 80 08 20)
0084 + DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0b c0 c0 10 10)
0086 + DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 0c 40 c0 10 10)
0088 + DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 0d 40 40 10 10)
0090 + DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 0e c0 40 10 10)
0092 + DC 07-F0 MDP_XRT_ROI_SET
0093 BC (cd 0f 80 80 06 06)
0094 + DC 07-F0 MDP_XRT_ROI_SET
0095 BC (cd 10 80 80 08 08)
```

```
0096 + DC 07-F0 MDP_XRT_FLD_ENA
0097 BC (d8)
0098 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0099 BC (c8)
0100 + DC 07-F0 MDP_XRT_AEC_RESET
0101 BC (d0)
0102 + DC 07-F0 MDP_XRT_ARS_DIS
0103 BC (d5)
0104 + DC 07-F0 MDP_XRT_FLD_RESET
0105 BC (da)
0106 + DC 07-F0 MDP_XRT_QT_PROG_SET
0107 BC (c4 04)
0108 + DC 07-F0 MDP_XRT_FL_PROG_SET
0109 BC (c5 03)
0110 . C. ----- Success Verify ? OK / NG ____
0111 C.
0112 C.
0113 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0114 C.
0115 + DC 07-F0 MDP_XRT_MODE_OBSV
0116 BC (c2)
0117 + TI 2012-02-25 10:15:02.0
0118 DC 07-F0 MDP_XRT_MODE_OBSV
0119 BC (c2)
0120 . C. ----- Success Verify ? OK / NG ____
0121 C.
0122 C. ***** XRT END *****
0123 C.
0124 . C. ***** MDP 'ûÃîñî»ö¼ÿñÊÂðñ¹ñèDCBC•x²è *****
0125 C. (¼ã°îÿÓÿÃÿÈÿÞÿËÿäÿçÿèñ¼¼ã¼Ä»Ûñ¹ñè)
0126 . S. DC-BC dcbc-402:DCBC
0127 (MDP_known_event)
0128 C.
0129 C.
0130 . C. ***** ÿDÿ¹•î Daily±;îÑñÊ'Øñ¹ñèDCBC•x²è *****
0131 . S. DC-BC dcbc-153:DCBC
0132 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0133 C.
0134 C.
0135 . C. ;ãLOSÿÃÿSÿËÿ¼Ä»Û;ã
0136 C.
0137 . C. ***** LOS *****
0138 C.
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*** OP Sequence for XRT ***

```

2012/02/25 10:25:54.0 XRT_CTRL_MANU_439_OG [0x1b7]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/02/25 10:26:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 00 53 1a 01 3f
2012/02/25 10:28:26.0 XRT_FOCUS_POSITION_409_OG [0x199]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2012/02/25 10:28:46.0 XRT_FLD_ENA_435_OG [0x1b3]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2012/02/25 10:28:48.0 XRT_FLRCTRL_ENA_403_OG [0x193]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2012/02/25 10:28:50.0 XRT_AEC_RESET_443_OG [0x1bb]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2012/02/25 10:28:52.0 XRT_ARS_DIS_431_OG [0x1af]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2012/02/25 10:28:54.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/02/25 10:28:56.0 XRT_QT_PROG_SET_441_OG [0x1b9]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 07
2012/02/25 10:28:58.0 XRT_FL_PROG_SET_414_OG [0x19e]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 03
2012/02/25 10:29:00.0 XRT_CTRL_AUTO_406_OG [0x196]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/02/25 15:16:00.0 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/02/25 15:16:02.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/02/25 15:16:04.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2012/02/25 15:19:14.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2012/02/25 15:39:00.0 XRT_Custom_418_OG [0x1a2]
2012/02/25 15:40:00.0 XRT_CTRL_AUTO_419_OG [0x1a3]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/02/25 16:52:00.0 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/02/25 16:52:02.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/02/25 16:52:04.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2012/02/25 16:55:14.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2012/02/25 17:15:30.0 XRT_Custom_418_OG [0x1a2]
2012/02/25 17:16:30.0 XRT_CTRL_AUTO_419_OG [0x1a3]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/02/25 17:59:54.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/02/25 17:59:56.0 XRT_FOCUS_POSITION_401_OG [0x191]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2012/02/25 18:00:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 00 00 00 00 00
2012/02/25 18:00:16.0 XRT_FLD_DIS_402_OG [0x192]
                        MDP_XRT_FLD_DIS 1 07-F0 d9
2012/02/25 18:00:18.0 XRT_FLRCTRL_DIS_433_OG [0x1b1]
                        MDP_XRT_FLRCTRL_DIS 1 07-F0 c9
2012/02/25 18:00:20.0 XRT_ARS_DIS_438_OG [0x1b6]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2012/02/25 18:02:58.0 XRT_QT_PROG_SET_425_OG [0x1a9]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 05
2012/02/25 18:03:00.0 XRT_CTRL_AUTO_406_OG [0x196]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/02/25 18:09:54.0 XRT_CTRL_MANU_439_OG [0x1b7]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/02/25 18:10:00.0 AOCs_OrE-point_Start_3_OG [0x099]
                        AOCU_NM 5 02-76 01 00 00 00 00
2012/02/25 18:12:26.0 XRT_FOCUS_POSITION_409_OG [0x199]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2012/02/25 18:12:46.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2012/02/25 18:12:48.0 XRT_FLRCTRL_ENA_413_OG [0x19d]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2012/02/25 18:12:50.0 XRT_AEC_RESET_443_OG [0x1bb]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2012/02/25 18:12:52.0 XRT_ARS_DIS_431_OG [0x1af]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2012/02/25 18:12:54.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/02/25 18:12:56.0 XRT_QT_PROG_SET_407_OG [0x197]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0f
2012/02/25 18:12:58.0 XRT_FL_PROG_SET_414_OG [0x19e]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 03
2012/02/25 18:13:00.0 XRT_CTRL_AUTO_406_OG [0x196]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/02/25 18:29:00.0 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/02/25 18:29:02.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/02/25 18:29:04.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2012/02/25 18:32:14.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2012/02/25 18:52:30.0 XRT_Custom_418_OG [0x1a2]

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2012/02/25	18:53:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/02/25	20:06:00.0	XRT_CTRL_MANU_408_OG [0x198]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/02/25	20:06:02.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/02/25	20:06:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/02/25	20:09:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/02/25	20:29:30.0	XRT_Custom_418_OG [0x1a2]								
2012/02/25	20:30:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/02/25	21:43:30.0	XRT_CTRL_MANU_408_OG [0x198]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/02/25	21:43:32.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/02/25	21:43:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/02/25	21:46:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/02/25	22:06:00.0	XRT_Custom_418_OG [0x1a2]								
2012/02/25	22:07:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/02/25	23:21:00.0	XRT_CTRL_MANU_408_OG [0x198]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/02/25	23:21:02.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/02/25	23:21:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/02/25	23:24:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/02/25	23:35:00.0	XRT_Custom_418_OG [0x1a2]								
2012/02/25	23:36:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/02/26	00:51:00.0	XRT_CTRL_MANU_408_OG [0x198]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/02/26	00:51:02.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/02/26	00:51:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/02/26	00:54:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/02/26	01:04:30.0	XRT_Custom_418_OG [0x1a2]								
2012/02/26	01:05:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/02/26	02:20:00.0	XRT_CTRL_MANU_408_OG [0x198]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/02/26	02:20:02.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/02/26	02:20:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/02/26	02:23:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/02/26	02:40:30.0	XRT_Custom_418_OG [0x1a2]								
2012/02/26	02:41:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/02/26	03:48:00.0	XRT_CTRL_MANU_408_OG [0x198]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/02/26	03:48:02.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/02/26	03:48:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/02/26	03:51:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/02/26	04:18:00.0	XRT_Custom_418_OG [0x1a2]								
2012/02/26	04:19:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/02/26	05:28:30.0	XRT_CTRL_MANU_408_OG [0x198]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/02/26	05:28:32.0	XRT_FLD_RESET_412_OG [0x19c]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2012/02/26	05:28:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2012/02/26	05:31:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2012/02/26	05:55:30.0	XRT_Custom_418_OG [0x1a2]								
2012/02/26	05:56:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2012/02/26	06:04:54.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2012/02/26	06:04:56.0	XRT_FOCUS_POSITION_401_OG [0x191]								
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00					
2012/02/26	06:05:00.0	AOCs_OrE-point_Start_2_OG [0x098]								
		AOCU_NM	5	02-76	00 00 00 00 00					
2012/02/26	06:05:16.0	XRT_FLD_DIS_402_OG [0x192]								
		MDP_XRT_FLD_DIS	1	07-F0	d9					
2012/02/26	06:05:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]								
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2012/02/26	06:05:20.0	XRT_ARS_DIS_438_OG [0x1b6]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2012/02/26	06:07:58.0	XRT_QT_PROG_SET_425_OG [0x1a9]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05					

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2012/02/26	06:08:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/26	06:14:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/26	06:15:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2012/02/26	06:17:26.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/02/26	06:17:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/02/26	06:17:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/02/26	06:17:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/02/26	06:17:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/02/26	06:17:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/26	06:17:56.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f				
2012/02/26	06:17:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2012/02/26	06:18:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/26	07:09:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/26	07:09:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/26	07:09:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/02/26	07:12:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/26	07:32:30.0	XRT_Custom_418_OG [0x1a2]							
2012/02/26	07:33:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/26	08:48:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/26	08:48:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/26	08:48:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/02/26	08:51:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/26	09:09:00.0	XRT_Custom_418_OG [0x1a2]							
2012/02/26	09:10:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/26	10:30:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/26	10:30:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/26	10:30:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/02/26	10:33:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/26	10:37:30.0	XRT_Custom_418_OG [0x1a2]							
2012/02/26	10:38:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/26	14:18:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/26	14:18:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/26	14:18:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/02/26	14:21:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/26	14:26:00.0	XRT_Custom_418_OG [0x1a2]							
2012/02/26	14:27:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/26	15:51:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/26	15:51:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/26	15:51:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/02/26	15:54:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/26	16:15:00.0	XRT_Custom_418_OG [0x1a2]							
2012/02/26	16:16:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/26	17:28:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/26	17:28:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/26	17:28:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/02/26	17:31:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/26	17:53:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/26	17:53:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/02/26	17:53:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				

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2012/02/26	17:53:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/02/26	17:53:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/02/26	17:53:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/02/26	17:56:28.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2012/02/26	17:56:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/26	18:03:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/26	18:03:30.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2012/02/26	18:05:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/02/26	18:06:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/02/26	18:06:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/02/26	18:06:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/02/26	18:06:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/02/26	18:06:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/26	18:06:26.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f				
2012/02/26	18:06:28.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2012/02/26	18:06:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/26	19:05:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/26	19:05:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/26	19:05:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/02/26	19:08:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/26	19:28:30.0	XRT_Custom_418_OG [0x1a2]							
2012/02/26	19:29:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/26	20:42:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/26	20:42:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/26	20:42:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/02/26	20:45:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/26	21:06:00.0	XRT_Custom_418_OG [0x1a2]							
2012/02/26	21:07:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/26	22:20:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/26	22:20:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/26	22:20:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/02/26	22:23:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/26	22:41:30.0	XRT_Custom_418_OG [0x1a2]							
2012/02/26	22:42:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/26	23:30:00.0	XRT_CTRL_MANU_442_OG [0x1ba]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/26	23:30:30.0	XRT_TCIB_XRT_S_HTR_A_ENA_417_OG [0x1a1]							
		TCIB_XRT_S_HTR_A_ENA	0	04-BC					
2012/02/27	23:30:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/27	23:30:02.0	XRT_TCIB_XRT_S_HTR_A_DIS_436_OG [0x1b4]							
		TCIB_XRT_S_HTR_A_DIS	0	04-C0					
2012/02/28	02:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2012/02/28	05:34:54.0	XRT_CTRL_MANU_430_OG [0x1ae]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/28	05:35:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00 2e f9 2e f9				
2012/02/28	05:37:32.0	XRT_FOCUS_POSITION_427_OG [0x1ab]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/02/28	05:37:52.0	XRT_QT_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2012/02/28	05:37:54.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/02/28	05:37:56.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/02/28	05:37:58.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/02/28	05:38:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/28	05:44:54.0	XRT_CTRL_MANU_430_OG [0x1ae]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				

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2012/02/28	05:45:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00	2e f9 d1 07
2012/02/28	05:47:32.0	XRT_FOCUS_POSITION_427_OG [0x1ab]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/02/28	05:47:52.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c	
2012/02/28	05:47:54.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/02/28	05:47:56.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/02/28	05:47:58.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/02/28	05:48:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/02/28	05:54:54.0	XRT_CTRL_MANU_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/02/28	05:55:00.0	AOCS_ORe-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 d1 07 d1 07	
2012/02/28	05:57:32.0	XRT_FOCUS_POSITION_427_OG [0x1ab]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/02/28	05:57:52.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13	
2012/02/28	05:57:54.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/02/28	05:57:56.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/02/28	05:57:58.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/02/28	05:58:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/02/28	06:04:54.0	XRT_CTRL_MANU_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/02/28	06:05:00.0	AOCS_ORe-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00 d1 07 2e f9	
2012/02/28	06:07:32.0	XRT_FOCUS_POSITION_427_OG [0x1ab]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/02/28	06:07:52.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06	
2012/02/28	06:07:54.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/02/28	06:07:56.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/02/28	06:07:58.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/02/28	06:08:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/02/28	06:14:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/02/28	06:14:56.0	XRT_FOCUS_RECALIBRATE_428_OG [0x1ac]	XRT_FOCUS_RECAL	2	07-F8	78 00	
2012/02/28	06:15:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2012/02/28	06:15:00.5	XRT_ROI_A_448_OG [0x1c0]	MDP_XRT_ROI_SET	6	07-F0	cd 06 85 83 0c 0c	
			MDP_XRT_ROI_SET	6	07-F0	cd 08 80 80 20 20	
			MDP_XRT_ROI_SET	6	07-F0	cd 09 80 80 20 08	
			MDP_XRT_ROI_SET	6	07-F0	cd 0a 80 80 08 20	
			MDP_XRT_ROI_SET	6	07-F0	cd 0f 80 80 06 06	
			MDP_XRT_ROI_SET	6	07-F0	cd 10 80 80 08 08	
2012/02/28	06:18:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/02/28	06:19:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/02/28	06:19:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/02/28	06:19:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/02/28	06:21:58.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05	
2012/02/28	06:22:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/02/28	06:24:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/02/28	06:25:00.0	AOCS_ORe-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	04 00 00 00 00	
2012/02/28	06:27:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2012/02/28	06:27:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/02/28	06:27:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/02/28	06:27:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/02/28	06:27:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/02/28	06:27:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/02/28	06:27:56.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e	
2012/02/28	06:27:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03	
2012/02/28	06:28:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	

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2012/02/28	06:44:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/28	06:44:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/28	06:44:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/28	06:47:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/28	07:08:30.0	XRT_Custom_418_OG [0x1a2]			
2012/02/28	07:09:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/28	08:23:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/28	08:23:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/28	08:23:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/28	08:26:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/28	08:45:00.0	XRT_Custom_418_OG [0x1a2]			
2012/02/28	08:46:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/28	10:04:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/28	10:04:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/28	10:04:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/28	10:07:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/28	10:38:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00