

XRT Timeline to be uploaded on 2012/05/17

Period: 2012/05/17 10:29:00 - 2012/06/22 12:00:00

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Normal mode

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XOB #1881: CME watch - 4x4 - AEC 2 - Ti/poly												
Term	Pointing (x, y)		Comment									
05/17 10:42:00 - 05/17 11:05:54	Fixed (936.0, 0.0)	# OP start + 10min: HOP130 (10:50-16:40 UT) #1										
05/17 11:09:00 - 05/17 11:21:54	Fixed (468.0, 0.0)	HOP130 #2										
05/17 12:19:30 - 05/17 12:32:24	Fixed (-468.0, 0.0)	#4										
05/17 12:35:30 - 05/17 12:48:24	Fixed (-936.0, 0.0)	#5										
05/17 12:51:30 - 05/17 12:58:24	Fixed (815.0, 470.0)	#6										
05/17 13:01:30 - 05/17 13:27:30	Fixed (411.0, 847.0)	#7										
05/17 13:58:00 - 05/17 14:10:54	Fixed (0.0, 941.0)	#8										
05/17 14:14:00 - 05/17 14:26:54	Fixed (-411.0, 847.0)	#9										
05/17 14:30:00 - 05/17 14:36:54	Fixed (-815.0, 470.0)	#10										
05/17 14:40:00 - 05/17 14:46:54	Fixed (815.0, -470.0)	#11										
05/17 14:50:00 - 05/17 15:57:54	Fixed (411.0, -847.0)	#12										
05/17 16:01:00 - 05/17 16:13:54	Fixed (0.0, -941.0)	#13										
05/17 16:17:00 - 05/17 16:29:54	Fixed (-411.0, -847.0)	#14										
05/17 16:33:00 - 05/17 16:39:54	Fixed (-815.0, -471.0)	#15										
PROG= 16 Inf.-time(s)												
└─ Subr= 1 1-time(s) 120.0sec												
└─ Seqn= 57 1-time(s) 4.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #18E1: HOP130-Synoptic 3 Filter- 2x2 Q98 Short exp 2 loop-2												
Term	Pointing (x, y)		Comment									
05/17 11:25:00 - 05/17 11:49:00	Fixed (0.0, 0.0)	#3										
PROG= 01 2-time(s)												
└─ Subr= 1 1-time(s) 180.0sec												
└─ Seqn= 82 1-time(s) 25.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 98 1-time(s) 25.0sec												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 12 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #183E: AR Standard-B(Morphology) with PFB, FW1=Open, Ti/Poly, 384x384 at 1064 1048, 80sec-cad												
Term	Pointing (x, y)		Comment									
05/17 16:43:00 - 05/17 17:59:54	Track (-46.7, 229.7)	@ 05/17 16:40:00	AR obs (NOAA11482)									
05/17 18:13:00 - 05/18 06:24:24	Track (-33.3, 229.6)	@ 05/17 18:10:00	AR 11482 obs									
05/18 06:37:30 - 05/18 07:23:00	Track (78.2, 228.5)	@ 05/18 06:34:30	track AR 11482									
05/18 10:03:00 - 05/18 17:22:00	Track (52.9, -251.1)	@ 05/18 10:00:00	track AR 11481, 11483									
05/18 18:18:30 - 05/19 05:52:54	Track (182.1, 227.0)	@ 05/18 18:15:30	track AR 11482									
05/19 06:06:00 - 05/19 08:01:00	Track (284.5, 225.0)	@ 05/19 06:03:00	track AR 11482									
PROG= 19 Inf.-time(s)												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 19 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 41 4-time(s) 2.0sec												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 16 30-time(s) 80.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	20.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	20.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	20.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	20.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #18EA: Synoptic Q95 2x2 - Al/mesh(12/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(24/1443) + G-band(16)												
Term	Pointing (x, y)		Comment									
05/17 18:03:00 - 05/17 18:09:54	Fixed (0.0, 0.0)	synoptic										
05/18 06:27:30 - 05/18 06:34:24	Fixed (0.0, 0.0)	synoptic, shifted 24.5 min										
05/18 18:08:30 - 05/18 18:15:24	Fixed (0.0, 0.0)	synoptic, shifted 5.5 min										
05/19 05:56:00 - 05/19 06:02:54	Fixed (0.0, 0.0)	synoptic, shifted -7.0 min										
PROG= 07 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 46 1-time(s) 4.0sec												

Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 5		1-time(s)		2.0sec								
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 69		1-time(s)		4.0sec								
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 4		1-time(s)		2.0sec								
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #174E: HOP81 2-filter - Ti/poly 8s, Al/mesh 4s, G-band - 384x384

Term	Pointing (x, y)	Comment
05/18 08:03:00 - 05/18 09:59:54	Fixed (-13.0, -975.0)	HOP200 (8-10 UT)
05/19 08:37:30 - 05/19 09:39:30	Fixed (-13.0, -975.0)	HOP200 (8-10 UT)
PROG= 10 Inf.-time(s)		
Subr= 1		30-time(s)
Seqn= 51		1-time(s)
Open/G-band	Open/G-band	open
Safe	Norm	63ms
Obs	1x1	384x384 (1064, 1048)
Q=90	0	0
2.0sec		
Subr= 2		30-time(s)
Seqn= 58		2-time(s)
Open/Al-mesh	Open/Al-mesh	close
Safe	Norm	4.00s
Obs	1x1	384x384 (1064, 1048)
Q=90	0	0
2.0sec		
Open/Ti-poly	Open/Ti-poly	close
Safe	Norm	8.00s
Obs	1x1	384x384 (1064, 1048)
Q=90	0	0
2.0sec		
Default Filter	Thicker Filter	VLS
mode	image	Exp.
CCD	Bin	ROI: size (center)
Comp.	AEC Buffer	Interval

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Flare mode

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XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)

Term	Pointing (x, y)	Comment
05/17 10:42:00 - 05/17 11:05:54	Fixed (936.0, 0.0)	# OP start + 10min: HOP130 (10:50-16:40 UT) #1
05/17 11:09:00 - 05/17 11:21:54	Fixed (468.0, 0.0)	HOP130 #2
05/17 12:19:30 - 05/17 12:32:24	Fixed (-468.0, 0.0)	#4
05/17 12:35:30 - 05/17 12:48:24	Fixed (-936.0, 0.0)	#5
05/17 12:51:30 - 05/17 12:58:24	Fixed (815.0, 470.0)	#6
05/17 13:01:30 - 05/17 13:27:30	Fixed (411.0, 847.0)	#7
05/17 13:58:00 - 05/17 14:10:54	Fixed (0.0, 941.0)	#8
05/17 14:14:00 - 05/17 14:26:54	Fixed (-411.0, 847.0)	#9
05/17 14:30:00 - 05/17 14:36:54	Fixed (-815.0, 470.0)	#10
05/17 14:40:00 - 05/17 14:46:54	Fixed (815.0, -470.0)	#11
05/17 14:50:00 - 05/17 15:57:54	Fixed (411.0, -847.0)	#12
05/17 16:01:00 - 05/17 16:13:54	Fixed (0.0, -941.0)	#13
05/17 16:17:00 - 05/17 16:29:54	Fixed (-411.0, -847.0)	#14
05/17 16:33:00 - 05/17 16:39:54	Fixed (-815.0, -471.0)	#15
05/17 16:43:00 - 05/17 17:59:54	Track (-46.7, 229.7) @ 05/17 16:40:00	AR obs (NOAA11482)
05/17 18:13:00 - 05/18 06:24:24	Track (-33.3, 229.6) @ 05/17 18:10:00	AR 11482 obs
05/18 06:37:30 - 05/18 07:23:00	Track (78.2, 228.5) @ 05/18 06:34:30	track AR 11482
05/18 08:03:00 - 05/18 09:59:54	Fixed (-13.0, -975.0)	HOP200 (8-10 UT)
05/18 10:03:00 - 05/18 17:22:00	Track (52.9, -251.1) @ 05/18 10:00:00	track AR 11481, 11483
05/18 18:18:30 - 05/19 05:52:54	Track (182.1, 227.0) @ 05/18 18:15:30	track AR 11482
05/19 06:06:00 - 05/19 08:01:00	Track (284.5, 225.0) @ 05/19 06:03:00	track AR 11482
05/19 08:37:30 - 05/19 09:39:30	Fixed (-13.0, -975.0)	HOP200 (8-10 UT)

PROG= 13 1-time(s)

Subr= 1		30-time(s)		20.0sec								
Seqn= 88		1-time(s)		2.0sec								
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn=100		1-time(s)		2.0sec								
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2		1-time(s)		2.0sec								
Seqn= 90		1-time(s)		2.0sec								
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3		30-time(s)		60.0sec								
Seqn= 88		1-time(s)		2.0sec								
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60		1-time(s)		2.0sec								
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2		1-time(s)		2.0sec								
Seqn= 90		1-time(s)		2.0sec								
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec

	Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3	30-time(s)	60.0sec											
	Seqn= 88	1-time(s)	2.0sec										
	Open/thick-AI	Open/thick-AI	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Seqn= 60	1-time(s)	2.0sec										
	Open/AI-mesh	Open/AI-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2	1-time(s)	2.0sec											
	Seqn= 90	1-time(s)	2.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3	30-time(s)	60.0sec											
	Seqn= 88	1-time(s)	2.0sec										
	Open/thick-AI	Open/thick-AI	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Seqn= 60	1-time(s)	2.0sec										
	Open/AI-mesh	Open/AI-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 4	24-time(s)	600.0sec											
	Seqn= 89	1-time(s)	2.0sec										
	Open/AI-mesh	Open/thick-AI	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/AI-mesh	Open/AI-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

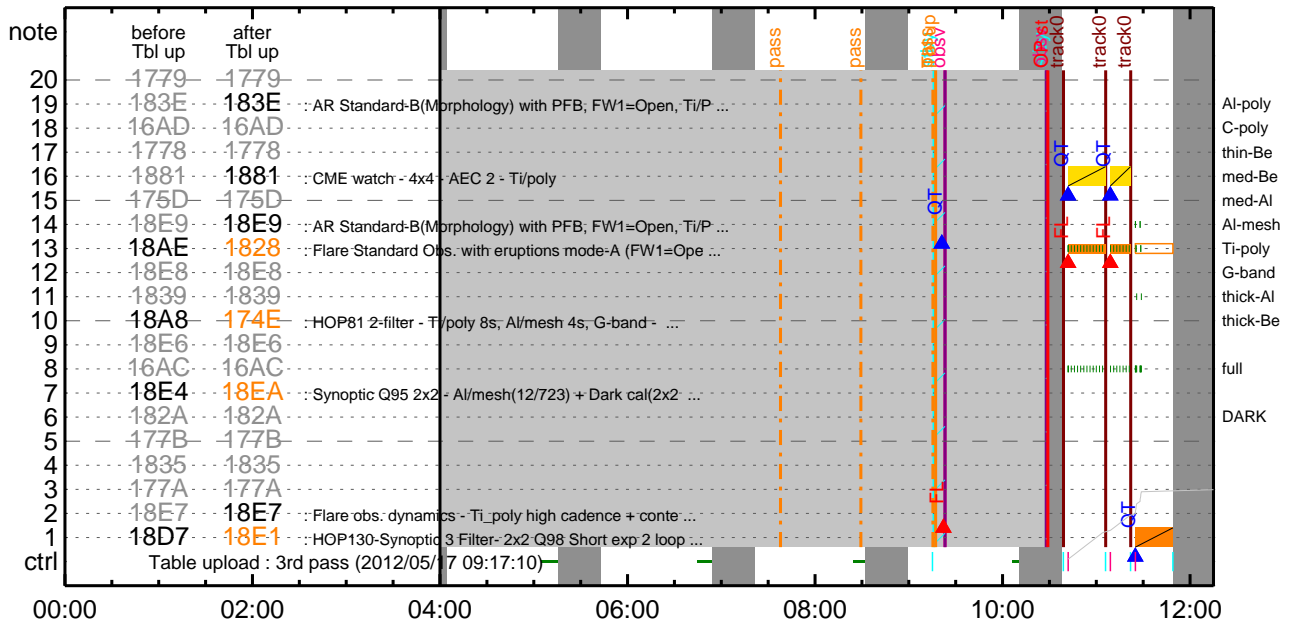
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Flare Detection

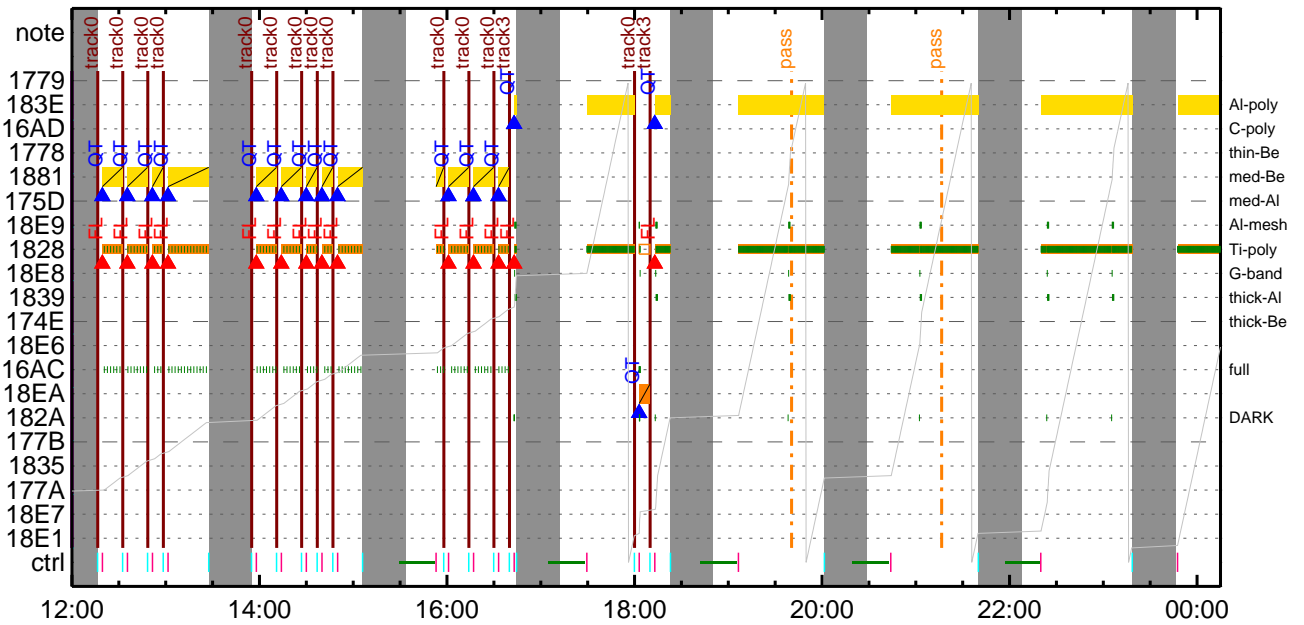
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FLD Patrol												
Term	Pointing (x, y)				Comment							
05/17 12:19:16 - 05/17 18:00:16	Fixed (-468.0, 0.0)				#4							
05/17 18:12:46 - 05/18 06:24:46	Track (-33.3, 229.6) @ 05/17 18:10:00				AR 11482 obs							
05/18 06:37:16 - 05/18 18:05:46	Track (78.2, 228.5) @ 05/18 06:34:30				track AR 11482							
05/18 18:18:16 - 05/19 05:53:16	Track (182.1, 227.0) @ 05/18 18:15:30				track AR 11482							
05/19 06:05:46 - 06/22 12:00:00	Track (284.5, 225.0) @ 05/19 06:03:00				track AR 11482							
	Open/Ti-poly	Open/thick-AI	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

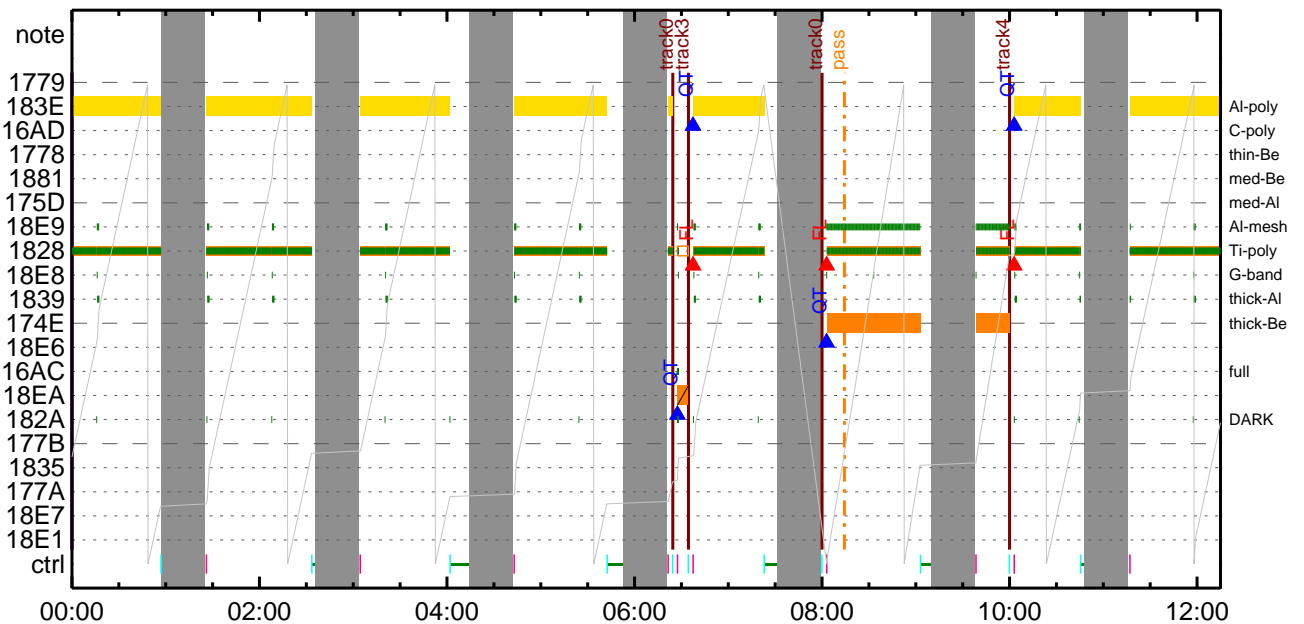
CMDI #0634 2012/05/17



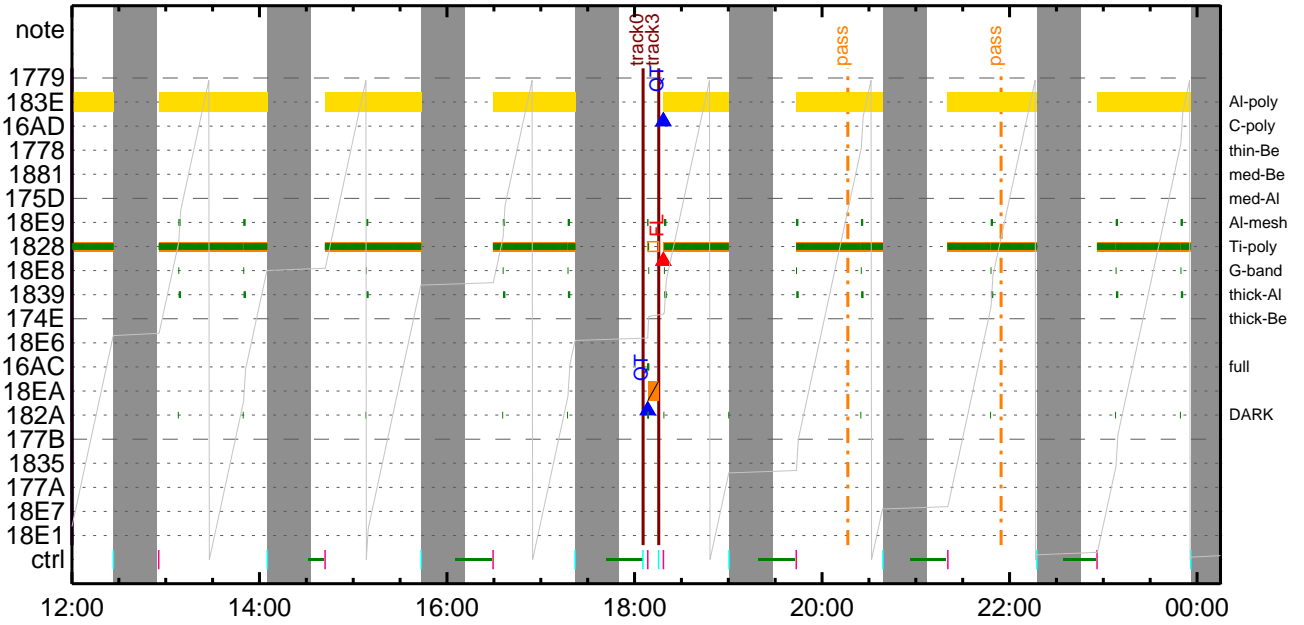
CMDI #0634 2012/05/17



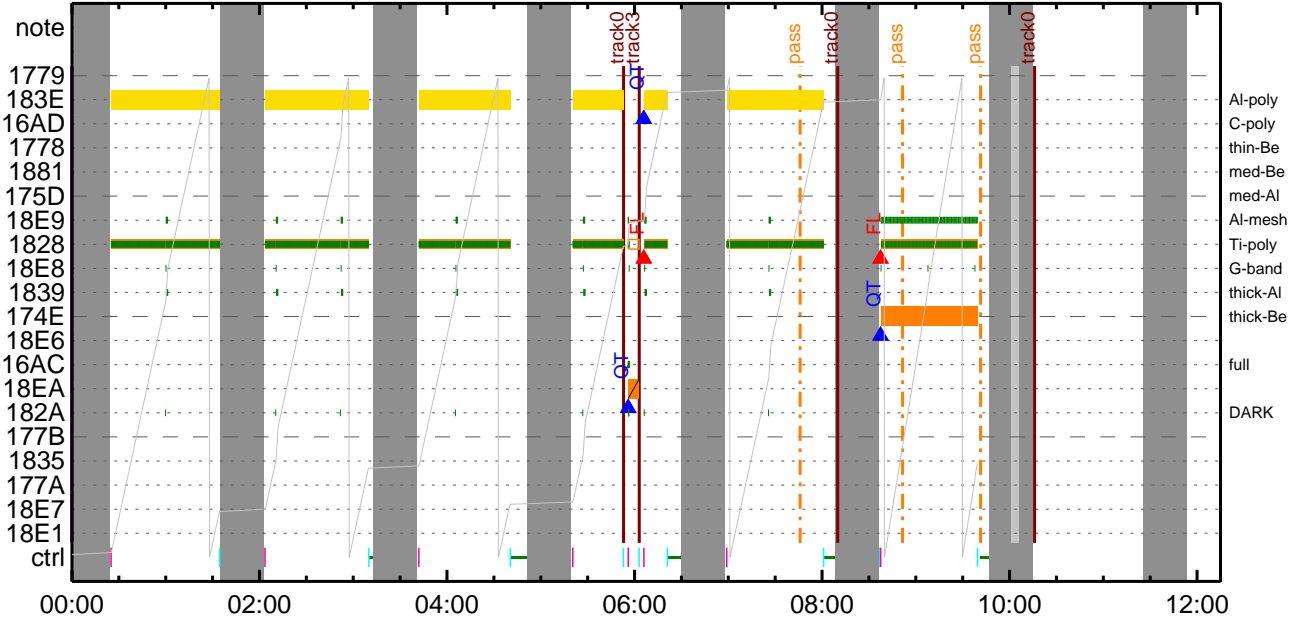
CMDI #0634 2012/05/18



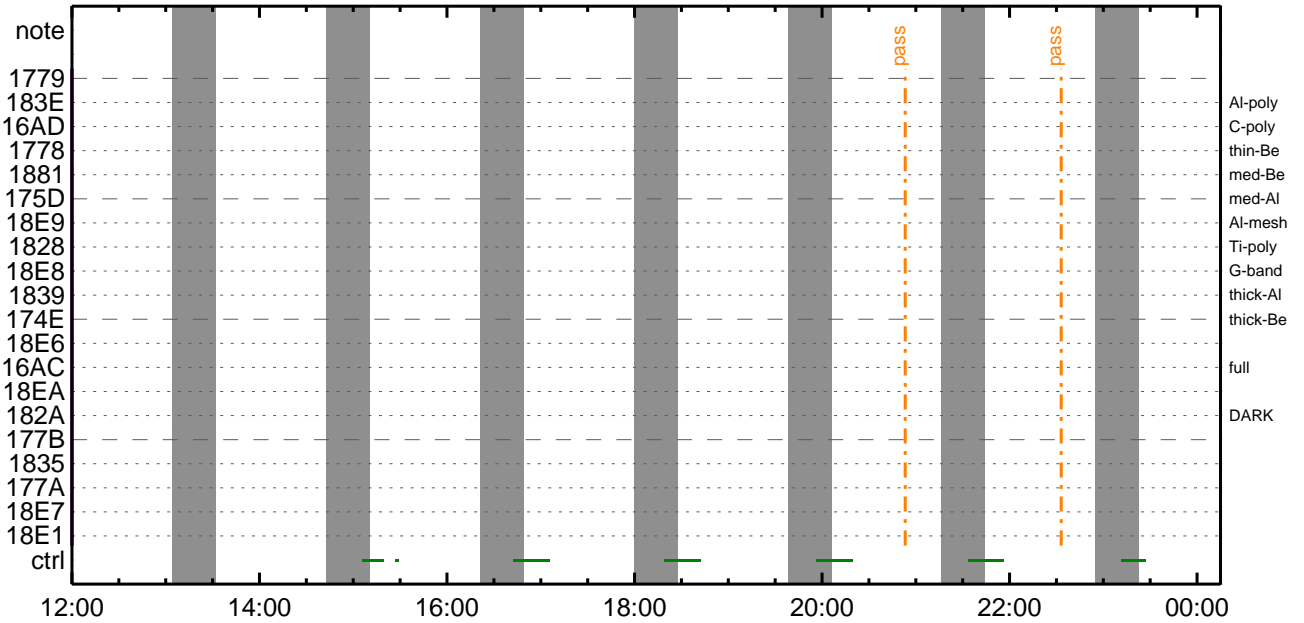
CMDI #0634 2012/05/18



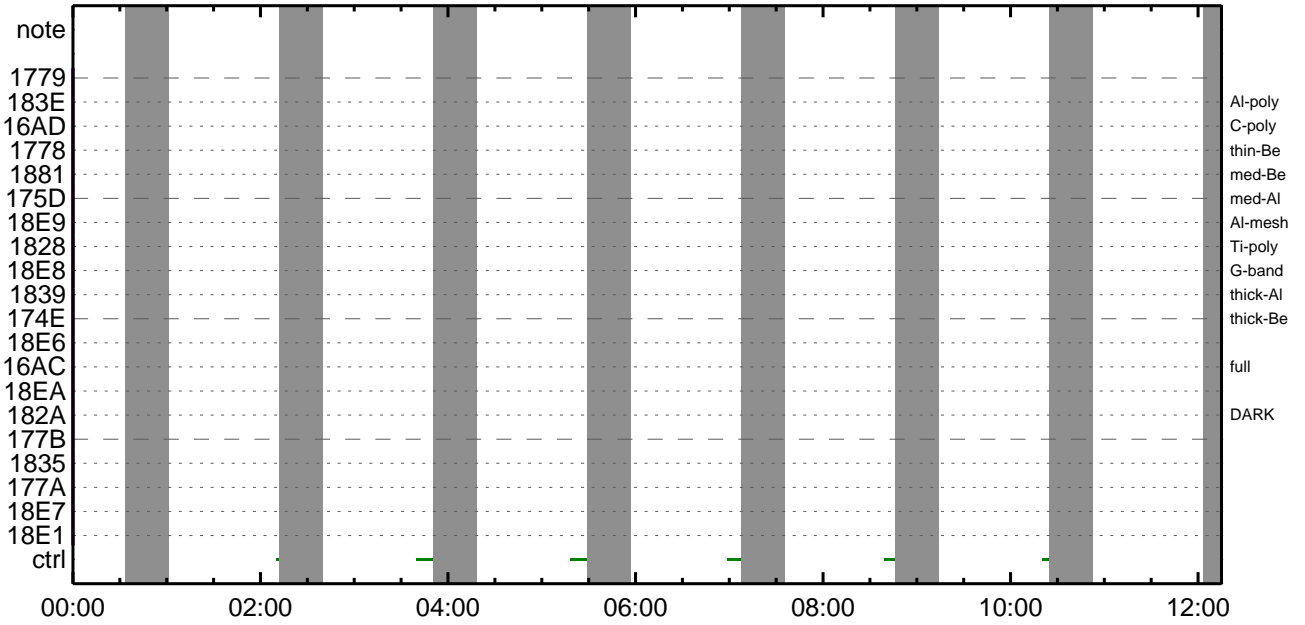
CMDI #0634 2012/05/19



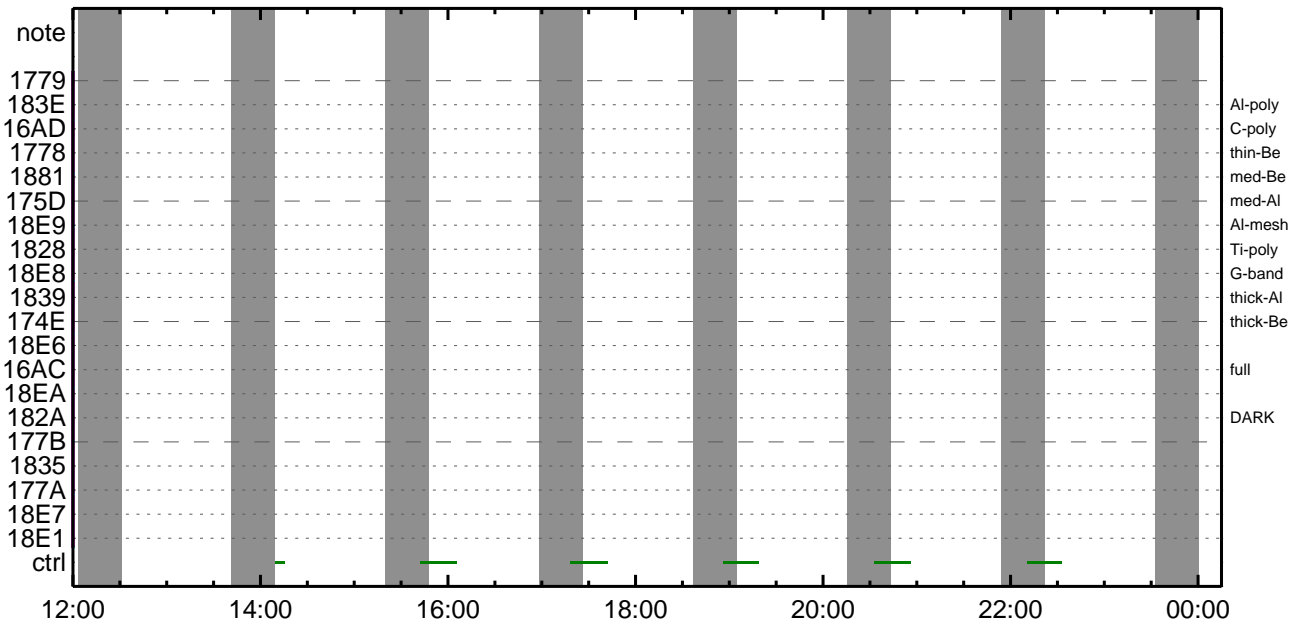
CMDI #0634 2012/05/19



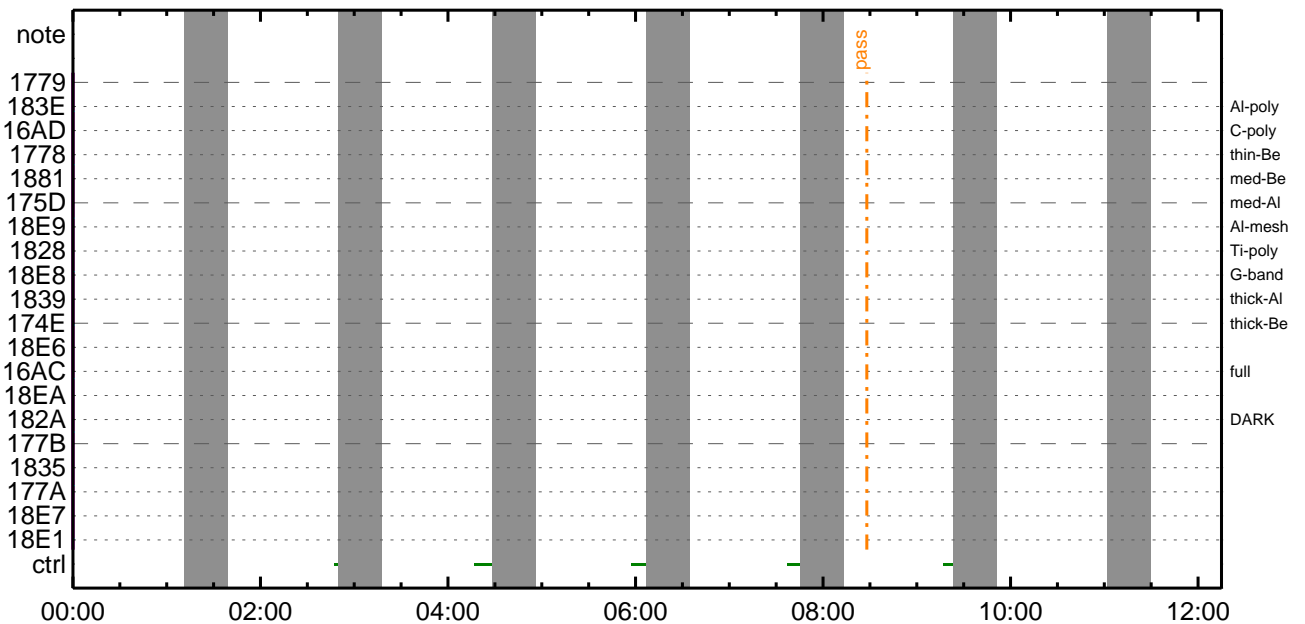
CMDI #0634 2012/05/20



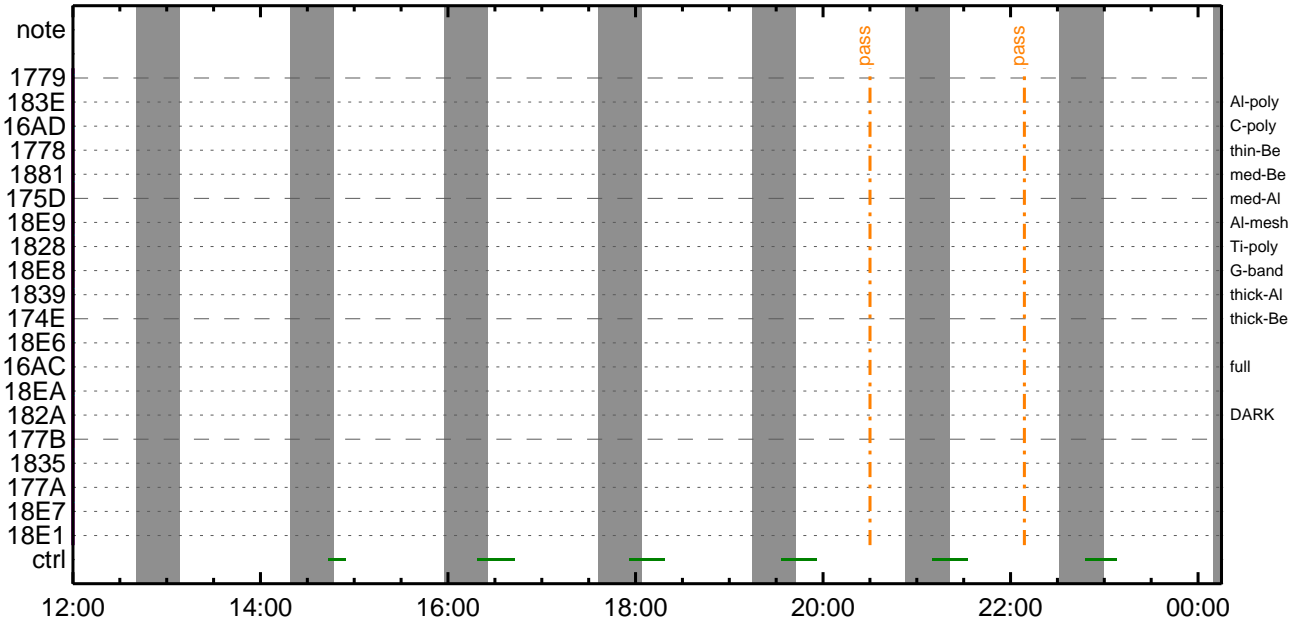
CMDI #0634 2012/05/20



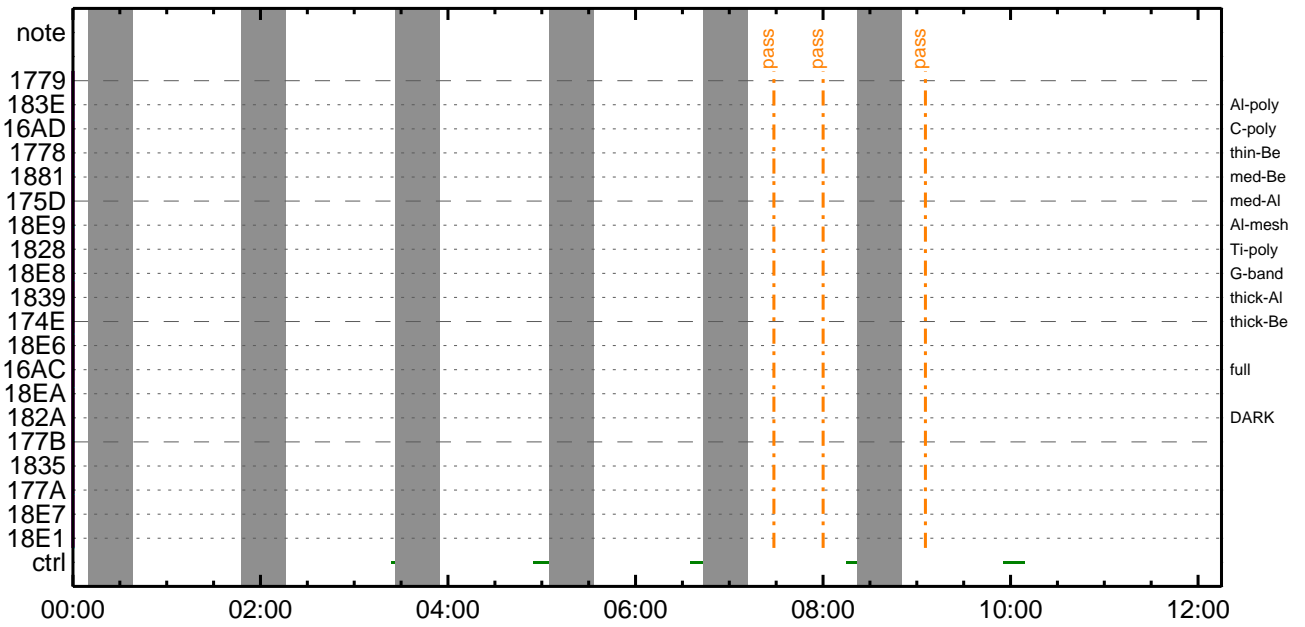
CMDI #0634 2012/05/21



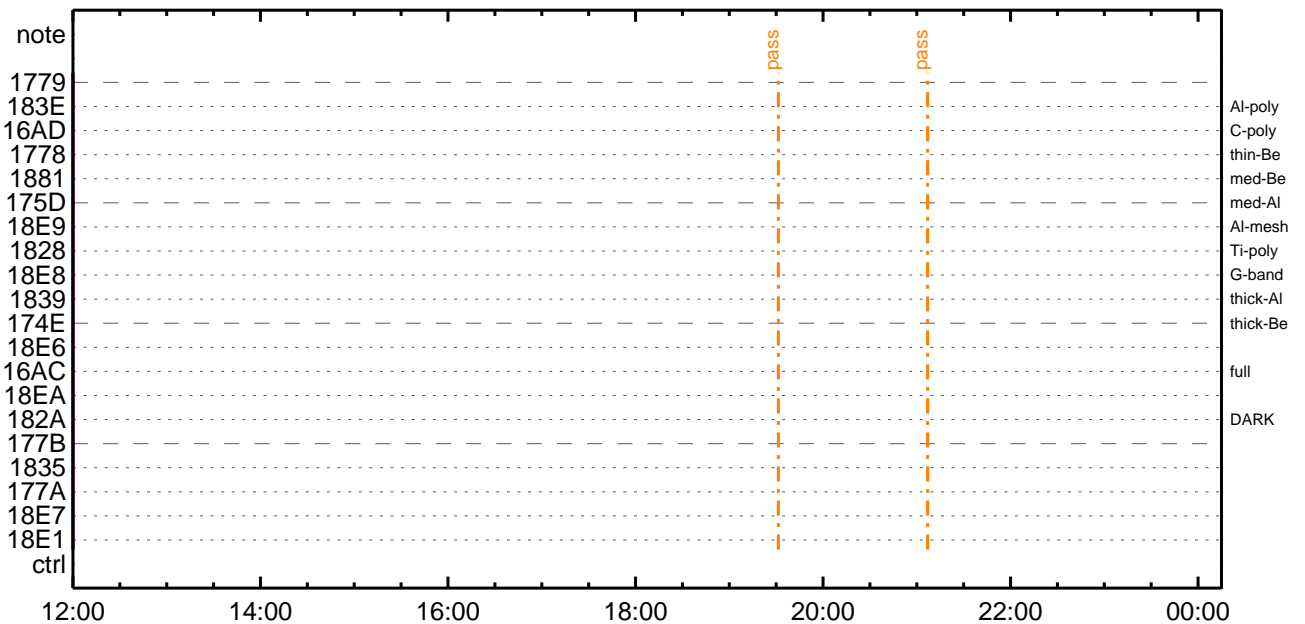
CMDI #0634 2012/05/21



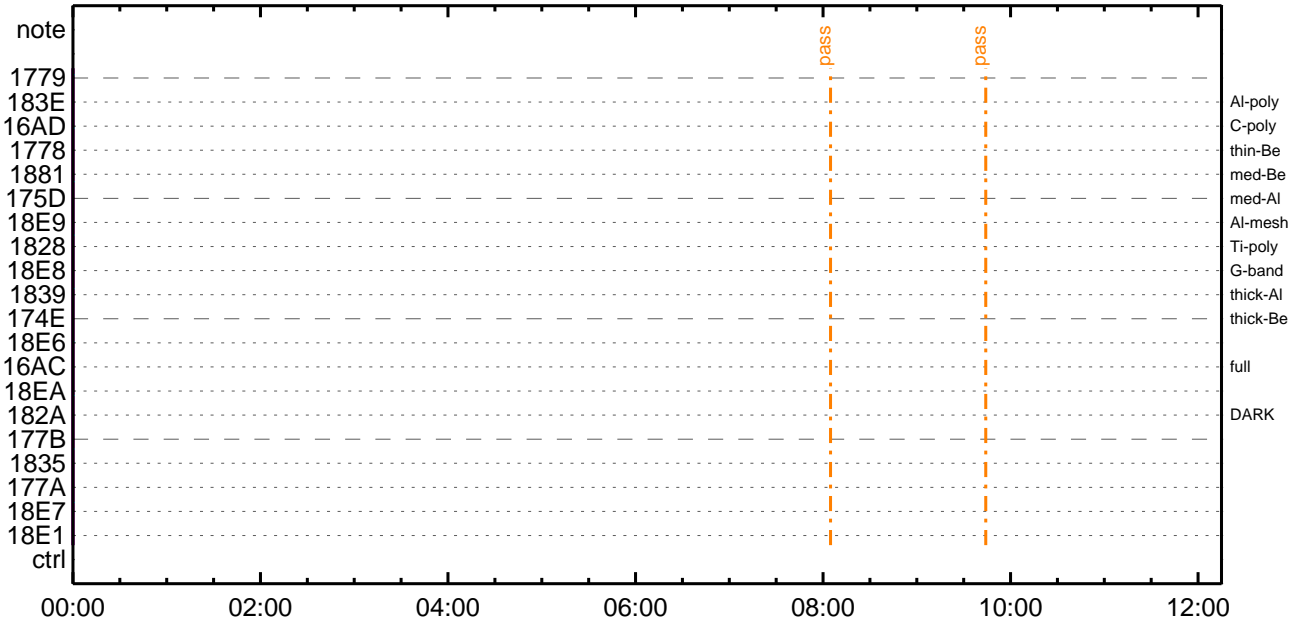
CMDI #0634 2012/05/22



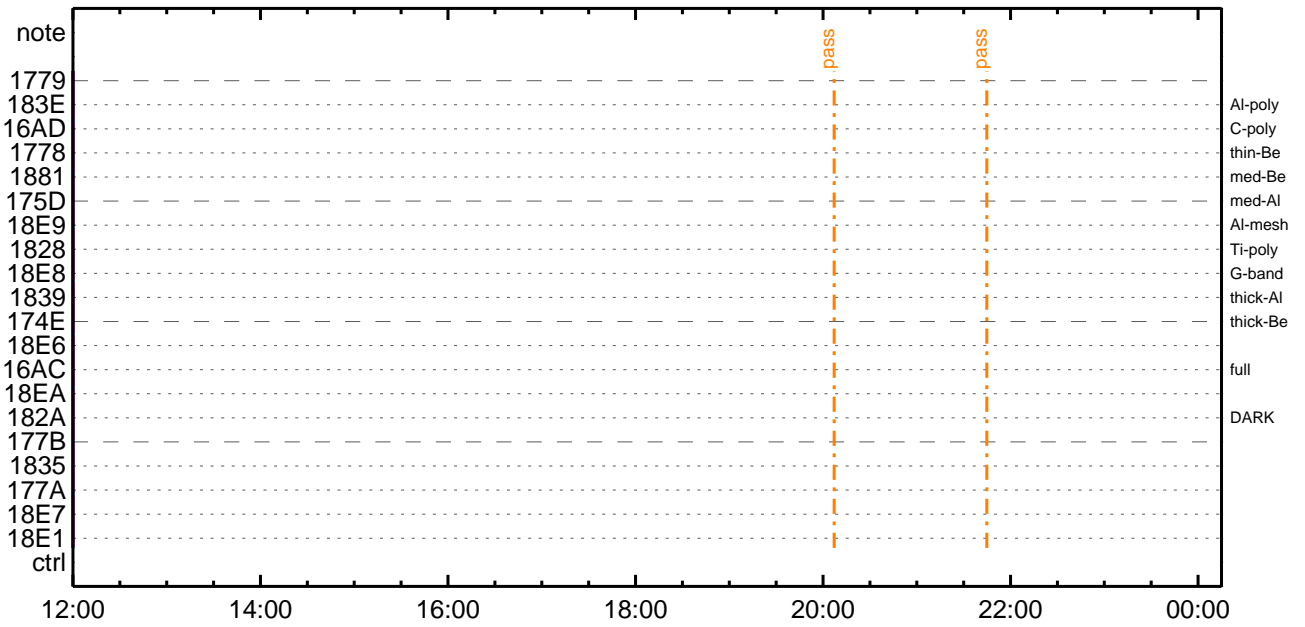
CMDI #0634 2012/05/22



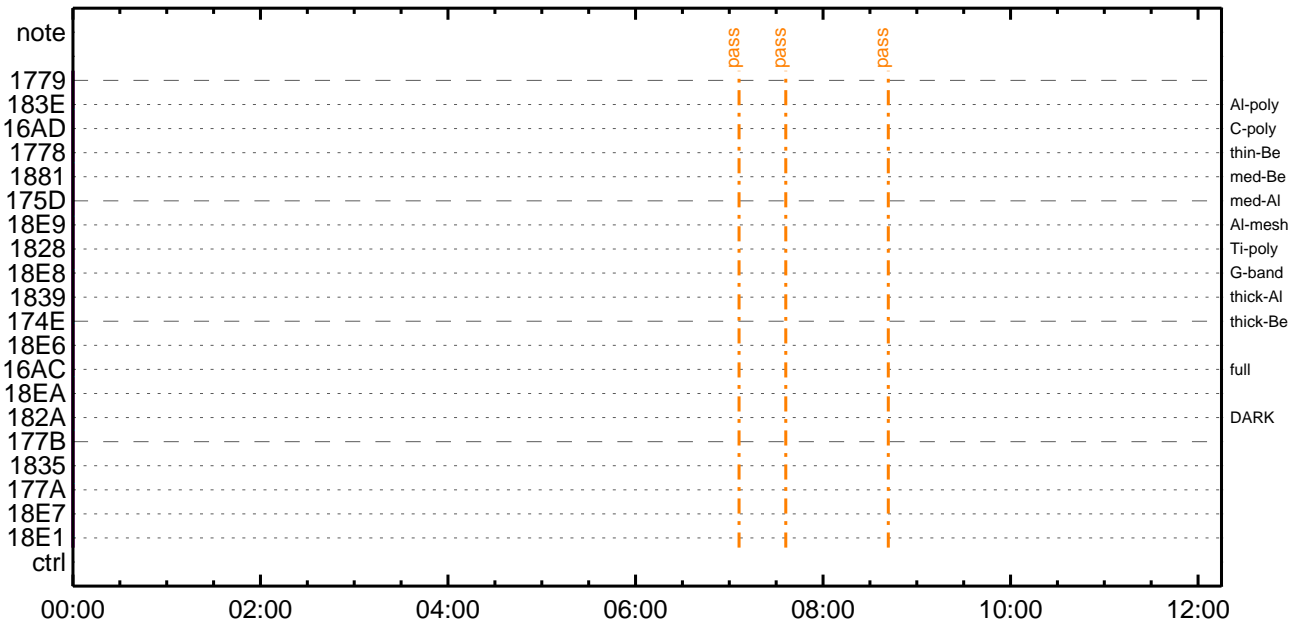
CMDI #0634 2012/05/23



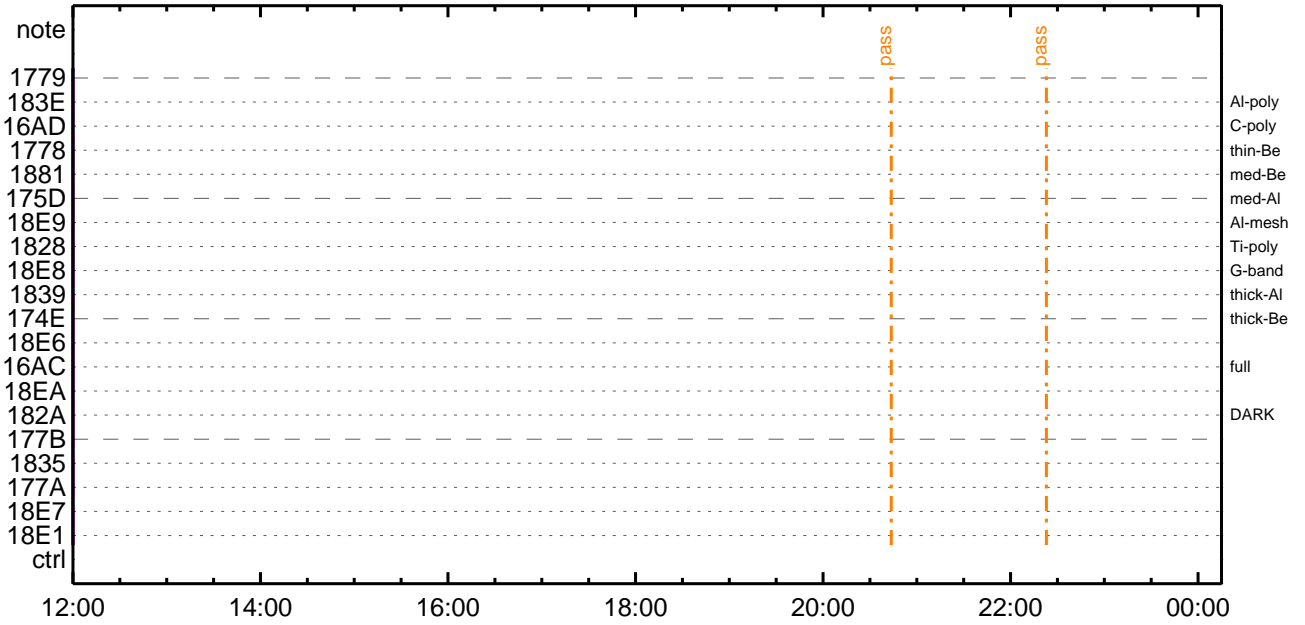
CMDI #0634 2012/05/23



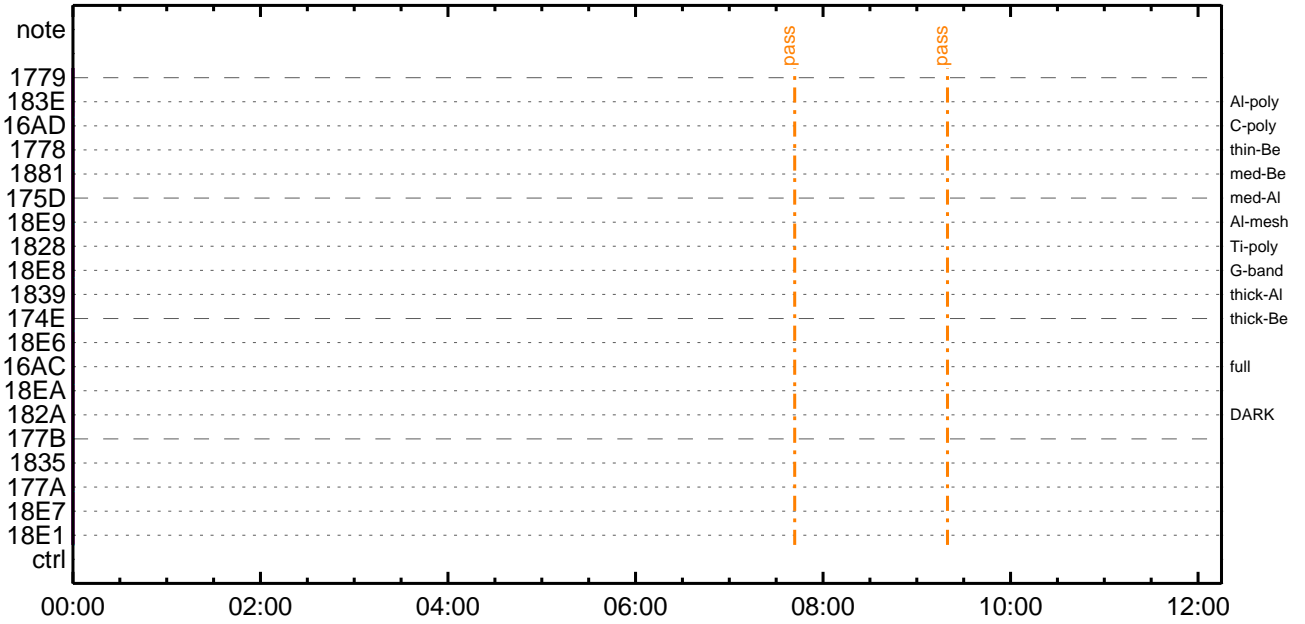
CMDI #0634 2012/05/24



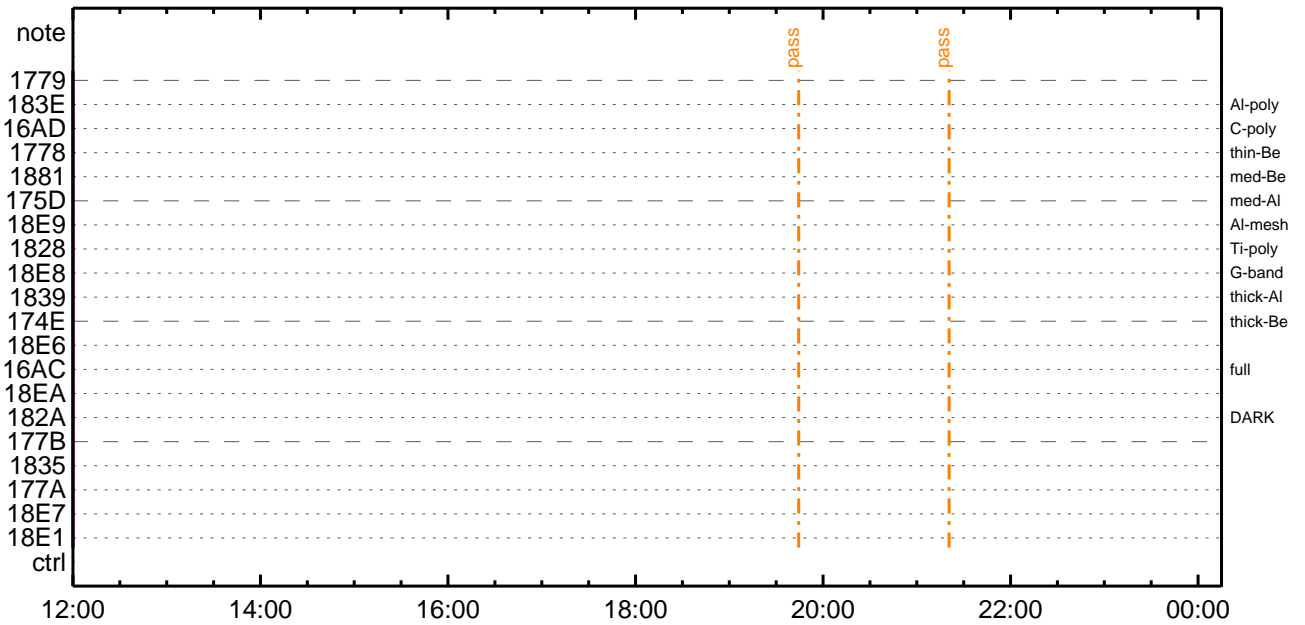
CMDI #0634 2012/05/24



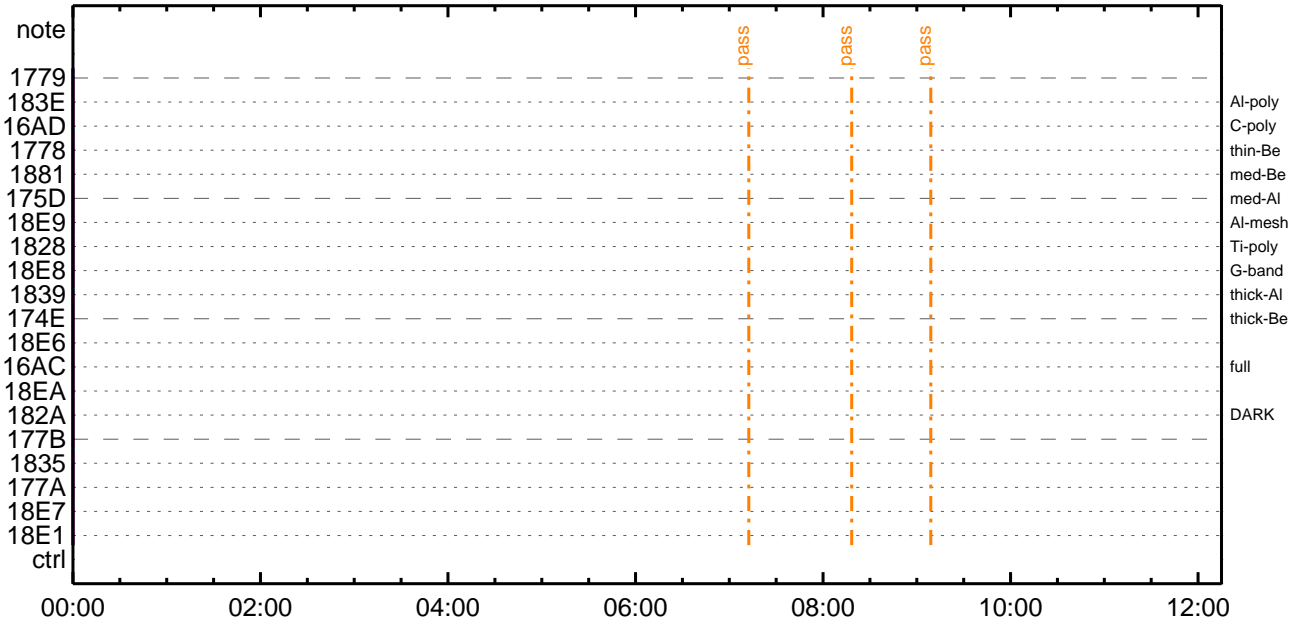
CMDI #0634 2012/05/25



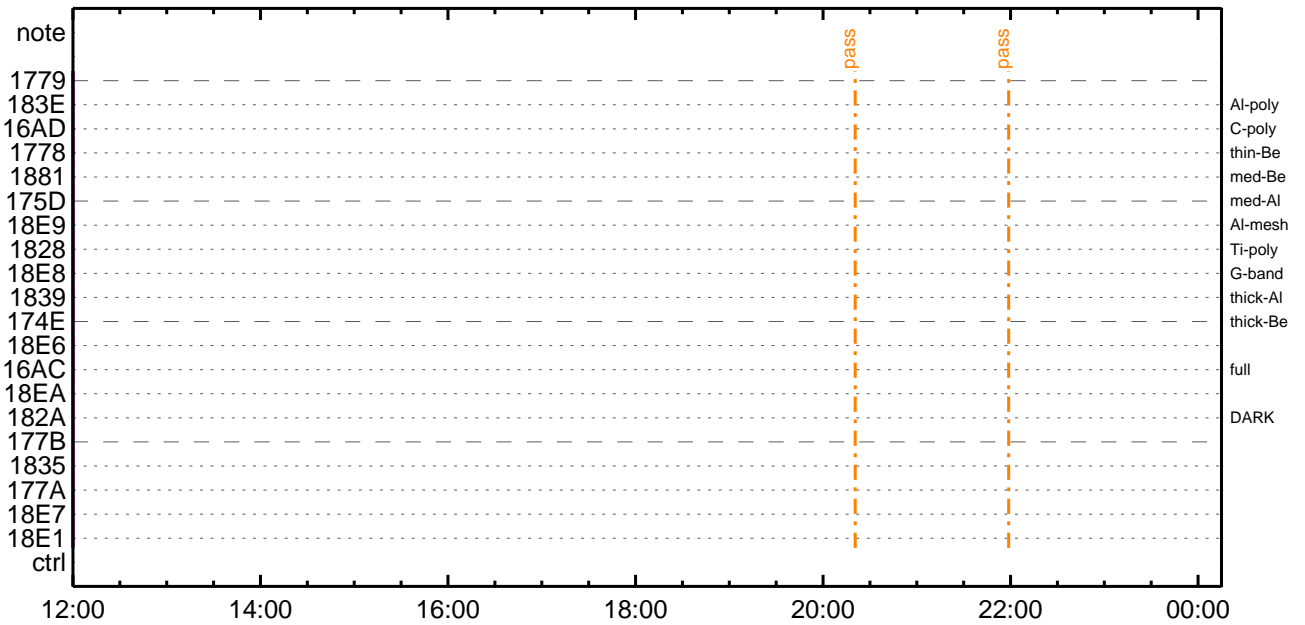
CMDI #0634 2012/05/25



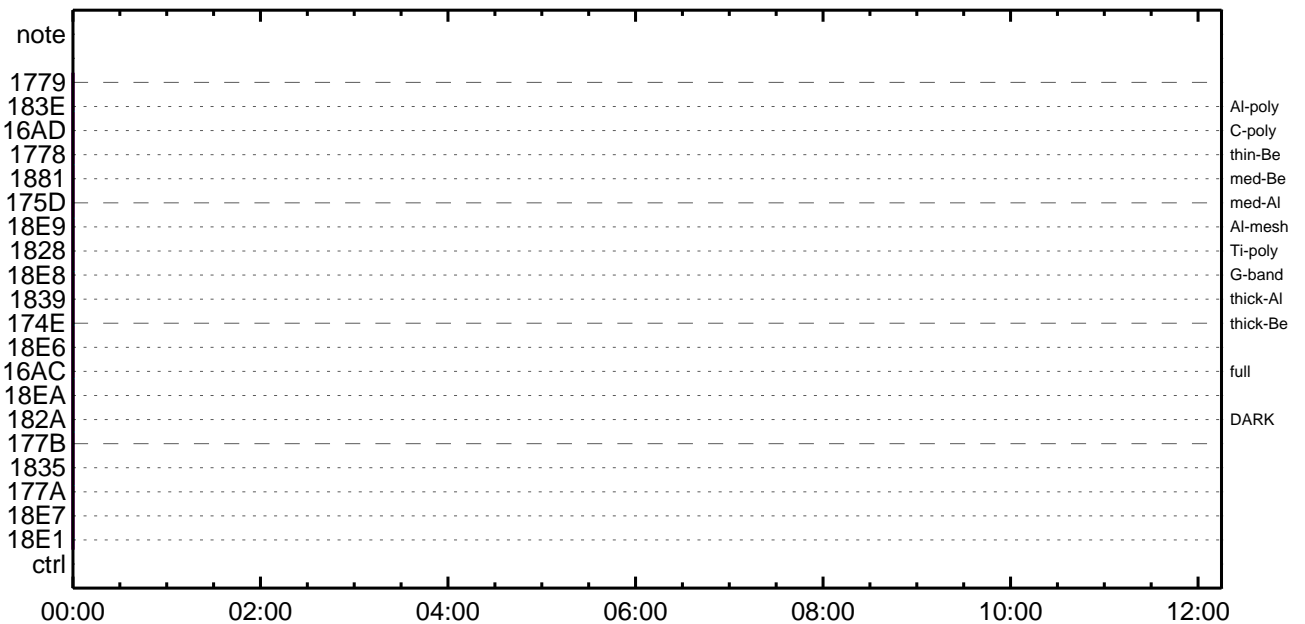
CMDI #0634 2012/05/26



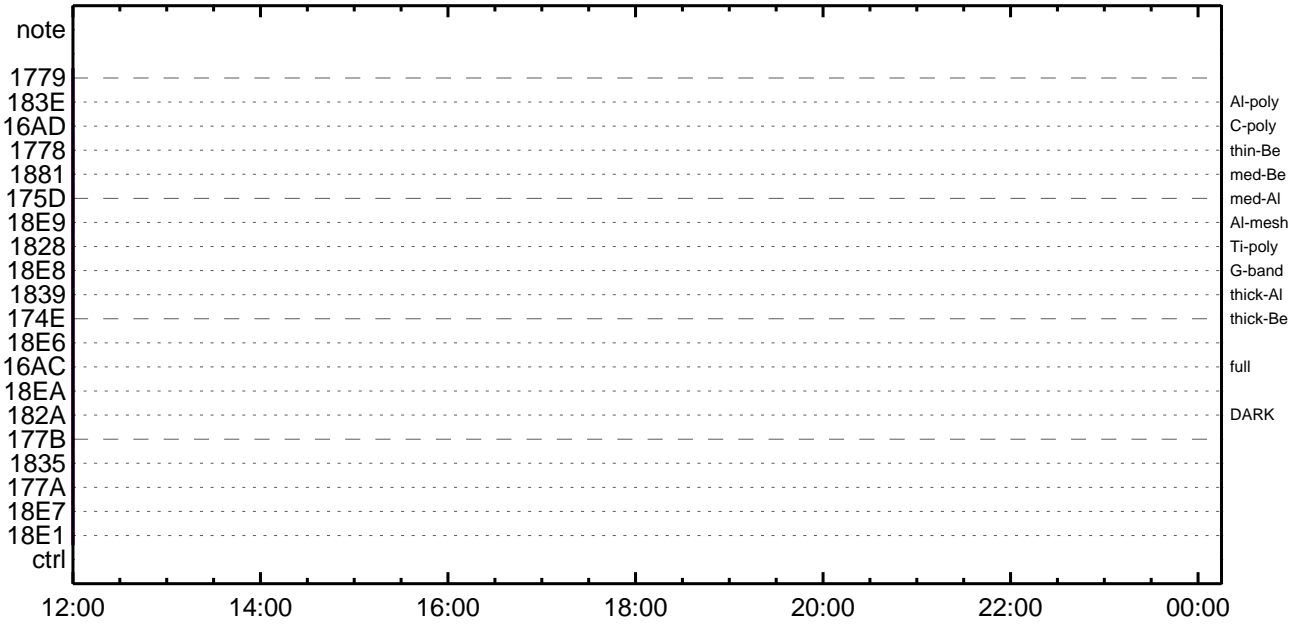
CMDI #0634 2012/05/26



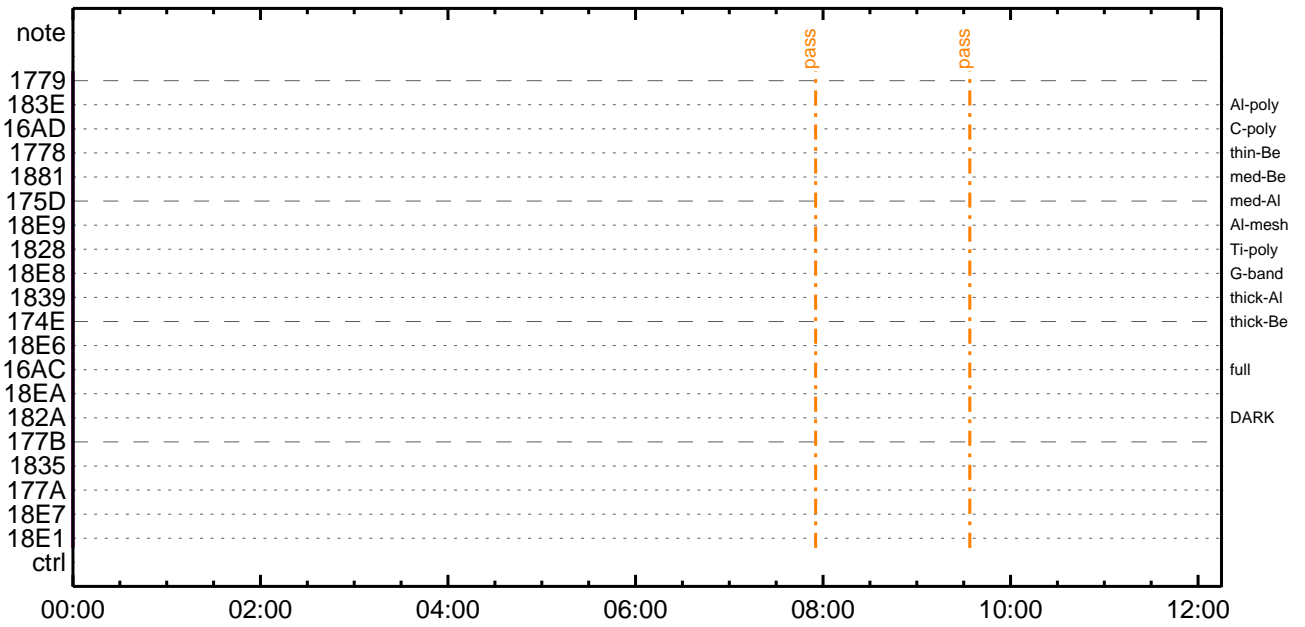
CMDI #0634 2012/05/27



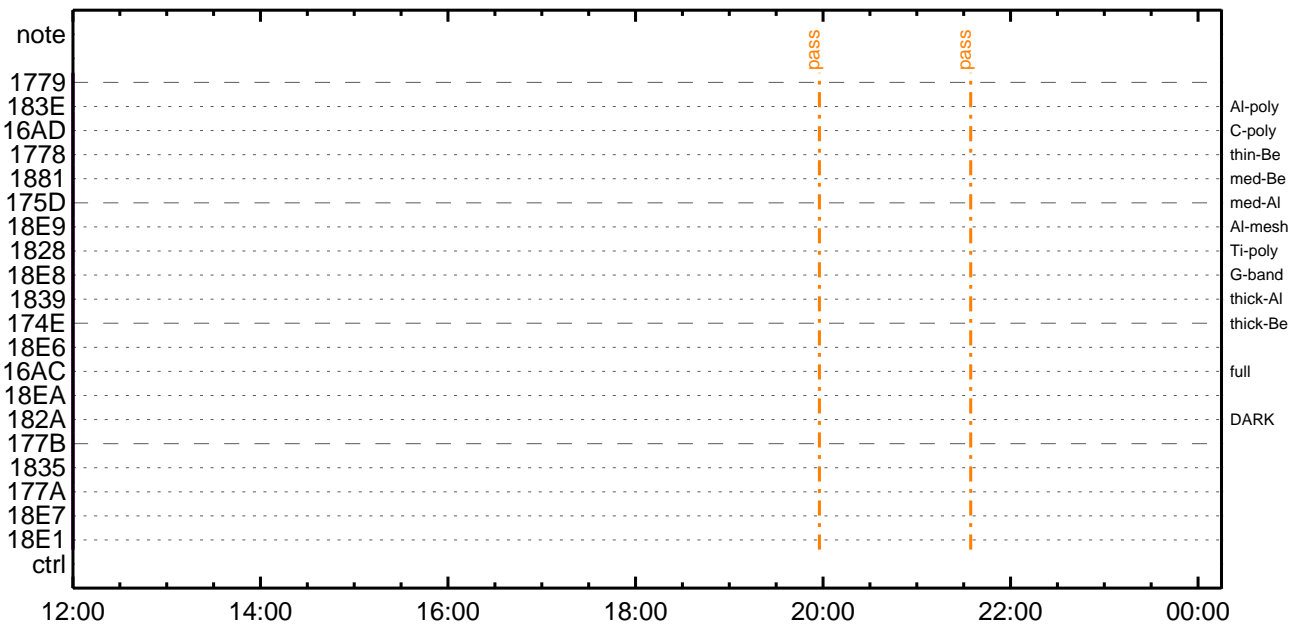
CMDI #0634 2012/05/27



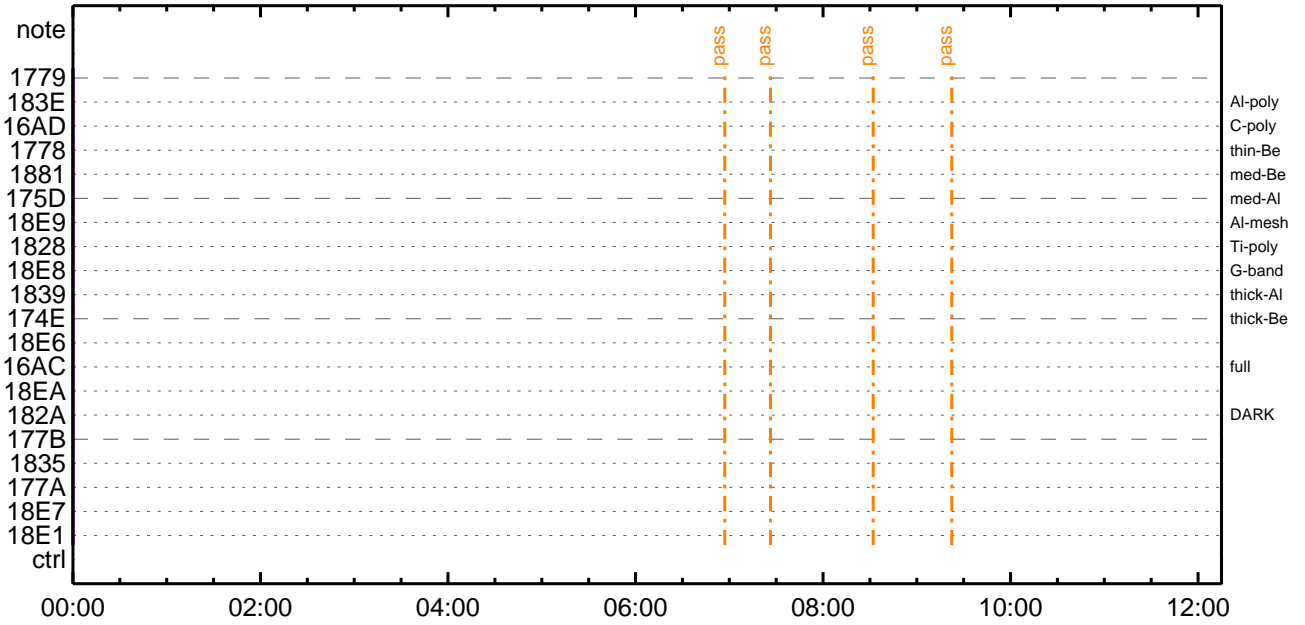
CMDI #0634 2012/05/28



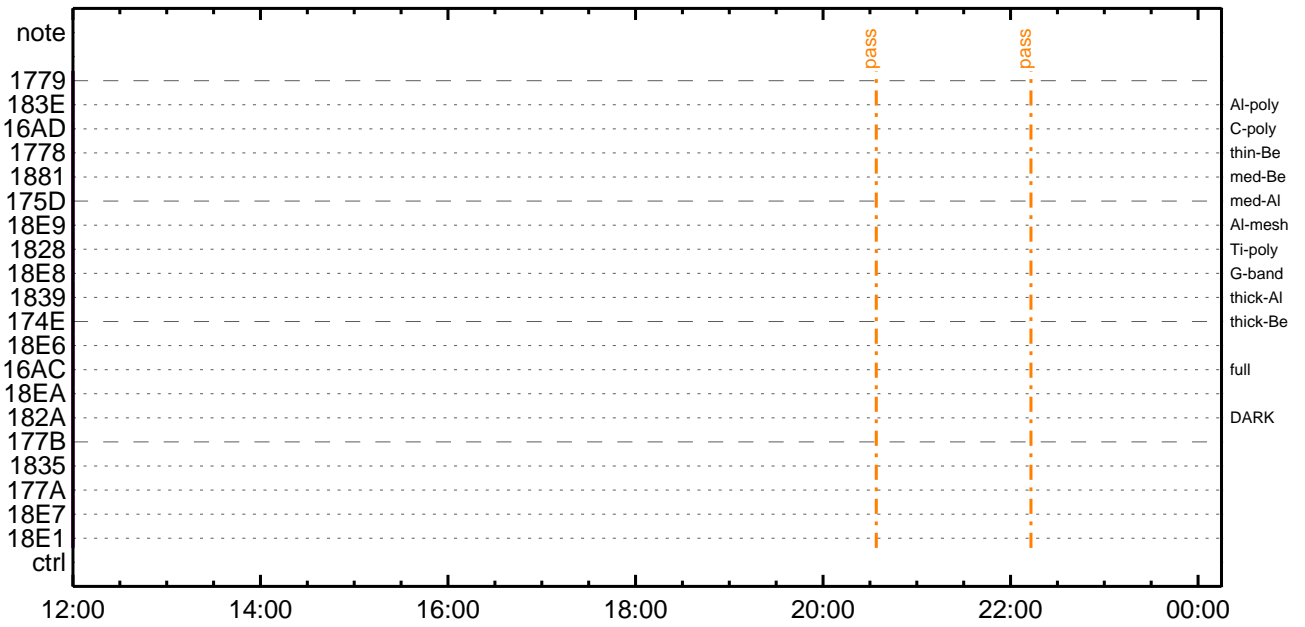
CMDI #0634 2012/05/28



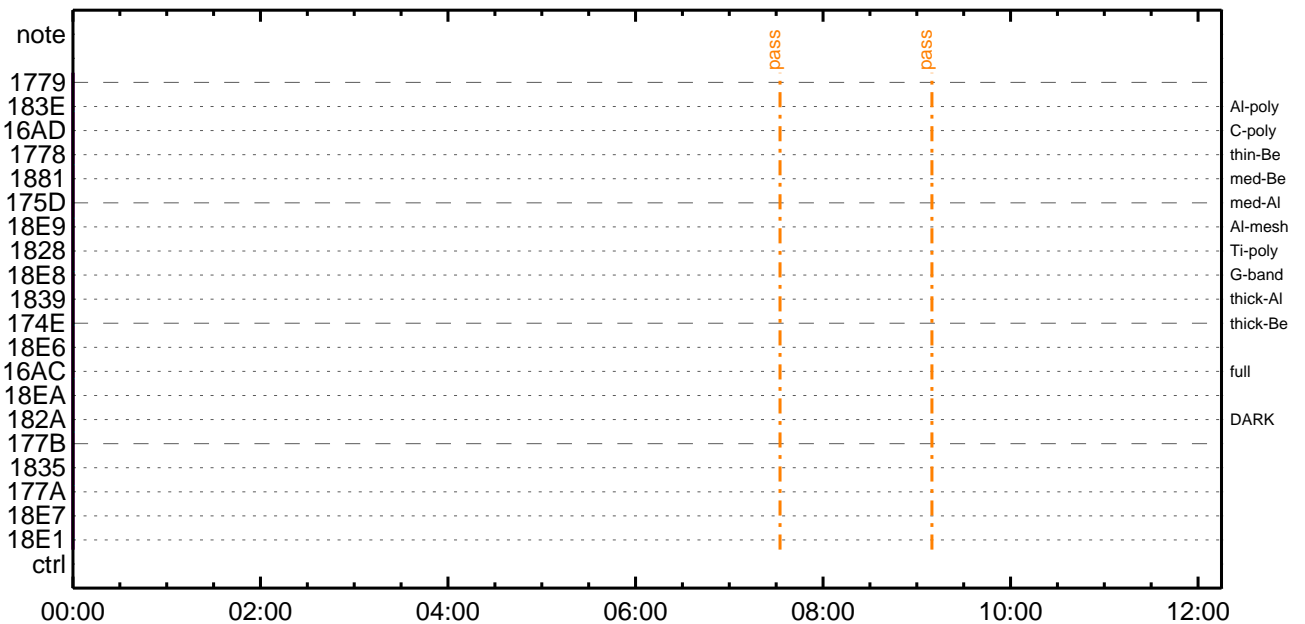
CMDI #0634 2012/05/29



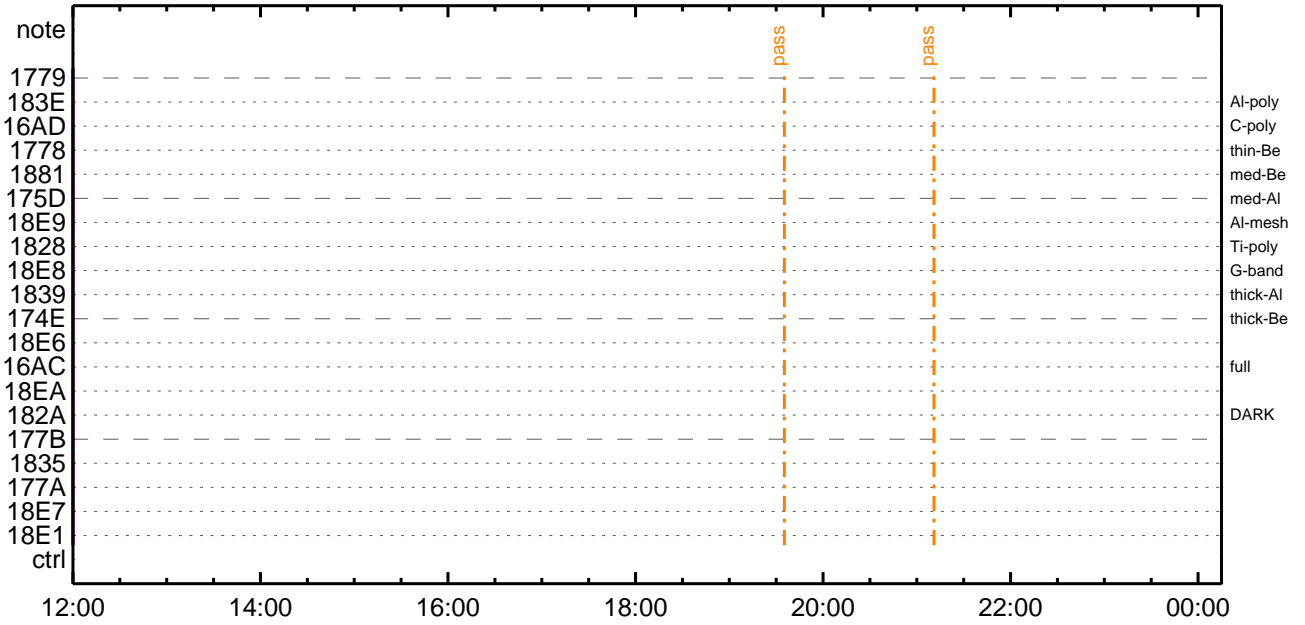
CMDI #0634 2012/05/29



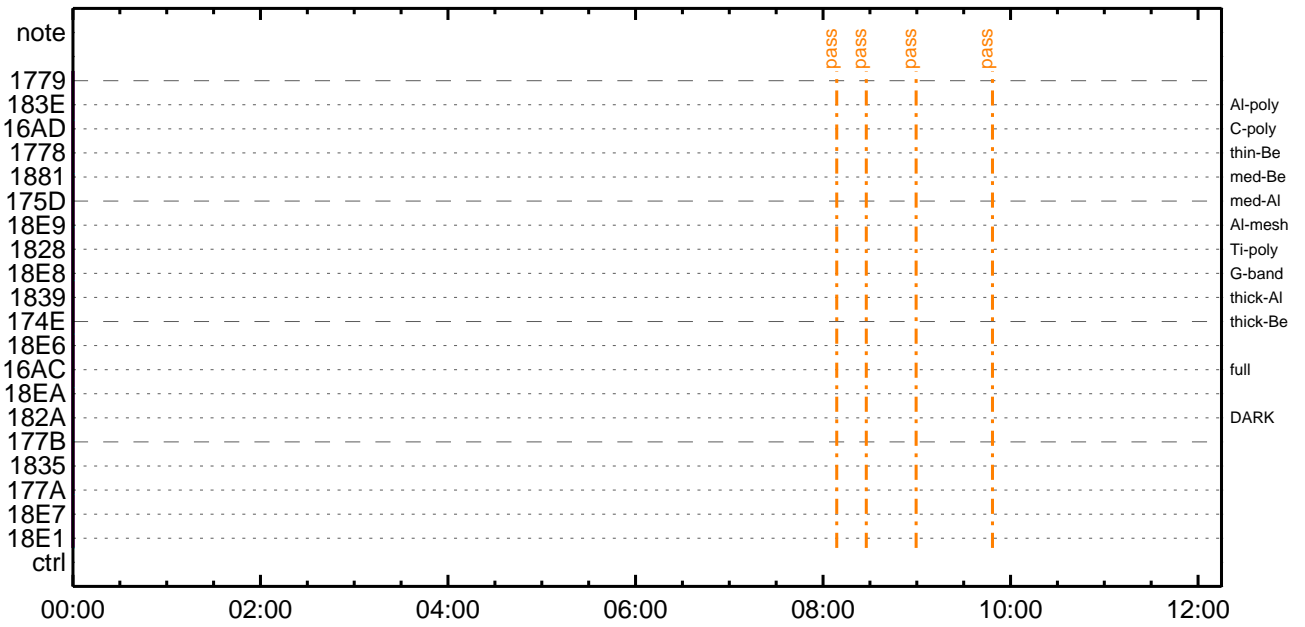
CMDI #0634 2012/05/30



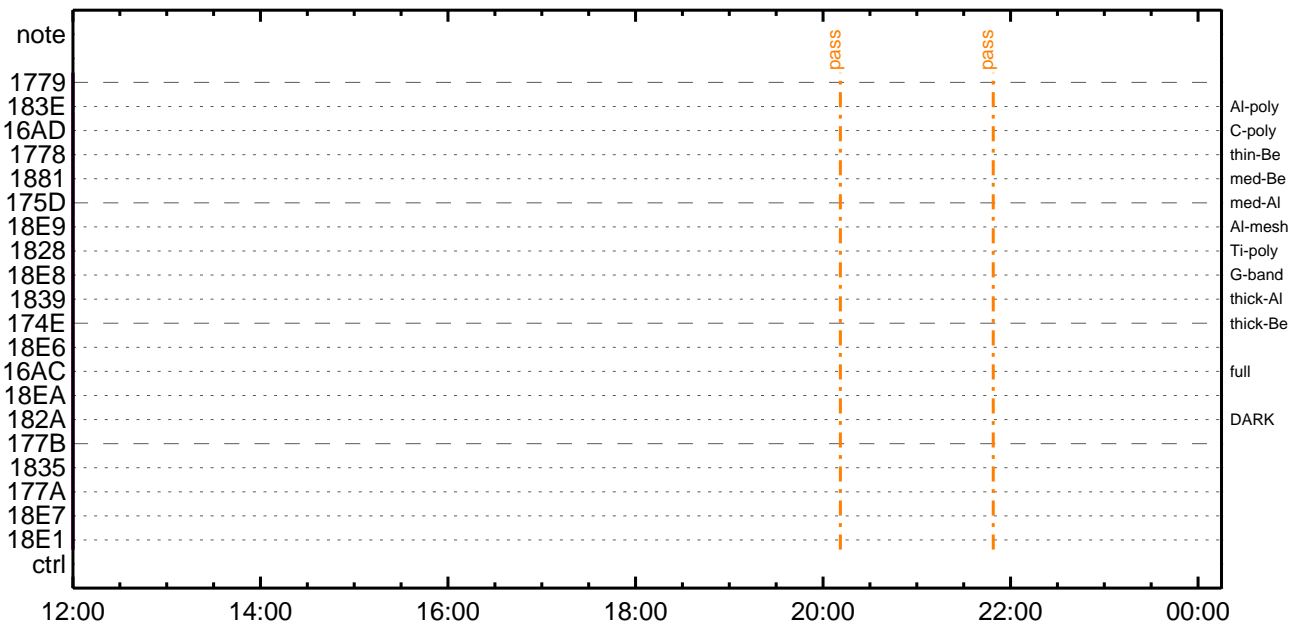
CMDI #0634 2012/05/30



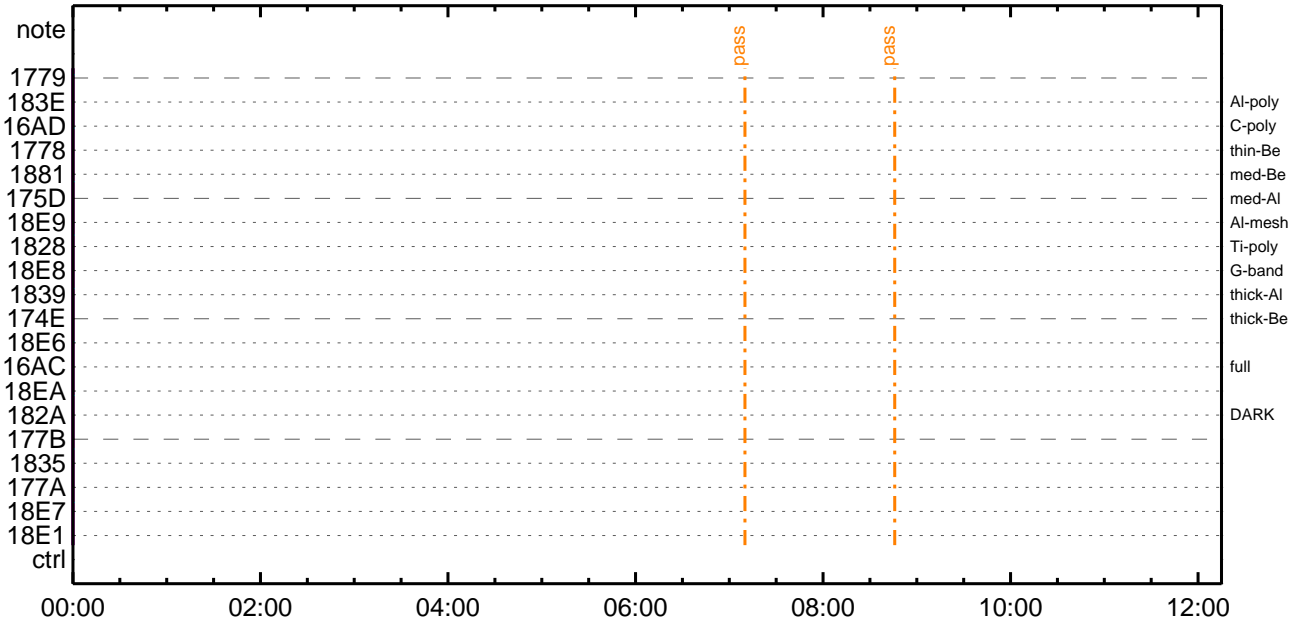
CMDI #0634 2012/05/31



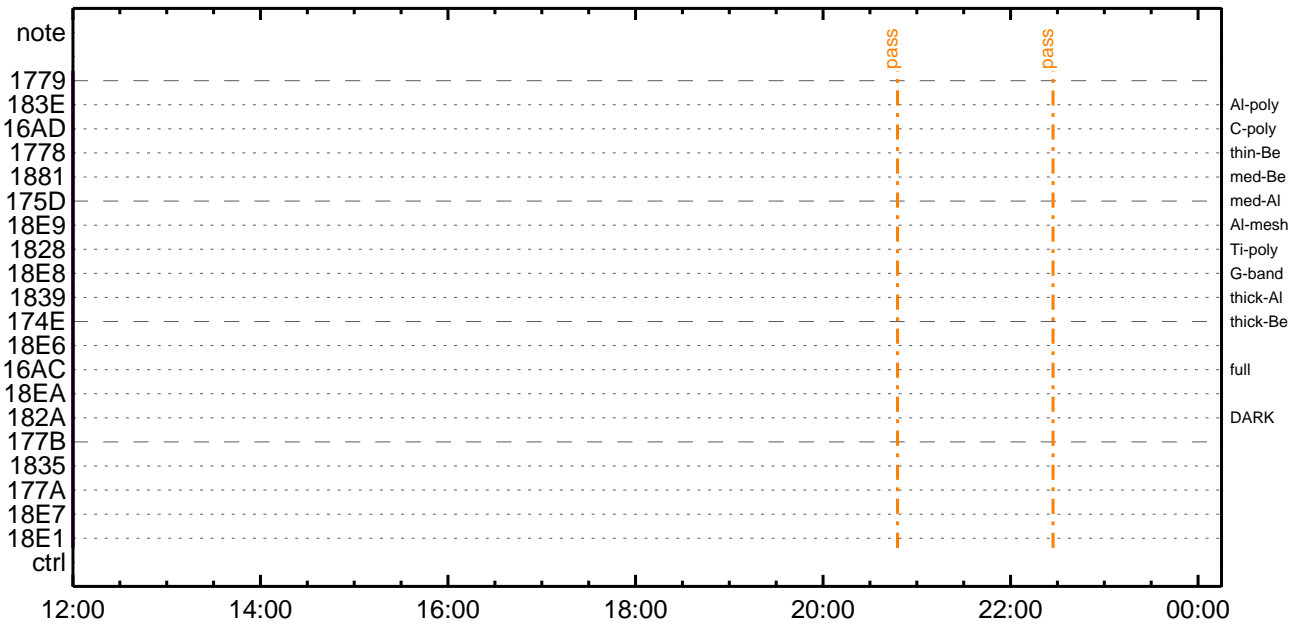
CMDI #0634 2012/05/31



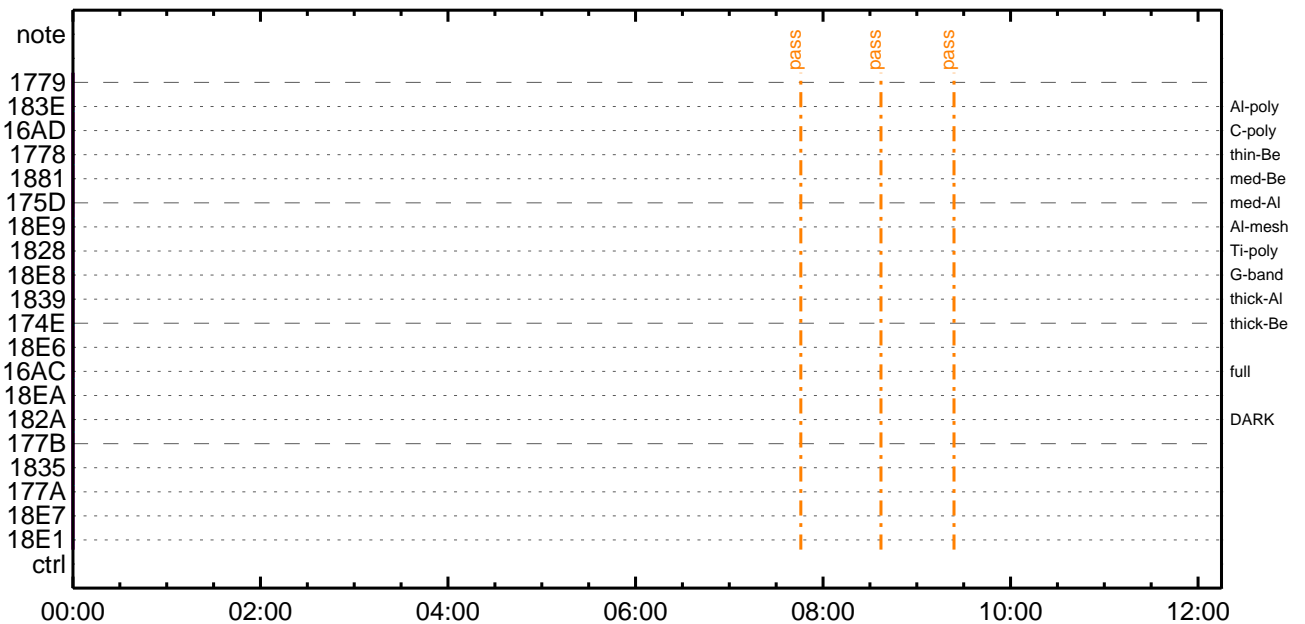
CMDI #0634 2012/06/01



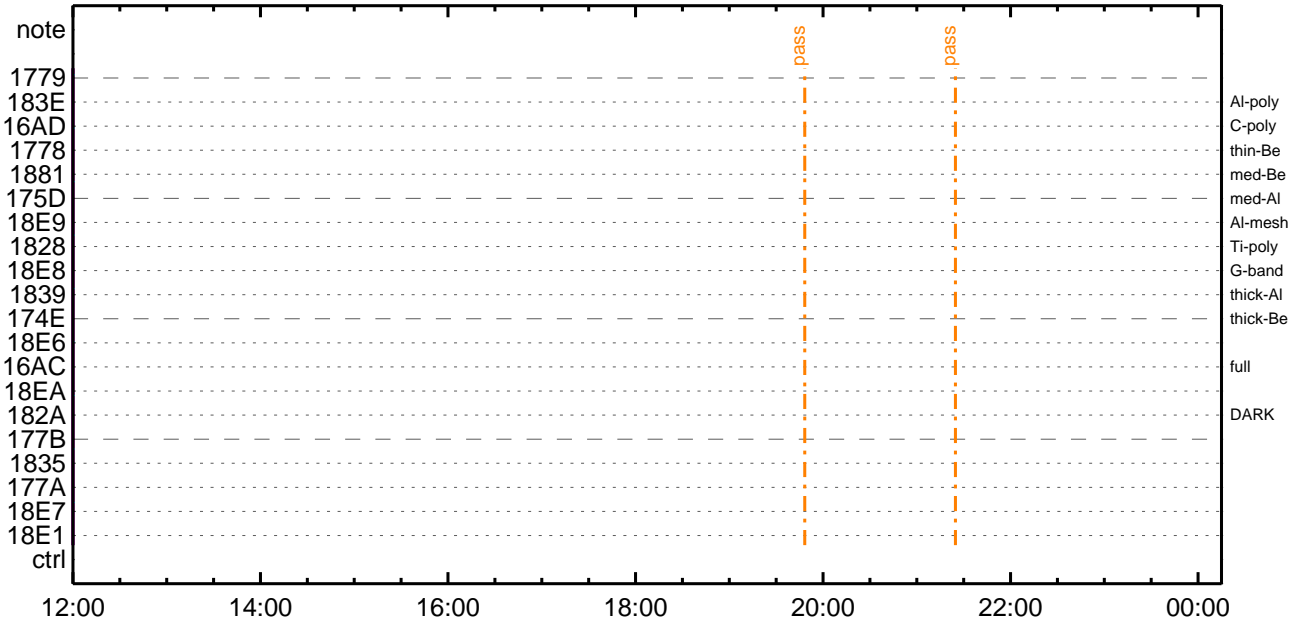
CMDI #0634 2012/06/01



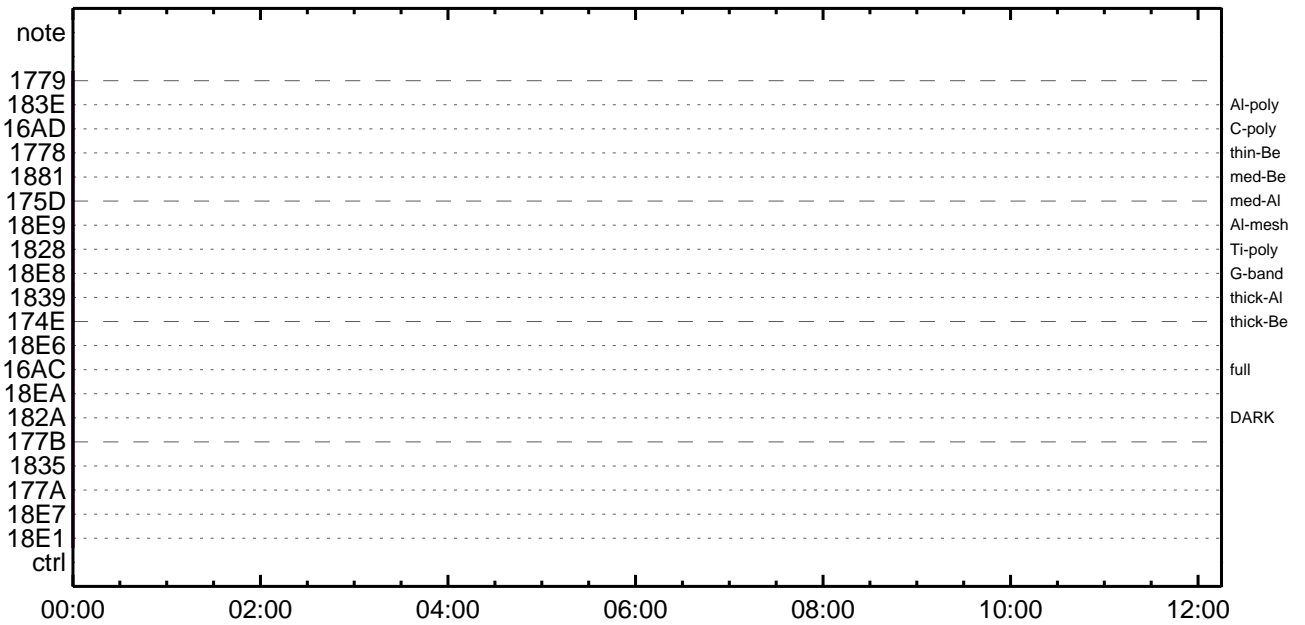
CMDI #0634 2012/06/02



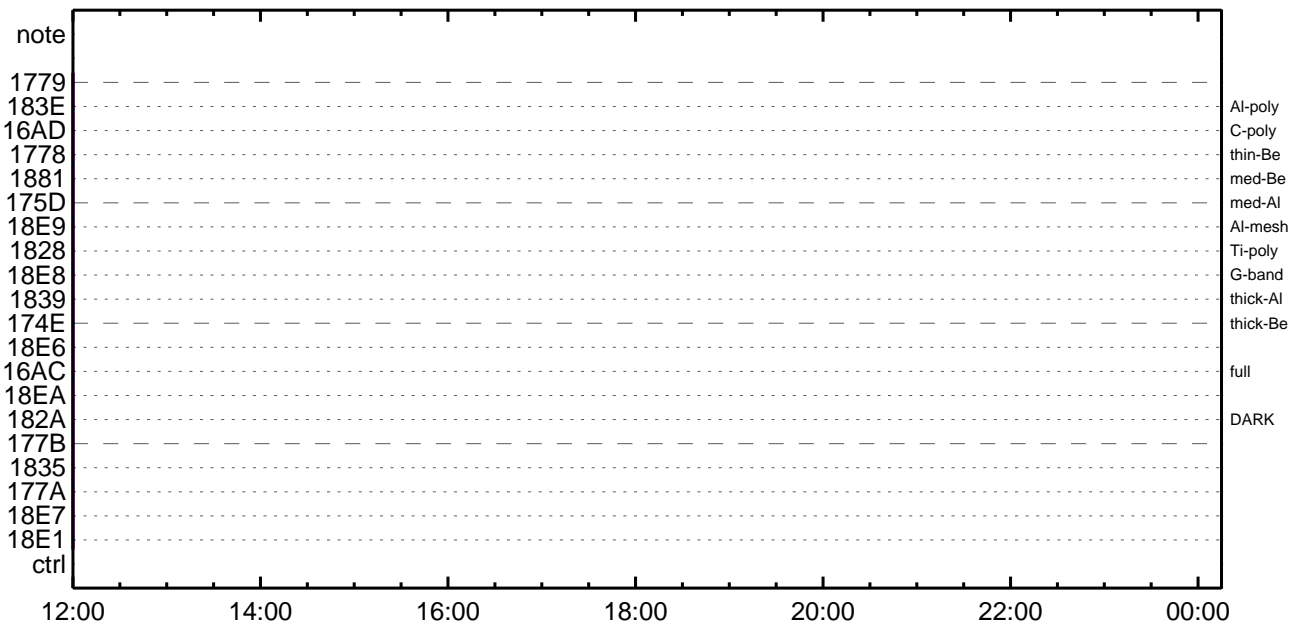
CMDI #0634 2012/06/02



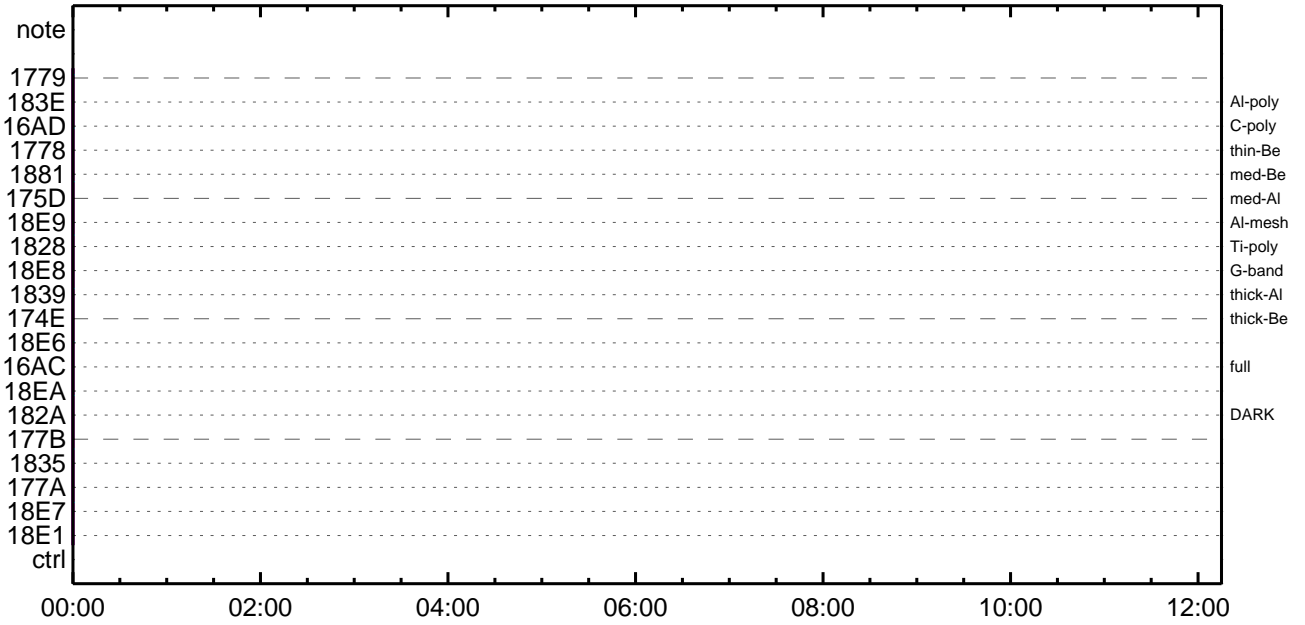
CMDI #0634 2012/06/03



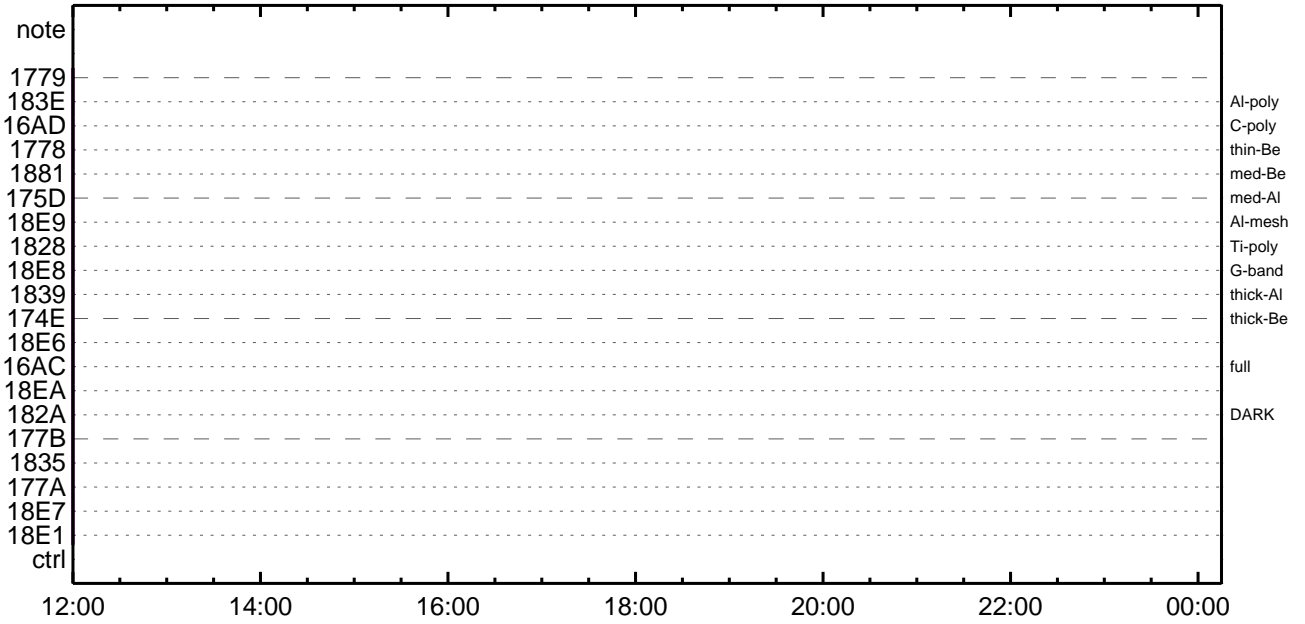
CMDI #0634 2012/06/03



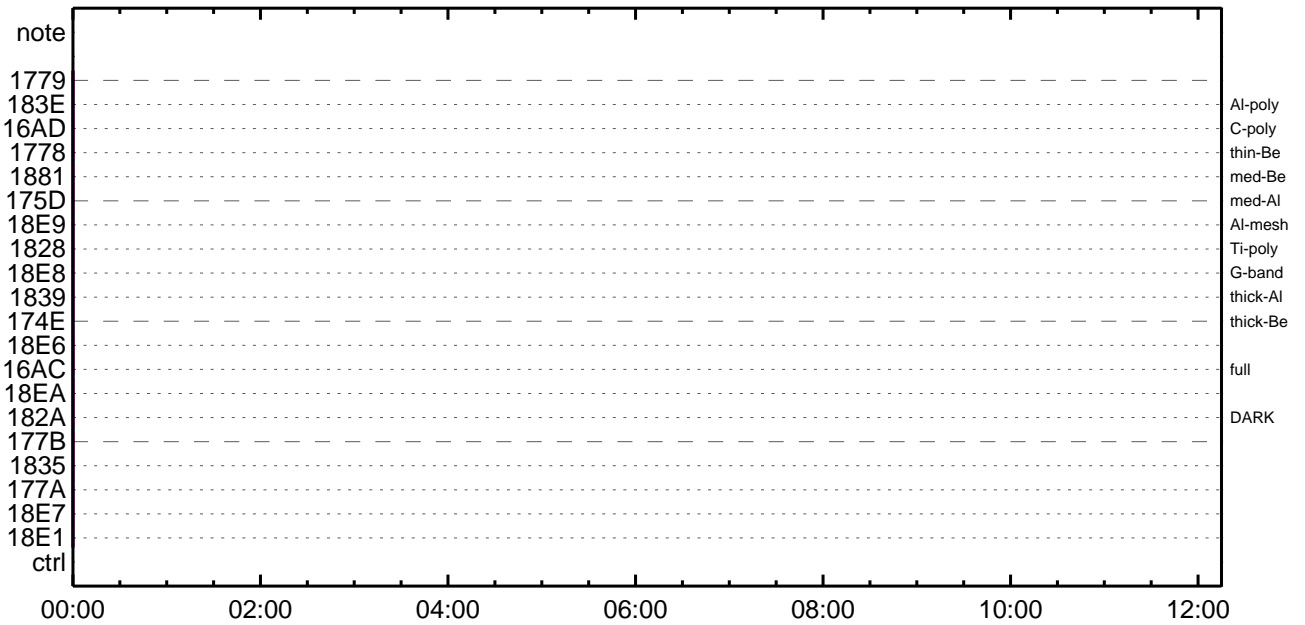
CMDI #0634 2012/06/04



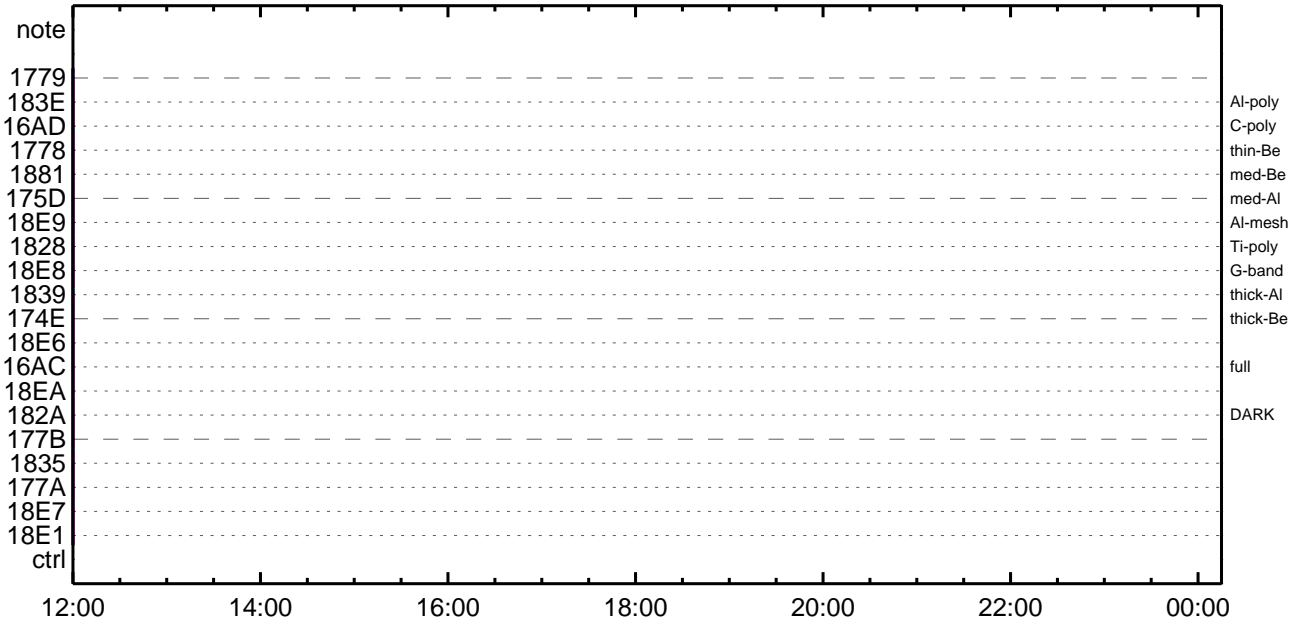
CMDI #0634 2012/06/04



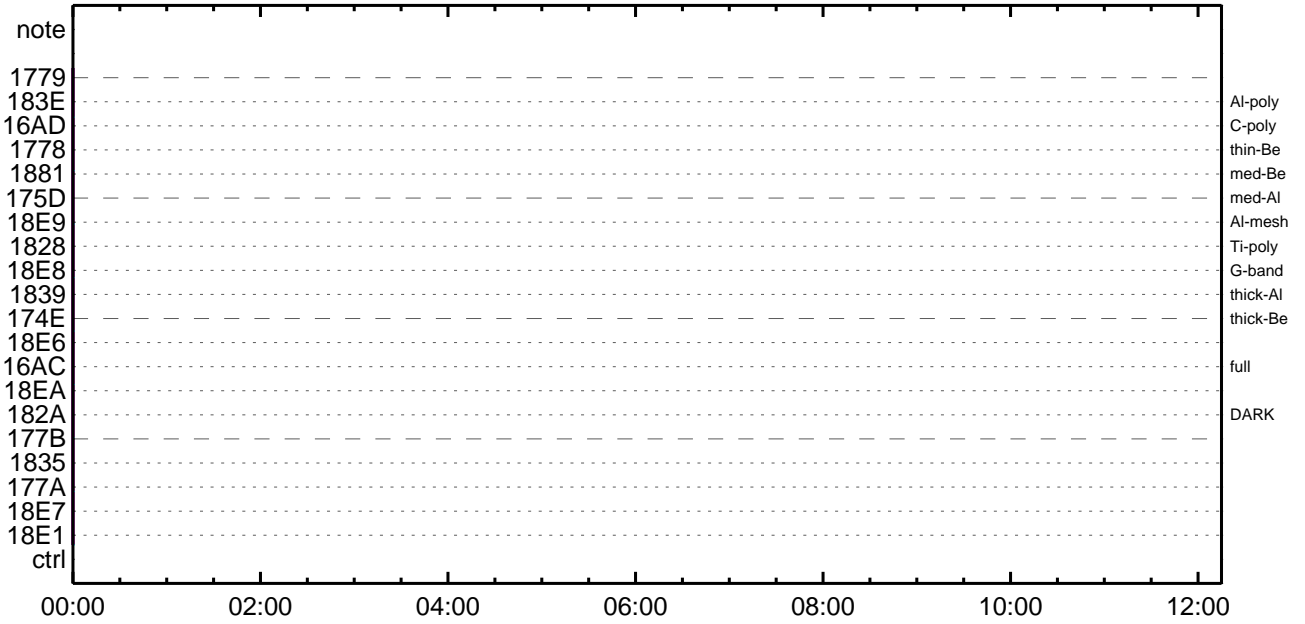
CMDI #0634 2012/06/05



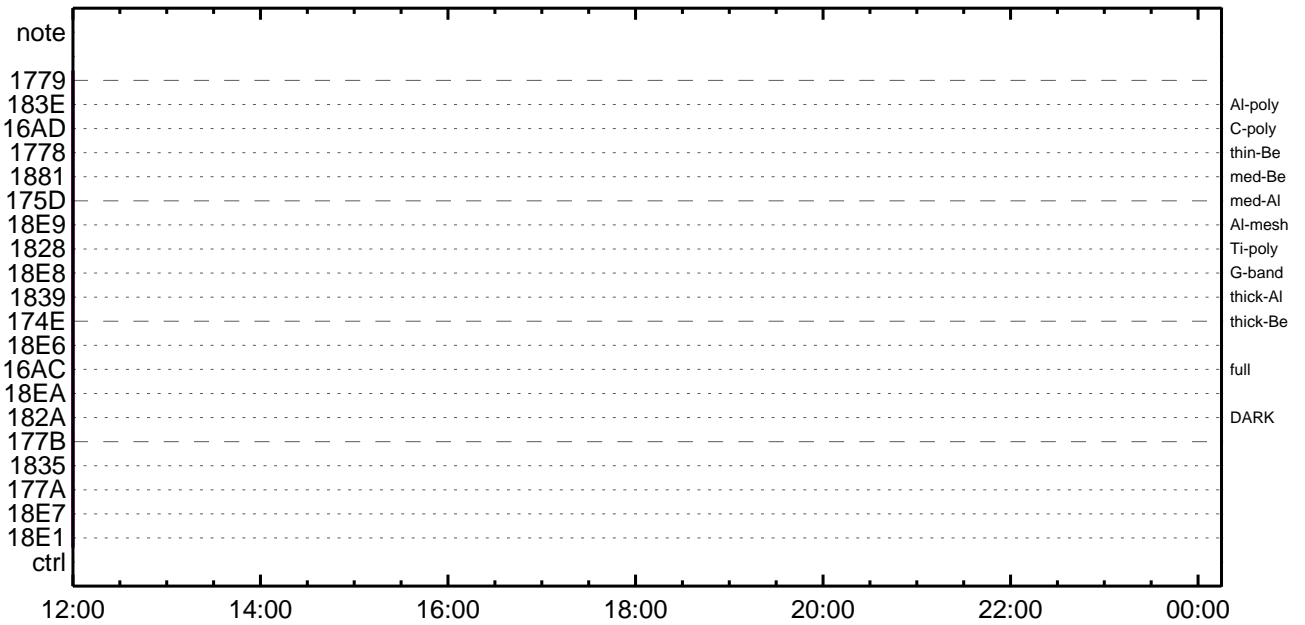
CMDI #0634 2012/06/05



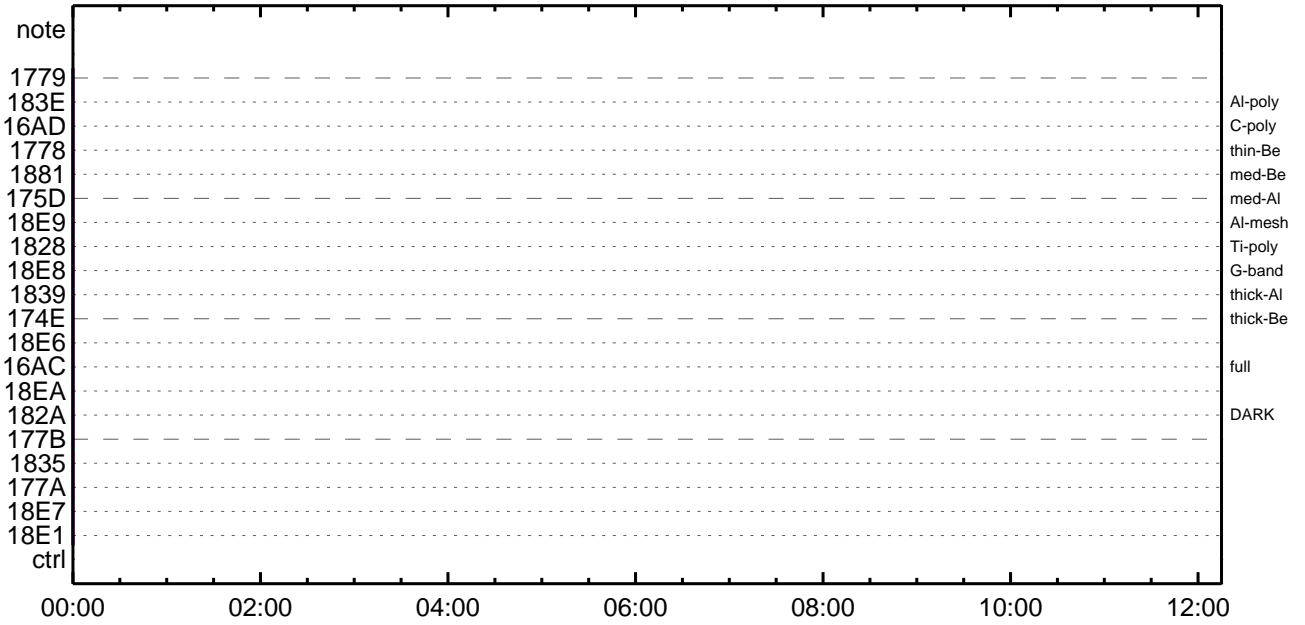
CMDI #0634 2012/06/06



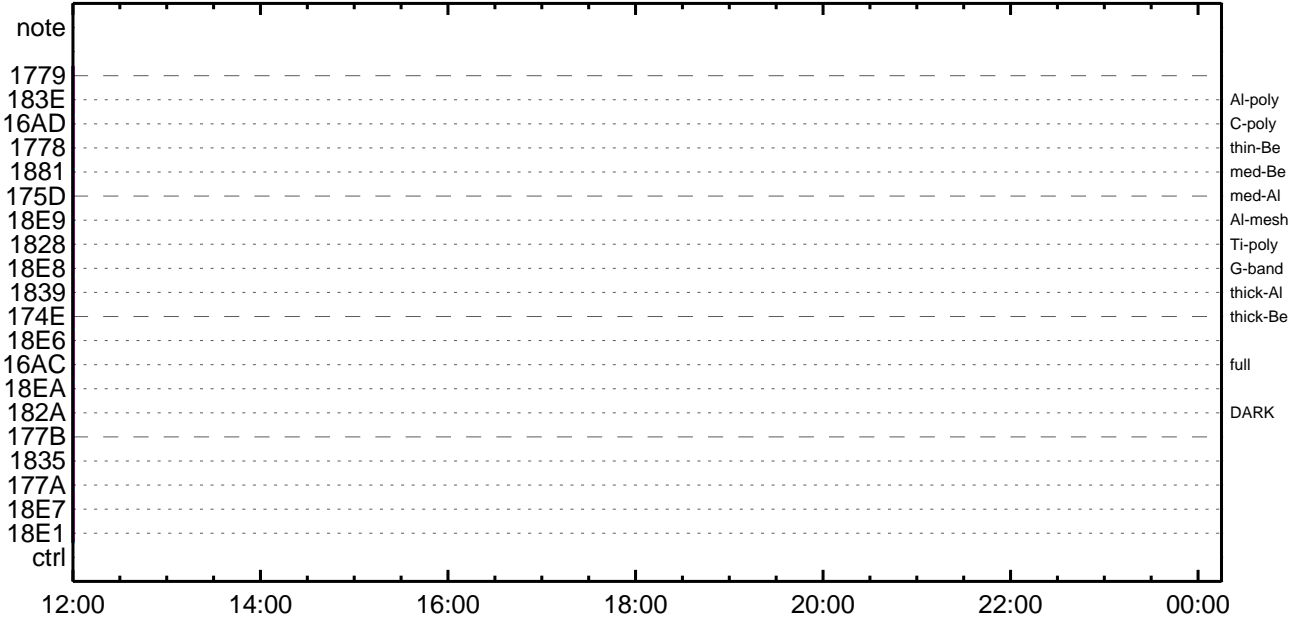
CMDI #0634 2012/06/06



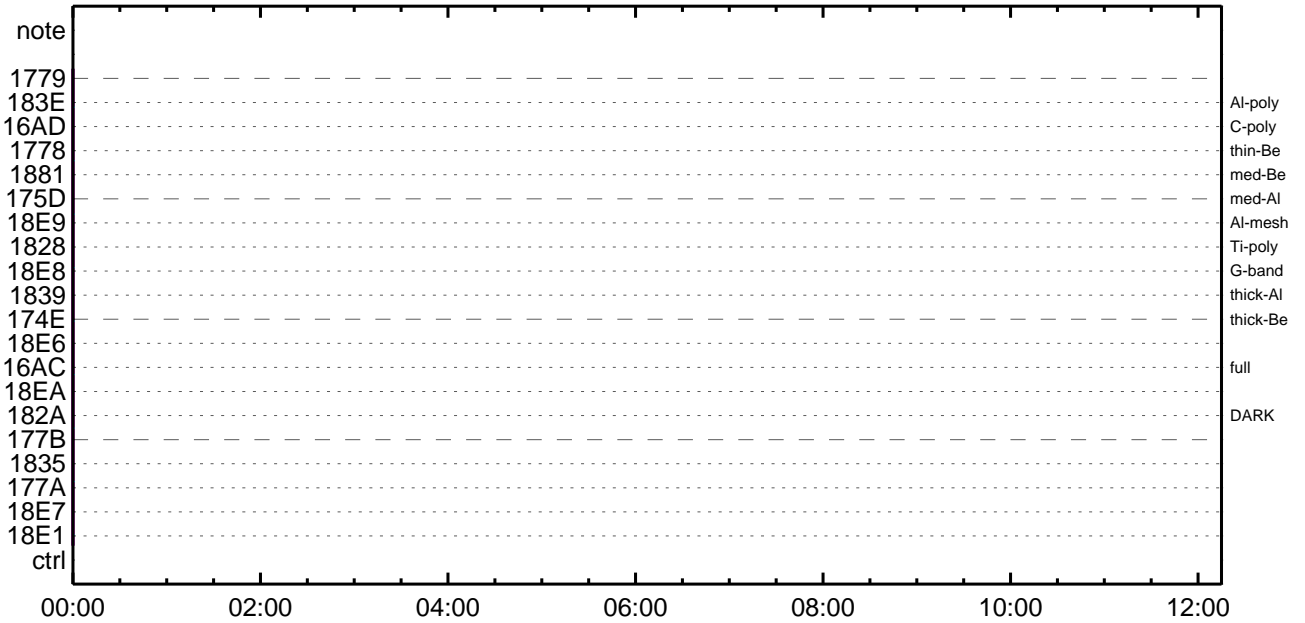
CMDI #0634 2012/06/07



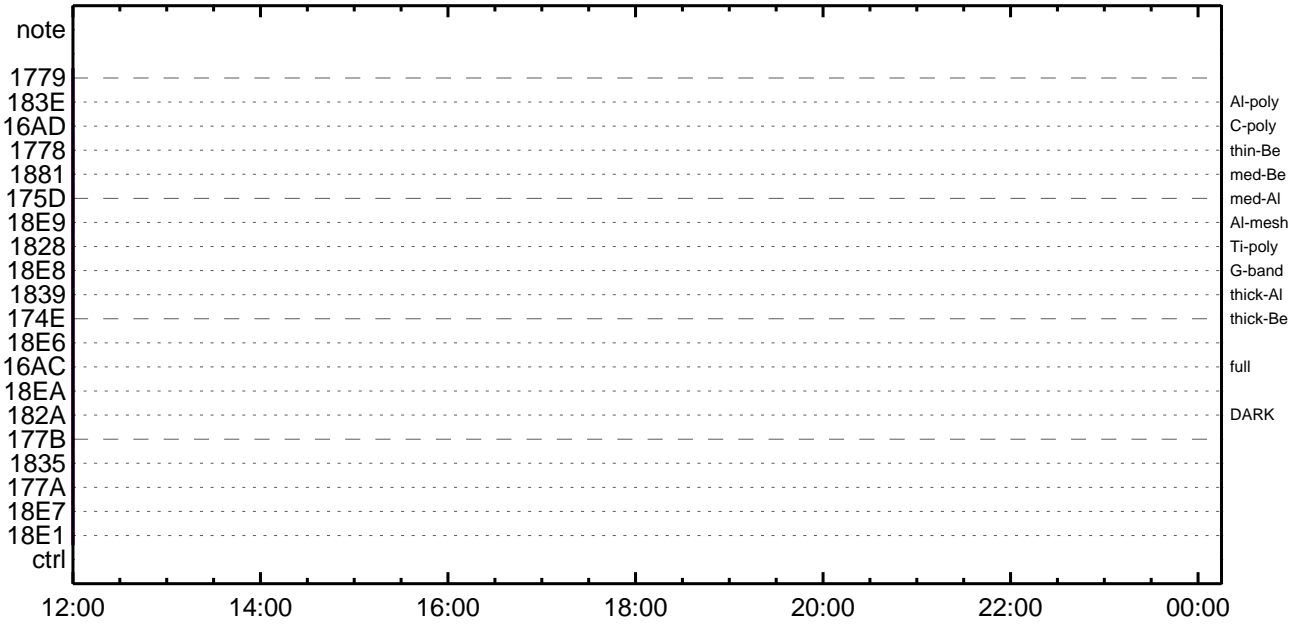
CMDI #0634 2012/06/07



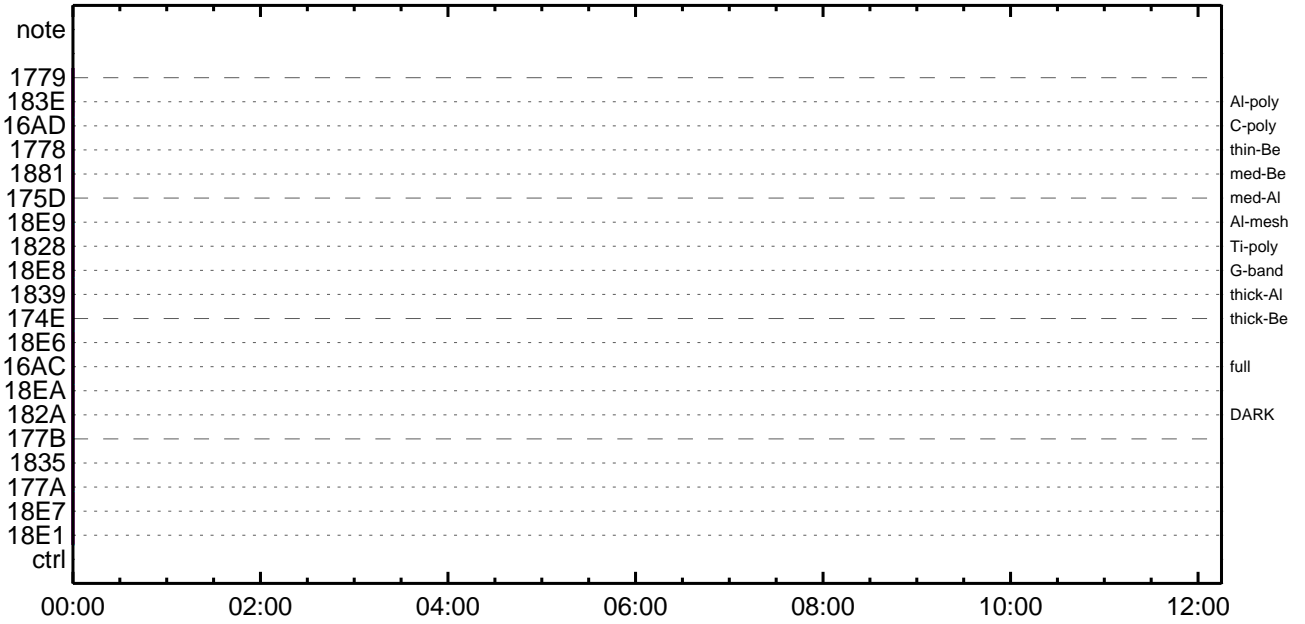
CMDI #0634 2012/06/08



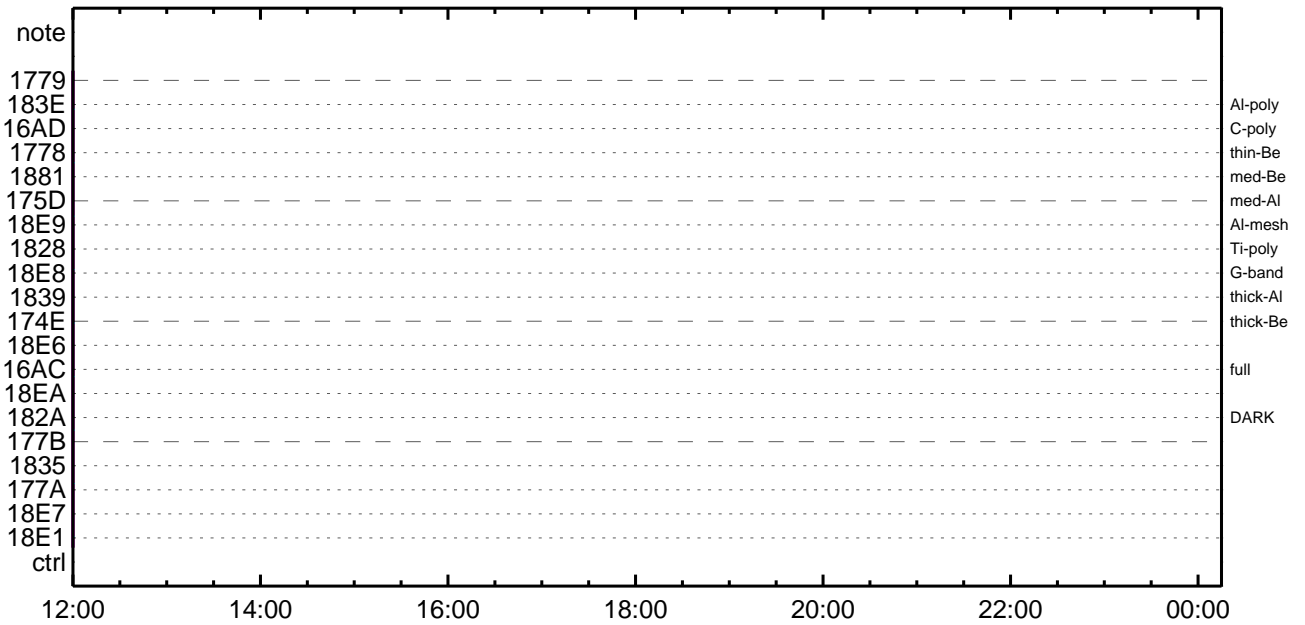
CMDI #0634 2012/06/08



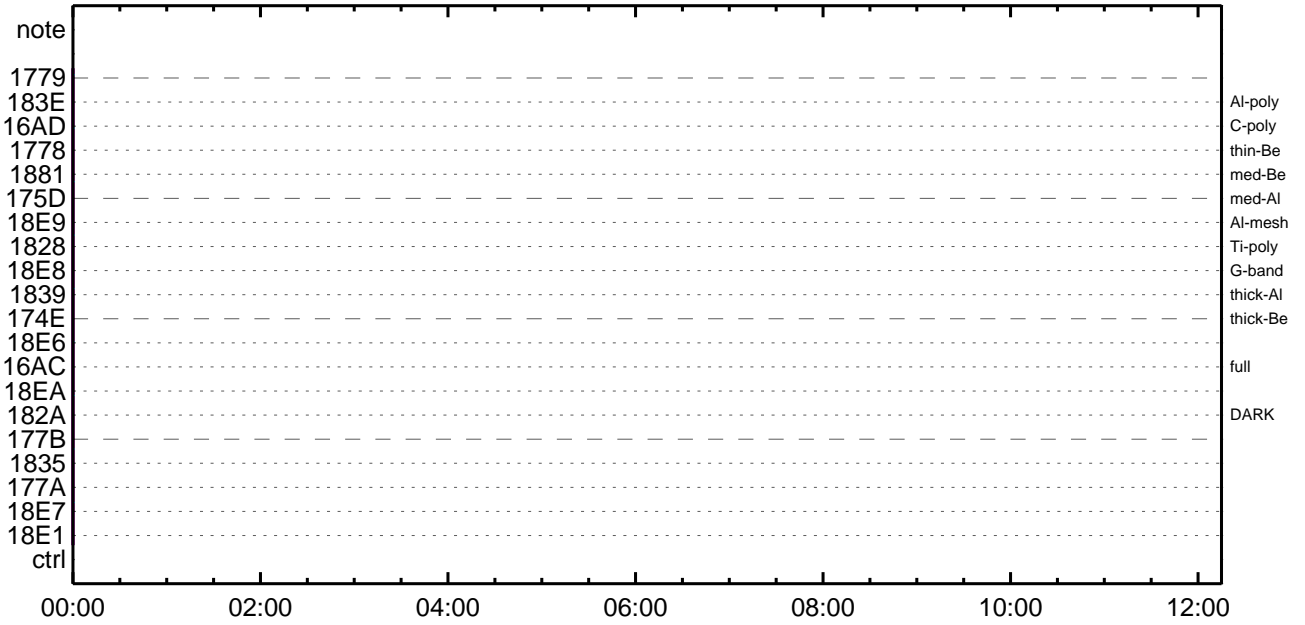
CMDI #0634 2012/06/09



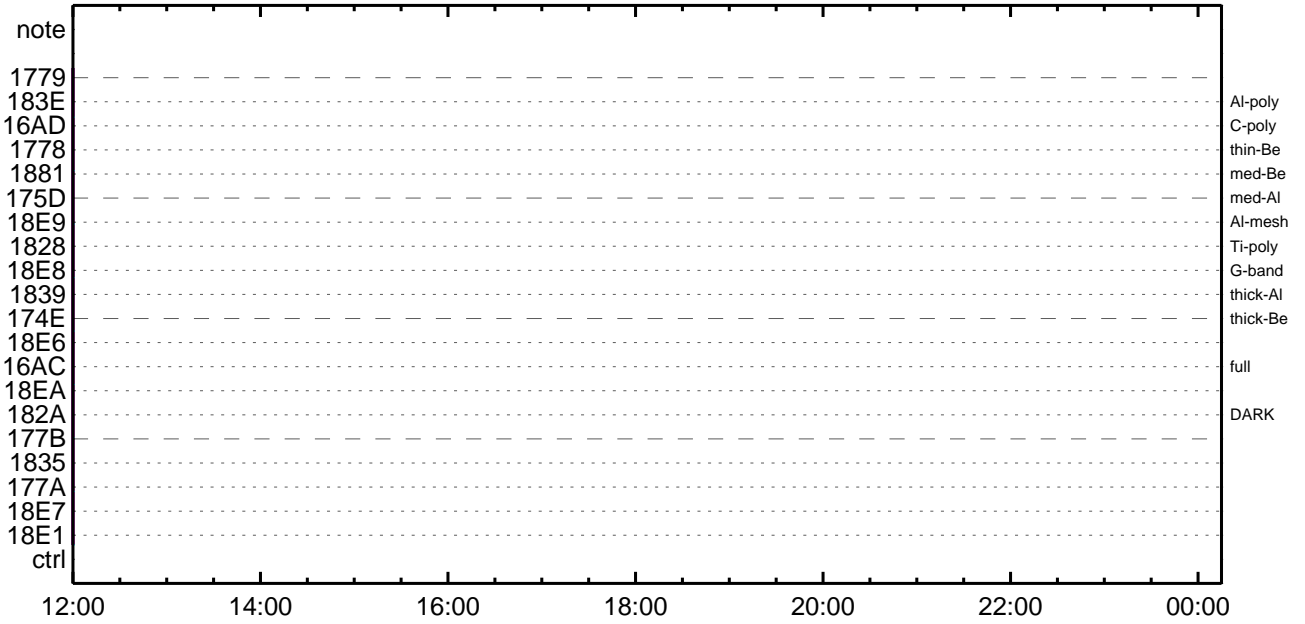
CMDI #0634 2012/06/09



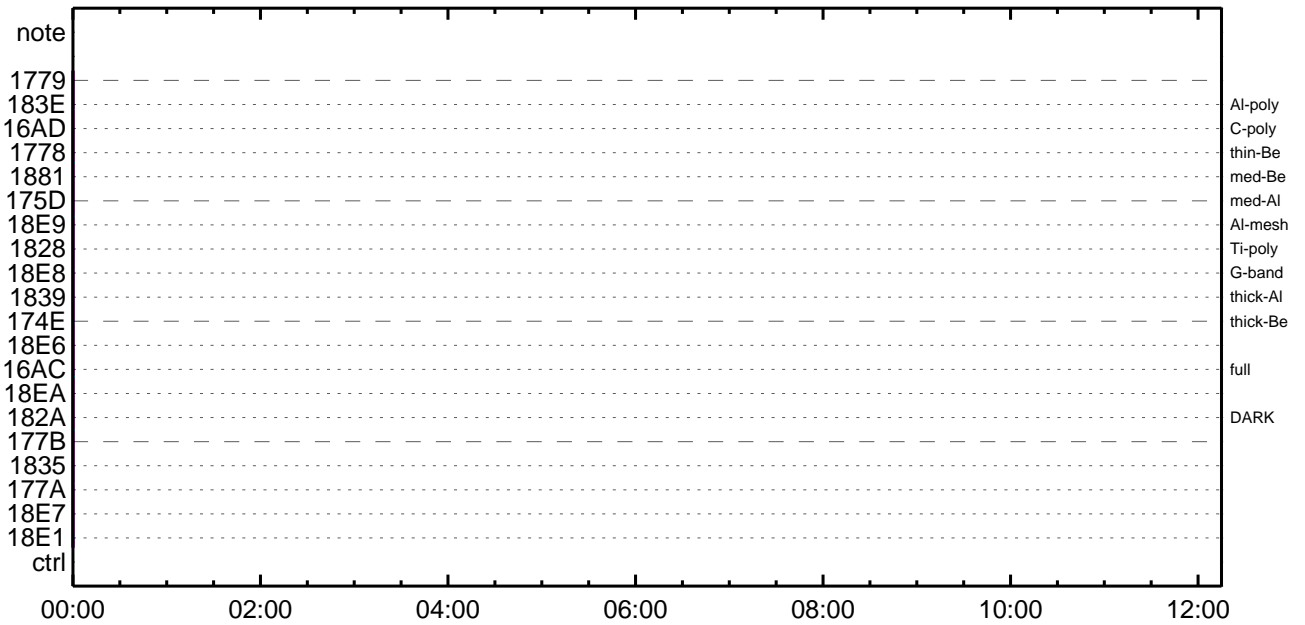
CMDI #0634 2012/06/10



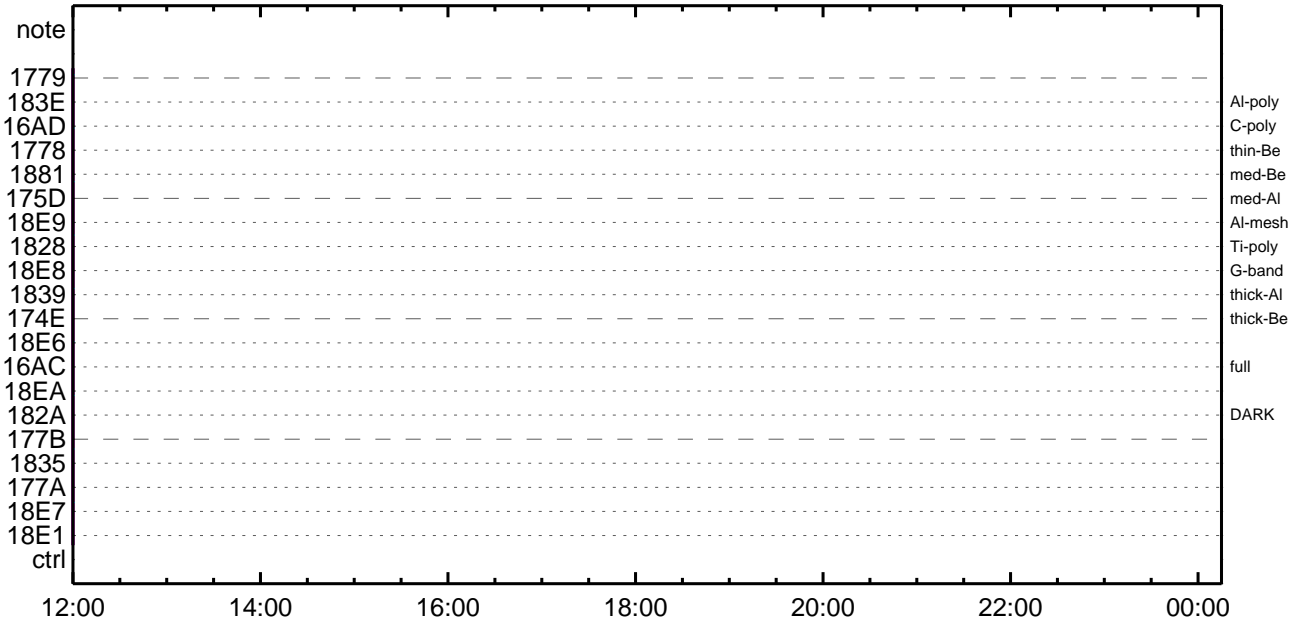
CMDI #0634 2012/06/10



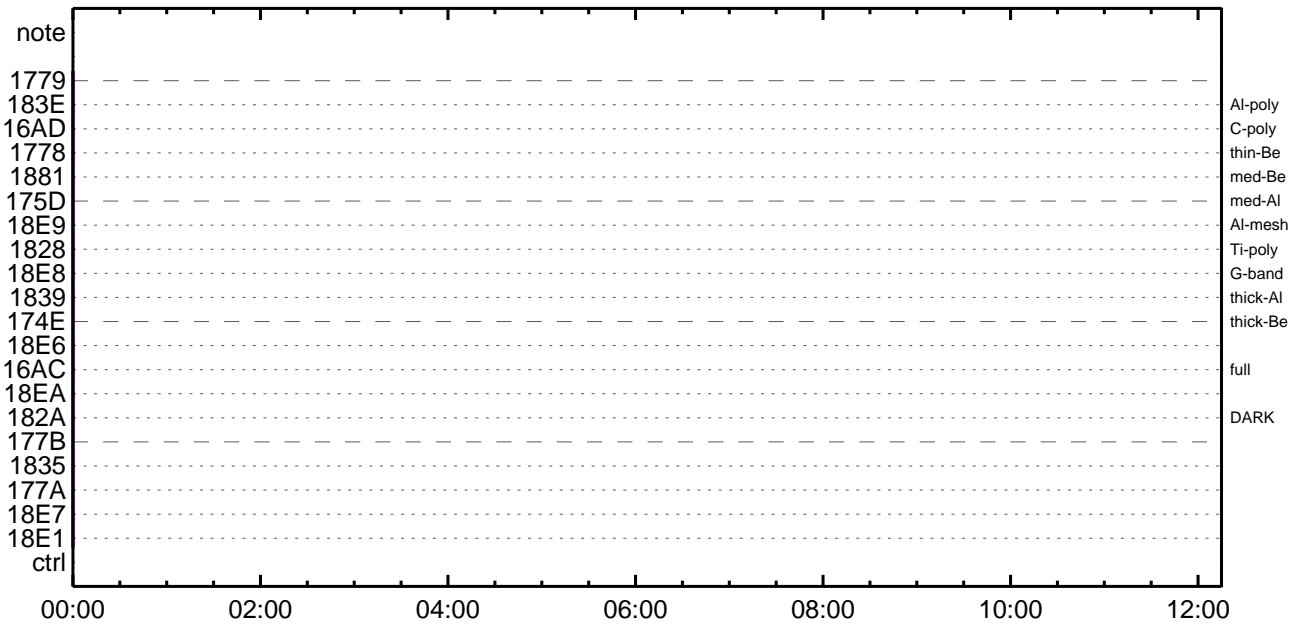
CMDI #0634 2012/06/11



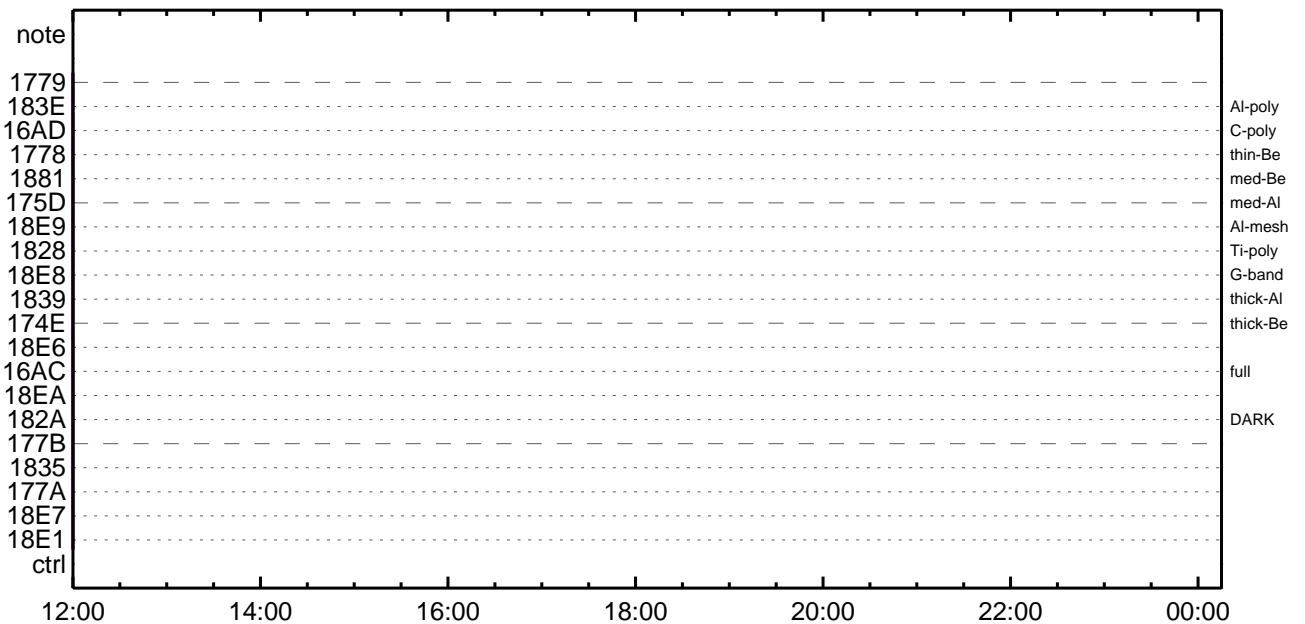
CMDI #0634 2012/06/11



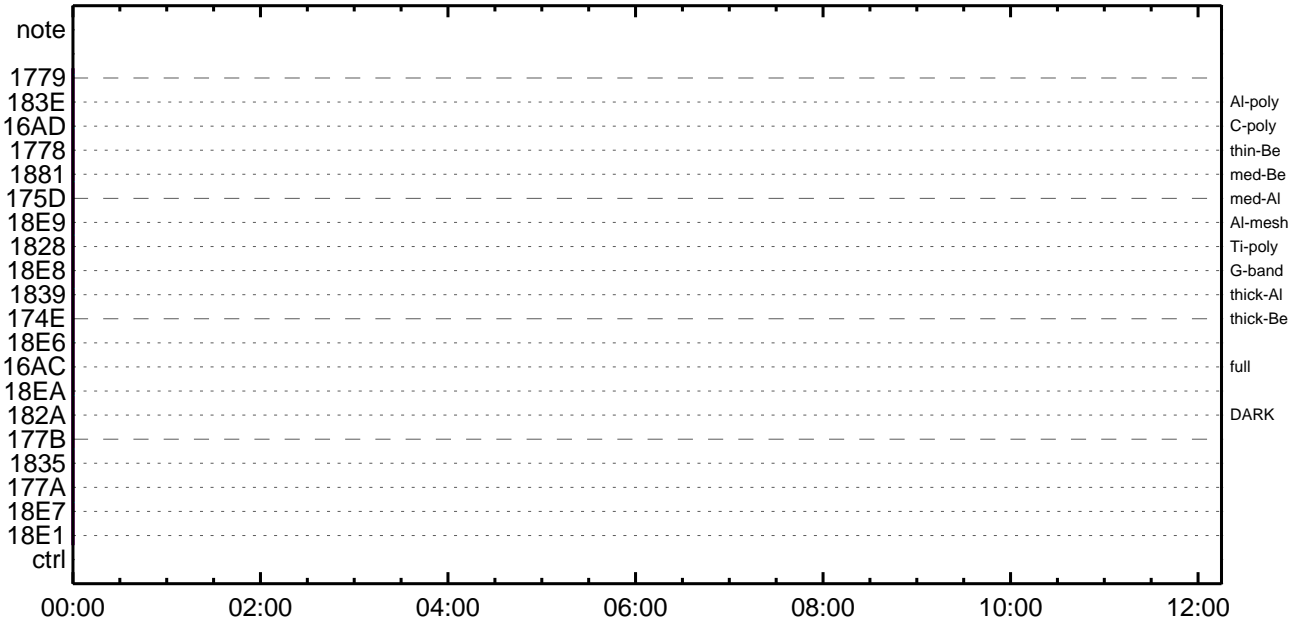
CMDI #0634 2012/06/12



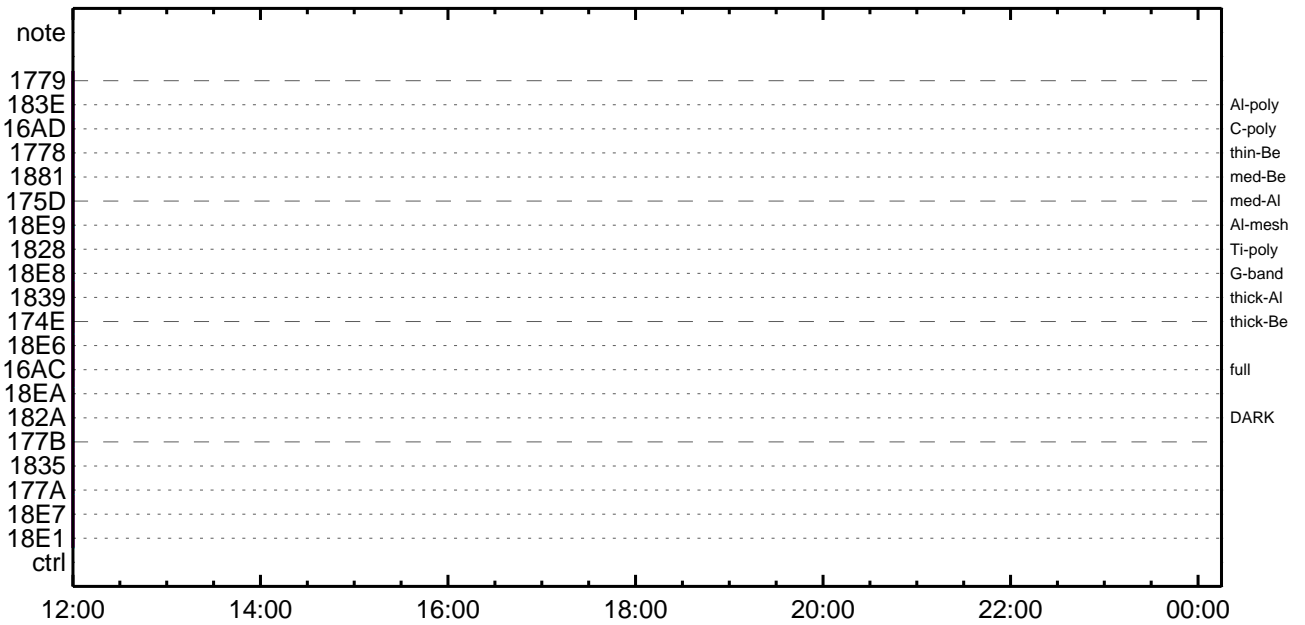
CMDI #0634 2012/06/12



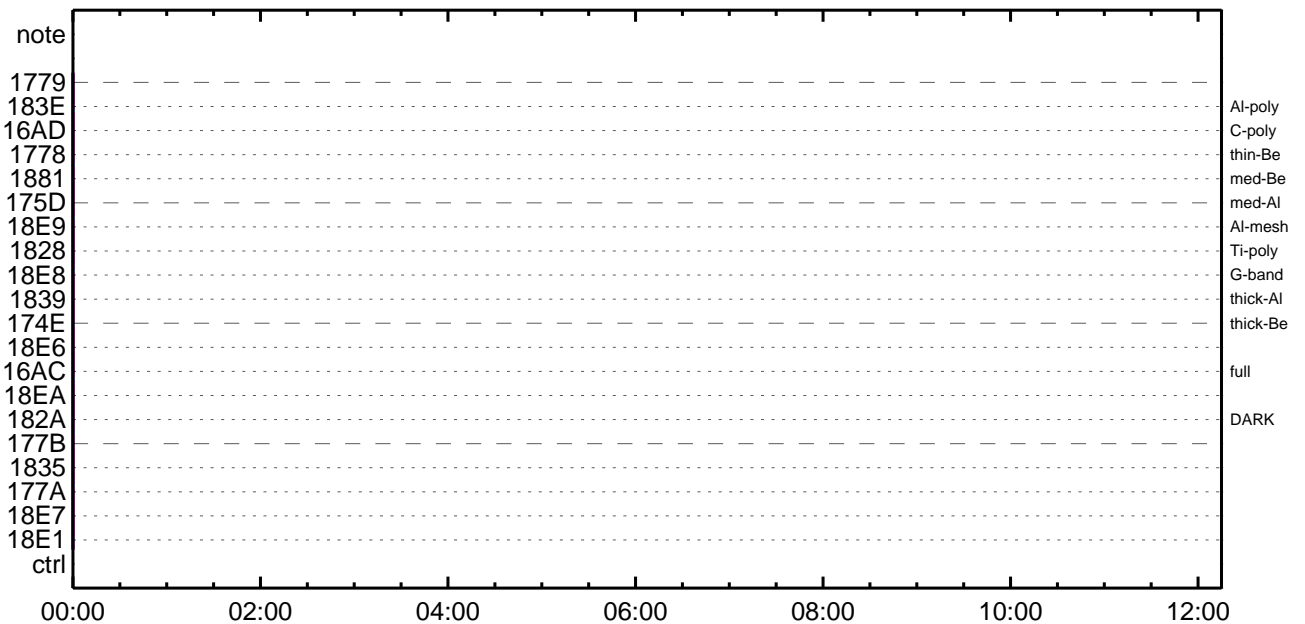
CMDI #0634 2012/06/13



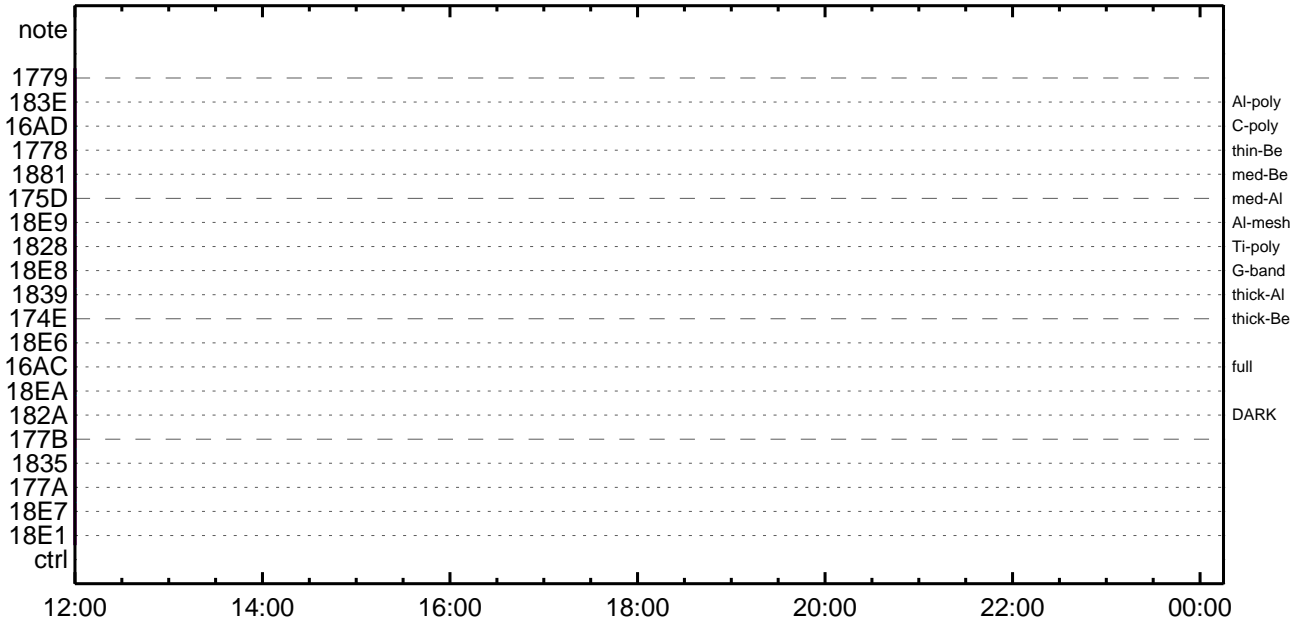
CMDI #0634 2012/06/13



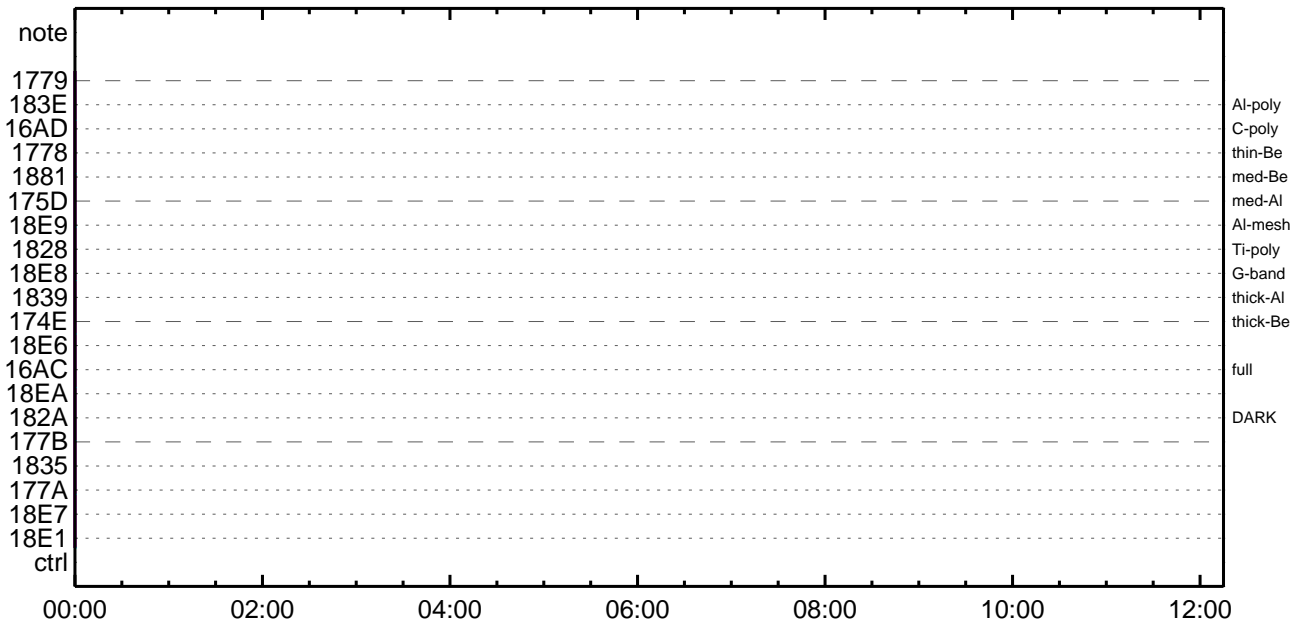
CMDI #0634 2012/06/14



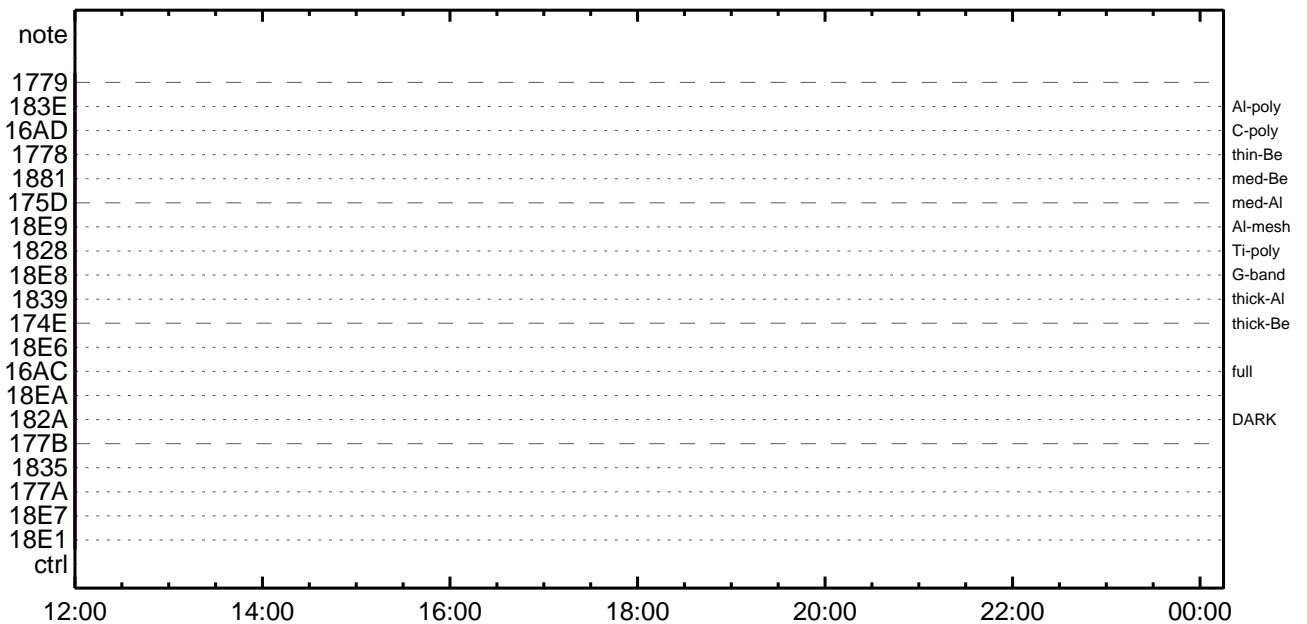
CMDI #0634 2012/06/14



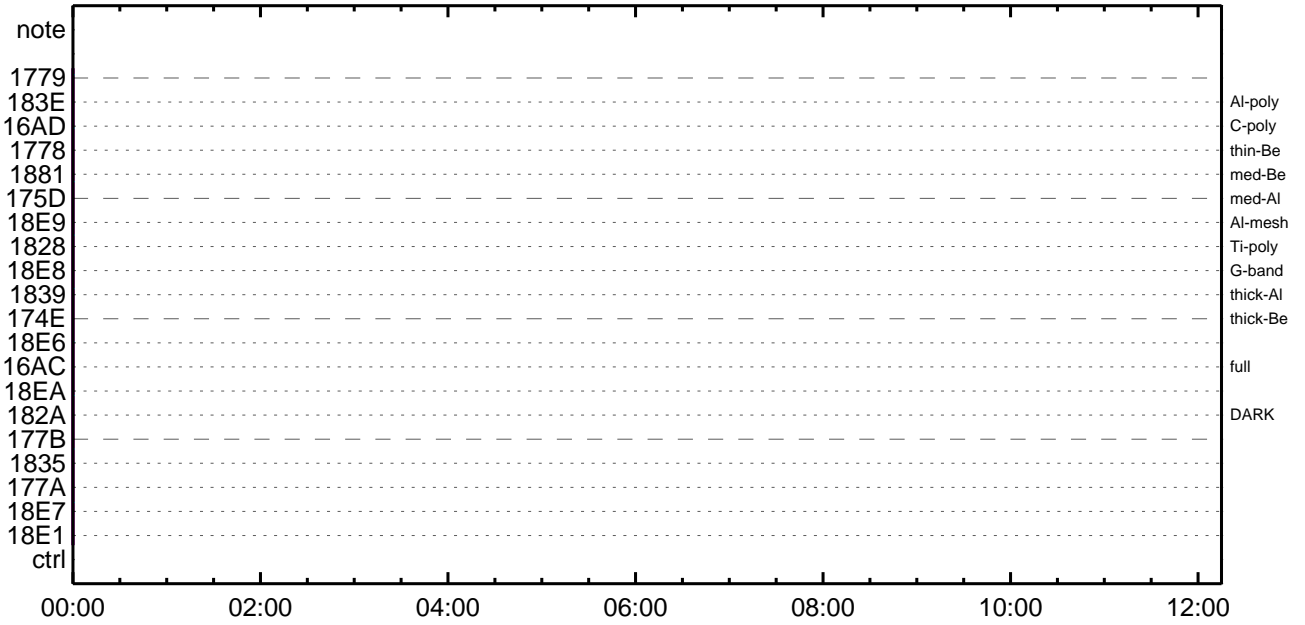
CMDI #0634 2012/06/15



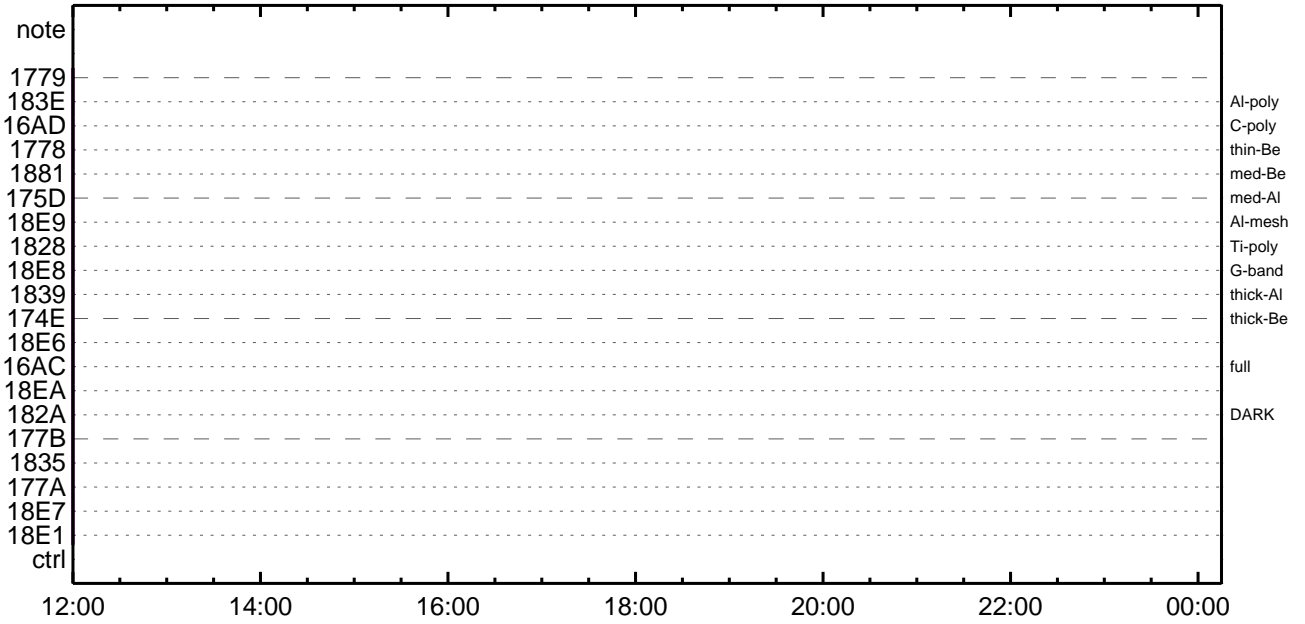
CMDI #0634 2012/06/15



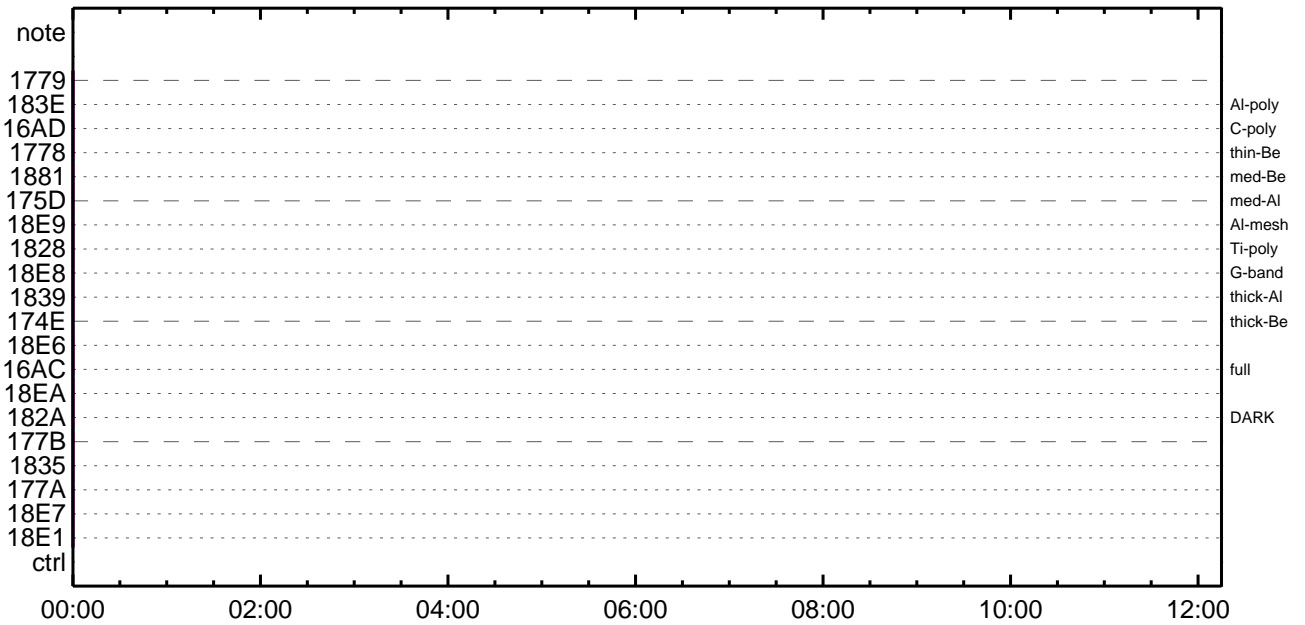
CMDI #0634 2012/06/16



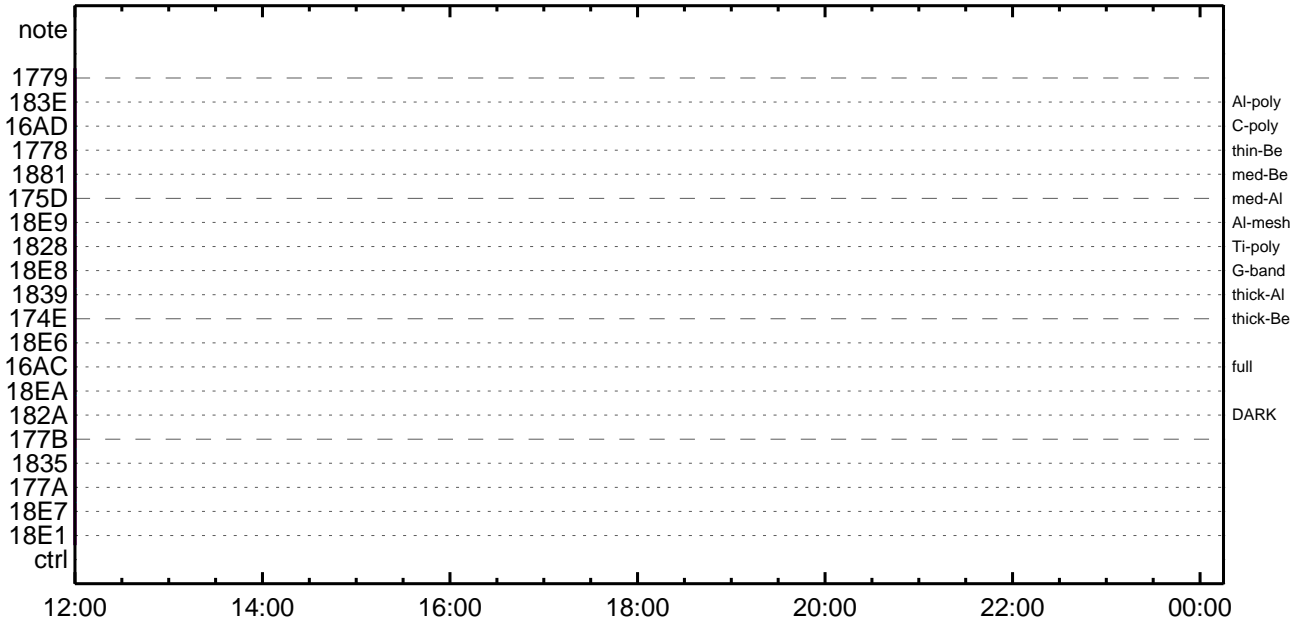
CMDI #0634 2012/06/16



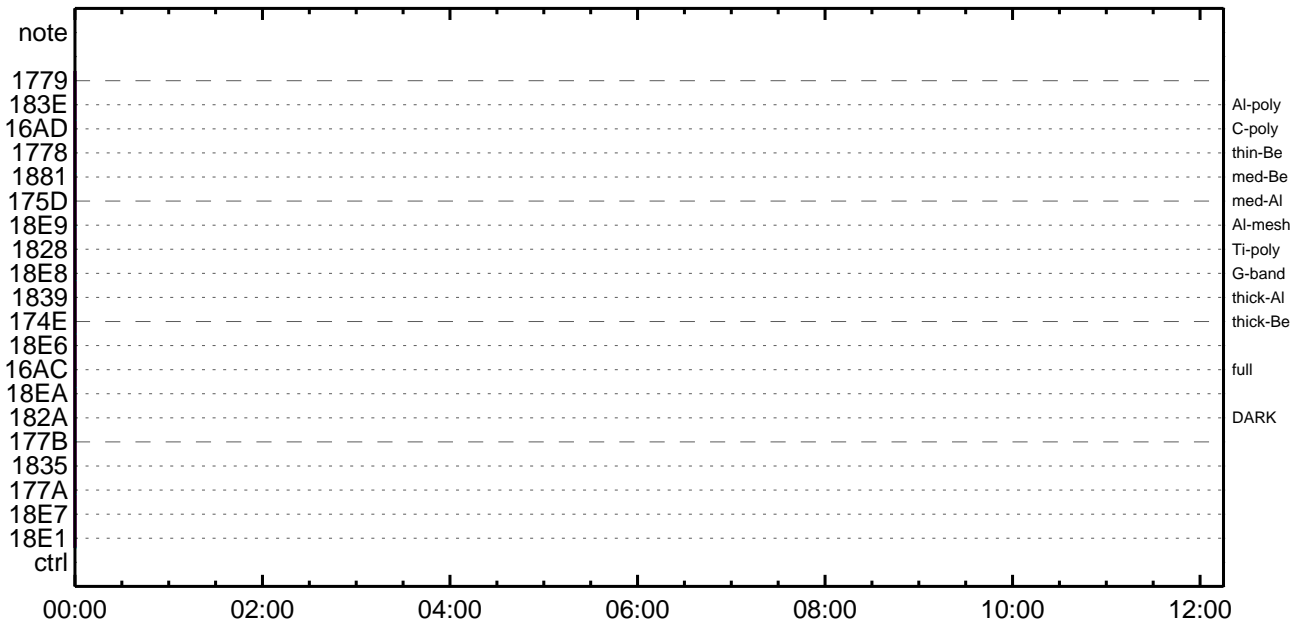
CMDI #0634 2012/06/17



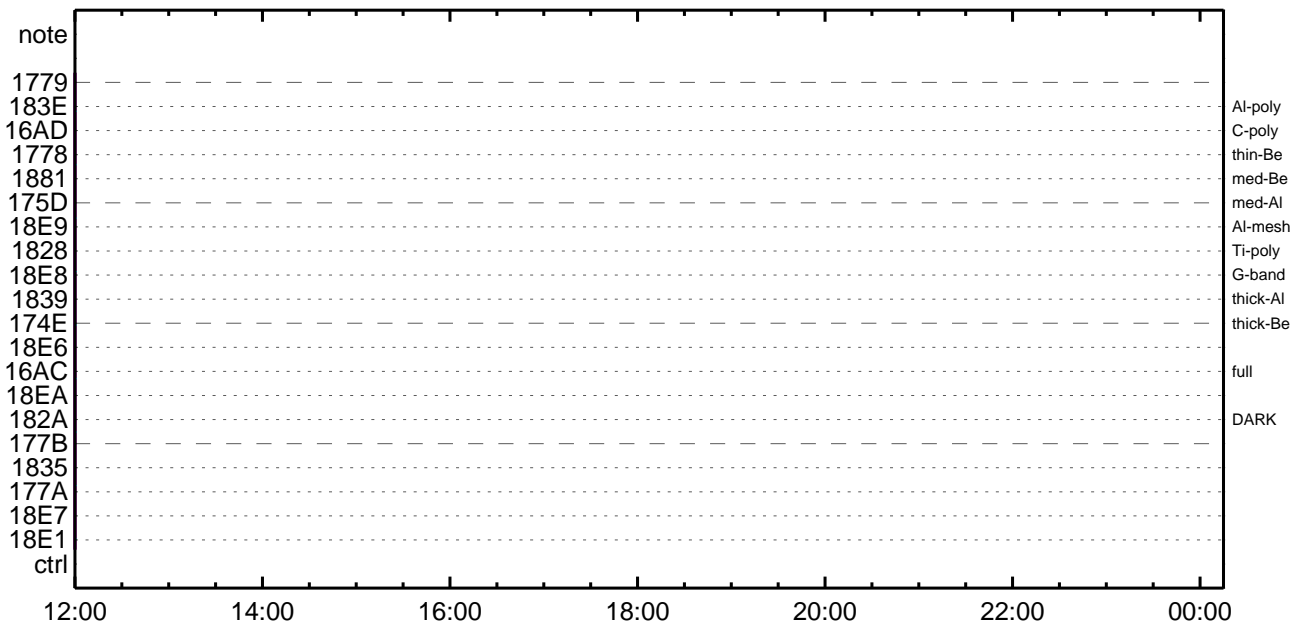
CMDI #0634 2012/06/17



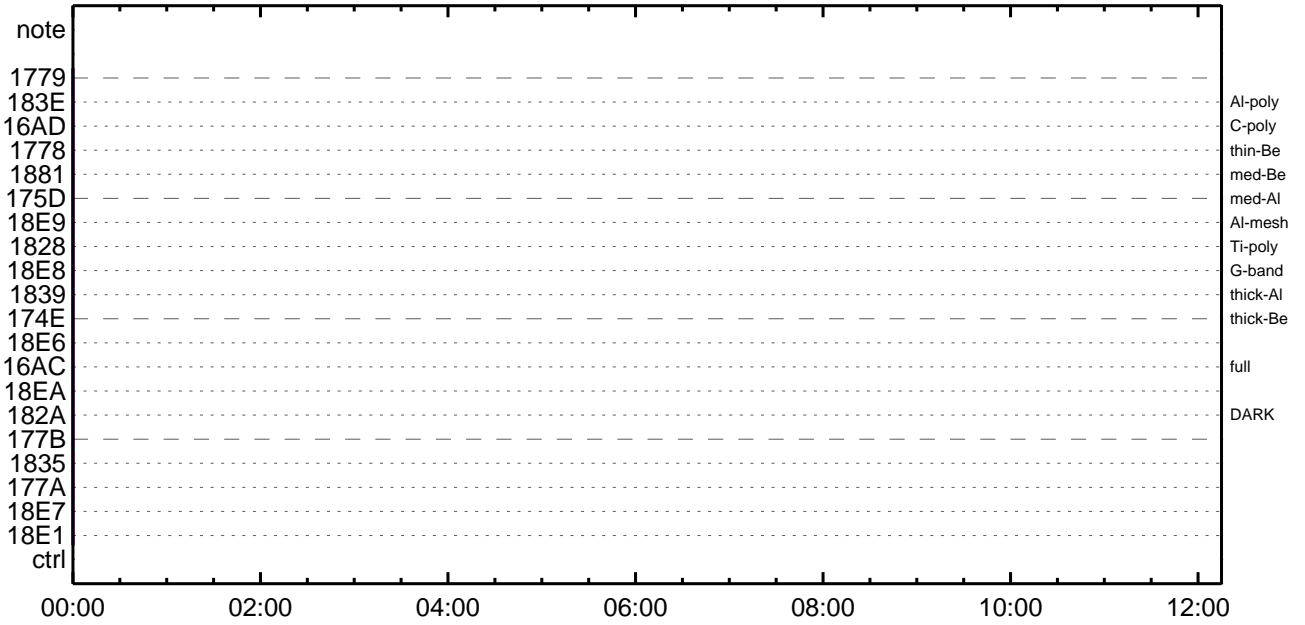
CMDI #0634 2012/06/18



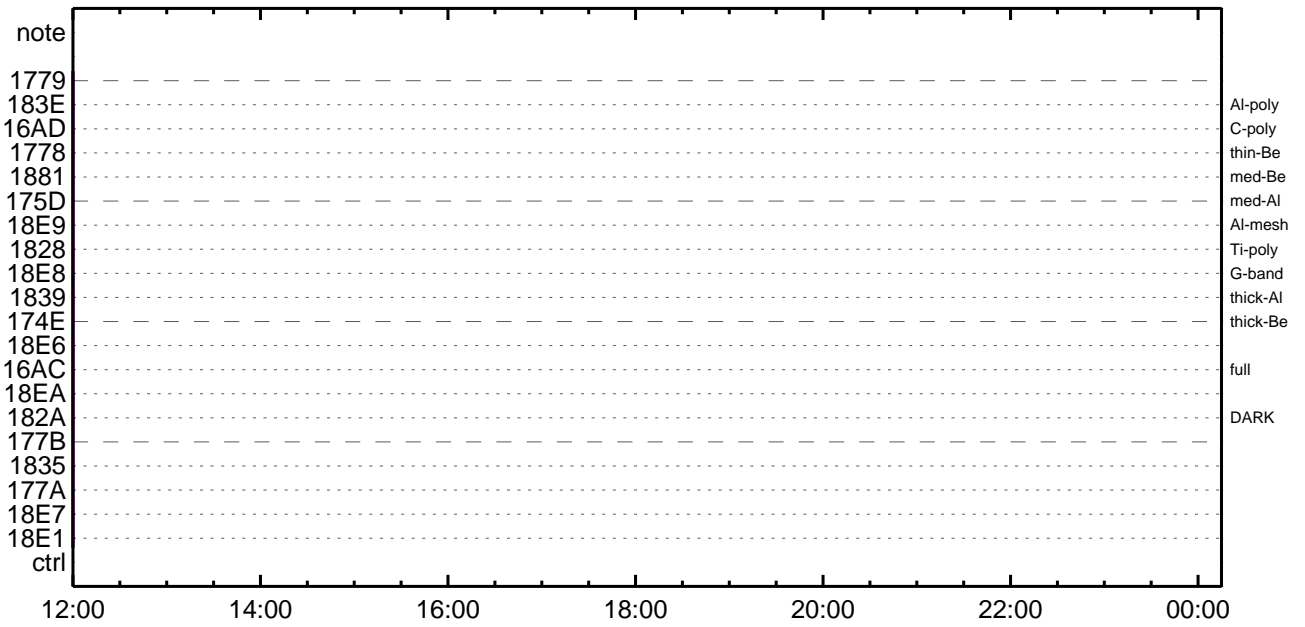
CMDI #0634 2012/06/18



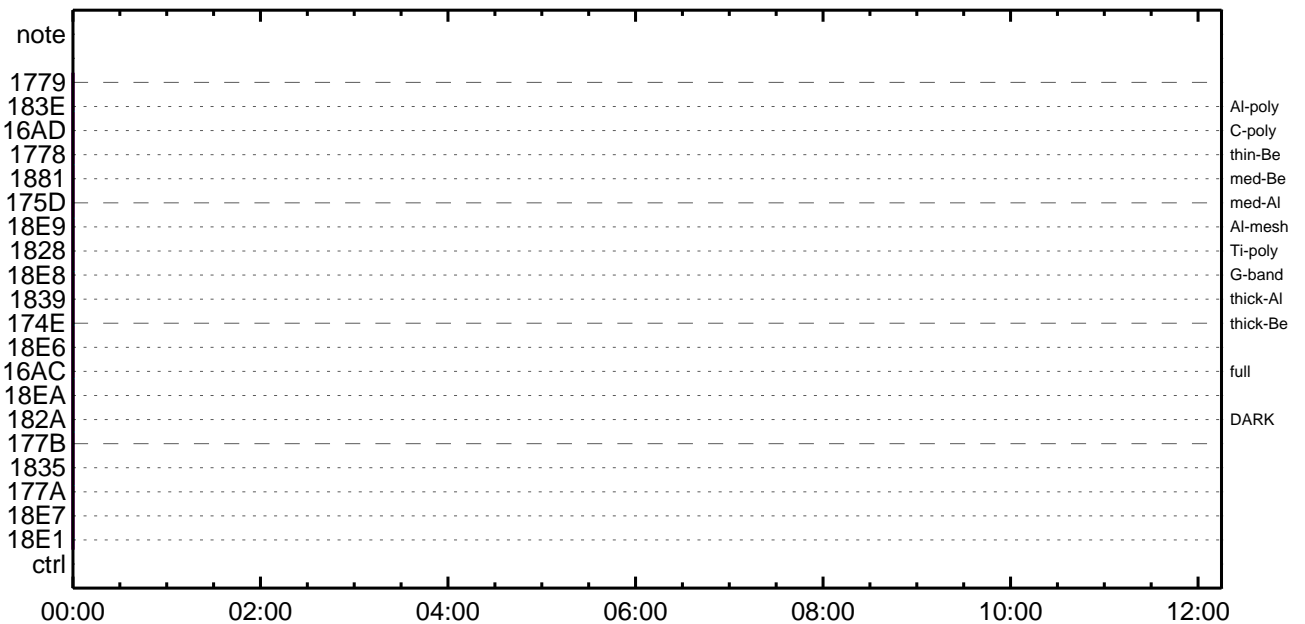
CMDI #0634 2012/06/19



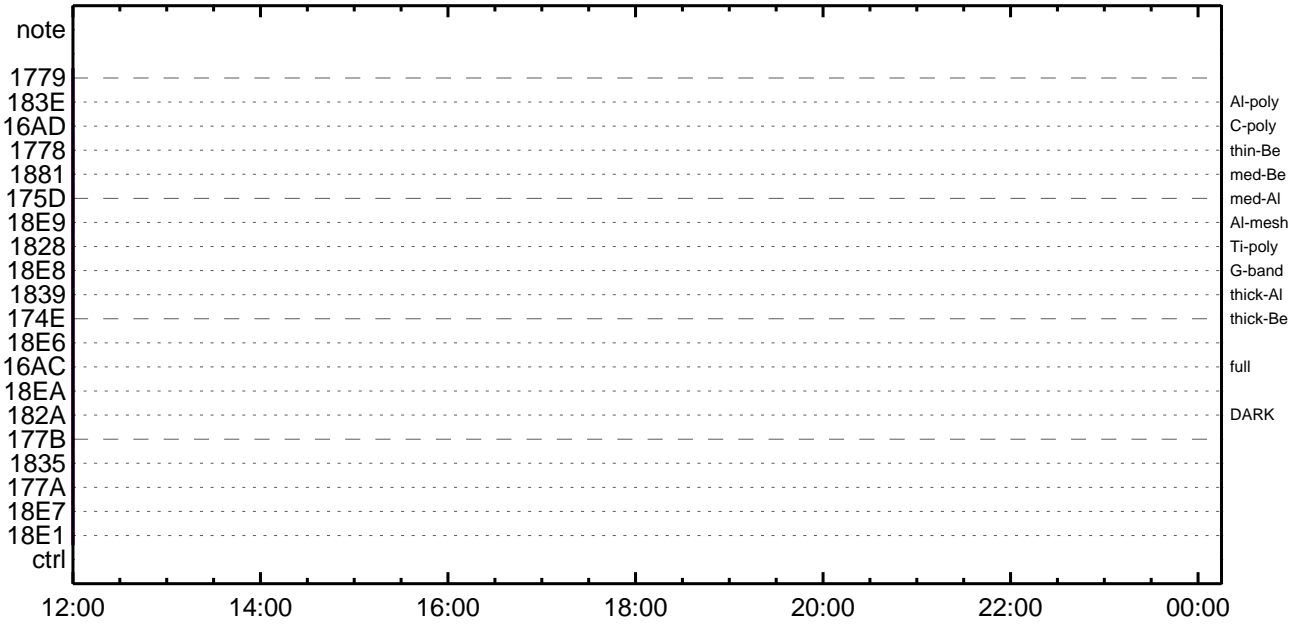
CMDI #0634 2012/06/19



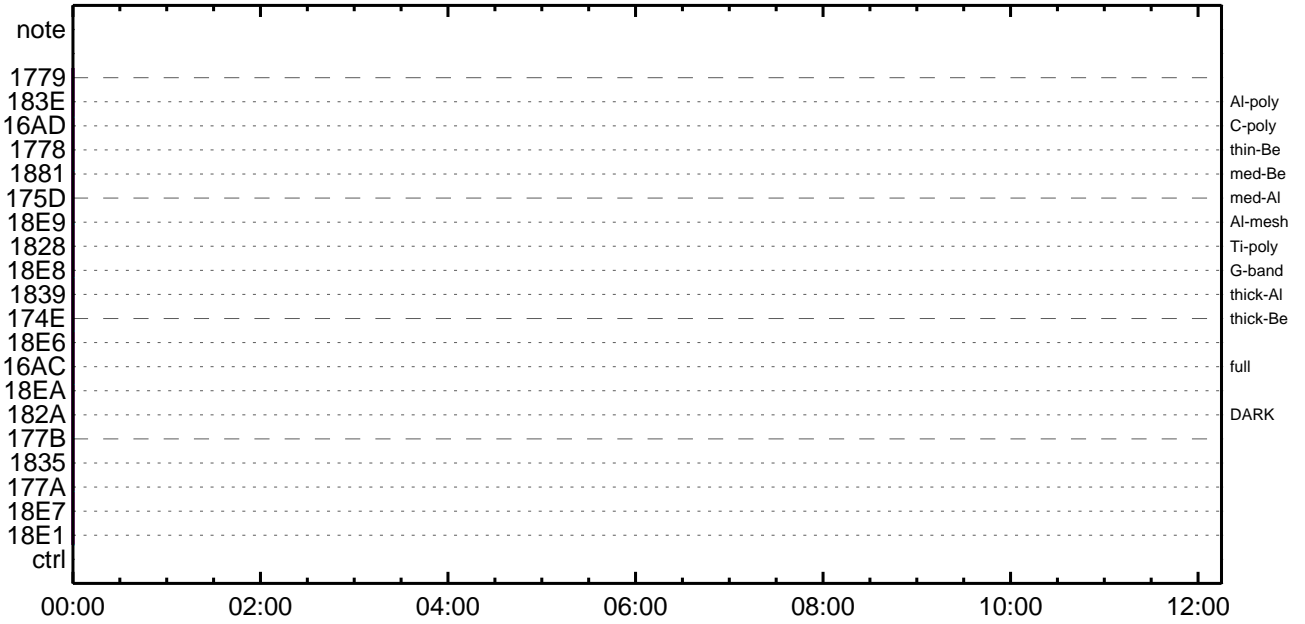
CMDI #0634 2012/06/20



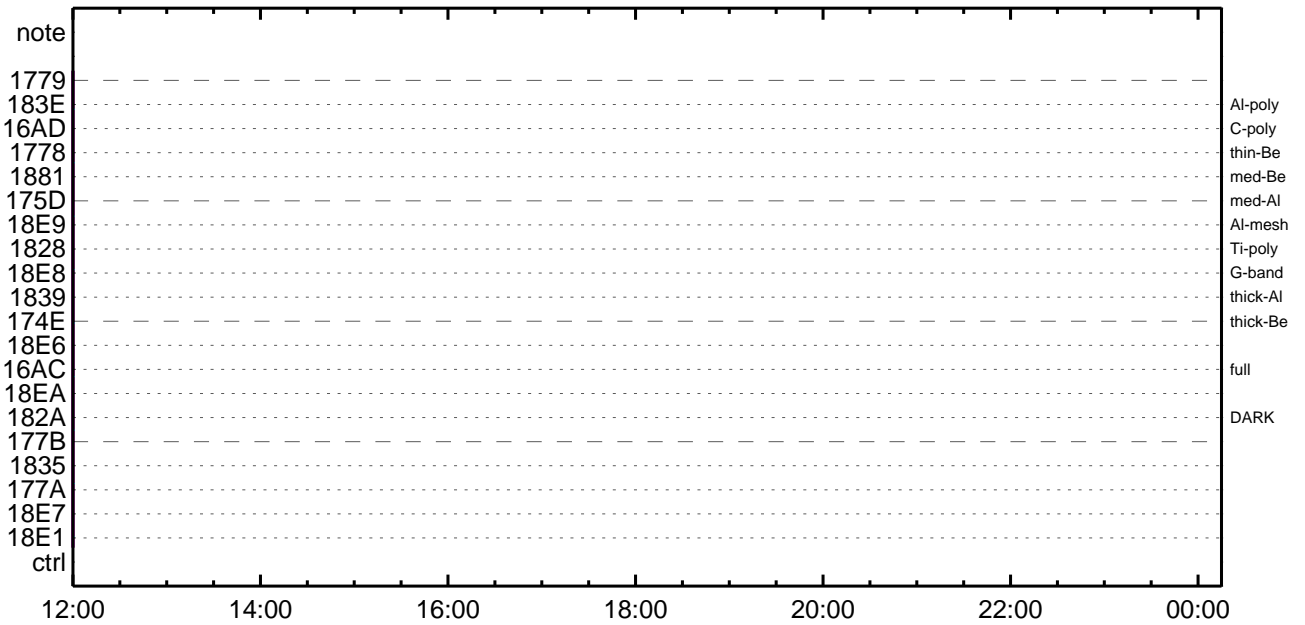
CMDI #0634 2012/06/20



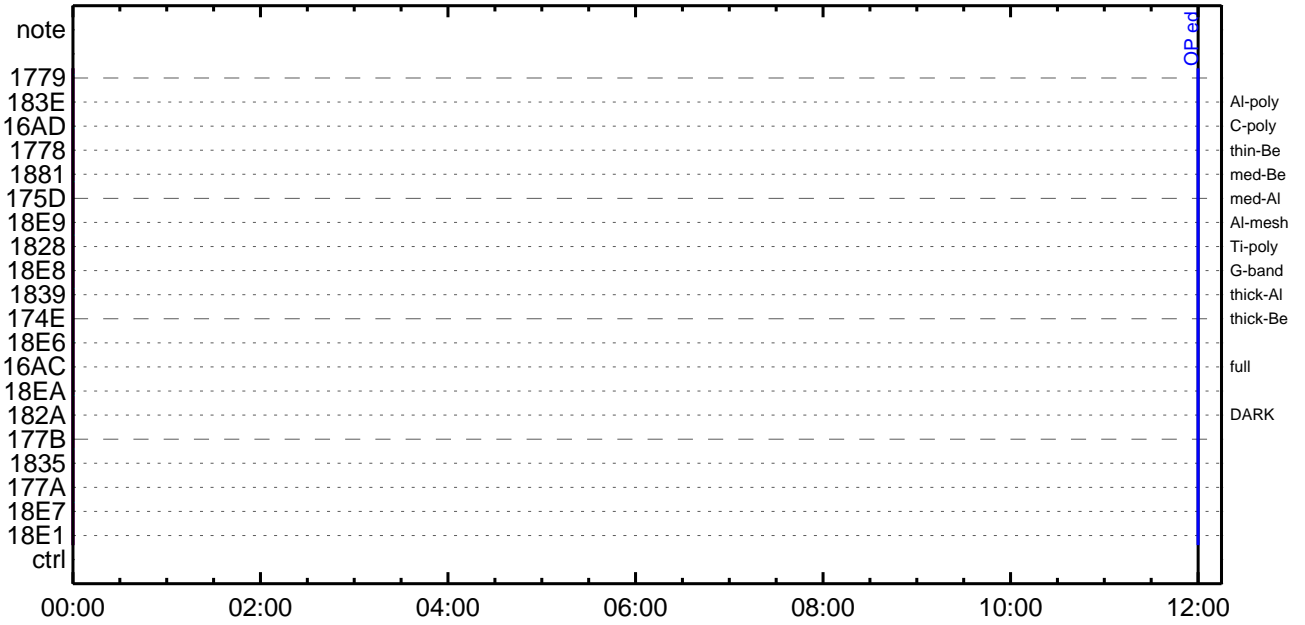
CMDI #0634 2012/06/21



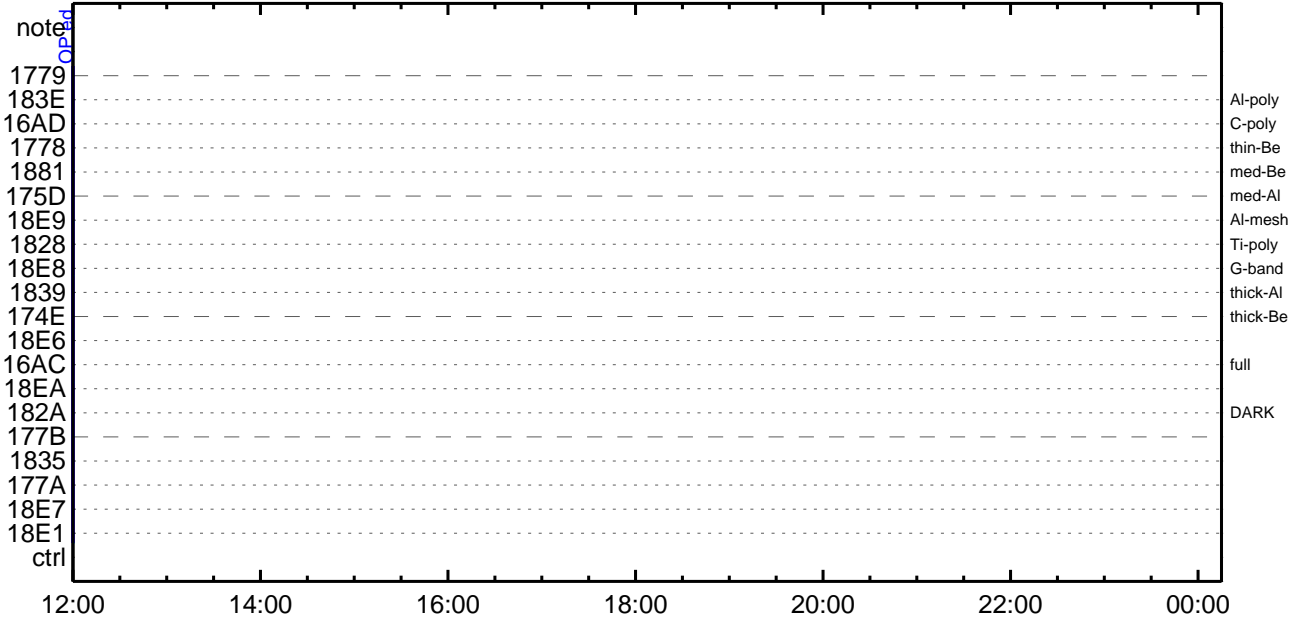
CMDI #0634 2012/06/21



CMDI #0634 2012/06/22



CMDI #0634 2012/06/22




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-878:OP
0104 ( )
0105 S. OG og-878:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½Ä´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOXx½ê½çòðÁÓÆòÇ¼ª°¬òE¼î¹çòÇòâ) *****
0167 C. DHUYâ;4YE;E½Y½, Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½î¹ç;ç°E²¼òîTI-CMDÁ÷¿@²î½Á¹Ôª°¬E²ò³òE;f
0180 C. ²²ò¿;çSET²EEDUMP²î½±°îYNY¹ç¹Ôª|²³òE;f
0181 C.
0182 C. TIY³Y²YóYÉòðÁDî¿(UT)
0183 +. TI 2012-05-17 10:24:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2012-05-17 10:24:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2012-05-17 10:24:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2012-05-17 10:28:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.      ÷÷[HK1_TI_CMD_NUM]                EQ      1COUNTUP
0198 C.
0199 C.      °Ê²¼□îÄë%îíñ□îîŷÄŷ§ŷÄŷ¹âiü
0200 C.      ÷÷[HK1_TI_CMD_ENA/DIS]            EQ      ENA
0201 C.      ÷÷[HK1_TI_CMD_NUM]                EQ      4
0202 C.      ÷÷[HK1_NEXT_EXEC_PIM]            EQ      DHU
0203 C.      ÷÷[HK1_NEXT_EXEC_DC]            EQ      0xB3
0204 C.
0205 C.      *****
0206 C.      TIîî°èŷÄŷÖŷ×
0207 C.      *****
0208 C.
0209 C.      TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.      ÷÷[HK1_DMP_TOP_ADRS_1]          EQ      07
0213 C.      ÷÷[HK1_DMP_TOP_ADRS_0]          EQ      2B
0214 C.      ÷÷[HK1_DMP_BLOCK_NUM]          EQ      3
0215 C.      ÷÷[HK1_DMP_REPEAT_NUM]        EQ      0
0216 C.      ÷÷[HK1_DMA_DMP_PIM]            EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.      ÷÷[HK1_PKT_FORM_NO]            EQ      7
0220 C.      ÷÷[HK1_PKT_GEN_TIME]            EQ      0.25 s
0221 C.      ÷÷[HK1_S_TLM_BIT_RATE]        EQ      32k
0222 C.      ÷÷[HK1_X_TLM_BIT_RATE]        EQ      4M
0223 C.      ÷÷[HK1_DMP_CHK_FLG]            EQ      EXEC
0224 C.
0225 C.      ŷÄŷÖŷ×½¹î»□ò³îÇ§
0226 C.      ÷÷[HK1_DMP_CHK_FLG]            EQ      NON
0227 C.
0228 C.      RAM ID=TI_TBL□îî¼Ê¹ç·è²îOK□ò³îÇ§
0229 C.
0230 C.      DHUŷâ;¼ŷÉ;Ê¼ŷ¼. ŷî;¼ŷÈ;Ê□òîä□¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.      ÷÷[HK1_PKT_FORM_NO]            EQ      2
0234 C.      ÷÷[HK1_PKT_GEN_TIME]            EQ      0.5S
0235 C.      ÷÷[HK1_S_TLM_BIT_RATE]        EQ      32K
0236 C.      ÷÷[HK1_X_TLM_BIT_RATE]        EQ      4M
0237 C.
0238 C.      *****
0239 C.      SOT TI command set
0240 C.      *****
0241 C.      Execute, after the success of OP upload.
0242 +. TI 2012-05-17 10:28:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C.      -----
0246 C.      HK1_TI_CMD_NUM      = 1 CNTUP [ ]
0247 C.      -----
0248 C.      ***** SOT END *****
0249 C.      Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C.      ***** Start EIS operation (TI set) *****
0253 C.      Execute, after the success of OP upload.
0254 C.      Set EIS TI-commands
0255 +. TI 2012-05-17 10:28:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2012-05-17 10:28:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.      [ ] [HK1_TI_CMD_NUM]            EQ      2 COUNTUP
0262 C.      ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C.      ***** XRT START *****
0267 C.      Execute, after the success of OP upload.
0268 +. TI 2012-05-17 10:28:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.      [ ] [HK1_TI_CMD_NUM]            EQ      1COUNTUP
0272 C.
0273 C.      ***** XRT END *****
0274 C.
0275 C.      ***** MDP ´úÄî□î»ö¼ŷ□èÄ□¹□èDCBC•x²è *****
0276 C.      (¼á°îŷÖŷÄŷÈŷŷŷÈŷáŷçŷè□È¼□□¼Ä»Ü□¹□è)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C.      ***** ŷĐŷ¹•î Daily±çîñ□èË¹□¹□èDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C.      ;ãLOSŷÄŷ§ŷÄŷ¹¼Ä»Ü;ã
0287 C.
0288 C.      ***** LOS *****
0289 C.

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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 80 80 20 20)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 08)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 08 20)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0f 80 80 06 06)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 10 80 80 08 08)
0138 + DC 07-F0 MDP_XRT_FLD_ENA
0139 BC (d8)
0140 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0141 BC (c8)
0142 + DC 07-F0 MDP_XRT_AEC_RESET
0143 BC (d0)
0144 + DC 07-F0 MDP_XRT_ARS_DIS
0145 BC (d5)
0146 + DC 07-F0 MDP_XRT_FLD_RESET
0147 BC (da)
0148 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0149 BC (c4 0e)
0150 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0151 BC (c5 02)
0152 . C. ----- Success Verify ? OK / NG ____
0153 C.
0154 C.
0155 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0156 C.
0157 +. DC 07-F0 MDP_XRT_MODE_OBSV
0158 BC (c2)
0159 +. TI 2012-05-17 10:28:02.0
0160 DC 07-F0 MDP_XRT_MODE_OBSV
0161 BC (c2)
0162 . C. ----- Success Verify ? OK / NG ____
0163 C.
0164 C. ***** XRT END *****
0165 . C. *****
0166 C. SOT table upload
0167 C. *****
0168 . C. < Stop FG table >
0169 +. DC 07-F0 MDP_FG_CTRL_MANU
0170 BC (51)
0171 . C. -----
0172 C. MDP_FG_CTRL_MODE = MANU [ ]
0173 C. -----
0174 C.
0175 . C. <Upload FG Observation Table>
0176 . S. RAM ram-268:MDP_OBS_F
0177 ( )
0178 C.
0179 . C. < Dump RAMID=MDP_OBS_F >
0180 +. DC 07-F0 MDP_DUMP_FGTBL
0181 BC (82 07 00 00 00 38 b8)
0182 C. -----
0183 C. MDP_OBS_F verify = OK/NG [ ]
0184 C. -----
0185 C.
0186 . C. < Stop SP table >
0187 +. DC 07-F0 MDP_SP_CTRL_MANU
0188 BC (61)
0189 C. -----
0190 C. MDP_SP_CTRL_MODE = MANU [ ]
0191 C. -----
0192 C.
0193 . C. <Upload SP Observation Table>

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0194 . S. RAM ram-287:MDP_OBS_S
0195 ( )
0196 C.
0197 . C. < Dump RAMID=MDP_OBS_S >
0198 +. DC 07-F0 MDP_DUMP_SPTBL
0199 BC (83 07 00 00 00 38 b8)
0200 C. -----
0201 C. MDP_OBS_S verify = OK/NG [ ]
0202 C. -----
0203 C.
0204 C. *****
0205 C. SOT TI command set
0206 C. *****
0207 C. Execute, after the success of TBL upload.
0208 +. TI 2012-05-17 10:28:18.0
0209 DC 07-F0 MDP_SOT_MODE_OBSV
0210 BC (40)
0211 C. -----
0212 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0213 C. -----
0214 C.
0215 C.
0216 . C. ***** MDP 'uãïñî»ö½ÿðËÄð¹æðDCBC·x²è *****
0217 C. (½â°îÿÖÿÄÿÈÿÏÿËÿäÿçÿè»Ë½»¼»½»¼»¾»Û¹»è)
0218 . S. DC-BC dcbc-402:DCBC
0219 (MDP_known_event)
0220 C.
0221 C.
0222 . C. ***** ½Dÿ¹·ï Daily±;îñ»Ë'ð¹»æðDCBC·x²è *****
0223 . S. DC-BC dcbc-153:DCBC
0224 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0225 C.
0226 C.
0227 . C. ;ãLOSÿÁÿSÿÿÄÿ-¼Ä»Û;ã
0228 C.
0229 . C. ***** LOS *****
0230 C.

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*** OP Sequence for XRT ***

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2012/05/17 10:38:54.0 XRT_CTRL_MANU_439_OG [0x1b7]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/05/17 10:39:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 00 00 00 ac cd
2012/05/17 10:41:26.0 XRT_FOCUS_POSITION_401_OG [0x191]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2012/05/17 10:41:46.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2012/05/17 10:41:48.0 XRT_FLRCTRL_ENA_413_OG [0x19d]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2012/05/17 10:41:50.0 XRT_AEC_RESET_443_OG [0x1bb]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2012/05/17 10:41:52.0 XRT_ARS_DIS_431_OG [0x1af]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2012/05/17 10:41:54.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/05/17 10:41:56.0 XRT_QT_PROG_SET_420_OG [0x1a4]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 10
2012/05/17 10:41:58.0 XRT_FL_PROG_SET_449_OG [0x1c1]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 0d
2012/05/17 10:42:00.0 XRT_CTRL_AUTO_406_OG [0x196]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/05/17 11:05:54.0 XRT_CTRL_MANU_439_OG [0x1b7]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/05/17 11:06:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 00 00 00 d6 67
2012/05/17 11:08:26.0 XRT_FOCUS_POSITION_401_OG [0x191]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2012/05/17 11:08:46.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2012/05/17 11:08:48.0 XRT_FLRCTRL_ENA_413_OG [0x19d]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2012/05/17 11:08:50.0 XRT_AEC_RESET_443_OG [0x1bb]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2012/05/17 11:08:52.0 XRT_ARS_DIS_431_OG [0x1af]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2012/05/17 11:08:54.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/05/17 11:08:56.0 XRT_QT_PROG_SET_420_OG [0x1a4]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 10
2012/05/17 11:08:58.0 XRT_FL_PROG_SET_449_OG [0x1c1]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 0d
2012/05/17 11:09:00.0 XRT_CTRL_AUTO_406_OG [0x196]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/05/17 11:21:54.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/05/17 11:21:56.0 XRT_FOCUS_POSITION_401_OG [0x191]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2012/05/17 11:22:00.0 AOCs_OrE-point_Start_3_OG [0x099]
                        AOCU_NM 5 02-76 00 00 00 00 00
2012/05/17 11:22:16.0 XRT_FLD_DIS_402_OG [0x192]
                        MDP_XRT_FLD_DIS 1 07-F0 d9
2012/05/17 11:22:18.0 XRT_FLRCTRL_DIS_433_OG [0x1b1]
                        MDP_XRT_FLRCTRL_DIS 1 07-F0 c9
2012/05/17 11:22:20.0 XRT_ARS_DIS_438_OG [0x1b6]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2012/05/17 11:24:58.0 XRT_QT_PROG_SET_444_OG [0x1bc]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 01
2012/05/17 11:25:00.0 XRT_CTRL_AUTO_406_OG [0x196]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/05/17 11:49:00.0 XRT_CTRL_MANU_408_OG [0x198]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/05/17 11:49:02.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/05/17 11:49:04.0 XRT_PREFLR_STRT_422_OG [0x1a6]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2012/05/17 11:52:14.0 XRT_PREFLR_STOP_424_OG [0x1a8]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2012/05/17 12:16:24.0 XRT_CTRL_MANU_439_OG [0x1b7]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2012/05/17 12:16:30.0 AOCs_OrE-point_Start_4_OG [0x09a]
                        AOCU_NM 5 02-76 00 00 00 29 99
2012/05/17 12:18:56.0 XRT_FOCUS_POSITION_401_OG [0x191]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2012/05/17 12:19:16.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2012/05/17 12:19:18.0 XRT_FLRCTRL_ENA_413_OG [0x19d]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2012/05/17 12:19:20.0 XRT_AEC_RESET_443_OG [0x1bb]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2012/05/17 12:19:22.0 XRT_ARS_DIS_431_OG [0x1af]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2012/05/17 12:19:24.0 XRT_FLD_RESET_412_OG [0x19c]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2012/05/17 12:19:26.0 XRT_QT_PROG_SET_420_OG [0x1a4]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 10
2012/05/17 12:19:28.0 XRT_FL_PROG_SET_449_OG [0x1c1]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 0d
2012/05/17 12:19:30.0 XRT_CTRL_AUTO_406_OG [0x196]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2012/05/17 12:32:24.0 XRT_CTRL_MANU_439_OG [0x1b7]

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2012/05/17	12:32:30.0	AOCS_ORe-point_Start_5_OG [0x09b]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
		AOCU_NM		5	02-76	00 00 00 53 33	
2012/05/17	12:34:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/05/17	12:35:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/05/17	12:35:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/05/17	12:35:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/05/17	12:35:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/05/17	12:35:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/17	12:35:26.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10	
2012/05/17	12:35:28.0	XRT_FL_PROG_SET_449_OG [0x1c1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2012/05/17	12:35:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/17	12:48:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/17	12:48:30.0	AOCS_ORe-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00 d6 36 b7 8e	
2012/05/17	12:50:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/05/17	12:51:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/05/17	12:51:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/05/17	12:51:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/05/17	12:51:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/05/17	12:51:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/17	12:51:26.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10	
2012/05/17	12:51:28.0	XRT_FL_PROG_SET_449_OG [0x1c1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2012/05/17	12:51:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/17	12:58:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/17	12:58:30.0	AOCS_ORe-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 b4 b5 db 75	
2012/05/17	13:00:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/05/17	13:01:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/05/17	13:01:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/05/17	13:01:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/05/17	13:01:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/05/17	13:01:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/17	13:01:26.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10	
2012/05/17	13:01:28.0	XRT_FL_PROG_SET_449_OG [0x1c1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2012/05/17	13:01:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/17	13:27:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/17	13:27:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/17	13:27:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/05/17	13:30:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/05/17	13:54:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/17	13:55:00.0	AOCS_ORe-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00 ac 5b 00 00	
2012/05/17	13:57:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/05/17	13:57:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/05/17	13:57:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/05/17	13:57:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/05/17	13:57:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/05/17	13:57:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/17	13:57:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10	
2012/05/17	13:57:58.0	XRT_FL_PROG_SET_449_OG [0x1c1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	

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2012/05/17	13:58:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/17	14:10:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/17	14:11:00.0	AOCS_ORe-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	00 b4 b5 24 8b	
2012/05/17	14:13:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/05/17	14:13:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/05/17	14:13:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/05/17	14:13:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/05/17	14:13:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/05/17	14:13:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/17	14:13:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10	
2012/05/17	14:13:58.0	XRT_FL_PROG_SET_449_OG [0x1c1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2012/05/17	14:14:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/17	14:26:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/17	14:27:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]	AOCU_NM	5	02-76	00 d6 36 48 72	
2012/05/17	14:29:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/05/17	14:29:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/05/17	14:29:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/05/17	14:29:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/05/17	14:29:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/05/17	14:29:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/17	14:29:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10	
2012/05/17	14:29:58.0	XRT_FL_PROG_SET_449_OG [0x1c1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2012/05/17	14:30:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/17	14:36:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/17	14:37:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]	AOCU_NM	5	02-76	00 29 ca b7 8e	
2012/05/17	14:39:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/05/17	14:39:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/05/17	14:39:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/05/17	14:39:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/05/17	14:39:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/05/17	14:39:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/17	14:39:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10	
2012/05/17	14:39:58.0	XRT_FL_PROG_SET_449_OG [0x1c1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2012/05/17	14:40:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/17	14:46:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/17	14:47:00.5	AOCS_ORe-point_Start_12_OG [0x0a2]	AOCU_NM	5	02-76	00 4b 4b db 75	
2012/05/17	14:49:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/05/17	14:49:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/05/17	14:49:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/05/17	14:49:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/05/17	14:49:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/05/17	14:49:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/17	14:49:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10	
2012/05/17	14:49:58.0	XRT_FL_PROG_SET_449_OG [0x1c1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2012/05/17	14:50:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/17	15:06:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/17	15:06:02.0	XRT_FLD_RESET_412_OG [0x19c]					

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2012/05/17	15:06:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/17	15:09:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/17	15:52:00.0	XRT_Custom_418_OG [0x1a2]								
2012/05/17	15:53:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/17	15:57:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/17	15:58:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]	AOCU_NM	5	02-76	00 53 a5 00 00				
2012/05/17	16:00:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/05/17	16:00:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/05/17	16:00:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/05/17	16:00:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/05/17	16:00:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/05/17	16:00:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/17	16:00:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2012/05/17	16:00:58.0	XRT_FL_PROG_SET_449_OG [0x1c1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2012/05/17	16:01:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/17	16:13:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/17	16:14:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]	AOCU_NM	5	02-76	00 4b 4b 24 8b				
2012/05/17	16:16:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/05/17	16:16:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/05/17	16:16:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/05/17	16:16:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/05/17	16:16:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/05/17	16:16:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/17	16:16:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2012/05/17	16:16:58.0	XRT_FL_PROG_SET_449_OG [0x1c1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2012/05/17	16:17:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/17	16:29:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/17	16:30:00.0	AOCS_ORe-point_Start_15_OG [0x0a5]	AOCU_NM	5	02-76	00 29 db 48 72				
2012/05/17	16:32:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/05/17	16:32:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/05/17	16:32:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/05/17	16:32:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/05/17	16:32:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/05/17	16:32:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/17	16:32:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2012/05/17	16:32:58.0	XRT_FL_PROG_SET_449_OG [0x1c1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2012/05/17	16:33:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/17	16:39:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/17	16:40:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	03 00 00 00 00				
2012/05/17	16:42:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/05/17	16:42:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/05/17	16:42:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/05/17	16:42:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/05/17	16:42:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/05/17	16:42:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/17	16:42:56.0	XRT_QT_PROG_SET_415_OG [0x19f]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2012/05/17	16:42:58.0	XRT_FL_PROG_SET_449_OG [0x1c1]								

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2012/05/17	16:43:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/17	16:44:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/17	16:44:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/17	16:44:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/05/17	16:47:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/05/17	17:28:30.0	XRT_Custom_418_OG [0x1a2]					
2012/05/17	17:29:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/17	17:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/17	17:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2012/05/17	18:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00	00
2012/05/17	18:00:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/05/17	18:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/05/17	18:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/05/17	18:02:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07
2012/05/17	18:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/17	18:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/17	18:10:00.0	AOCS_Ore-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	03 00 00 00	00
2012/05/17	18:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00
2012/05/17	18:12:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/05/17	18:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/05/17	18:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/05/17	18:12:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/05/17	18:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/17	18:12:56.0	XRT_QT_PROG_SET_415_OG [0x19f]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	13
2012/05/17	18:12:58.0	XRT_FL_PROG_SET_449_OG [0x1c1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2012/05/17	18:13:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/17	18:23:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/17	18:23:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/17	18:23:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/05/17	18:26:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/05/17	19:05:30.0	XRT_Custom_418_OG [0x1a2]					
2012/05/17	19:06:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/17	20:01:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/17	20:01:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/17	20:01:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/05/17	20:04:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/05/17	20:43:00.0	XRT_Custom_418_OG [0x1a2]					
2012/05/17	20:44:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/17	21:40:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/17	21:40:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/17	21:40:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/05/17	21:43:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/05/17	22:19:00.0	XRT_Custom_418_OG [0x1a2]					
2012/05/17	22:20:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/05/17	23:18:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/05/17	23:18:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/05/17	23:18:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/05/17	23:21:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	

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2012/05/17	23:46:30.0	XRT_Custom_418_OG [0x1a2]							
2012/05/17	23:47:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/18	00:57:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/18	00:57:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/18	00:57:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/18	01:00:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/18	01:25:00.0	XRT_Custom_418_OG [0x1a2]							
2012/05/18	01:26:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/18	02:33:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/18	02:33:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/18	02:33:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/18	02:36:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/18	03:03:30.0	XRT_Custom_418_OG [0x1a2]							
2012/05/18	03:04:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/18	04:02:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/18	04:02:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/18	04:02:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/18	04:05:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/18	04:42:00.0	XRT_Custom_418_OG [0x1a2]							
2012/05/18	04:43:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/18	05:42:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/18	05:42:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/18	05:42:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/18	05:45:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/18	06:20:30.0	XRT_Custom_418_OG [0x1a2]							
2012/05/18	06:21:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/18	06:24:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/18	06:24:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/05/18	06:24:30.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/05/18	06:24:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/05/18	06:24:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/05/18	06:24:50.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/05/18	06:27:28.0	XRT_QT_PROG_SET_423_OG [0x1a7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2012/05/18	06:27:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/18	06:34:24.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/18	06:34:30.0	AOCS_Ore-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2012/05/18	06:36:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/05/18	06:37:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/05/18	06:37:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/05/18	06:37:20.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/05/18	06:37:22.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/05/18	06:37:24.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/18	06:37:26.0	XRT_QT_PROG_SET_415_OG [0x19f]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2012/05/18	06:37:28.0	XRT_FL_PROG_SET_449_OG [0x1c1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2012/05/18	06:37:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/18	07:23:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/18	07:23:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/18	07:23:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/18	07:26:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				

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2012/05/18	07:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/18	08:00:00.0	AOCS_Orе-point_Start_17_OG [0x0a7]	AOCU_NM	5	02-76	00	56	a7	01	26
2012/05/18	08:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/05/18	08:02:46.0	XRT_FLD_ENA_405_OG [0x195]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/05/18	08:02:48.0	XRT_FLRCTRL_ENA_440_OG [0x1b8]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/05/18	08:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/05/18	08:02:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/05/18	08:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/18	08:02:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2012/05/18	08:02:58.0	XRT_FL_PROG_SET_449_OG [0x1c1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2012/05/18	08:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/18	09:03:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/18	09:03:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/18	09:03:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/18	09:06:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/18	09:37:30.0	XRT_Custom_418_OG [0x1a2]								
2012/05/18	09:38:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/18	09:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/18	10:00:00.0	AOCS_Orе-point_Start_18_OG [0x0a8]	AOCU_NM	5	02-76	04	00	00	00	00
2012/05/18	10:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/05/18	10:02:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/05/18	10:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/05/18	10:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/05/18	10:02:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/05/18	10:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/18	10:02:56.0	XRT_QT_PROG_SET_415_OG [0x19f]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	13			
2012/05/18	10:02:58.0	XRT_FL_PROG_SET_449_OG [0x1c1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2012/05/18	10:03:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/18	10:45:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/18	10:45:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/18	10:45:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/18	10:48:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/18	11:16:00.0	XRT_Custom_418_OG [0x1a2]								
2012/05/18	11:17:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/18	12:26:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/18	12:26:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/18	12:26:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/18	12:29:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/18	12:54:30.0	XRT_Custom_418_OG [0x1a2]								
2012/05/18	12:55:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/18	14:05:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/18	14:05:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/18	14:05:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/18	14:08:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/18	14:41:00.0	XRT_Custom_418_OG [0x1a2]								
2012/05/18	14:42:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/18	15:43:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/18	15:43:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/18	15:43:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]								

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2012/05/18	15:46:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e8				
2012/05/18	16:28:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/18	16:29:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/18	17:22:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/18	17:22:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/18	17:22:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/18	17:25:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/18	18:05:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/18	18:05:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/05/18	18:05:30.5	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00				
2012/05/18	18:05:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/05/18	18:05:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/05/18	18:05:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/05/18	18:08:28.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2012/05/18	18:08:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/18	18:15:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/18	18:15:30.0	AOCS_Ore-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	03 00 00 00 00				
2012/05/18	18:17:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/05/18	18:18:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/05/18	18:18:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/05/18	18:18:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/05/18	18:18:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/05/18	18:18:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/18	18:18:26.0	XRT_QT_PROG_SET_415_OG [0x19f]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2012/05/18	18:18:28.0	XRT_FL_PROG_SET_449_OG [0x1c1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2012/05/18	18:18:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/18	19:00:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/18	19:00:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/18	19:00:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/18	19:03:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/18	19:42:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/18	19:43:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/18	20:39:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/18	20:39:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/18	20:39:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/18	20:42:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/18	21:19:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/18	21:20:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/18	22:17:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/18	22:17:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/18	22:17:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/18	22:20:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/18	22:55:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/05/18	22:56:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/05/18	23:56:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/05/18	23:56:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/05/18	23:56:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/05/18	23:59:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				

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2012/05/19	00:24:00.0	XRT_Custom_418_OG [0x1a2]			
2012/05/19	00:25:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/19	01:34:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/19	01:34:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/19	01:34:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/19	01:37:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/19	02:02:30.0	XRT_Custom_418_OG [0x1a2]			
2012/05/19	02:03:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/19	03:10:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/19	03:10:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/19	03:10:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/19	03:13:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/19	03:41:00.0	XRT_Custom_418_OG [0x1a2]			
2012/05/19	03:42:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/19	04:40:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/19	04:40:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/19	04:40:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/19	04:43:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/19	05:19:30.0	XRT_Custom_418_OG [0x1a2]			
2012/05/19	05:20:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/19	05:52:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/19	05:52:56.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/05/19	05:53:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2012/05/19	05:53:16.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2012/05/19	05:53:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/05/19	05:53:20.0	XRT_ARS_DIS_438_OG [0x1b6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/05/19	05:55:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2012/05/19	05:56:00.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/19	06:02:54.0	XRT_CTRL_MANU_439_OG [0x1b7]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/19	06:03:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]			
		AOCU_NM	5	02-76	03 00 00 00 00
2012/05/19	06:05:26.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/05/19	06:05:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2012/05/19	06:05:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/05/19	06:05:50.0	XRT_AEC_RESET_443_OG [0x1bb]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2012/05/19	06:05:52.0	XRT_ARS_DIS_431_OG [0x1af]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2012/05/19	06:05:54.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/19	06:05:56.0	XRT_QT_PROG_SET_415_OG [0x19f]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2012/05/19	06:05:58.0	XRT_FL_PROG_SET_449_OG [0x1c1]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2012/05/19	06:06:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/19	06:21:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/19	06:21:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/19	06:21:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/19	06:24:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/05/19	06:58:00.0	XRT_Custom_418_OG [0x1a2]			
2012/05/19	06:59:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/05/19	08:01:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/05/19	08:01:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2012/05/19	08:01:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/05/19	08:04:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9

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2012/05/19	08:10:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]	AOCU_NM	5	02-76	00	56	a7	01	26
2012/05/19	08:36:54.0	XRT_CTRL_MANU_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0			c1		
2012/05/19	08:36:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/05/19	08:37:16.0	XRT_FLD_ENA_405_OG [0x195]	MDP_XRT_FLD_ENA	1	07-F0			d8		
2012/05/19	08:37:18.0	XRT_FLRCTRL_ENA_440_OG [0x1b8]	MDP_XRT_FLRCTRL_ENA	1	07-F0			c8		
2012/05/19	08:37:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0			d0		
2012/05/19	08:37:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0			d5		
2012/05/19	08:37:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0			da		
2012/05/19	08:37:26.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0			c4	0a	
2012/05/19	08:37:28.0	XRT_FL_PROG_SET_449_OG [0x1c1]	MDP_XRT_FL_PROG_SET	2	07-F0			c5	0d	
2012/05/19	08:37:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0			c0		
2012/05/19	09:39:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0			c1		
2012/05/19	10:16:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00	00	00	00