

XRT Timeline to be uploaded on 2012/09/04

Period: 2012/09/04 10:07:00 - 2012/09/08 10:57:00

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Normal mode

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XOB #192A: AR Standard-A(Filter-Ratio) with PFB, 384x384 at 1064 1048, shorter thin-Be, thick Al and Al/Poly context, With G-band (45ms), 150s cad

Term	Pointing (x, y)	Comment
09/04 10:20:00 - 09/04 13:59:54	Track (583.0, -90.2) ^{Ⓢ 09/04 10:17:00}	# OP start + 10min, HOP 213, AR 11560
09/04 14:03:00 - 09/04 16:42:30	Track (-357.8, -397.3) ^{Ⓢ 09/04 14:00:00}	AR 11564 tracking
09/05 07:25:00 - 09/05 08:39:30	Track (-217.0, -403.5) ^{Ⓢ 09/05 07:10:00}	AR 11564 tracking
09/05 09:03:00 - 09/05 13:59:54	Track (733.7, -71.9) ^{Ⓢ 09/05 09:00:00}	HOP 213, AR 11560
09/05 14:03:00 - 09/05 17:59:54	Track (-159.1, -405.1) ^{Ⓢ 09/05 14:00:00}	AR 11564 tracking
09/06 09:03:00 - 09/06 09:18:00	Track (854.1, -48.5) ^{Ⓢ 09/06 09:00:00}	HOP 213, AR 11560

PROG= 19 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec										
Seqn= 19 2-time(s) 2.0sec										
Open/G-band Open/G-band close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2 2-time(s) 2.0sec										
Seqn= 18 1-time(s) 2.0sec										
Open/Ti-poly Open/thick-Al close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band Open/G-band open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 32 4-time(s) 2.0sec										
Al-poly/Open thin-Be/Open close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly Open/thick-Be close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open med-Be/Open close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al Open/thick-Al close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 33 30-time(s) 150.0sec										
thin-Be/Open Open/thick-Al close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly Open/thick-Be close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec
thin-Be/Open Open/thick-Al close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly Open/thick-Be close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
thin-Be/Open Open/thick-Al close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly Open/thick-Be close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
thin-Be/Open Open/thick-Al close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly Open/thick-Be close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #1926: Synoptic Q95 2x2 - Al/mesh(44/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(88/1443) + Thin-Be(250)

Term	Pointing (x, y)	Comment
09/04 17:10:00 - 09/04 17:16:54	Fixed (0.0, 0.0)	synoptic, shifted (30 min earlier)
09/05 06:03:00 - 09/05 07:00:00	Fixed (0.0, 0.0)	synoptic, extended 1 hr for SOT engineering
09/05 18:03:00 - 09/05 18:09:54	Fixed (0.0, 0.0)	synoptic
09/06 05:33:00 - 09/06 05:39:54	Fixed (0.0, 0.0)	synoptic, shifted -30.0 min

PROG= 05 1-time(s)

Subr= 1 1-time(s) 12.0sec										
Seqn= 50 1-time(s) 4.0sec										
Open/Al-mesh Open/Al-mesh close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh Open/Al-mesh close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 6 1-time(s) 2.0sec										
Open/Ti-poly Open/thick-Al close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly Open/thick-Al close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly Open/thick-Al close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly Open/thick-Al close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly Open/thick-Al close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 51 1-time(s) 4.0sec										
Open/Ti-poly Open/Ti-poly close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly Open/Ti-poly close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54 1-time(s) 2.0sec										
thin-Be/Open thin-Be/Open close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open thin-Be/Open close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 8 1-time(s) 2.0sec										
Open/G-band Open/G-band open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #162D: Full Sun - CME Watch - C/Poly + Thin-Be + Med-Be - 4x4 - AEC2

Term	Pointing (x, y)	Comment
09/04 17:20:00 - 09/04 21:16:54	Fixed (-945.0, 0.0)	CoMP cross-calib. (EIS)

PROG= 03 Inf.-time(s)

Subr= 1 4-time(s) 480.0sec										
Seqn= 48 1-time(s) 60.0sec										
C-poly/Open med-Be/Open close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Seqn= 46 1-time(s) 60.0sec										
thin-Be/Open med-Be/Open close	Safe	Norm	4.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Seqn= 45 1-time(s) 60.0sec										
med-Be/Open med-Be/Open close	Safe	Norm	22.6s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Subr= 2 1-time(s) 60.0sec										
Seqn= 49 1-time(s) 30.0sec										

C-poly/Open	C-poly/Open	close	Safe	Dark	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1923: AR Standard-A(Filter-Ratio) with PFB, 384x384 at 1064 1048, shorter thin-Be, thick Al and Al/Poly context, With G-band (45ms), 60s cad

Term	Pointing (x, y)	Comment
09/04 21:20:00 - 09/05 05:59:54	Track (-299.0, -400.3) @ 09/04 21:17:00	AR 11564 tracking
09/05 18:13:00 - 09/06 05:29:54	Track (-123.4, -405.9) @ 09/05 18:10:00	AR 11564 tracking
09/06 05:43:00 - 09/06 08:59:54	Track (-24.0, -407.0) @ 09/06 05:40:00	AR 11564 tracking

PROG= 17 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec												
Seqn= 19 2-time(s) 2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2 2-time(s) 2.0sec												
Seqn= 18 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 32 4-time(s) 2.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 33 30-time(s) 60.0sec												
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops

Term	Pointing (x, y)	Comment
09/04 10:20:00 - 09/04 13:59:54	Track (583.0, -90.2) @ 09/04 10:17:00	# OP start + 10min, HOP 213, AR 11560
09/04 14:03:00 - 09/04 16:42:30	Track (-357.8, -397.3) @ 09/04 14:00:00	AR 11564 tracking
09/04 17:20:00 - 09/04 21:16:54	Fixed (-945.0, 0.0)	CoMP cross-calib. (EIS)
09/04 21:20:00 - 09/05 05:59:54	Track (-299.0, -400.3) @ 09/04 21:17:00	AR 11564 tracking
09/05 07:25:00 - 09/05 08:39:30	Track (-217.0, -403.5) @ 09/05 07:10:00	AR 11564 tracking
09/05 09:03:00 - 09/05 13:59:54	Track (733.7, -71.9) @ 09/05 09:00:00	HOP 213, AR 11560
09/05 14:03:00 - 09/05 17:59:54	Track (-159.1, -405.1) @ 09/05 14:00:00	AR 11564 tracking
09/05 18:13:00 - 09/06 05:29:54	Track (-123.4, -405.9) @ 09/05 18:10:00	AR 11564 tracking
09/06 05:43:00 - 09/06 08:59:54	Track (-24.0, -407.0) @ 09/06 05:40:00	AR 11564 tracking
09/06 09:03:00 - 09/06 09:18:00	Track (854.1, -48.5) @ 09/06 09:00:00	HOP 213, AR 11560

PROG= 16 15-time(s)

Subr= 1 45-time(s) 10.0sec												
Seqn= 35 1-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 10.0sec												
Seqn= 36 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 37 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 38 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

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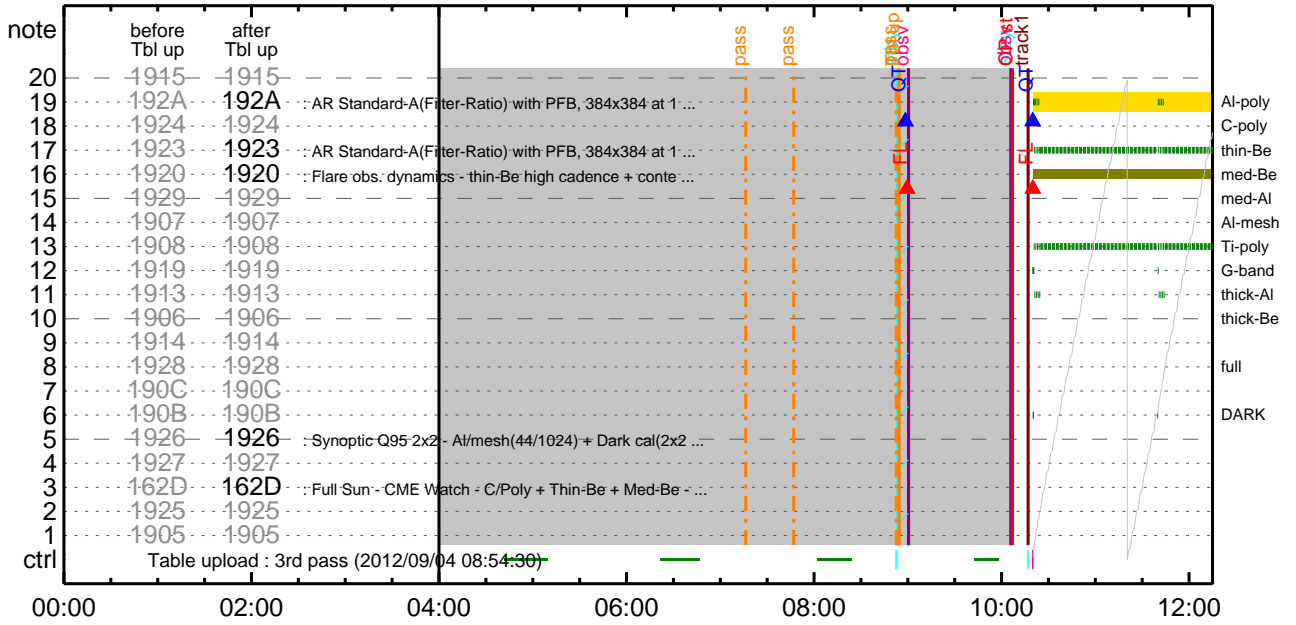
Flare Detection

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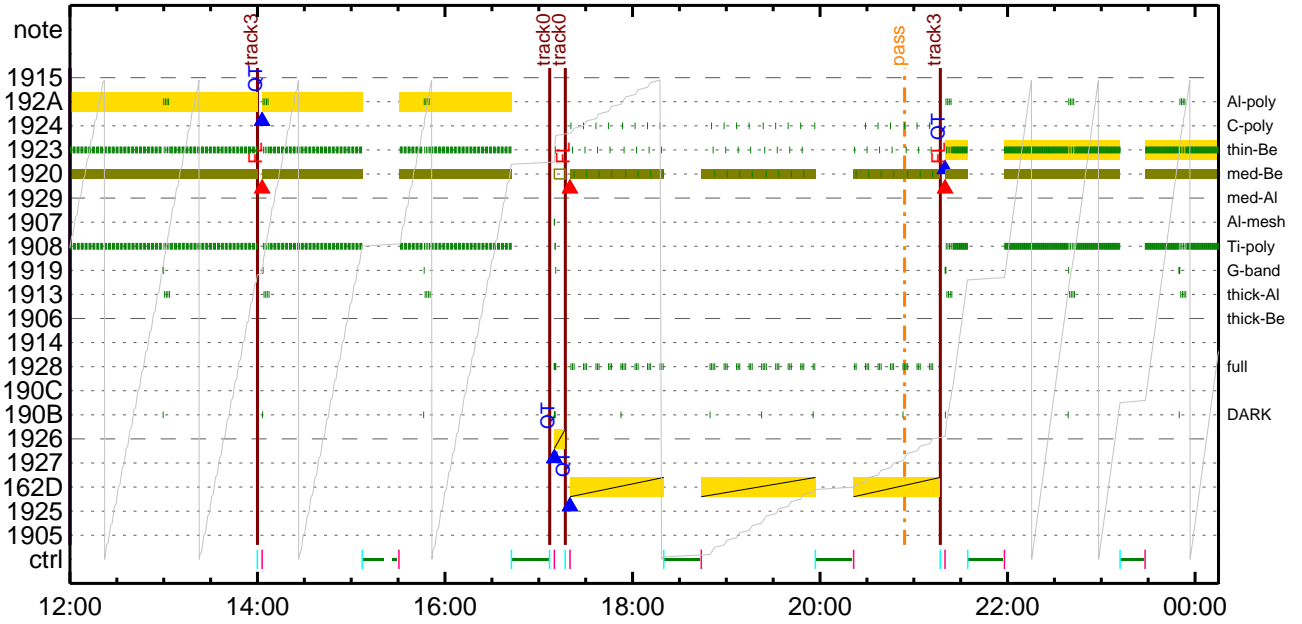
FLD Patrol

Term	Pointing (x, y)	Comment										
09/04 17:19:46 - 09/05 06:00:16	Fixed (-945.0, 0.0)	CoMP cross-calib. (EIS)										
09/05 07:12:46 - 09/05 18:00:16	Track (-217.0, -403.5) @ 09/05 07:10:00	AR 11564 tracking										
09/05 18:12:46 - 09/06 05:30:16	Track (-123.4, -405.9) @ 09/05 18:10:00	AR 11564 tracking										
09/06 05:42:46 - 09/08 10:57:00	Track (-24.0, -407.0) @ 09/06 05:40:00	AR 11564 tracking										
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50			30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

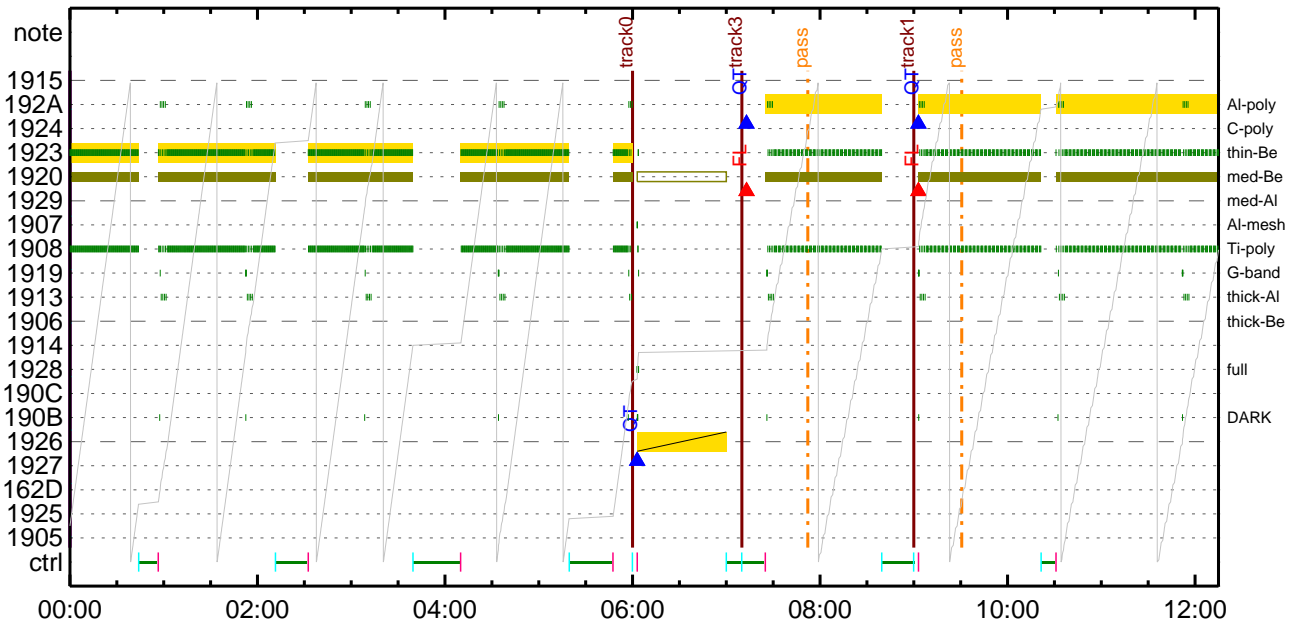
CMDI #0893 2012/09/04



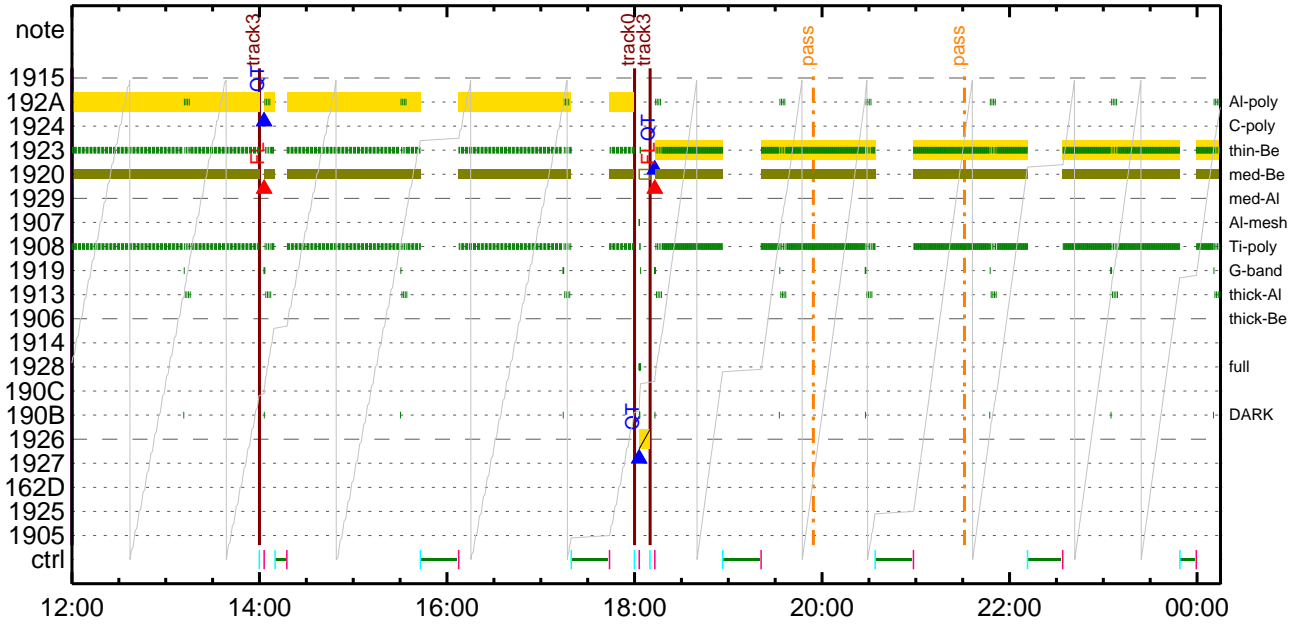
CMDI #0893 2012/09/04



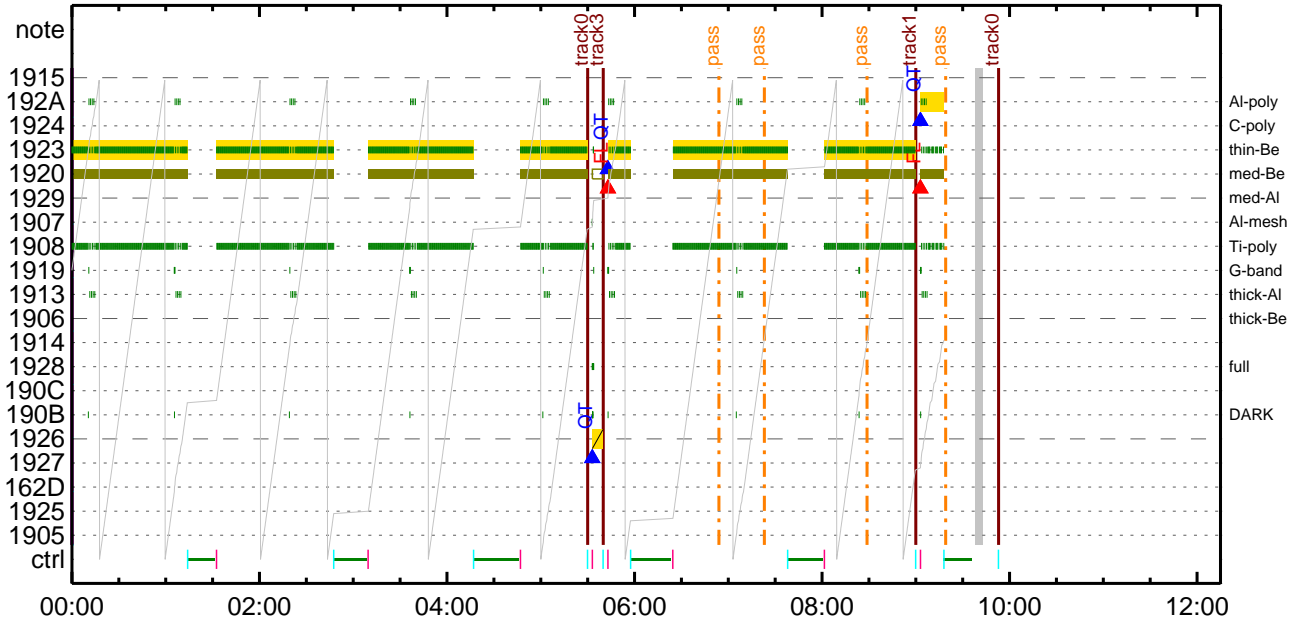
CMDI #0893 2012/09/05



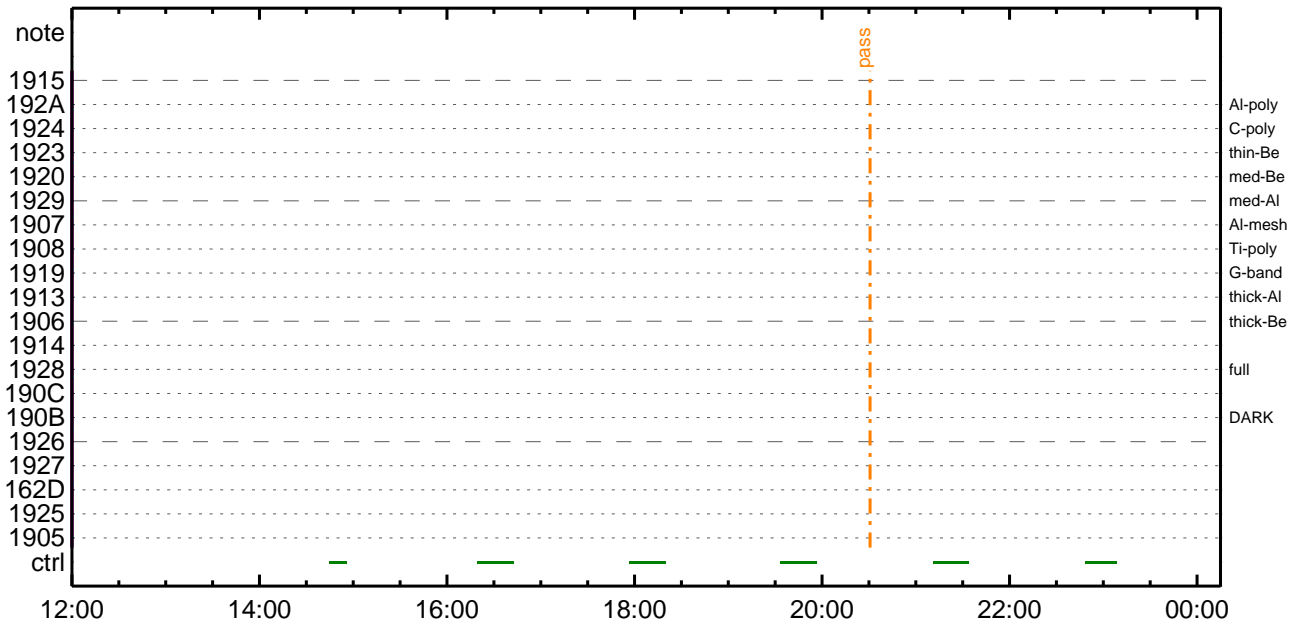
CMDI #0893 2012/09/05



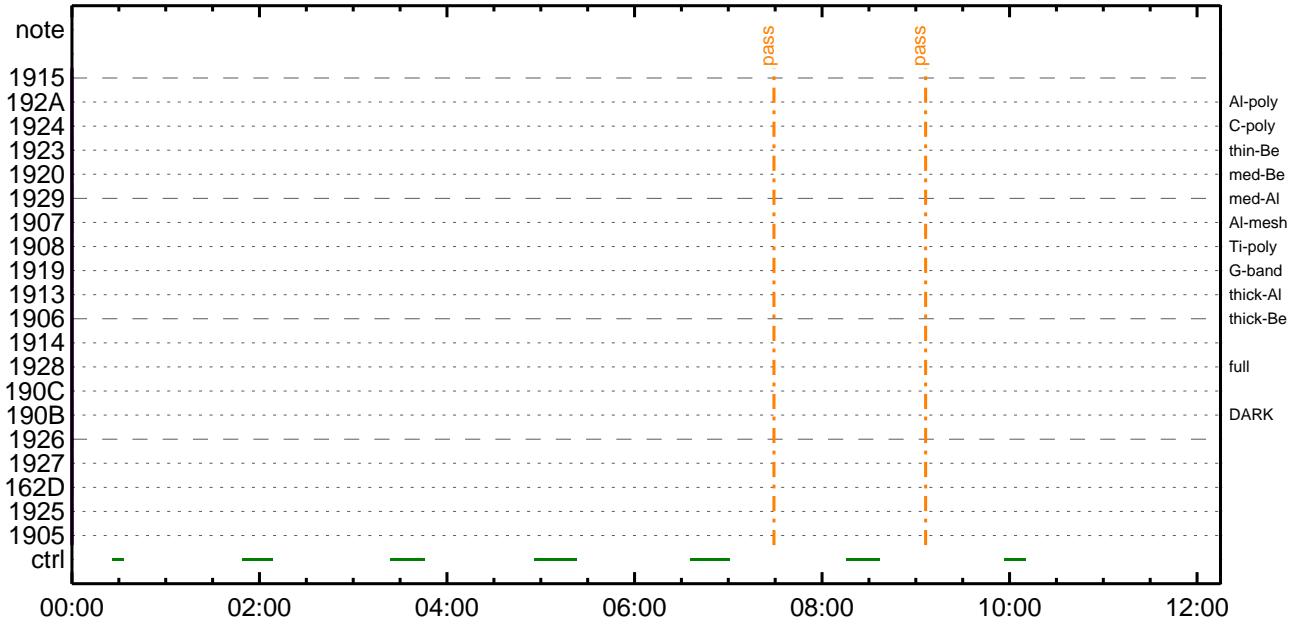
CMDI #0893 2012/09/06



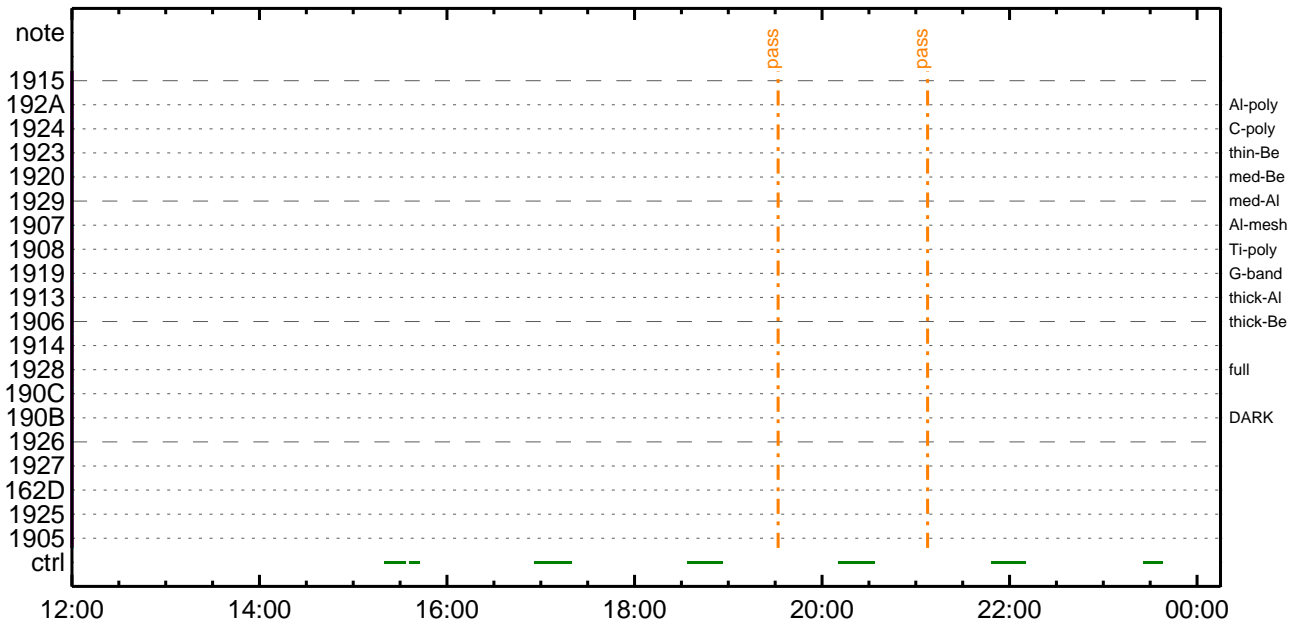
CMDI #0893 2012/09/06



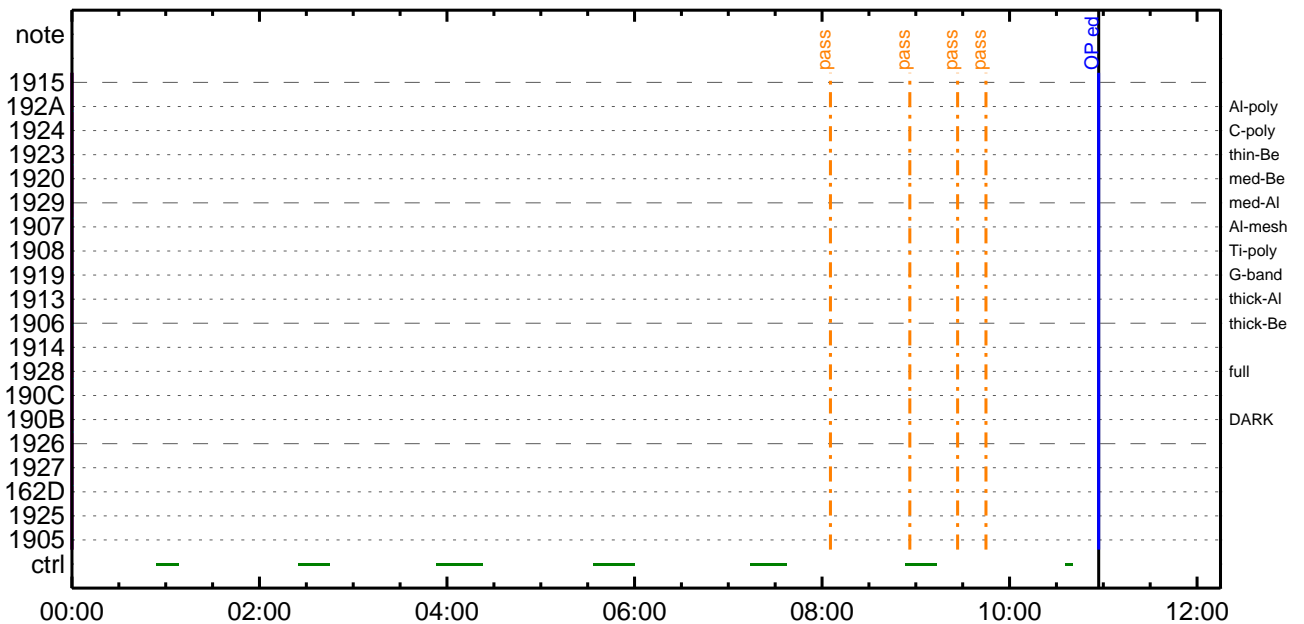
CMDI #0893 2012/09/07



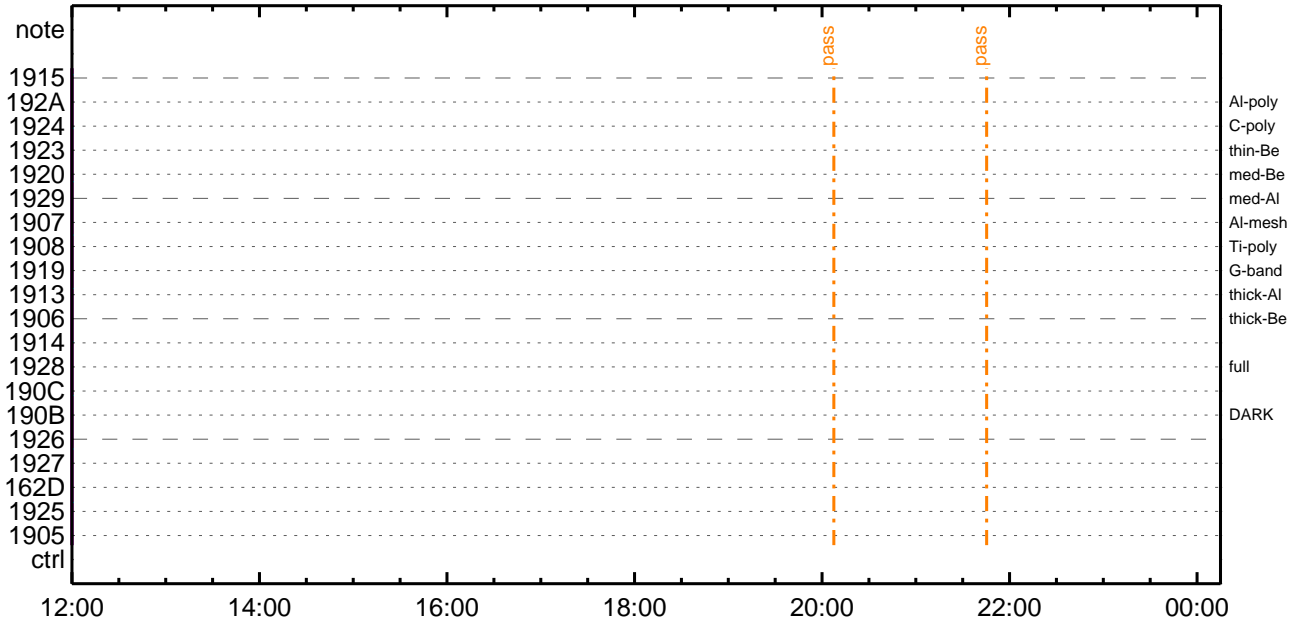
CMDI #0893 2012/09/07



CMDI #0893 2012/09/08



CMDI #0893 2012/09/08




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-122:OP
0104 ( )
0105 S. OG og-122:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½Ä´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOXx½ê½çòðÁÓÆòÇ¼ª°¬òE¼î¹çòÇòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½î¹ç;ç°E²¼òî½TI-CMDÁ÷¿@²î½Á¹Ôª°¬E²ò³òE;f
0180 C. ²ò³òE;çSET²EEDUMP²î½±°îYÑY¹ç¹Ôª|²³òE;f
0181 C.
0182 C. TIY³Y½Y½E²òðÁDî¿(UT)
0183 +. TI 2012-09-04 10:02:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2012-09-04 10:02:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2012-09-04 10:02:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 . C. < Dump RAMID=MDP_DPL >
0196 +. DC 07-F0 MDP_DUMP_FGTBL
0197 BC (82 07 00 38 b8 00 40)
0198 C. -----
0199 C. MDP_DPL verify = OK [ ]
0200 C. -----
0201 C.
0202 C. STS_CHK=ON=
0203 C.
0204 . C. < Update MDP DSC PAR1 >
0205 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0206 BC (4c)
0207 C. MDP_CMD_CODE = F04C0700[ ]
0208 C. MDP_CMD_CNT (count-up 1) [ ]
0209 C. -----
0210 C.
0211 . C.
0212 . C. < Resume FG table (auto mode) >
0213 +. DC 07-F0 MDP_FG_CTRL_AUTO
0214 BC (50)
0215 . C. -----
0216 C. MDP_FG_CTRL_MODE = AUTO [ ]
0217 C. -----
0218 C.
0219 C. *****
0220 C. SOT TI command set
0221 C. *****
0222 C. Execute, after the success of TBL upload.
0223 +. TI 2012-09-04 10:06:18.0
0224 DC 07-F0 MDP_SOT_MODE_OBSV
0225 BC (40)
0226 . C. -----
0227 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0228 C. -----
0229 C.
0230 C. Only when FG_CTRL_AUTO is used in RT.
0231 +. TI 2012-09-04 10:06:20.0
0232 DC 07-F0 MDP_FG_CTRL_AUTO
0233 BC (50)
0234 . C. -----
0235 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0236 C. -----
0237 C. ***** SOT END *****
0238 C.
0239 . C. ***** MDP `uãîï»ö%ýðÉâð¹²³DCBC•x²è *****
0240 C. (%ã°îÿÓÿÃÿÈÿpÿËÿáÿçÿè³¼½¾»Û¹²è)
0241 . S. DC-BC dcbc-402:DCBC
0242 (MDP_known_event)
0243 C.
0244 C.
0245 . C. ***** ÿÐÿ¹•Ï Daily±¿ÍñðÉ´Ø¹²³DCBC•x²è *****
0246 . S. DC-BC dcbc-153:DCBC
0247 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0248 C.
0249 C.
0250 . C. ;ãLOSÿÁÿ§ÿÃÿ-¼Â»Û;ä
0251 C.
0252 . C. ***** LOS *****
0253 C.

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*** OP Sequence for XRT ***

2012/09/04	10:16:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/04	10:17:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2012/09/04	10:19:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/09/04	10:19:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/09/04	10:19:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/09/04	10:19:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/09/04	10:19:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/09/04	10:19:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/04	10:19:56.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2012/09/04	10:19:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/09/04	10:20:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/04	13:59:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/04	14:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2012/09/04	14:02:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/09/04	14:02:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/09/04	14:02:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/09/04	14:02:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/09/04	14:02:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/09/04	14:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/04	14:02:56.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2012/09/04	14:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/09/04	14:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/04	15:07:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/04	15:07:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/04	15:07:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/04	15:10:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/04	15:29:30.0	XRT_Custom_434_OG [0x1b2]							
2012/09/04	15:30:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/04	16:42:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/04	16:42:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/04	16:42:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/04	16:45:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/04	17:06:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/04	17:06:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/09/04	17:07:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/09/04	17:07:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/09/04	17:07:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/09/04	17:07:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/09/04	17:09:58.0	XRT_QT_PROG_SET_441_OG [0x1b9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2012/09/04	17:10:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/04	17:16:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/04	17:17:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 00 00 54 00				
2012/09/04	17:19:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/09/04	17:19:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/09/04	17:19:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/09/04	17:19:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				

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2012/09/04	17:19:52.0	XRT_ARS_DIS_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/09/04	17:19:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/04	17:19:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
2012/09/04	17:19:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2012/09/04	17:20:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/04	18:20:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/04	18:20:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/04	18:20:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/04	18:23:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/04	18:43:00.0	XRT_Custom_434_OG [0x1b2]					
2012/09/04	18:44:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/04	19:57:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/04	19:57:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/04	19:57:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/04	20:00:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/04	20:20:30.0	XRT_Custom_434_OG [0x1b2]					
2012/09/04	20:21:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/04	21:16:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/04	21:17:00.0	AOCS_Or-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	03 00 00 00 00	
2012/09/04	21:19:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2012/09/04	21:19:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/09/04	21:19:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/09/04	21:19:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/09/04	21:19:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/09/04	21:19:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/04	21:19:56.0	XRT_QT_PROG_SET_411_OG [0x19b]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11
2012/09/04	21:19:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10
2012/09/04	21:20:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/04	21:34:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/04	21:34:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/04	21:34:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/04	21:37:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/04	21:57:00.0	XRT_Custom_434_OG [0x1b2]					
2012/09/04	21:58:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/04	23:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/04	23:12:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/04	23:12:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/04	23:15:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/04	23:27:00.0	XRT_Custom_434_OG [0x1b2]					
2012/09/04	23:28:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/05	00:44:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/05	00:44:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/05	00:44:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/09/05	00:47:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/09/05	00:55:30.0	XRT_Custom_434_OG [0x1b2]					
2012/09/05	00:56:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/09/05	02:11:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/09/05	02:11:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/09/05	02:11:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	

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2012/09/05	02:14:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/05	02:31:30.0	XRT_Custom_434_OG [0x1b2]							
2012/09/05	02:32:30.5	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/05	03:39:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/05	03:39:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/05	03:39:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/05	03:42:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/05	04:09:00.0	XRT_Custom_434_OG [0x1b2]							
2012/09/05	04:10:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/05	05:19:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/05	05:19:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/05	05:19:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/05	05:22:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/05	05:46:30.0	XRT_Custom_434_OG [0x1b2]							
2012/09/05	05:47:30.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/05	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/05	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2012/09/05	06:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2012/09/05	06:00:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/09/05	06:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/09/05	06:00:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/09/05	06:02:58.0	XRT_QT_PROG_SET_441_OG [0x1b9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2012/09/05	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/05	07:00:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/05	07:00:02.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/05	07:00:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/05	07:03:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/05	07:09:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/05	07:10:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2012/09/05	07:12:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/09/05	07:12:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/09/05	07:12:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/09/05	07:12:50.0	XRT_AEC_RESET_423_OG [0x1a7]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/09/05	07:12:52.0	XRT_ARS_DIS_438_OG [0x1b6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/09/05	07:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/05	07:12:56.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2012/09/05	07:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 10				
2012/09/05	07:24:00.0	XRT_Custom_434_OG [0x1b2]							
2012/09/05	07:25:00.0	XRT_CTRL_AUTO_413_OG [0x19d]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/05	08:39:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/05	08:39:32.0	XRT_FLD_RESET_424_OG [0x1a8]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/05	08:39:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/05	08:42:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/05	08:59:54.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/05	09:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2012/09/05	09:02:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2012/09/05	09:02:46.0	XRT_FLD_ENA_428_OG [0x1ac]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/09/05	09:02:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/09/05	09:02:50.0	XRT_AEC_RESET_423_OG [0x1a7]							

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2012/09/05	09:02:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_AEC_RESET	1	07-F0	d0				
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/09/05	09:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/05	09:02:56.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	13			
2012/09/05	09:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2012/09/05	09:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/05	10:21:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/05	10:21:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/05	10:21:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/05	10:24:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/05	10:30:00.0	XRT_Custom_434_OG [0x1b2]								
2012/09/05	10:31:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/05	13:59:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/05	14:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	03	00	00	00	00
2012/09/05	14:02:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/09/05	14:02:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/09/05	14:02:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/09/05	14:02:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/09/05	14:02:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/09/05	14:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/05	14:02:56.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	13			
2012/09/05	14:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	10			
2012/09/05	14:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/05	14:10:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/05	14:10:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/05	14:10:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/05	14:13:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/05	14:16:30.0	XRT_Custom_434_OG [0x1b2]								
2012/09/05	14:17:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/05	15:43:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/05	15:43:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/05	15:43:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/05	15:46:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/05	16:06:30.0	XRT_Custom_434_OG [0x1b2]								
2012/09/05	16:07:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/05	17:19:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/05	17:19:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/09/05	17:19:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/09/05	17:22:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/09/05	17:43:00.0	XRT_Custom_434_OG [0x1b2]								
2012/09/05	17:44:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/09/05	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/09/05	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2012/09/05	18:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00	00	00	00
2012/09/05	18:00:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/09/05	18:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/09/05	18:00:20.0	XRT_ARS_DIS_406_OG [0x196]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/09/05	18:02:58.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			
2012/09/05	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				

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2012/09/05	18:09:54.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/05	18:10:00.0	AOCs_Or-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	03 00 00 00 00
2012/09/05	18:12:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/09/05	18:12:46.0	XRT_FLD_ENA_428_OG [0x1ac]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/09/05	18:12:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/09/05	18:12:50.0	XRT_AEC_RESET_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/09/05	18:12:52.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/09/05	18:12:54.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/09/05	18:12:56.0	XRT_QT_PROG_SET_411_OG [0x19b]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2012/09/05	18:12:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 10
2012/09/05	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/09/05	18:56:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/05	18:56:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/09/05	18:56:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/09/05	18:59:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/09/05	19:20:00.0	XRT_Custom_434_OG [0x1b2]				
2012/09/05	19:21:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/09/05	20:34:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/05	20:34:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/09/05	20:34:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/09/05	20:37:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/09/05	20:57:30.0	XRT_Custom_434_OG [0x1b2]				
2012/09/05	20:58:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/09/05	22:11:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/05	22:11:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/09/05	22:11:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/09/05	22:14:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/09/05	22:33:00.0	XRT_Custom_434_OG [0x1b2]				
2012/09/05	22:34:00.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/09/05	23:49:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/05	23:49:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/09/05	23:49:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/09/05	23:52:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/09/05	23:58:30.0	XRT_Custom_434_OG [0x1b2]				
2012/09/05	23:59:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/09/06	01:14:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/06	01:14:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/09/06	01:14:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/09/06	01:17:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/09/06	01:31:30.0	XRT_Custom_434_OG [0x1b2]				
2012/09/06	01:32:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/09/06	02:47:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/06	02:47:32.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/09/06	02:47:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/09/06	02:50:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/09/06	03:08:30.0	XRT_Custom_434_OG [0x1b2]				
2012/09/06	03:09:30.0	XRT_CTRL_AUTO_413_OG [0x19d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/09/06	04:17:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/06	04:17:02.0	XRT_FLD_RESET_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/09/06	04:17:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]				

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2012/09/06	04:20:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/09/06	04:46:00.0	XRT_Custom_434_OG [0x1b2]						
2012/09/06	04:47:00.0	XRT_CTRL_AUTO_413_OG [0x19d]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/09/06	05:29:54.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/09/06	05:29:56.0	XRT_FOCUS_POSITION_403_OG [0x193]						
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2012/09/06	05:30:00.5	AOCS_ORe-point_Start_3_OG [0x099]						
			AOCU_NM	5	02-76	00 00 00 00 00		
2012/09/06	05:30:16.0	XRT_FLD_DIS_404_OG [0x194]						
			MDP_XRT_FLD_DIS	1	07-F0	d9		
2012/09/06	05:30:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]						
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2012/09/06	05:30:20.0	XRT_ARS_DIS_406_OG [0x196]						
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/09/06	05:32:58.0	XRT_QT_PROG_SET_441_OG [0x1b9]						
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 05		
2012/09/06	05:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/09/06	05:39:54.0	XRT_CTRL_MANU_447_OG [0x1bf]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/09/06	05:40:00.0	AOCS_ORe-point_Start_2_OG [0x098]						
			AOCU_NM	5	02-76	03 00 00 00 00		
2012/09/06	05:42:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]						
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2012/09/06	05:42:46.0	XRT_FLD_ENA_428_OG [0x1ac]						
			MDP_XRT_FLD_ENA	1	07-F0	d8		
2012/09/06	05:42:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]						
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2012/09/06	05:42:50.0	XRT_AEC_RESET_423_OG [0x1a7]						
			MDP_XRT_AEC_RESET	1	07-F0	d0		
2012/09/06	05:42:52.0	XRT_ARS_DIS_438_OG [0x1b6]						
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/09/06	05:42:54.0	XRT_FLD_RESET_424_OG [0x1a8]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/09/06	05:42:56.0	XRT_QT_PROG_SET_411_OG [0x19b]						
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 11		
2012/09/06	05:42:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]						
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 10		
2012/09/06	05:43:00.5	XRT_CTRL_AUTO_408_OG [0x198]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/09/06	05:57:30.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/09/06	05:57:32.0	XRT_FLD_RESET_424_OG [0x1a8]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/09/06	05:57:34.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/09/06	06:00:44.0	XRT_PREFLR_STOP_433_OG [0x1b1]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/09/06	06:23:30.0	XRT_Custom_434_OG [0x1b2]						
2012/09/06	06:24:30.5	XRT_CTRL_AUTO_413_OG [0x19d]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/09/06	07:38:00.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/09/06	07:38:02.0	XRT_FLD_RESET_424_OG [0x1a8]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/09/06	07:38:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2012/09/06	07:41:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2012/09/06	08:00:30.0	XRT_Custom_434_OG [0x1b2]						
2012/09/06	08:01:30.0	XRT_CTRL_AUTO_413_OG [0x19d]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/09/06	08:59:54.0	XRT_CTRL_MANU_447_OG [0x1bf]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/09/06	09:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]						
			AOCU_NM	5	02-76	01 00 00 00 00		
2012/09/06	09:02:26.0	XRT_FOCUS_POSITION_420_OG [0x1a4]						
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2012/09/06	09:02:46.0	XRT_FLD_ENA_428_OG [0x1ac]						
			MDP_XRT_FLD_ENA	1	07-F0	d8		
2012/09/06	09:02:48.0	XRT_FLRCTRL_ENA_429_OG [0x1ad]						
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2012/09/06	09:02:50.0	XRT_AEC_RESET_423_OG [0x1a7]						
			MDP_XRT_AEC_RESET	1	07-F0	d0		
2012/09/06	09:02:52.0	XRT_ARS_DIS_438_OG [0x1b6]						
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2012/09/06	09:02:54.0	XRT_FLD_RESET_424_OG [0x1a8]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/09/06	09:02:56.0	XRT_QT_PROG_SET_410_OG [0x19a]						
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 13		
2012/09/06	09:02:58.0	XRT_FL_PROG_SET_444_OG [0x1bc]						
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 10		
2012/09/06	09:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2012/09/06	09:18:00.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2012/09/06	09:18:02.0	XRT_FLD_RESET_424_OG [0x1a8]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2012/09/06	09:18:04.0	XRT_PREFLR_STRT_432_OG [0x1b0]						

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2012/09/06	09:21:14.0	XRT_PREFLR_STOP_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/09/06	09:52:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/09/06	09:53:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00