XRT Timeline to be uploaded on 2012/12/06

Period: 2012/12/06 10:28:00 - 2012/12/11 10:17:00

* * * * * **Normal mode** * * * * *

XOB #190F: HOP81 2-filter - Ti/poly 8s, Al/mesh 4s, G-band - 384x384 45ms

Term	Pointing (x, y)	Comment
12/06 10:41:00 - 12/06 14:10:00	Fixed (0.0, 940.0)	# OP start + 10min, streamer study, N pole.

PROG= 11 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 61 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 62 1-time(s) 30.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec

Subr= 3 30-time(s) 2.0sec

Seqn= 60 2-time(s) 30.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec

XOB #192F: Synoptic Q95 2x2 - Al/mesh(33/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(64/1443) + Thin-Be(181

Term	Pointing (x, y)	Comment
12/06 18:03:00 - 12/06 18:09:54	Fixed (0.0, 0.0)	synoptic
12/07 06:20:30 - 12/07 06:27:24	Fixed (0.0, 0.0)	synoptic, shifted 17.5 min
12/07 18:07:00 - 12/07 18:13:54	Fixed (0.0, 0.0)	synoptic, shifted 4.0 min
12/08 05:54:00 - 12/08 06:00:54	Fixed (0.0, 0.0)	synoptic, shifted -9.0 min

PROG= 13 1-time(s)

Subr= 1 1-time(s) 14.0sec

Seqn= 64 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 6 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 70 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 67 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 69 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 68 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec

XOB #1732: Dark - Med-Al+Thick-Be - 8x8 - 512x512

Term	Pointing (x, y)	Comment
12/06 18:13:00 - 12/06 20:18:00	Track (-353.8, 202.1) @ 12/06 18:10:00	# Returning active area for EIS.
12/07 18:17:00 - 12/07 20:43:54	Track (-140.4, 204.2) @ 12/07 18:14:00	# Returning active area obs for EIS.

PROG= 10 1-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 81 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
med-Al/thick-Be	med-Al/thick-Be	close	Safe	Dark	500ms	Obs	8x8	512x512 (1024, 1024)	DPCM	0	0	2.0sec

XOB #1937: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 120s-cad w/ G-Band VLS Closed Test

Term	Pointing (x, y)	Comment
12/06 20:43:00 - 12/07 06:17:24	Track (653.4, 122.7) @ 12/06 20:40:00	* AR 11623.
12/07 20:47:00 - 12/08 05:50:54	Track (800.3, 124.1) @ 12/07 20:44:00	* Track AR 11623.
12/08 06:04:00 - 12/08 09:35:00	Track (845.7, 124.4) @ 12/08 06:01:00	# Cont.

PROG= 03 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 18 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Seqn= 19 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec

Seqn= 65 4-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Al-poly/Open	Al-poly/thick-Be	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 73 70-time(s) 120.0sec

thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	12.5sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	12.5sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	12.5sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	12.5sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #18B5: HOP201 thin-Be, Ti/Poly, 512x512 at 1064 1048, 120s cad

Term	Pointing (x, y)	Comment
12/07 06:30:30 - 12/07 17:40:30	Fixed (-895.0, -400.0)	# ToO HOP 201, CME heating.

PROG= 14 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 63 1-time(s) 2.0sec

Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec

Seqn= 89 30-time(s) 120.0sec

thin-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

Flare mode

XOB #1920: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops

Term	Pointing (x, y)	Comment
12/06 10:41:00 - 12/06 14:10:00	Fixed (0.0, 940.0)	# OP start + 10min, streamer study, N pole.
12/06 18:13:00 - 12/06 20:18:00	Track (-353.8, 202.1) @ 12/06 18:10:00	# Returning active area for EIS.
12/06 20:43:00 - 12/07 06:17:24	Track (653.4, 122.7) @ 12/06 20:40:00	* AR 11623.
12/07 06:30:30 - 12/07 17:40:30	Fixed (-895.0, -400.0)	# ToO HOP 201, CME heating.
12/07 18:17:00 - 12/07 20:43:54	Track (-140.4, 204.2) @ 12/07 18:14:00	# Returning active area obs for EIS.
12/07 20:47:00 - 12/08 05:50:54	Track (800.3, 124.1) @ 12/07 20:44:00	* Track AR 11623.
12/08 06:04:00 - 12/08 09:35:00	Track (845.7, 124.4) @ 12/08 06:01:00	# Cont.

PROG= 16 15-time(s)

Subr= 1 45-time(s) 10.0sec

Seqn= 35 1-time(s) 2.0sec

thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Subr= 2 1-time(s) 10.0sec

Seqn= 36 1-time(s) 2.0sec

med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Seqn= 37 1-time(s) 2.0sec

Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec

Seqn= 38 1-time(s) 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

Active Region Search

NOT USED

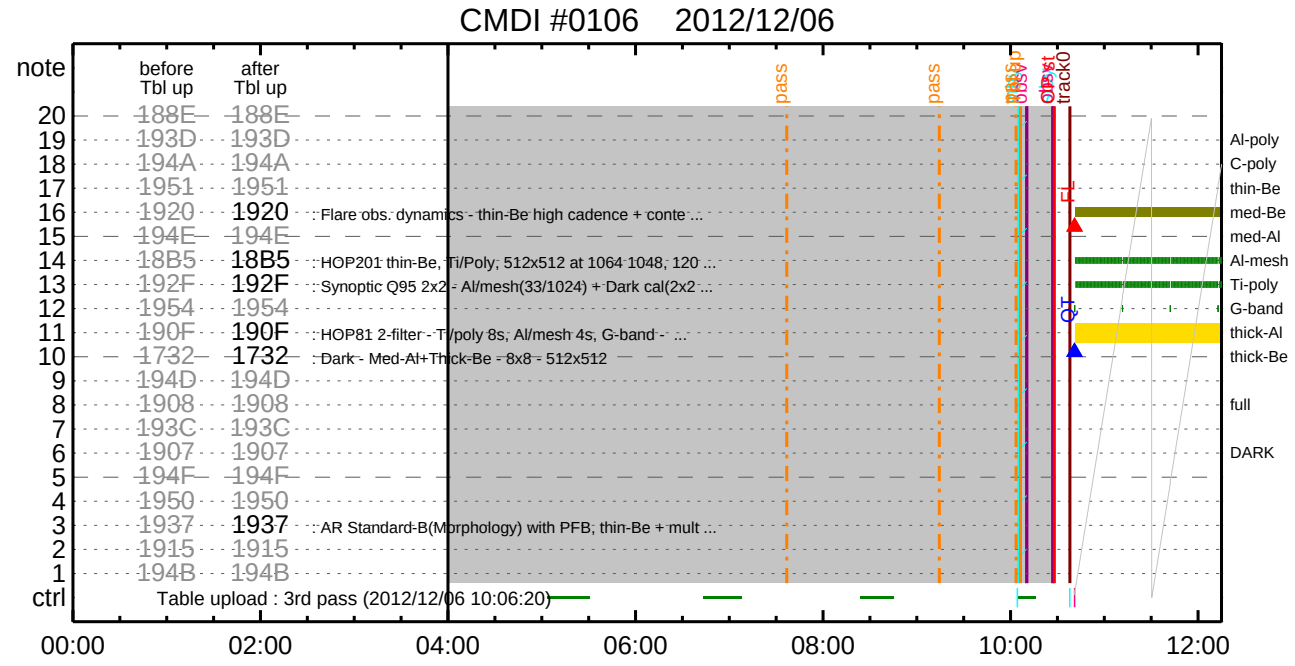
Flare Detection

FLD Patrol

Term	Pointing (x, y)	Comment
12/06 10:40:46 - 12/06 18:00:16	Fixed (0.0, 940.0)	# OP start + 10min, streamer study, N pole.
12/06 18:12:46 - 12/07 06:17:46	Track (-353.8, 202.1) @ 12/06 18:10:00	# Returning active area for EIS.
12/07 06:30:16 - 12/07 18:04:16	Fixed (-895.0, -400.0)	# ToO HOP 201, CME heating.
12/07 18:16:46 - 12/08 05:51:16	Track (-140.4, 204.2) @ 12/07 18:14:00	# Returning active area obs for EIS.
12/08 06:03:46 - 12/11 10:17:00	Track (845.7, 124.4) @ 12/08 06:01:00	# Cont.

Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval


CMDI #0106 2012/12/06
note
before Tbl up
after Tbl up
20 188E 188E
19 193D 193D
18 194A 194A
17 1951 1951
16 1920 1920 : Flare obs. dynamics - thin-Be high cadence + conte ...
15 194E 194E
14 18B5 18B5 : HOP201 thin-Be, Ti/Poly, 512x512 at 1064 1048, 120 ...
13 192F 192F : Synoptic Q95 2x2 - Al/mesh(33/1024) + Dark cal(2x2 ...
12 1954 1954
11 190F 190F : HOP81 2-filter - Ti/poly 8s, Al/mesh 4s, G-band - ...
10 1732 1732 : Dark - Med-Al+Thick-Be - 8x8 - 512x512
9 194D 194D
8 1908 1908
7 193C 193C
6 1907 1907
5 194F 194F
4 1950 1950
3 1937 1937 : AR Standard-B(Morphology) with PFB, thin-Be + mult ...
2 1915 1915
1 194B 194B
ctrl Table upload : 3rd pass (2012/12/06 10:06:20)
pass
pass
track0
track0
FL
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
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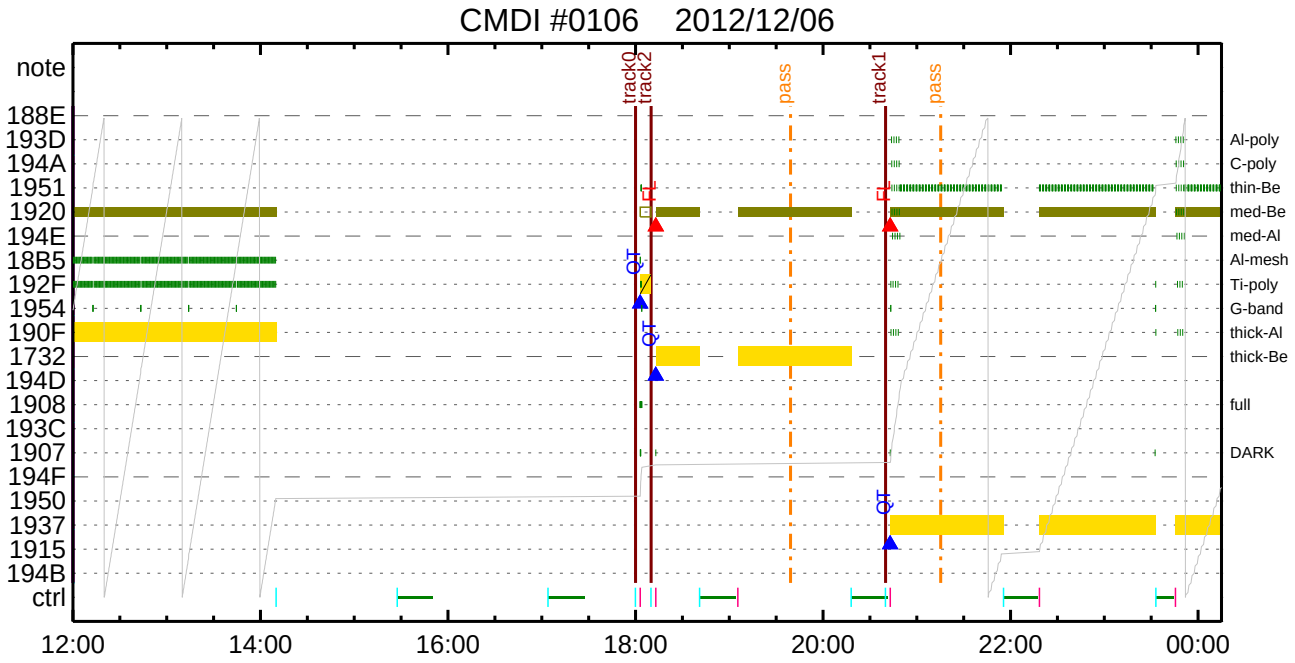
12:00


CMDI #0106 2012/12/06
note
188E
193D
194A
1951
1920
194E
18B5
192F
1954
190F
1732
194D
1908
193C
1907
194F
1950
1937
1915
194B
ctrl
track0
track2
track1
pass
pass
FL
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
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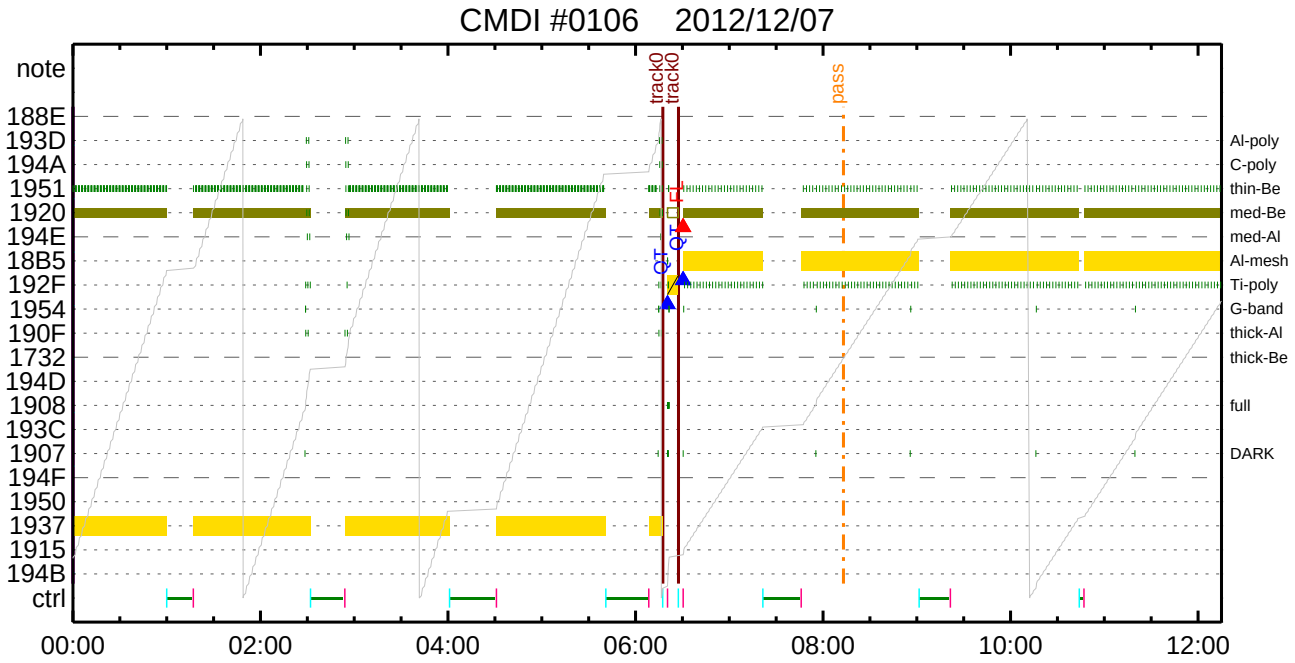
CMDI #0106 2012/12/07
note
188E
193D
194A
1951
1920
194E
18B5
192F
1954
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1937
1915
194B
ctrl
track0
track0
pass
FL
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
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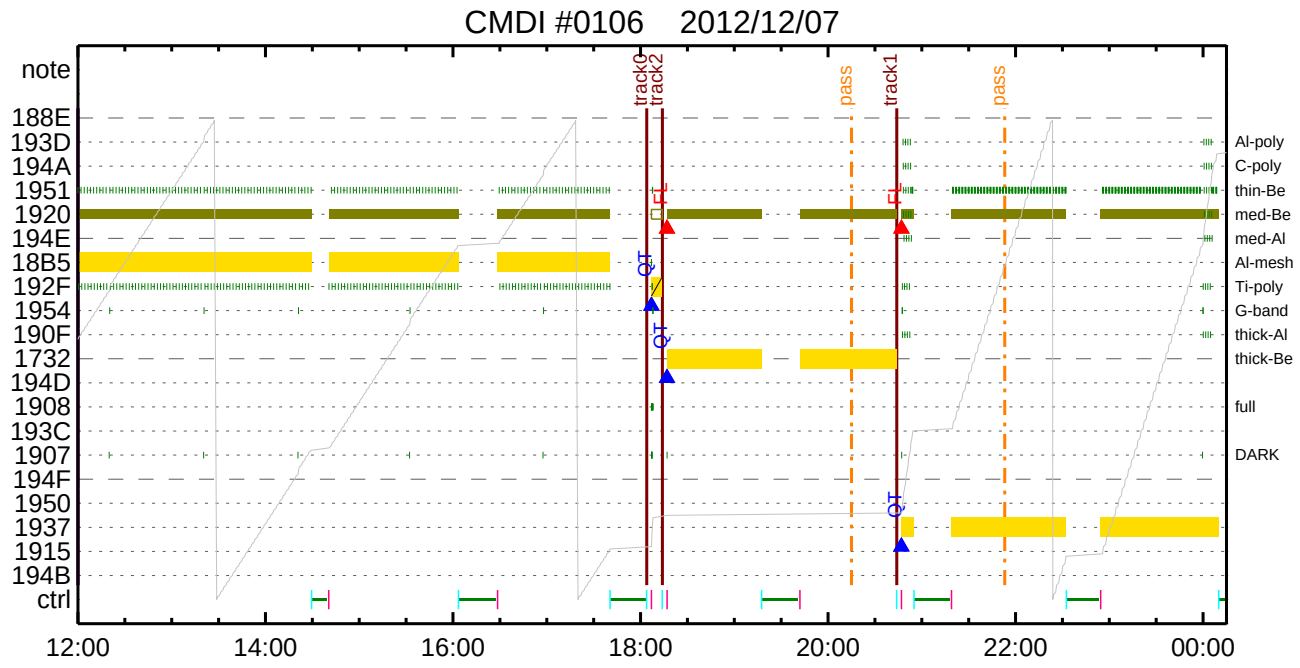
CMDI #0106 2012/12/07
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188E
193D
194A
1951
1920
194E
18B5
192F
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190F
1732
194D
1908
193C
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track1
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Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
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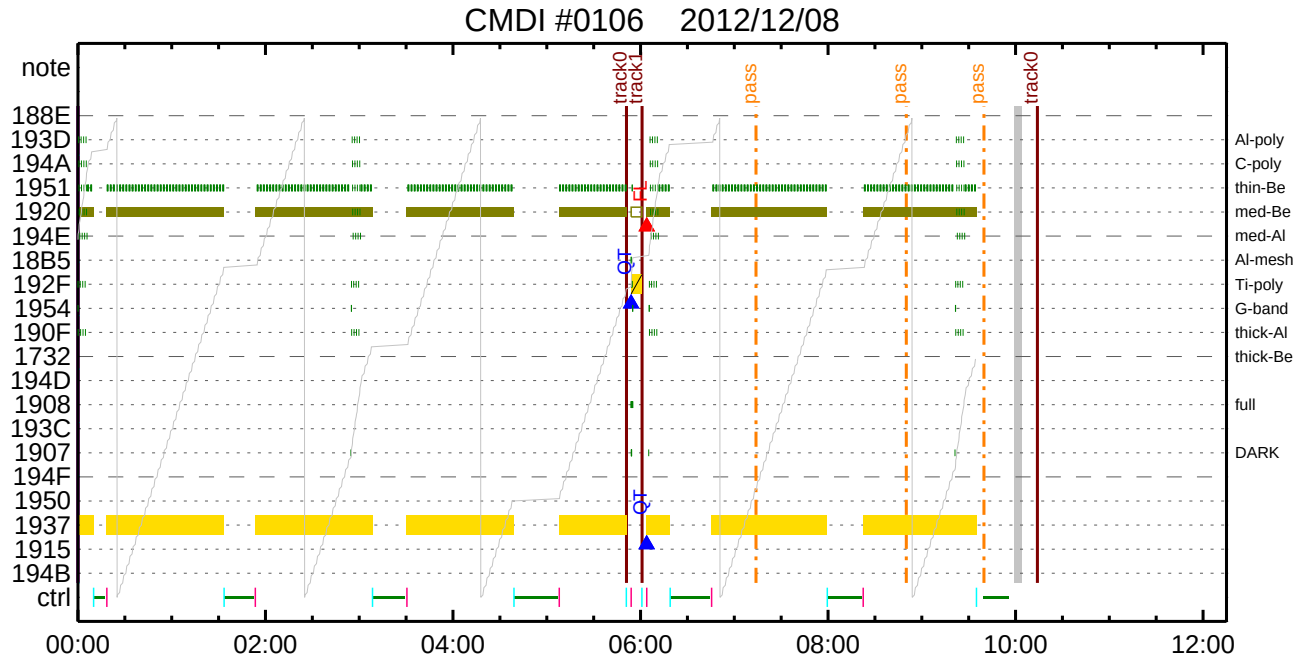
CMDI #0106 2012/12/08
note
188E
193D
194A
1951
1920
194E
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Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
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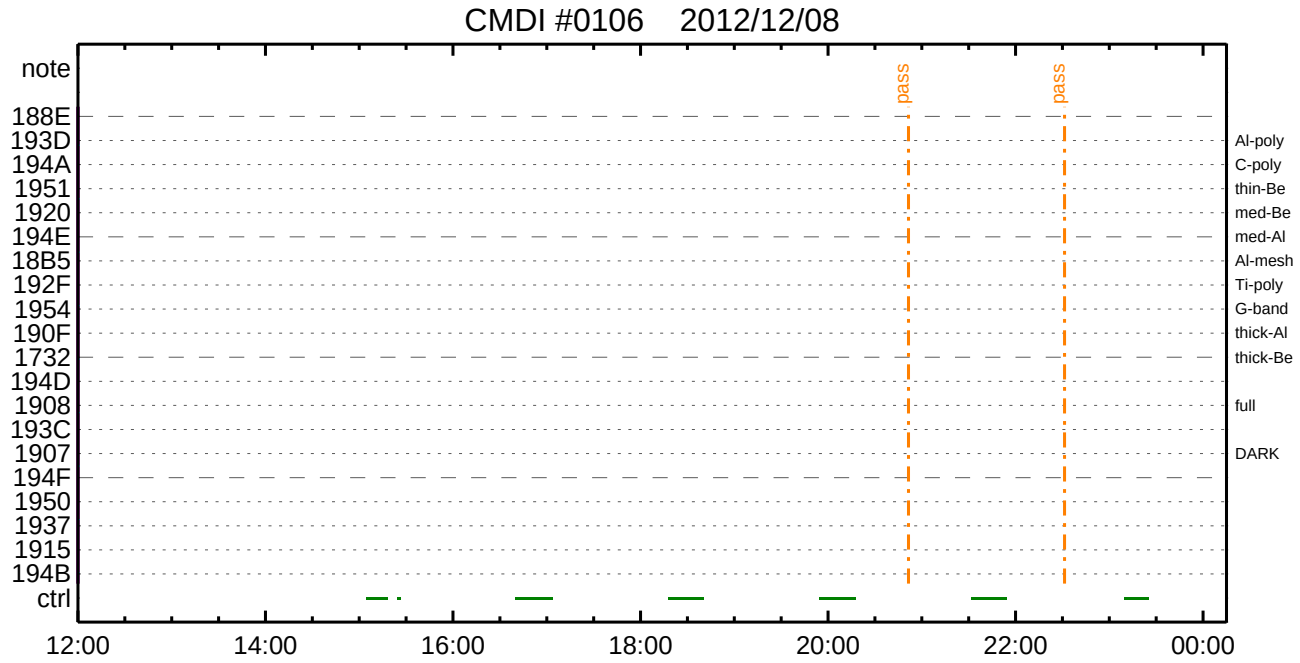
CMDI #0106 2012/12/08
note
188E
193D
194A
1951
1920
194E
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1908
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1950
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ctrl
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Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
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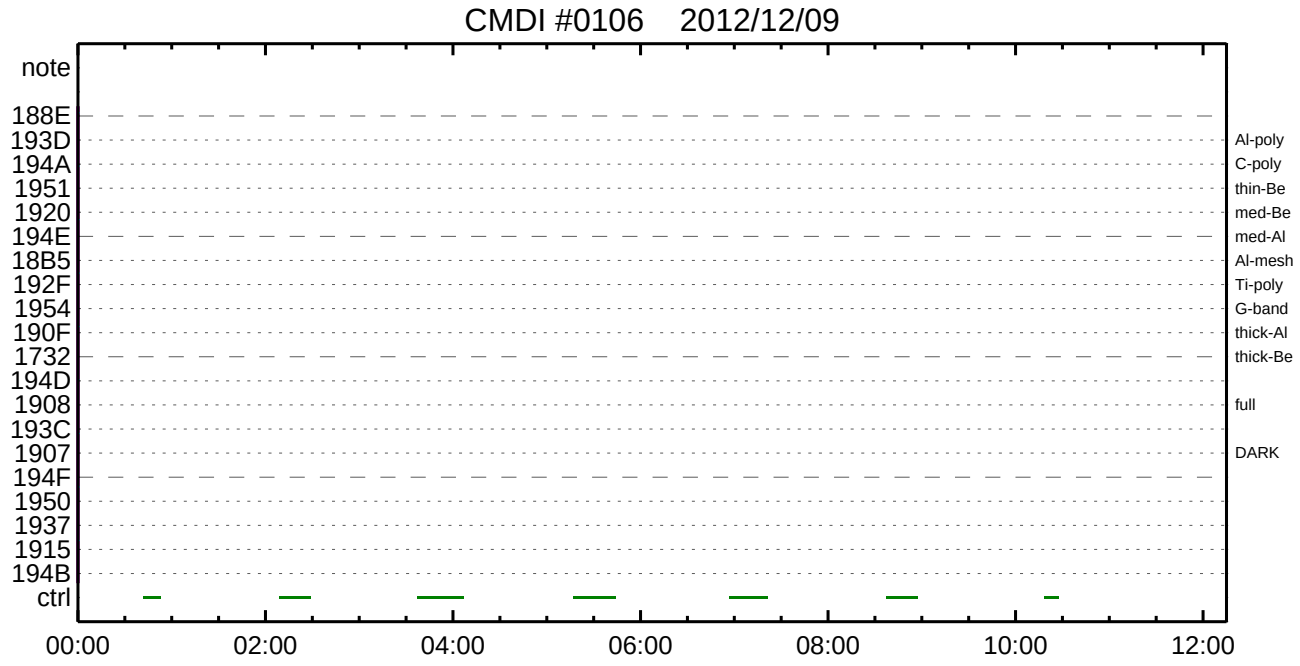
CMDI #0106 2012/12/09
note
188E
193D
194A
1951
1920
194E
18B5
192F
1954
190F
1732
194D
1908
193C
1907
194F
1950
1937
1915
194B
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
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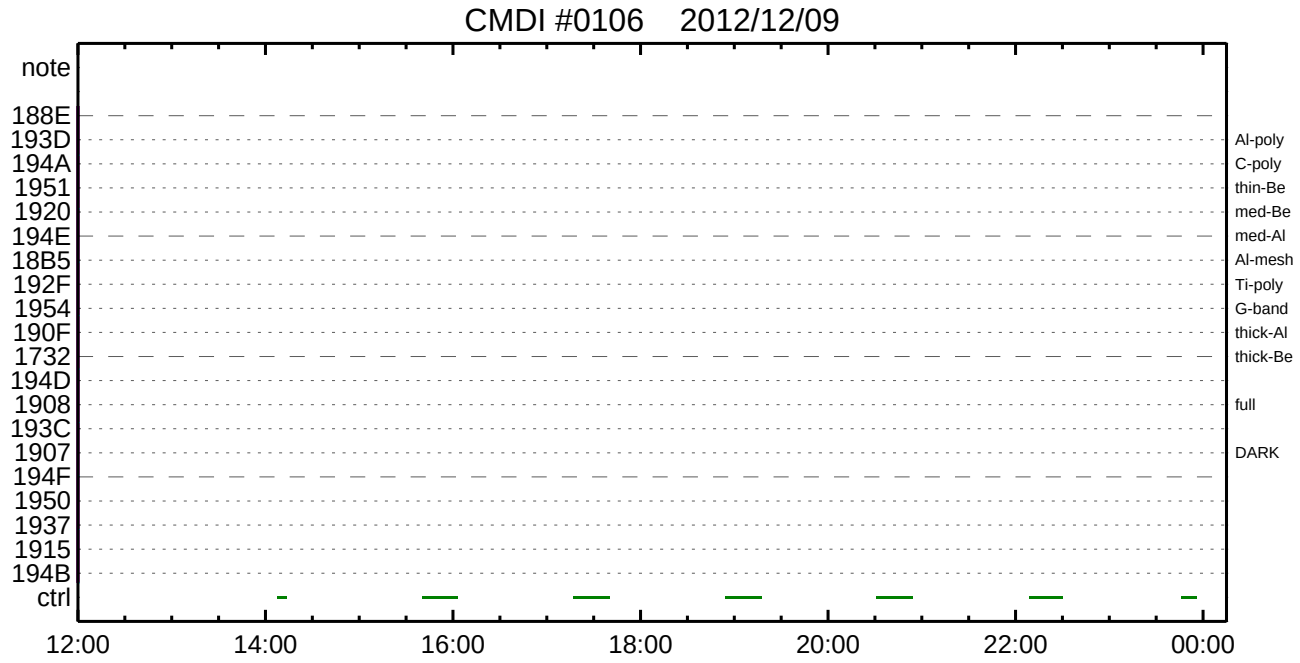
12:00


CMDI #0106 2012/12/09
note
188E
193D
194A
1951
1920
194E
18B5
192F
1954
190F
1732
194D
1908
193C
1907
194F
1950
1937
1915
194B
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
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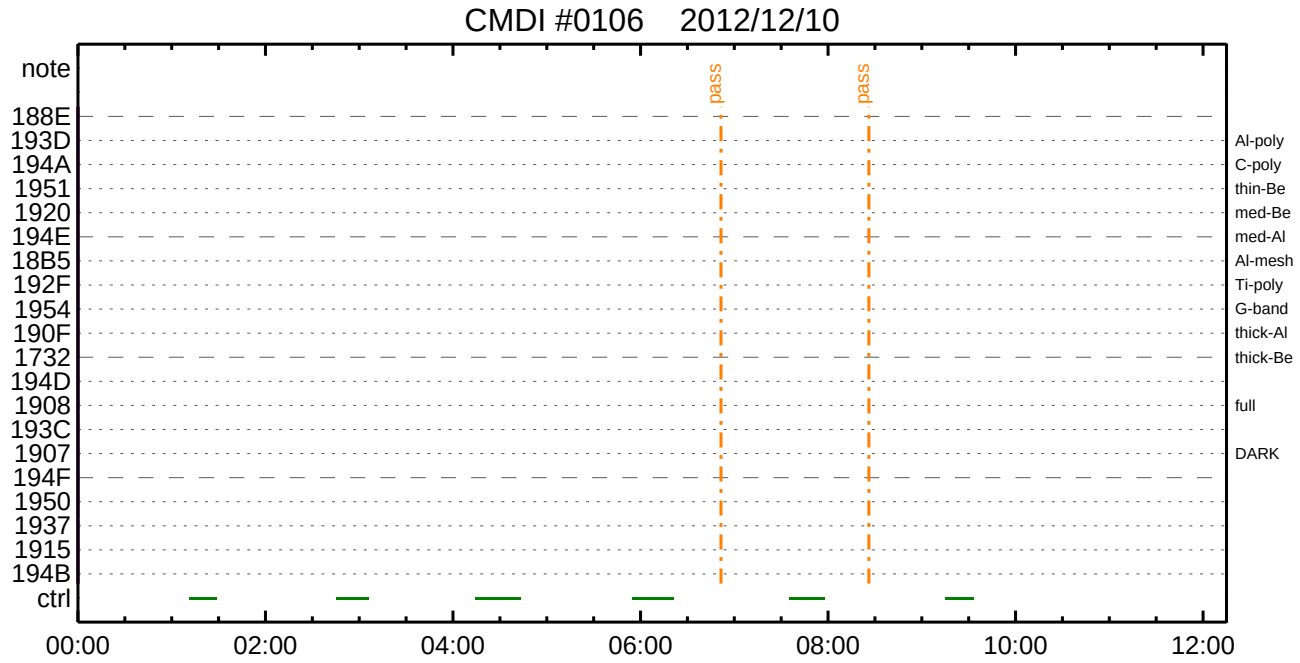
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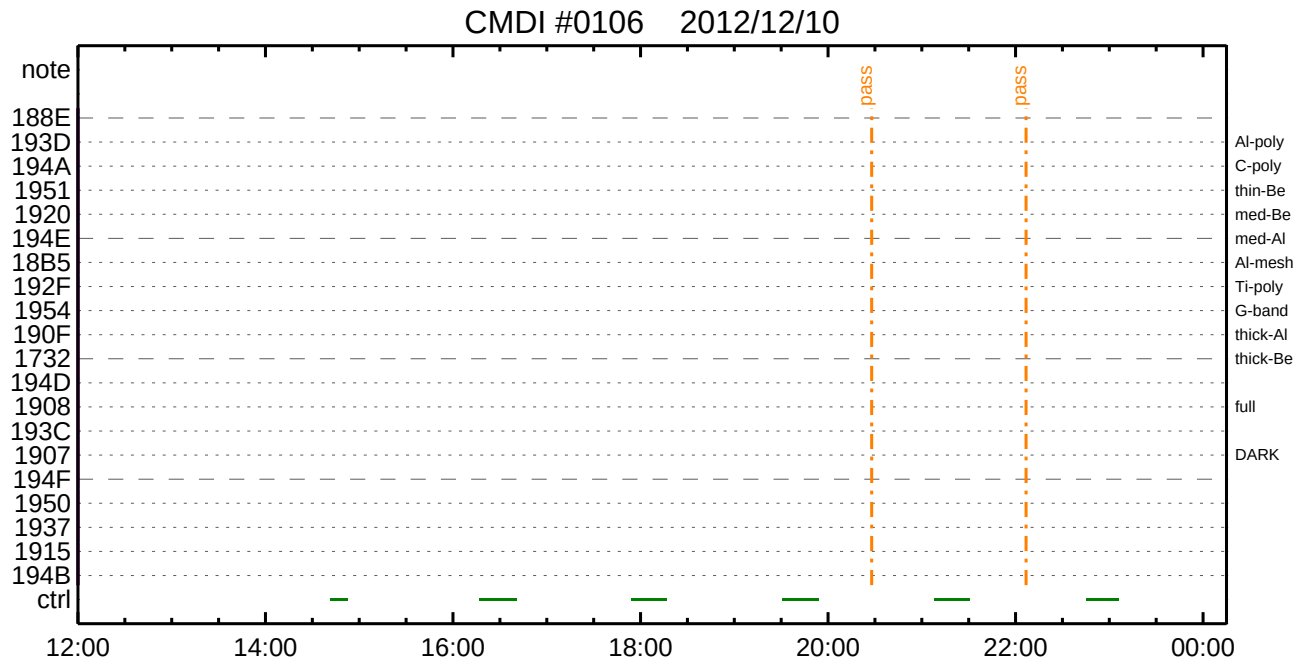
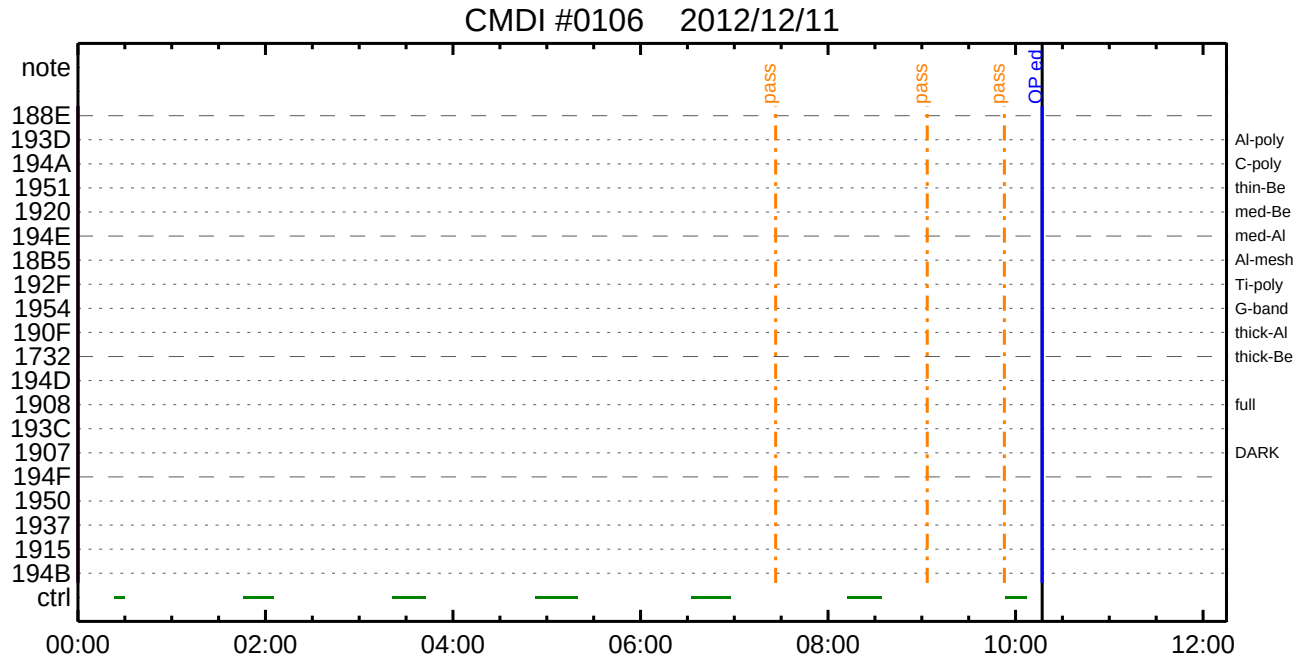
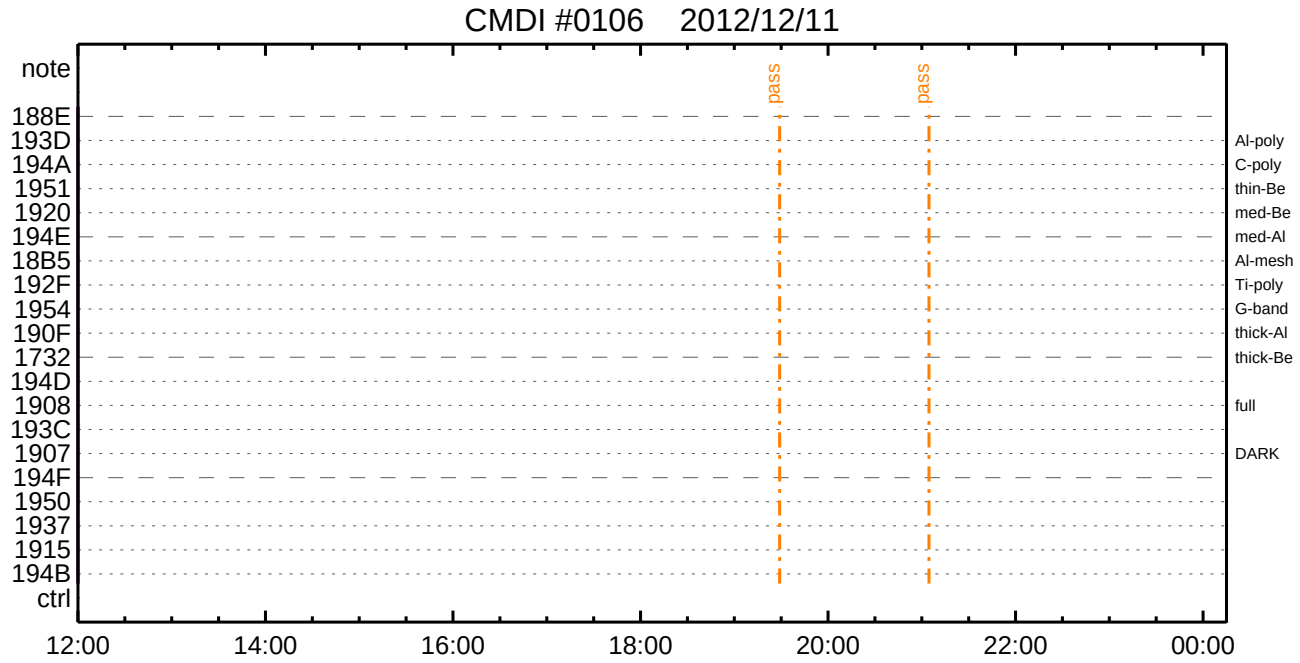
note
pass
pass
188E
193D
194A
1951
1920
194E
18B5
192F
1954
190F
1732
194D
1908
193C
1907
194F
1950
1937
1915
194B
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0106 2012/12/10
note
188E
193D
194A
1951
1920
194E
18B5
192F
1954
190F
1732
194D
1908
193C
1907
194F
1950
1937
1915
194B
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0106 2012/12/11
note
188E
193D
194A
1951
1920
194E
18B5
192F
1954
190F
1732
194D
1908
193C
1907
194F
1950
1937
1915
194B
ctrl
pass
pass
pass
OP ed
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0106 2012/12/11
note
188E
193D
194A
1951
1920
194E
18B5
192F
1954
190F
1732
194D
1908
193C
1907
194F
1950
1937
1915
194B
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

```
(a) Spacecraft Operation Procedure (real-commands)

main-315 2012-12-06 13:49:03 289 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇÎ»þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Äº²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ****************************
0099     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0100     C. ****************************
0101     C.
0102   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0103   . S. OP    op-315:OP
0104     ()
0105   . S. OG    og-315:OG
0106     ()
0107     C.
0108   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0109     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0110   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111     BC       (20 00 7f 01 02)
0112     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0113     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0114     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0115     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0116     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0117   +. DC 01-22 DHU_MODE_CHNG
0118     BC       (07 0b f8)
0119     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0120     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0121     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0122     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0123     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0124   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0125     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0126   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0127     C.
0128     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0129   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130     BC       (20 80 7f 01 02)
0131     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0132     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0133     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0134     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0135     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0136   +. DC 01-22 DHU_MODE_CHNG
0137     BC       (07 0b f8)
0138     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0139     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0140     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0141     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0142     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0143   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0144     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0145   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0146     C.
0147     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149     BC       (21 00 41 01 02)
0150     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0151     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0152     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0153     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0154     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0155   +. DC 01-22 DHU_MODE_CHNG
0156     BC       (07 0b f8)
0157     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0158     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0159     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0160     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0161     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0162   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0163     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0164   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0165     C.
0166   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0167     C. DHU¥â¡¼¥É¡Ê¼ý¸¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0168   +. DC 01-22 DHU_MODE_CHNG
0169     BC       (02 0a f8)
0170     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     2
0171     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0172     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0173     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0174     C.
0175   . C. ********************************
0176     C. TI-CMD SET (OPOG STOP/COPY/START)
0177     C. ********************************
0178     C.
0179   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
0180     C.           ¤Þ¤¿¡¢¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¿¼Ç¹Ô¤¦¤³¤È¡£
0181     C.
0182   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
0183   +. TI 2012-12-06 10:23:00.0
0184     DC 01-B3 DHU_OP_STOP
0185     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0186     C.
0187   +. TI 2012-12-06 10:23:01.0
0188     DC 01-B4 DHU_OP_COPY
0189     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0190     C.
0191   +. TI 2012-12-06 10:23:01.0
0192     DC 01-B5 DHU_OPOG_COPY
0193     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
```

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0194     C.
0195   +. TI 2012-12-06 10:27:59.5
0196     DC 01-B2 DHU_OP_START
0197     C.                        ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0198     C.
0199     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0200     C.                        ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0201     C.                        ¢¢[HK1_TI_CMD_NUM]              EQ     4
0202     C.                        ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0203     C.                        ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0204     C.
0205    . C. ****************************
0206     C. TIÎÎ°è¥À¥ó¥×
0207     C. ****************************
0208     C.
0209     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0210   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211     BC       (03 ab 03 01 02)
0212     C.                        ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0213     C.                        ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0214     C.                        ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0215     C.                        ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0216     C.                        ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0217   +. DC 01-22 DHU_MODE_CHNG
0218     BC       (07 0b f8)
0219     C.                        ¢¢[HK1_PKT_FORM_NO]             EQ     7
0220     C.                        ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0221     C.                        ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0222     C.                        ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0223     C.                        ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0224     C.
0225    . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0226     C.                        ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0227     C.
0228    . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0229     C.
0230    . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0231   +. DC 01-22 DHU_MODE_CHNG
0232     BC       (02 0a f8)
0233     C.                        ¢¢[HK1_PKT_FORM_NO]             EQ     2
0234     C.                        ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0235     C.                        ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0236     C.                        ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0237     C.
0238    . C. Stop EIS observation and temporarily disable EIS mode changes
0239     C.
0240     C.
0241     C. ********** Start EIS operation (TI set) **********
0242     C. Execute, after the success of OP upload.
0243     C. Set EIS TI-commands
0244   +. TI 2012-12-06 10:27:30.0
0245     DC 07-FC EIS_MODE_MANU
0246     BC       (21 02)
0247   +. TI 2012-12-06 10:27:40.0
0248     DC 07-FC EIS_MODE_CHG_DIS
0249     BC       (22)
0250    . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0251     C. *********** End EIS operation (TI set) ***********
0252     C.
0253     C.
0254     C.
0255     C. ************ XRT START ***********
0256     C. Execute, after the success of OP upload.
0257   +. TI 2012-12-06 10:27:00.0
0258     DC 07-F0 MDP_XRT_MODE_STBY
0259     BC       (c3)
0260    . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0261     C.
0262     C. ************ XRT END ***********
0263     C. ******************************
0264     C.  SOT TI command set
0265     C. ******************************
0266     C. Execute, after the success of OP upload.
0267   +. TI 2012-12-06 10:27:16.0
0268     DC 07-F0 MDP_SOT_MODE_STBY
0269     BC       (41)
0270    . C. ------------------------------------
0271     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0272     C. ------------------------------------
0273     C. ************ SOT END ***********
0274     C.
0275    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0276     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0277    . S. DC-BC dcbc-402:DCBC
0278     (MDP_known_event)
0279     C.
0280     C.
0281    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0282    . S. DC-BC dcbc-153:DCBC
0283     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284     C.
0285     C.
0286    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0287     C.
0288    . C. *********** LOS ***********
0289     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-316 2012-12-06 13:49:03 166 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇÎ»þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Äº²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0099     C. Upload the Orbit Element and the Target Attitude
0100     C. RAM-ID:TARGET_ATT
0101   . S. RAM   ram-150:TARGET_ATT
0102     ()
0103     C.
0104     C.
0105     C. Set the dump memory area of TARGET_ATT
0106   +. DC 02-48 AOCU_DUMP_SET
0107     BC       (07 00 00 00 18 00)
0108     C.
0109     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110     C.
0111     C.
0112     C. Change the TLMFormatNo for the AOCS Dump Format
0113   +. DC 01-22 DHU_MODE_CHNG
0114     BC       (04 0b f8)
0115     C.
0116     C. Wait for AOCSDUMP to end
0117     C.
0118   . C. Check the dump memory
0119     C.
0120     C.                                      Result = OK [ ]
0121     C.
0122   +. DC 01-22 DHU_MODE_CHNG
0123     BC       (02 0a f8)
0124     C.
0125     C.                         <A_***>[TLM STS] FMT = 2 [ ]
0126     C.
0127   +. DC 02-8E AOCU_ORB_UPD
0128    . C.
0129    . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130   +. DC 07-FC EIS_MODE_MANU
0131     BC       (21 02)
0132    . C. Verify EIS in MANUAL mode
0133    . C. Estimated OBSTBL upload time is 10s
0134     C. ********************************
0135     C. EIS START OBSTBL LOAD
0136     C. ********************************
0137    . S. RAM   ram-820:EIS_OBSTBL
0138     ()
0139   +. DC 07-FC EIS_DUMP_OBSTBL
0140     BC       (07 07 07 00 00 70 00)
0141     C.
0142     C. Execute, after the success of OBSTBL upload.
0143     C. Set EIS TI-commands
0144   +. TI 2012-12-06 10:27:50.0
0145     DC 07-FC EIS_MODE_CHG_ENA
0146     BC       (20)
0147    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0148     C. ********************************
0149     C. EIS END OBSTBL LOAD
0150     C. ********************************
0151     C.
0152    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0153     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0154    . S. DC-BC dcbc-402:DCBC
0155     (MDP_known_event)
0156     C.
0157     C.
0158    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0159    . S. DC-BC dcbc-153:DCBC
0160     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0161     C.
0162     C.
0163    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0164     C.
0165    . C. *********** LOS ***********
0166     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-317 2012-12-06 13:49:03 128 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014     C.
0015     C. ************ XRT START ************
0016     C.
0017   +. DC 07-F0 MDP_XRT_CTRL_MANU
0018     BC       (c1)
0019   +  DC 07-F0 MDP_XRT_MODE_STBY
0020     BC       (c3)
0021   . C. ----------- Success Verify ?              OK / NG____
0022     C.
0023     C. XRT Obs. Table Upload
0024   . S. RAM   ram-291:MDP_OBS_X
0025     ()
0026     C.
0027   +. DC 07-F0 MDP_DUMP_XRTTBL
0028     BC       (84 07 00 00 00 3a d4)
0029   . C. ----------- Comparison Check ?                        OK / ERR ____
0030     C.
0031     C.
0032   +. DC 07-F0 MDP_XRT_ROI_SET
0033     BC       (cd 01 b1 b1 04 04)
0034   +  DC 07-F0 MDP_XRT_ROI_SET
0035     BC       (cd 02 b1 b1 08 08)
0036   +  DC 07-F0 MDP_XRT_ROI_SET
0037     BC       (cd 03 b1 b1 08 08)
0038   +  DC 07-F0 MDP_XRT_ROI_SET
0039     BC       (cd 04 b1 b1 06 06)
0040   +  DC 07-F0 MDP_XRT_ROI_SET
0041     BC       (cd 05 85 83 06 06)
0042   +  DC 07-F0 MDP_XRT_ROI_SET
0043     BC       (cd 06 85 83 08 08)
0044   +  DC 07-F0 MDP_XRT_ROI_SET
0045     BC       (cd 07 85 83 06 06)
0046   +  DC 07-F0 MDP_XRT_ROI_SET
0047     BC       (cd 08 80 80 20 20)
0048   +  DC 07-F0 MDP_XRT_ROI_SET
0049     BC       (cd 09 80 80 20 08)
0050   +  DC 07-F0 MDP_XRT_ROI_SET
0051     BC       (cd 0a 80 80 08 20)
0052   +  DC 07-F0 MDP_XRT_ROI_SET
0053     BC       (cd 0b 80 80 08 08)
0054   +  DC 07-F0 MDP_XRT_ROI_SET
0055     BC       (cd 0f 80 80 06 06)
0056   +  DC 07-F0 MDP_XRT_ROI_SET
0057     BC       (cd 10 80 80 08 08)
0058   +  DC 07-F0 MDP_XRT_FLD_DIS
0059     BC       (d9)
0060   +  DC 07-F0 MDP_XRT_FLRCTRL_DIS
0061     BC       (c9)
0062   +  DC 07-F0 MDP_XRT_AEC_RESET
0063     BC       (d0)
0064   +  DC 07-F0 MDP_XRT_ARS_DIS
0065     BC       (d5)
0066   +  DC 07-F0 MDP_XRT_FLD_RESET
0067     BC       (da)
0068   . C. ---------- Success Verify ?           OK / NG ____
0069     C.
0070     C.
0071   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0072     C.
0073   +. DC 07-F0 MDP_XRT_MODE_OBSV
0074     BC       (c2)
0075   +. TI 2012-12-06 10:27:02.0
0076     DC 07-F0 MDP_XRT_MODE_OBSV
0077     BC       (c2)
0078   . C. ---------- Success Verify ?           OK / NG ____
0079     C.
0080     C. ************ XRT END ************
0081   . C. ********************************
0082     C.  SOT table upload
0083     C. ********************************
0084   . C. < Stop FG table >
0085   +. DC 07-F0 MDP_FG_CTRL_MANU
0086     BC       (51)
0087   . C. ------------------------------------
0088     C.  MDP_FG_CTRL_MODE      = MANU    [ ]
0089     C. ------------------------------------
0090     C.
0091   . C. <Upload FG Observation Table>
0092   . S. RAM   ram-262:MDP_OBS_F
0093     ()
0094     C.
0095   . C. < Dump RAMID=MDP_OBS_F >
```

```
0096  +. DC 07-F0 MDP_DUMP_FGTBL
0097     BC       (82 07 00 00 00 38 b8)
0098     C. ------------------------------------
0099     C.  MDP_OBS_F verify = OK/NG        [ ]
0100     C. ------------------------------------
0101     C.
0102     C. ******************************
0103     C.  SOT TI command set
0104     C. ******************************
0105     C. Execute, after the success of TBL upload.
0106  +. TI 2012-12-06 10:27:18.0
0107     DC 07-F0 MDP_SOT_MODE_OBSV
0108     BC       (40)
0109   . C. ------------------------------------
0110     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0111     C. ------------------------------------
0112     C.
0113     C.
0114   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0115     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0116   . S. DC-BC dcbc-402:DCBC
0117     (MDP_known_event)
0118     C.
0119     C.
0120   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0121   . S. DC-BC dcbc-153:DCBC
0122     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0123     C.
0124     C.
0125   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0126     C.
0127   . C. ********** LOS **********
0128     C.
```

```
*** OP Sequence for XRT ***

2012/12/06  10:37:54.0  XRT_CTRL_MANU_447_OG [0x1bf]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/12/06  10:38:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 00 ac 73 00 00
2012/12/06  10:40:26.0  XRT_FOCUS_POSITION_420_OG [0x1a4]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2012/12/06  10:40:46.0  XRT_FLD_ENA_418_OG [0x1a2]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2012/12/06  10:40:48.0  XRT_FLRCTRL_ENA_401_OG [0x191]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2012/12/06  10:40:50.0  XRT_AEC_RESET_423_OG [0x1a7]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2012/12/06  10:40:52.0  XRT_ARS_DIS_412_OG [0x19c]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2012/12/06  10:40:54.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/12/06  10:40:56.0  XRT_QT_PROG_SET_410_OG [0x19a]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2012/12/06  10:40:58.0  XRT_FL_PROG_SET_444_OG [0x1bc]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2012/12/06  10:41:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/12/06  14:10:00.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/12/06  15:27:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/12/06  15:27:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/12/06  15:27:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/12/06  15:30:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/12/06  17:04:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/12/06  17:04:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/12/06  17:04:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/12/06  17:07:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/12/06  17:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/12/06  17:59:56.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/12/06  18:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2012/12/06  18:00:16.0  XRT_FLD_DIS_404_OG [0x194]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2012/12/06  18:00:18.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2012/12/06  18:00:20.0  XRT_ARS_DIS_406_OG [0x196]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2012/12/06  18:02:58.0  XRT_QT_PROG_SET_419_OG [0x1a3]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2012/12/06  18:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/12/06  18:09:54.0  XRT_CTRL_MANU_447_OG [0x1bf]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/12/06  18:10:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 02 00 00 00 00
2012/12/06  18:12:26.0  XRT_FOCUS_POSITION_420_OG [0x1a4]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2012/12/06  18:12:46.0  XRT_FLD_ENA_428_OG [0x1ac]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2012/12/06  18:12:48.0  XRT_FLRCTRL_ENA_429_OG [0x1ad]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2012/12/06  18:12:50.0  XRT_AEC_RESET_423_OG [0x1a7]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2012/12/06  18:12:52.0  XRT_ARS_DIS_438_OG [0x1b6]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2012/12/06  18:12:54.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/12/06  18:12:56.0  XRT_QT_PROG_SET_436_OG [0x1b4]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0a
2012/12/06  18:12:58.0  XRT_FL_PROG_SET_444_OG [0x1bc]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2012/12/06  18:13:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/12/06  18:41:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/12/06  18:41:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/12/06  18:41:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/12/06  18:44:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/12/06  19:04:30.0  XRT_Custom_434_OG [0x1b2]
2012/12/06  19:05:30.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/12/06  20:18:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
```

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2012/12/06  20:18:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/12/06  20:18:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/12/06  20:21:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/12/06  20:39:54.0  XRT_CTRL_MANU_447_OG [0x1bf]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/12/06  20:40:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 01 00 00 00 00
2012/12/06  20:42:26.0  XRT_FOCUS_POSITION_420_OG [0x1a4]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2012/12/06  20:42:46.0  XRT_FLD_ENA_428_OG [0x1ac]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2012/12/06  20:42:48.0  XRT_FLRCTRL_ENA_429_OG [0x1ad]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2012/12/06  20:42:50.0  XRT_AEC_RESET_423_OG [0x1a7]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2012/12/06  20:42:52.0  XRT_ARS_DIS_438_OG [0x1b6]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2012/12/06  20:42:54.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/12/06  20:42:56.0  XRT_QT_PROG_SET_427_OG [0x1ab]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 03
2012/12/06  20:42:58.0  XRT_FL_PROG_SET_444_OG [0x1bc]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2012/12/06  20:43:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/12/06  21:55:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/12/06  21:55:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/12/06  21:55:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/12/06  21:58:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/12/06  22:17:30.0  XRT_Custom_434_OG [0x1b2]
2012/12/06  22:18:30.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/12/06  23:33:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/12/06  23:33:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/12/06  23:33:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/12/06  23:36:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/12/06  23:44:30.0  XRT_Custom_434_OG [0x1b2]
2012/12/06  23:45:30.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/12/07  01:00:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/12/07  01:00:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/12/07  01:00:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/12/07  01:03:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/12/07  01:16:00.0  XRT_Custom_434_OG [0x1b2]
2012/12/07  01:17:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/12/07  02:32:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/12/07  02:32:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/12/07  02:32:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/12/07  02:35:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/12/07  02:53:00.0  XRT_Custom_434_OG [0x1b2]
2012/12/07  02:54:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/12/07  04:01:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/12/07  04:01:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/12/07  04:01:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/12/07  04:04:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/12/07  04:30:00.0  XRT_Custom_434_OG [0x1b2]
2012/12/07  04:31:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/12/07  05:41:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/12/07  05:41:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/12/07  05:41:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/12/07  05:44:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/12/07  06:07:30.0  XRT_Custom_434_OG [0x1b2]
2012/12/07  06:08:30.0  XRT_CTRL_AUTO_413_OG [0x19d]
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                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/12/07  06:17:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/12/07  06:17:26.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/12/07  06:17:30.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2012/12/07  06:17:46.0  XRT_FLD_DIS_404_OG [0x194]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2012/12/07  06:17:48.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2012/12/07  06:17:50.0  XRT_ARS_DIS_406_OG [0x196]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2012/12/07  06:20:28.0  XRT_QT_PROG_SET_419_OG [0x1a3]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2012/12/07  06:20:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/12/07  06:27:24.0  XRT_CTRL_MANU_447_OG [0x1bf]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/12/07  06:27:30.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                        AOCU_NM              5 02-76 00 23 8d 4f 8d
2012/12/07  06:29:56.0  XRT_FOCUS_POSITION_420_OG [0x1a4]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2012/12/07  06:30:16.0  XRT_FLD_ENA_428_OG [0x1ac]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2012/12/07  06:30:18.0  XRT_FLRCTRL_ENA_429_OG [0x1ad]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2012/12/07  06:30:20.0  XRT_AEC_RESET_423_OG [0x1a7]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2012/12/07  06:30:22.0  XRT_ARS_DIS_438_OG [0x1b6]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2012/12/07  06:30:24.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/12/07  06:30:26.0  XRT_QT_PROG_SET_415_OG [0x19f]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0e
2012/12/07  06:30:28.0  XRT_FL_PROG_SET_444_OG [0x1bc]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2012/12/07  06:30:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/12/07  07:21:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/12/07  07:21:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/12/07  07:21:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/12/07  07:24:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/12/07  07:45:00.0  XRT_Custom_434_OG [0x1b2]
2012/12/07  07:46:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/12/07  09:01:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/12/07  09:01:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/12/07  09:01:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/12/07  09:04:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/12/07  09:20:30.0  XRT_Custom_434_OG [0x1b2]
2012/12/07  09:21:30.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/12/07  10:44:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/12/07  10:44:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/12/07  10:44:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/12/07  10:46:00.0  XRT_Custom_434_OG [0x1b2]
2012/12/07  10:47:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/12/07  10:47:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/12/07  14:29:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/12/07  14:29:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/12/07  14:29:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/12/07  14:32:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/12/07  14:39:30.0  XRT_Custom_434_OG [0x1b2]
2012/12/07  14:40:30.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/12/07  16:03:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/12/07  16:03:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/12/07  16:03:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/12/07  16:06:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/12/07  16:27:30.0  XRT_Custom_434_OG [0x1b2]
2012/12/07  16:28:30.0  XRT_CTRL_AUTO_413_OG [0x19d]
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                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/12/07  17:40:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/12/07  17:40:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/12/07  17:40:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/12/07  17:43:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/12/07  18:03:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/12/07  18:03:56.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/12/07  18:04:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2012/12/07  18:04:16.0  XRT_FLD_DIS_404_OG [0x194]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2012/12/07  18:04:18.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2012/12/07  18:04:20.0  XRT_ARS_DIS_406_OG [0x196]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2012/12/07  18:06:58.0  XRT_QT_PROG_SET_419_OG [0x1a3]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2012/12/07  18:07:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/12/07  18:13:54.0  XRT_CTRL_MANU_447_OG [0x1bf]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/12/07  18:14:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                        AOCU_NM              5 02-76 02 00 00 00 00
2012/12/07  18:16:26.0  XRT_FOCUS_POSITION_420_OG [0x1a4]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2012/12/07  18:16:46.0  XRT_FLD_ENA_428_OG [0x1ac]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2012/12/07  18:16:48.0  XRT_FLRCTRL_ENA_429_OG [0x1ad]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2012/12/07  18:16:50.0  XRT_AEC_RESET_423_OG [0x1a7]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2012/12/07  18:16:52.0  XRT_ARS_DIS_438_OG [0x1b6]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2012/12/07  18:16:54.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/12/07  18:16:56.0  XRT_QT_PROG_SET_436_OG [0x1b4]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0a
2012/12/07  18:16:58.0  XRT_FL_PROG_SET_444_OG [0x1bc]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2012/12/07  18:17:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/12/07  19:17:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/12/07  19:17:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/12/07  19:17:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/12/07  19:20:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/12/07  19:41:00.0  XRT_Custom_434_OG [0x1b2]
2012/12/07  19:42:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/12/07  20:43:54.0  XRT_CTRL_MANU_447_OG [0x1bf]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/12/07  20:44:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                        AOCU_NM              5 02-76 01 00 00 00 00
2012/12/07  20:46:26.0  XRT_FOCUS_POSITION_420_OG [0x1a4]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2012/12/07  20:46:46.0  XRT_FLD_ENA_428_OG [0x1ac]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2012/12/07  20:46:48.0  XRT_FLRCTRL_ENA_429_OG [0x1ad]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2012/12/07  20:46:50.0  XRT_AEC_RESET_423_OG [0x1a7]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2012/12/07  20:46:52.0  XRT_ARS_DIS_438_OG [0x1b6]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2012/12/07  20:46:54.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/12/07  20:46:56.0  XRT_QT_PROG_SET_427_OG [0x1ab]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 03
2012/12/07  20:46:58.0  XRT_FL_PROG_SET_444_OG [0x1bc]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2012/12/07  20:47:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/12/07  20:55:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/12/07  20:55:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2012/12/07  20:55:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/12/07  20:58:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/12/07  21:18:00.0  XRT_Custom_434_OG [0x1b2]
2012/12/07  21:19:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/12/07  22:32:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
```

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2012/12/07  22:32:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/12/07  22:32:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/12/07  22:35:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/12/07  22:53:30.0  XRT_Custom_434_OG [0x1b2]
2012/12/07  22:54:30.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/12/08  00:10:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/12/08  00:10:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/12/08  00:10:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/12/08  00:13:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/12/08  00:17:30.0  XRT_Custom_434_OG [0x1b2]
2012/12/08  00:18:30.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/12/08  01:33:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/12/08  01:33:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/12/08  01:33:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/12/08  01:36:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/12/08  01:52:30.0  XRT_Custom_434_OG [0x1b2]
2012/12/08  01:53:30.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/12/08  03:08:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/12/08  03:08:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/12/08  03:08:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/12/08  03:11:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/12/08  03:29:30.0  XRT_Custom_434_OG [0x1b2]
2012/12/08  03:30:30.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/12/08  04:39:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/12/08  04:39:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/12/08  04:39:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/12/08  04:42:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/12/08  05:07:00.0  XRT_Custom_434_OG [0x1b2]
2012/12/08  05:08:00.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/12/08  05:50:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/12/08  05:50:56.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2012/12/08  05:51:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2012/12/08  05:51:16.0  XRT_FLD_DIS_404_OG [0x194]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2012/12/08  05:51:18.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2012/12/08  05:51:20.0  XRT_ARS_DIS_406_OG [0x196]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2012/12/08  05:53:58.0  XRT_QT_PROG_SET_419_OG [0x1a3]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2012/12/08  05:54:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/12/08  06:00:54.0  XRT_CTRL_MANU_447_OG [0x1bf]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/12/08  06:01:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 01 00 00 00 00
2012/12/08  06:03:26.0  XRT_FOCUS_POSITION_420_OG [0x1a4]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2012/12/08  06:03:46.0  XRT_FLD_ENA_428_OG [0x1ac]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2012/12/08  06:03:48.0  XRT_FLRCTRL_ENA_429_OG [0x1ad]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2012/12/08  06:03:50.0  XRT_AEC_RESET_423_OG [0x1a7]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2012/12/08  06:03:52.0  XRT_ARS_DIS_438_OG [0x1b6]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2012/12/08  06:03:54.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/12/08  06:03:56.0  XRT_QT_PROG_SET_427_OG [0x1ab]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 03
2012/12/08  06:03:58.0  XRT_FL_PROG_SET_444_OG [0x1bc]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2012/12/08  06:04:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/12/08  06:19:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
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2012/12/08  06:19:02.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/12/08  06:19:04.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/12/08  06:22:14.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/12/08  06:44:30.0  XRT_Custom_434_OG [0x1b2]
2012/12/08  06:45:30.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/12/08  07:59:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/12/08  07:59:32.0  XRT_FLD_RESET_424_OG [0x1a8]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2012/12/08  07:59:34.0  XRT_PREFLR_STRT_432_OG [0x1b0]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2012/12/08  08:02:44.0  XRT_PREFLR_STOP_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2012/12/08  08:21:30.0  XRT_Custom_434_OG [0x1b2]
2012/12/08  08:22:30.0  XRT_CTRL_AUTO_413_OG [0x19d]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2012/12/08  09:35:00.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2012/12/08  10:14:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
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