

XRT Timeline to be uploaded on 2013/05/09

Period: 2013/05/09 10:21:00 - 2013/05/14 09:56:00

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Normal mode

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XOB #198C: Synoptic Q95 2x2 - Al/mesh(12/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(24/1443) + G-band(8)

Term	Pointing (x, y)	Comment
05/09 18:00:30 - 05/09 18:07:24	Fixed (0.0, 0.0)	synoptic, shifted -2.5 min
05/10 03:48:06 - 05/10 03:54:54	Fixed (0.0, 0.0)	# Eclipse Observations at disk center. Synoptic at 3.45UT.
05/10 18:23:30 - 05/10 18:26:24	Fixed (0.0, 0.0)	synoptic, shifted 16.5 min
05/11 06:05:30 - 05/11 06:12:24	Fixed (0.0, 0.0)	synoptic, shifted 2.5 min
PROG= 18 1-time(s)		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 55 1-time(s) 4.0sec		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 5 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ Seqn= 56 1-time(s) 4.0sec		
Open/Ti-poly	Open/Ti-poly close	Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 66 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1982: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh, Ti/Poly-long - w leak image-33 ms

Term	Pointing (x, y)	Comment
05/09 18:10:30 - 05/09 18:17:24	Fixed (-528.4, -528.4)	# XRT post bake-out quadrant pointings 1/4.
PROG= 03 1-time(s)		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 1 1-time(s) 12.0sec		
Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Dark 44ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Dark 44ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 6 2-time(s) 2.0sec		
Open/Al-mesh	Open/Ti-poly close	Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 8.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 12 1-time(s) 2.0sec		
Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ Seqn= 7 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1983: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh, Ti/Poly-long - w leak image-33 ms

Term	Pointing (x, y)	Comment
05/09 18:20:30 - 05/09 18:27:24	Fixed (528.4, -528.4)	# 2/4
PROG= 10 1-time(s)		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 2 1-time(s) 12.0sec		
Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Dark 44ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Dark 44ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 6 2-time(s) 2.0sec		
Open/Al-mesh	Open/Ti-poly close	Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 8.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 12 1-time(s) 2.0sec		
Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ Seqn= 7 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1984: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant - Al/mesh, Ti/Poly-long - w leak image-33 ms

Term	Pointing (x, y)	Comment
05/09 18:30:30 - 05/09 19:16:54	Fixed (528.4, 528.4)	# 3/4
PROG= 09 1-time(s)		

Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 3 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 6 2-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 3 2-time(s) 2.0sec													
└─ Seqn= 12 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 7 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1985: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh, Ti/Poly-long - w leak image-33 ms

Term	Pointing (x, y)	Comment
05/09 19:20:00 - 05/09 19:26:54	Fixed (-528.4, 528.4)	# 4/4

PROG= 04 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 4 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 6 2-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Subr= 3 1-time(s) 2.0sec													
└─ Seqn= 12 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 7 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #190B: G-Band Alignment with North Pole Q90 2x2(G-band only) - 12msec - 5min cadence - Partial Sun-wNGT-2

Term	Pointing (x, y)	Comment
05/09 19:30:04 - 05/09 21:33:54	Fixed (0.0, 945.0)	# Coalignment at the North Pole.

PROG= 15 1-time(s)													
└─ Subr= 1 1-time(s) 360.0sec													
└─ Seqn= 32 24-time(s) 300.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1993: Eclipse20130509_Ti-Poly_Al-mesh_Movie_DCpnt-duration-25min-30s_cad

Term	Pointing (x, y)	Comment
05/09 21:37:00 - 05/09 21:51:30	Fixed (0.0, 0.0)	# Eclipse Observations at disk center. Synoptic at 3.45UT.
05/10 03:18:36 - 05/10 03:45:00	Fixed (0.0, 0.0)	# Eclipse Observations at disk center. Synoptic at 3.45UT.

PROG= 11 1-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 68 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=92	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=92	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=92	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	63ms	Obs	4x4	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 69 50-time(s) 30.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=92	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1992: Eclipse20130509_Ti-Poly_Al-mesh_Movie_DCpnt-duration-25min

Term	Pointing (x, y)	Comment
05/10 01:38:06 - 05/10 02:44:30	Fixed (0.0, 0.0)	# Eclipse Observations at disk center. Synoptic at 3.45UT.

PROG= 01 1-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 68 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec

Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=92	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=92	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=92	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	63ms	Obs	4x4	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 69 75-time(s) 20.0sec												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=92	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #190C: G-Band Alignment with East limb Q90 2x2 (G-band only) - 12msec- 8 min cadence-wNGT-2												
Term			Pointing (x, y)				Comment					
05/10 03:57:58 - 05/10 05:54:30			Fixed (-945.0, 0.0)				# Coalignment at the East limb.					
PROG= 17 1-time(s)												
Subr= 1 1-time(s) 360.0sec												
Seqn= 33 15-time(s) 480.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1994: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 384x384 at 1064 1048, 180s cad With G-band and VLS closed												
Term			Pointing (x, y)				Comment					
05/10 06:34:06 - 05/10 17:33:30			Track (207.1, 225.3) @ 05/10 05:55:00				# AR 11739.					
05/10 18:29:30 - 05/11 00:07:00			Track (315.7, 222.6) @ 05/10 18:26:30				# AR 11739.					
05/11 04:08:00 - 05/11 06:02:24			Track (396.3, 220.1) @ 05/11 04:05:00				# AR 11739.					
05/11 06:15:30 - 05/11 09:37:00			Track (413.6, 219.5) @ 05/11 06:12:30				# AR 11739.					
PROG= 14 Inf-time(s)												
Subr= 2 1-time(s) 2.0sec												
Seqn= 67 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 62 4-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 63 40-time(s) 240.0sec												
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

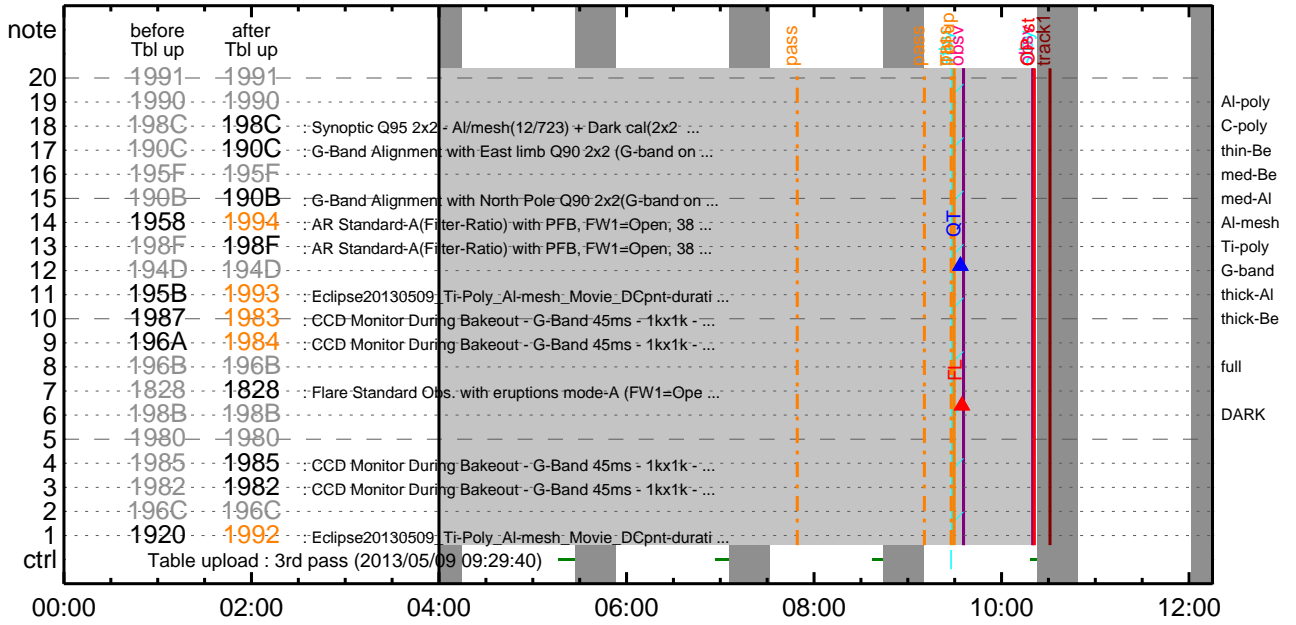
* * * * *

Flare mode

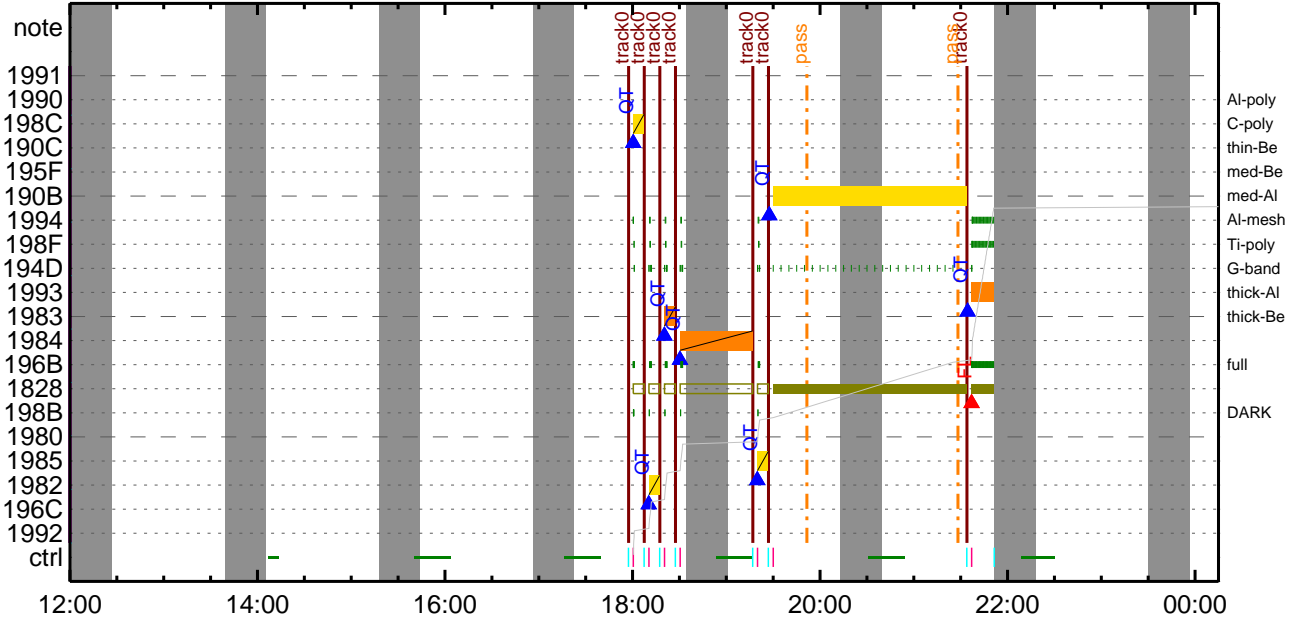
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XOB #1828: Flare Standard Obs. with eruptions mode-A (FW1=Open)												
Term			Pointing (x, y)				Comment					
05/09 19:30:04 - 05/09 21:33:54			Fixed (0.0, 945.0)				# Coalignment at the North Pole.					
05/09 21:37:00 - 05/09 21:51:30			Fixed (0.0, 0.0)				# Eclipse Observations at disk center. Synoptic at 3.45UT.					
05/10 01:38:06 - 05/10 02:44:30			Fixed (0.0, 0.0)				# Eclipse Observations at disk center. Synoptic at 3.45UT.					
05/10 03:18:36 - 05/10 03:45:00			Fixed (0.0, 0.0)				# Eclipse Observations at disk center. Synoptic at 3.45UT.					
05/10 03:57:58 - 05/10 05:54:30			Fixed (-945.0, 0.0)				# Coalignment at the East limb.					
05/10 06:34:06 - 05/10 17:33:30			Track (207.1, 225.3) @ 05/10 05:55:00				# AR 11739.					
05/10 18:29:30 - 05/11 00:07:00			Track (315.7, 222.6) @ 05/10 18:26:30				# AR 11739.					
05/11 04:08:00 - 05/11 06:02:24			Track (396.3, 220.1) @ 05/11 04:05:00				# AR 11739.					
05/11 06:15:30 - 05/11 09:37:00			Track (413.6, 219.5) @ 05/11 06:12:30				# AR 11739.					
PROG= 07 1-time(s)												
Subr= 1 30-time(s) 20.0sec												
Seqn= 58 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 64 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 59 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 58 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

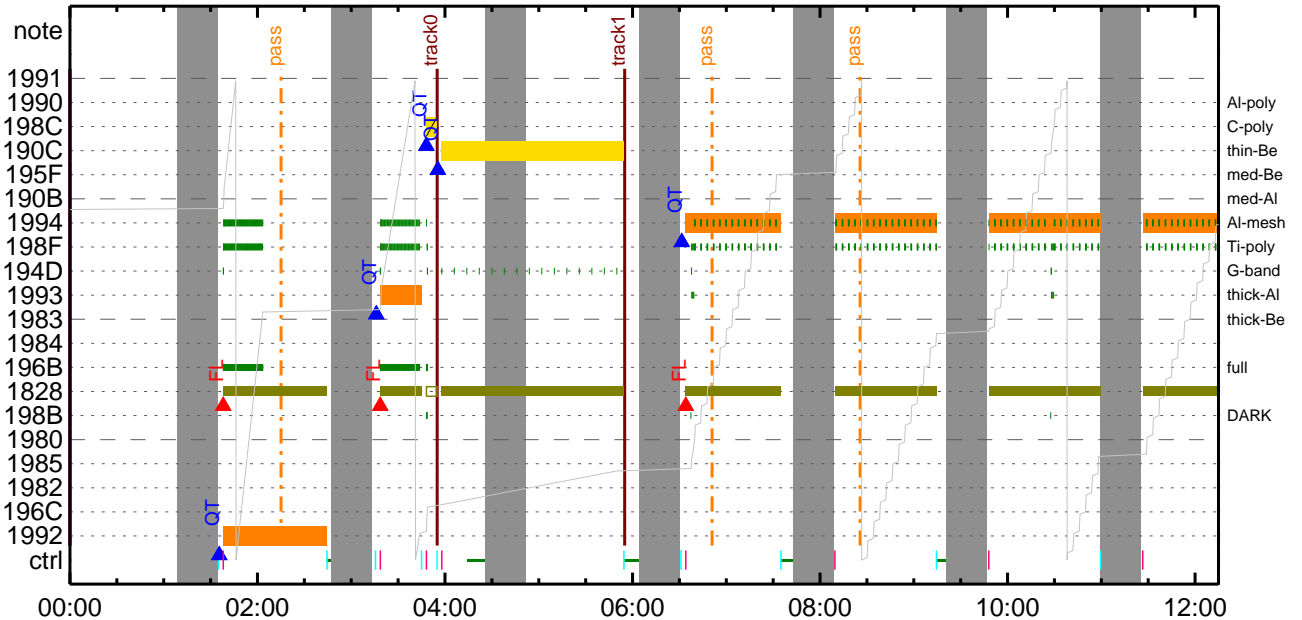
CMDI #0445 2013/05/09



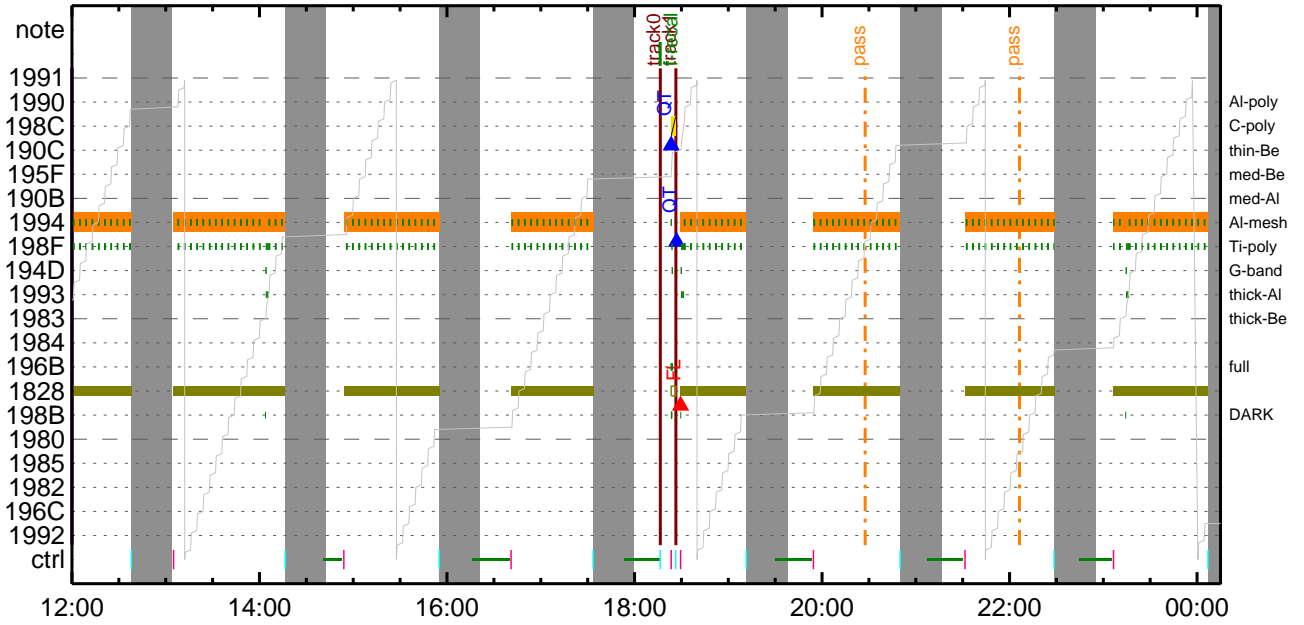
CMDI #0445 2013/05/09



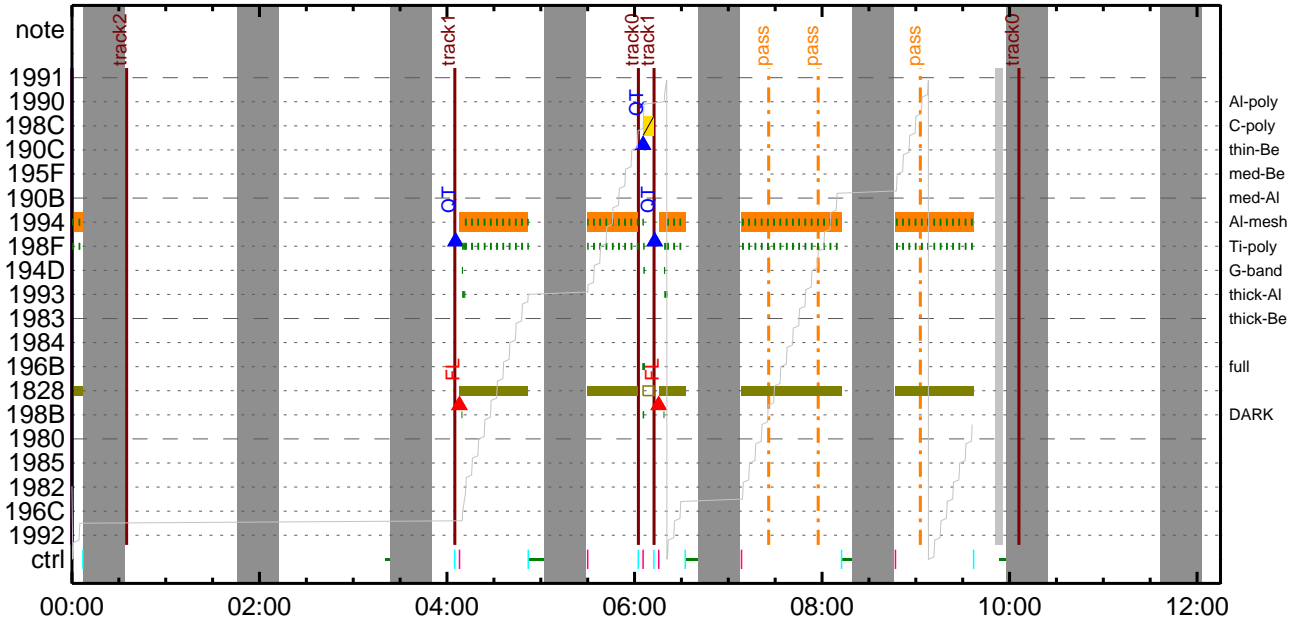
CMDI #0445 2013/05/10



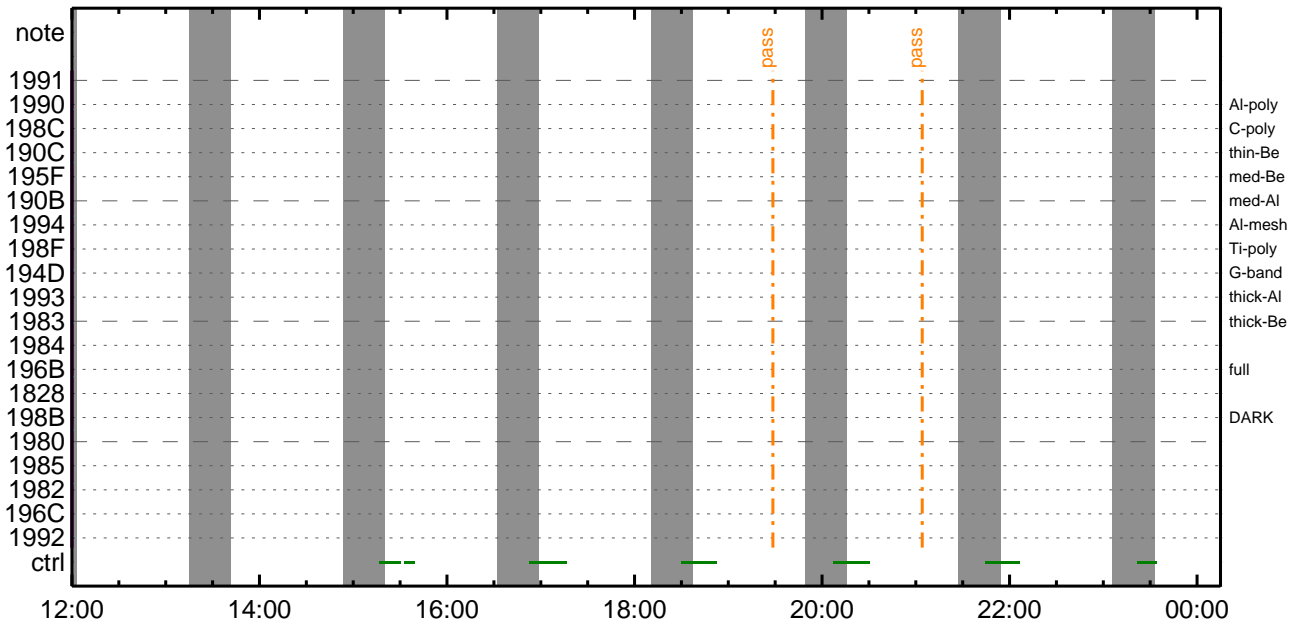
CMDI #0445 2013/05/10



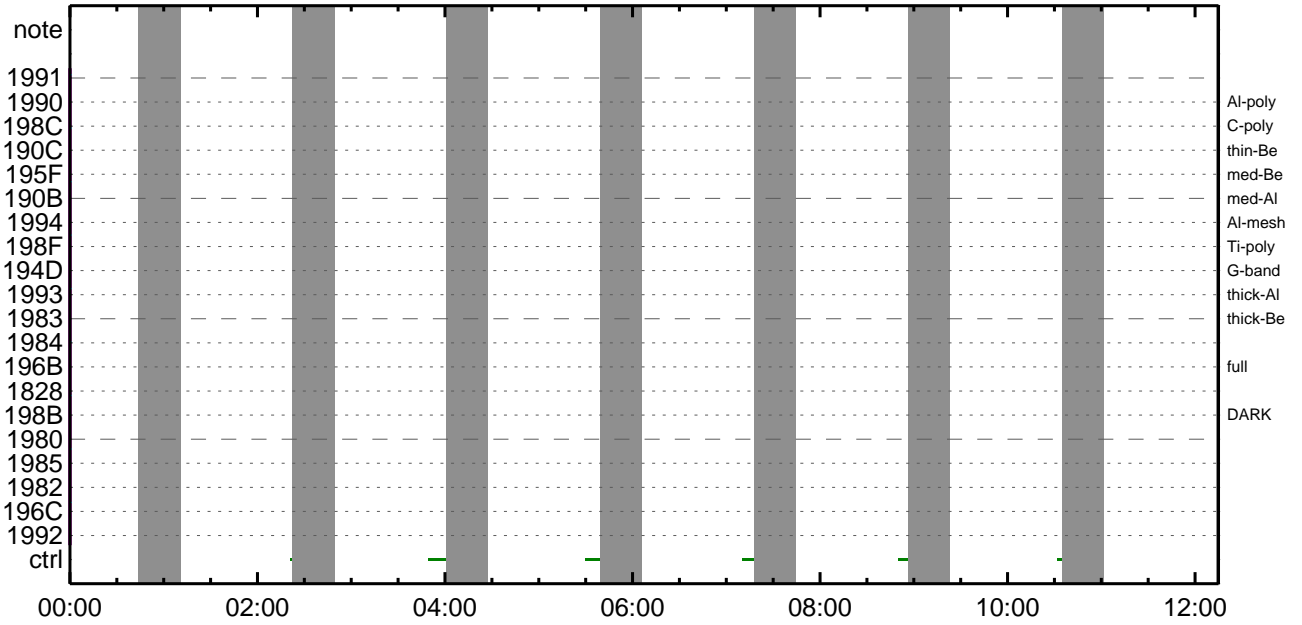
CMDI #0445 2013/05/11



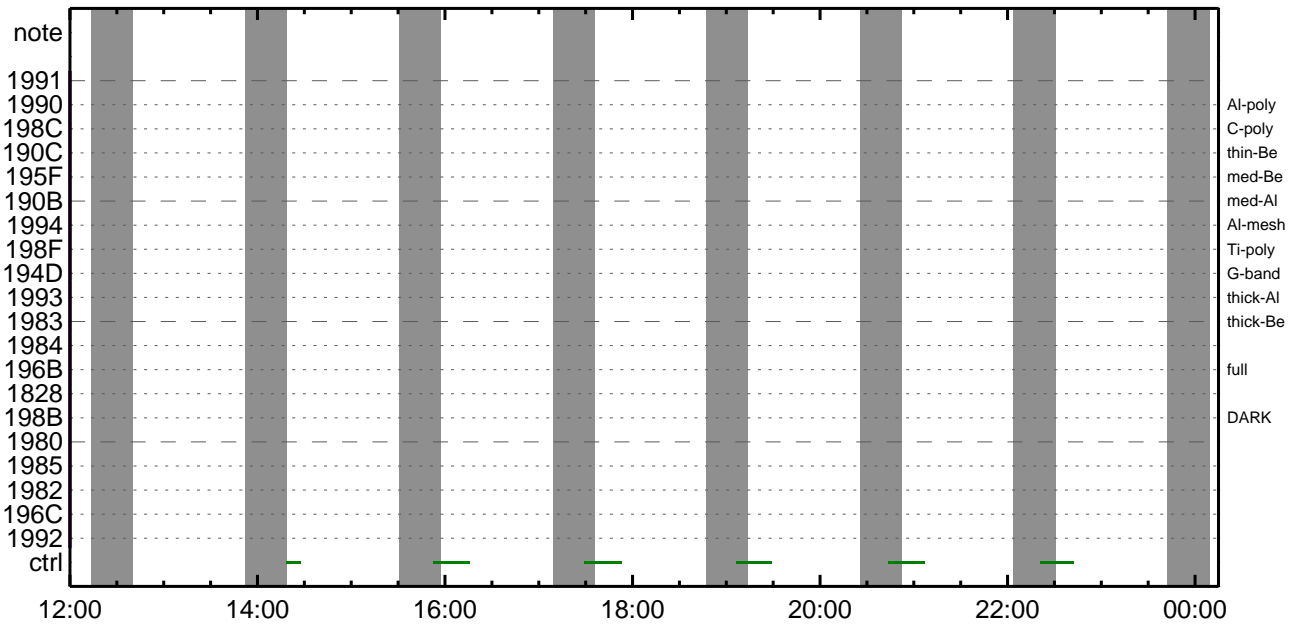
CMDI #0445 2013/05/11



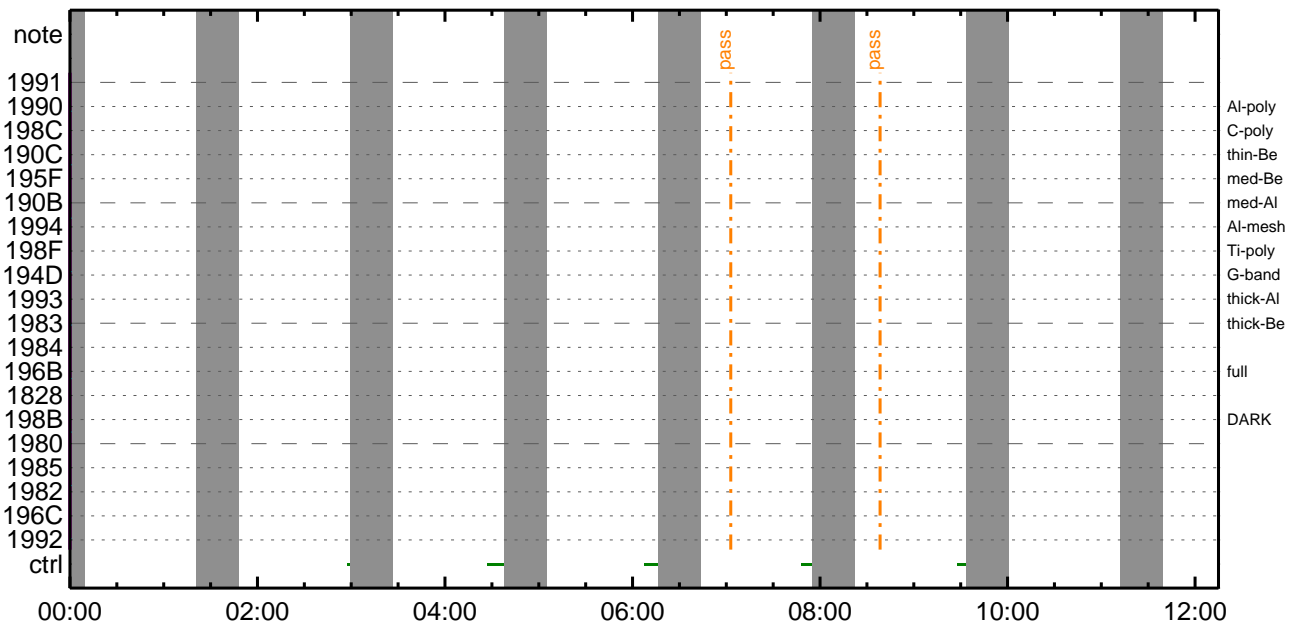
CMDI #0445 2013/05/12



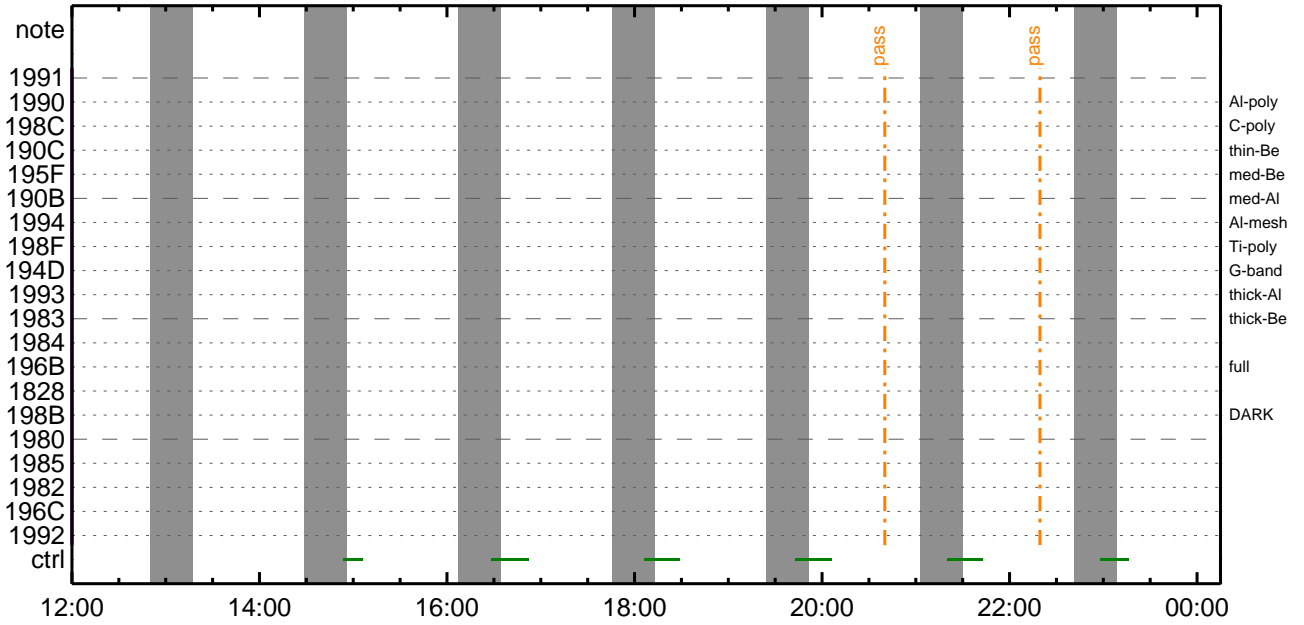
CMDI #0445 2013/05/12



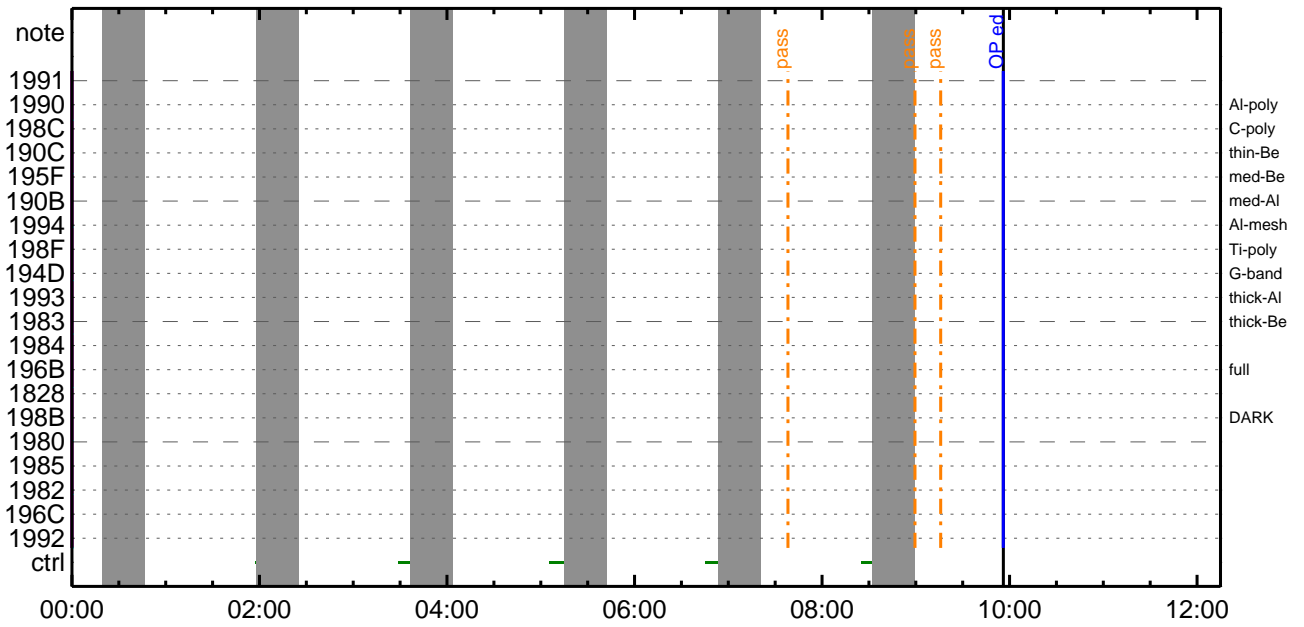
CMDI #0445 2013/05/13



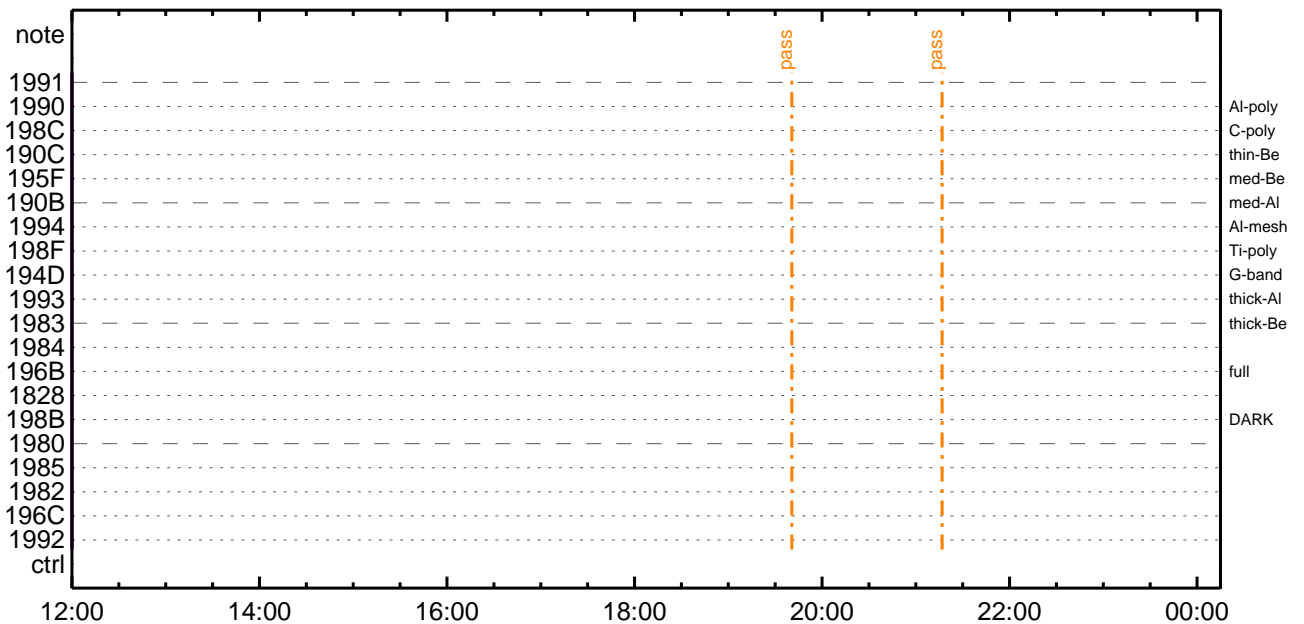
CMDI #0445 2013/05/13



CMDI #0445 2013/05/14



CMDI #0445 2013/05/14




```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-637:OP
0104 ( )
0105 S. OG og-637:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ª î»ò³ ÎÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGªî½ª¹ç•è² İOKªò³ ÎÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ª î»ò³ ÎÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGªî½ª¹ç•è² İOKªò³ ÎÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ª î»ò³ ÎÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPªî½ª¹ç•è² İOKªò³ ÎÇ§
0165 C.
0166 C. ***** °ê²¼ª İ½ª¹ç•è² İOKªò³ ÎÇ§ *****
0167 C. DHUYâ;4YE;ê½ª¹ç•è² İOKªò³ ÎÇ§
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOADª-Á÷ç@NGªî½ª¹ç•è² İOKªò³ ÎÇ§
0180 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0181 C.
0182 C. TIY³YpYóYêªò³ ÎÇ§ (UT)
0183 +. TI 2013-05-09 10:16:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2013-05-09 10:16:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2013-05-09 10:16:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2013-05-09 10:20:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼ïãîÄè%îíñòîîŷÄŷ§ŷÄŷ-¹àîÛ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]                    EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]                        EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]                     EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]                       EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]                    EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]                    EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]                      EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]                     EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]                        EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]                        EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]                       EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]                     EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]                     EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]                         EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»ò³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]                         EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLîî½È¹ç•è²îOKòò³îç§
0229 C.
0230 C. DHUŷâ;¼ŷÈ;È¼ŷ¼.ŷî;¼ŷÈ;Èòòîãò¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]                        EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]                       EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]                     EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]                     EQ      4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2013-05-09 10:20:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2013-05-09 10:20:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.          [ ] [HK1_TI_CMD_NUM]                      EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C. *****
0255 C. SOT TI command set
0256 C. *****
0257 C. Execute, after the success of OP upload.
0258 +. TI 2013-05-09 10:20:16.0
0259 DC 07-F0 MDP_SOT_MODE_STBY
0260 BC      (41)
0261 C. -----
0262 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0263 C. -----
0264 C. ***** SOT END *****
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2013-05-09 10:20:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃîãî»ö¼ŷòÈÄò¹òèDCBC•x²è *****
0276 C. (%ã°îŷÖŷÄŷÈŷŷŷÄŷçŷèè¼òò¼Ä»Ûò¹è)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷĐŷ¹•î Daily±çîñòÈ´òò¹òèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼Ä»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

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(a) Spacecraft Operation Procedure (real-commands)

```

main-639 2013-05-09 12:31:53 222 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÄY-¼Á»Û;ä
0005 C.
0006 C. YÀYß;¼Y³YÞYÓYÉÁ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;ÈçðÅµ°Æ»Í×ÁÇøÍYçYÁY×YÍ;¼YÉ;ÈÈÈµ°ííÈ;ÈøÈ¼°ÇÖµ°ç¼í¹ççÍ;çÀ®. ùµ¹µèµµçÁ+ç®µ°èµµµ³µÈ;ç
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+ç®µ;ON
0016 C. *****
0017 C. ç°ÆÀ. í×ÈYµäLòSµççí»p´Öµð¹íí. µ.; çÉÖÍ×µÈXÁÖONµí¹Öµèµíµèµµ³µÈ;ç
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 +. DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 +. DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYÓYÉYÍYÄY-¾ÖÅÖµ°ççµé; ç°È²¼µí°ÆÀ. ¼è¾çµð½Á¹Öµ¹µè;ç
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°ÆÀ.
0033 C. *****
0034 C. ç° RESTART;ÈPT1;Èµ°ççµµ¼í¹ççÍ; ç°È²¼µí°ÆÀ¹Öµ»µ°; çDCBC-150µøçÈµà;ç
0035 C.
0036 . C. ;ãPT1°ÆÀ. ³«»Í;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 +. DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 +. DC 06-B3 DR_REP_START
0041 +. DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö. ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö. ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö. ;¼Ú)
0045 C.
0046 . C. ;ãYçYÓYÆYÈÁÙÅØ;ÈÁ·Á°²óÈø;È. áµí°ÆÀ. °Æ³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 +. DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö. ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö. ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö. ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÀ. µ-¼«Æ°Áá»ßµ°çç. á; ç°È²¼µð½Á¹Öµ¹µè;ç
0055 C. YçYÓYÆYÈÁÙÅØµ°Á·Á°²óÈøµ-¾áµµ¼í¹ççÍ°í»µ¹µèµµçÁÖµÁ;ç
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°ÆÀ.
0059 C. *****
0060 C. ç° RESTART;ÈPT2;Èµ°ççµµ¼í¹ççÍ; ç°È²¼µí°ÆÀ¹Öµ»µ°; çDCBC-151µøçÈµà;ç
0061 C.
0062 . C. ;ãPT2°ÆÀ. ³«»Í;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 +. DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 +. DC 06-B3 DR_REP_START
0067 +. DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö. ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö. ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö. ;¼Ú)
0071 C.
0072 . C. ;ãYçYÓYÆYÈÁÙÅØ;ÈÁ·Á°²óÈø;È. áµí°ÆÀ. °Æ³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 +. DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö. ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö. ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö. ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÀ. Áá»ß;çXÁ+ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÀ. Áá»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 +. DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+ç®µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 +. DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF

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```

0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 80 80 06 06)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 20)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 20 08)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 80 80 08 20)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0b c0 c0 10 10)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0c 40 c0 10 10)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 0d 40 40 10 10)
0142 + DC 07-F0 MDP_XRT_ROI_SET
0143 BC (cd 0e c0 40 10 10)
0144 + DC 07-F0 MDP_XRT_ROI_SET
0145 BC (cd 0f 80 80 06 06)
0146 + DC 07-F0 MDP_XRT_ROI_SET
0147 BC (cd 10 80 80 08 08)
0148 + DC 07-F0 MDP_XRT_FLD_ENA
0149 BC (d8)
0150 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0151 BC (c8)
0152 + DC 07-F0 MDP_XRT_AEC_RESET
0153 BC (d0)
0154 + DC 07-F0 MDP_XRT_ARS_DIS
0155 BC (d5)
0156 + DC 07-F0 MDP_XRT_FLD_RESET
0157 BC (da)
0158 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0159 BC (c4 0d)
0160 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0161 BC (c5 07)
0162 . C. ----- Success Verify ? OK / NG ____
0163 C.
0164 C.
0165 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0166 C.
0167 +. DC 07-F0 MDP_XRT_MODE_OBSV
0168 BC (c2)
0169 +. TI 2013-05-09 10:20:02.0
0170 DC 07-F0 MDP_XRT_MODE_OBSV
0171 BC (c2)
0172 . C. ----- Success Verify ? OK / NG ____
0173 C.
0174 C. ***** XRT END *****
0175 . C. *****
0176 C. SOT table upload
0177 C. *****
0178 . C. < Stop FG table >
0179 +. DC 07-F0 MDP_FG_CTRL_MANU
0180 BC (51)
0181 . C. -----
0182 C. MDP_FG_CTRL_MODE = MANU [ ]
0183 C. -----
0184 C.
0185 . C. <Upload FG Observation Table>
0186 . S. RAM ram-268:MDP_OBS_F
0187 ( )
0188 C.
0189 . C. < Dump RAMID=MDP_OBS_F >
0190 +. DC 07-F0 MDP_DUMP_FGTBL
0191 BC (82 07 00 00 00 38 b8)
0192 C. -----
0193 C. MDP_OBS_F verify = OK/NG [ ]

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```
0194 C. -----
0195 C.
0196 C. *****
0197 C. SOT TI command set
0198 C. *****
0199 C. Execute, after the success of TBL upload.
0200 +. TI 2013-05-09 10:20:18.0
0201 DC 07-F0 MDP_SOT_MODE_OBSV
0202 BC (40)
0203 . C. -----
0204 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0205 C. -----
0206 C.
0207 C.
0208 . C. ***** MDP 'úÃîðî»ô¼ÝðÊÂð¹ñèDCBC•x²è *****
0209 C. (¼ã°îÝÓÝÄÝËÝÞÝËÝÁÝçÝèñÈ¼ñ¼Ã»Ûñ¹è)
0210 . S. DC-BC dcbc-402:DCBC
0211 (MDP_known_event)
0212 C.
0213 C.
0214 . C. ***** ÝÐÝ¹•Ï Daily±¿ÎÑñË´Øñ¹èDCBC•x²è *****
0215 . S. DC-BC dcbc-153:DCBC
0216 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0217 C.
0218 C.
0219 . C. ;ãLOSÝÁÝ$ÝÄÝ¬¼Ã»Û;ã
0220 C.
0221 . C. ***** LOS *****
0222 C.
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*** OP Sequence for XRT ***

2013/05/09	10:31:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01	00	00	00	00
2013/05/09	17:57:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/05/09	17:57:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2013/05/09	17:57:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2013/05/09	17:57:46.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/05/09	18:00:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/05/09	18:00:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/05/09	18:00:28.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2013/05/09	18:00:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/05/09	18:07:24.0	XRT_CTRL_MANU_420_OG [0x1a4]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/05/09	18:07:30.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	2e	f9	2e	f9
2013/05/09	18:10:02.0	XRT_FOCUS_POSITION_421_OG [0x1a5]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2013/05/09	18:10:22.0	XRT_QT_PROG_SET_449_OG [0x1c1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2013/05/09	18:10:24.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/05/09	18:10:26.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/05/09	18:10:28.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/05/09	18:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/05/09	18:17:24.0	XRT_CTRL_MANU_420_OG [0x1a4]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/05/09	18:17:30.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	2e	f9	d1	07
2013/05/09	18:20:02.0	XRT_FOCUS_POSITION_421_OG [0x1a5]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2013/05/09	18:20:22.0	XRT_QT_PROG_SET_441_OG [0x1b9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2013/05/09	18:20:24.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/05/09	18:20:26.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/05/09	18:20:28.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/05/09	18:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/05/09	18:27:24.0	XRT_CTRL_MANU_420_OG [0x1a4]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/05/09	18:27:30.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	d1	07	d1	07
2013/05/09	18:30:02.0	XRT_FOCUS_POSITION_421_OG [0x1a5]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2013/05/09	18:30:22.0	XRT_QT_PROG_SET_429_OG [0x1ad]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	09			
2013/05/09	18:30:24.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/05/09	18:30:26.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/05/09	18:30:28.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/05/09	18:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/05/09	19:16:54.0	XRT_CTRL_MANU_420_OG [0x1a4]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/05/09	19:17:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	d1	07	2e	f9
2013/05/09	19:19:32.0	XRT_FOCUS_POSITION_421_OG [0x1a5]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2013/05/09	19:19:52.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2013/05/09	19:19:54.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/05/09	19:19:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/05/09	19:19:58.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/05/09	19:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/05/09	19:26:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/05/09	19:26:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2013/05/09	19:27:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	ac	00	00	00
2013/05/09	19:27:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/05/09	19:27:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							

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2013/05/09	19:27:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
			MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/05/09	19:27:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/05/09	19:27:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/05/09	19:27:26.0	XRT_ROI_A_442_OG [0x1ba]	MDP_XRT_ROI_SET	6	07-F0	cd 05 85 83 06 06		
			MDP_XRT_ROI_SET	6	07-F0	cd 06 85 83 06 06		
			MDP_XRT_ROI_SET	6	07-F0	cd 07 80 80 06 06		
			MDP_XRT_ROI_SET	6	07-F0	cd 08 80 80 20 20		
			MDP_XRT_ROI_SET	6	07-F0	cd 09 80 80 20 08		
			MDP_XRT_ROI_SET	6	07-F0	cd 0a 80 80 08 20		
			MDP_XRT_ROI_SET	6	07-F0	cd 0b 80 60 20 18		
			MDP_XRT_ROI_SET	6	07-F0	cd 0c a0 80 18 20		
2013/05/09	19:27:26.5	XRT_ROI_B_432_OG [0x1b0]	MDP_XRT_ROI_SET	6	07-F0	cd 0c a0 80 18 20		
			MDP_XRT_ROI_SET	6	07-F0	cd 0f 80 80 06 06		
			MDP_XRT_ROI_SET	6	07-F0	cd 10 80 80 08 08		
2013/05/09	19:27:31.5	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f		
2013/05/09	19:30:04.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/05/09	21:33:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/05/09	21:33:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/05/09	21:34:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00		
2013/05/09	21:34:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/05/09	21:34:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/05/09	21:34:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/05/09	21:34:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/05/09	21:34:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/05/09	21:34:26.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b		
2013/05/09	21:36:58.0	XRT_FL_PROG_SET_435_OG [0x1b3]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07		
2013/05/09	21:37:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/05/09	21:51:30.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/05/10	01:35:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/05/10	01:35:02.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/05/10	01:35:22.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/05/10	01:35:24.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/05/10	01:35:26.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/05/10	01:35:28.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/05/10	01:35:30.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/05/10	01:35:32.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01		
2013/05/10	01:38:04.0	XRT_FL_PROG_SET_435_OG [0x1b3]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07		
2013/05/10	01:38:06.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/05/10	02:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/05/10	02:44:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/05/10	02:44:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/05/10	02:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/05/10	03:15:30.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/05/10	03:15:32.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/05/10	03:15:52.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/05/10	03:15:54.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/05/10	03:15:56.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/05/10	03:15:58.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/05/10	03:16:00.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/05/10	03:16:02.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b		
2013/05/10	03:18:34.0	XRT_FL_PROG_SET_435_OG [0x1b3]						

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2013/05/10	03:18:36.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/05/10	03:45:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/05/10	03:45:02.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2013/05/10	03:45:22.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/05/10	03:48:00.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/05/10	03:48:02.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/05/10	03:48:04.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12
2013/05/10	03:48:06.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/05/10	03:54:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/05/10	03:54:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00
2013/05/10	03:55:00.0	AOCS_OrE-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00 00 00	54 00
2013/05/10	03:55:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/05/10	03:55:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/05/10	03:55:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/05/10	03:55:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/05/10	03:55:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/05/10	03:55:26.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11
2013/05/10	03:57:58.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/05/10	05:54:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/05/10	05:54:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/05/10	05:54:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/05/10	05:55:00.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00	00 00
2013/05/10	05:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/05/10	06:31:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/05/10	06:31:02.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00
2013/05/10	06:31:22.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/05/10	06:31:24.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/05/10	06:31:26.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/05/10	06:31:28.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/05/10	06:31:30.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/05/10	06:31:32.0	XRT_QT_PROG_SET_443_OG [0x1bb]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e
2013/05/10	06:34:04.0	XRT_FL_PROG_SET_435_OG [0x1b3]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2013/05/10	06:34:06.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/05/10	07:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/05/10	07:35:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/05/10	07:35:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/05/10	07:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/05/10	08:08:30.0	XRT_Custom_430_OG [0x1ae]					
2013/05/10	08:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/05/10	09:14:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/05/10	09:14:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/05/10	09:14:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/05/10	09:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/05/10	09:47:00.0	XRT_Custom_430_OG [0x1ae]					
2013/05/10	09:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/05/10	10:59:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/05/10	10:59:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	

2013/05/10	10:59:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/05/10	11:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/05/10	11:25:30.0	XRT_Custom_430_OG [0x1ae]							
2013/05/10	11:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/05/10	12:38:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/05/10	12:38:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/05/10	12:38:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/05/10	12:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/05/10	13:04:00.0	XRT_Custom_430_OG [0x1ae]							
2013/05/10	13:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/05/10	14:16:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/05/10	14:16:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/05/10	14:16:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/05/10	14:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/05/10	14:53:00.0	XRT_Custom_430_OG [0x1ae]							
2013/05/10	14:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/05/10	15:55:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/05/10	15:55:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/05/10	15:55:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/05/10	15:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/05/10	16:40:00.0	XRT_Custom_430_OG [0x1ae]							
2013/05/10	16:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/05/10	17:33:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/05/10	17:33:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/05/10	17:33:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/05/10	17:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/05/10	18:16:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/05/10	18:16:26.0	XRT_FOCUS_RECALIBRATE_433_OG [0x1b1]							
		XRT_FOCUS_RECAL	2	07-F8	78 00				
2013/05/10	18:16:30.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2013/05/10	18:20:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/05/10	18:20:46.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/05/10	18:23:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/05/10	18:23:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/05/10	18:23:28.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2013/05/10	18:23:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/05/10	18:26:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/05/10	18:26:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/05/10	18:26:30.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2013/05/10	18:26:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/05/10	18:26:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/05/10	18:26:50.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/05/10	18:26:52.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/05/10	18:26:54.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/05/10	18:26:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2013/05/10	18:29:28.0	XRT_FL_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2013/05/10	18:29:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/05/10	19:11:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/05/10	19:11:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/05/10	19:11:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							

2013/05/10	19:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/05/10	19:53:30.0	XRT_Custom_430_OG [0x1ae]						
2013/05/10	19:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/05/10	20:50:00.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/05/10	20:50:02.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/05/10	20:50:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/05/10	20:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/05/10	21:30:30.0	XRT_Custom_430_OG [0x1ae]						
2013/05/10	21:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/05/10	22:28:30.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/05/10	22:28:32.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/05/10	22:28:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/05/10	22:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/05/10	23:05:30.0	XRT_Custom_430_OG [0x1ae]						
2013/05/10	23:06:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/05/11	00:07:00.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/05/11	00:07:02.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/05/11	00:07:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/05/11	00:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/05/11	00:35:00.0	AOCS_ORe-point_Start_9_OG [0x09f]						
			AOCU_NM	5	02-76	02 00 00 00 00		
2013/05/11	04:04:54.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/05/11	04:04:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/05/11	04:05:00.0	AOCS_ORe-point_Start_1_OG [0x097]						
			AOCU_NM	5	02-76	01 00 00 00 00		
2013/05/11	04:05:16.0	XRT_FLD_ENA_411_OG [0x19b]						
			MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/05/11	04:05:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/05/11	04:05:20.0	XRT_AEC_RESET_413_OG [0x19d]						
			MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/05/11	04:05:22.0	XRT_ARS_DIS_423_OG [0x1a7]						
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/05/11	04:05:24.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/05/11	04:05:26.0	XRT_QT_PROG_SET_443_OG [0x1bb]						
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e		
2013/05/11	04:07:58.0	XRT_FL_PROG_SET_435_OG [0x1b3]						
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 07		
2013/05/11	04:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/05/11	04:52:00.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/05/11	04:52:02.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/05/11	04:52:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/05/11	04:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/05/11	05:29:00.0	XRT_Custom_430_OG [0x1ae]						
2013/05/11	05:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/05/11	06:02:24.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/05/11	06:02:26.0	XRT_FOCUS_POSITION_403_OG [0x193]						
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2013/05/11	06:02:30.0	AOCS_ORe-point_Start_2_OG [0x098]						
			AOCU_NM	5	02-76	00 00 00 00 00		
2013/05/11	06:02:46.0	XRT_FLD_DIS_434_OG [0x1b2]						
			MDP_XRT_FLD_DIS	1	07-F0	d9		
2013/05/11	06:05:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]						
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2013/05/11	06:05:26.0	XRT_ARS_DIS_423_OG [0x1a7]						
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/05/11	06:05:28.0	XRT_QT_PROG_SET_447_OG [0x1bf]						
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 12		
2013/05/11	06:05:30.0	XRT_CTRL_AUTO_408_OG [0x198]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/05/11	06:12:24.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/05/11	06:12:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/05/11	06:12:30.0	AOCS_ORe-point_Start_1_OG [0x097]						
			AOCU_NM	5	02-76	01 00 00 00 00		

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2013/05/11	06:12:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2013/05/11	06:12:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/05/11	06:12:50.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2013/05/11	06:12:52.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/05/11	06:12:54.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/05/11	06:12:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2013/05/11	06:15:28.0	XRT_FL_PROG_SET_435_OG [0x1b3]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2013/05/11	06:15:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/05/11	06:32:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/05/11	06:32:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/05/11	06:32:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/05/11	06:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/05/11	07:07:30.0	XRT_Custom_430_OG [0x1ae]			
2013/05/11	07:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/05/11	08:12:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/05/11	08:12:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/05/11	08:12:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/05/11	08:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/05/11	08:46:00.0	XRT_Custom_430_OG [0x1ae]			
2013/05/11	08:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/05/11	09:37:00.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/05/11	10:06:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00