

XRT Timeline to be uploaded on 2013/07/06

Period: 2013/07/06 10:24:00 - 2013/07/11 10:12:00

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Normal mode

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XOB #199F: HOP130-Synoptic 3 Filter- 2x2 Q95 (Almesh 12/128/723ms; Tipoly 24/362/1443ms; Althick 65536ms) with G-band												
Term	Pointing (x, y)				Comment							
07/06 11:09:00 - 07/06 11:21:54	Fixed (0.0, 0.0)				HOP130 3/15							
PROG= 19 2-time(s)												
└─ Subr= 1 1-time(s) 180.0sec												
└─ Seqn= 85 1-time(s) 25.0sec												
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 86 1-time(s) 25.0sec												
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 75 1-time(s) 2.0sec												
└─ Open/thick-Al	Open/thick-Al	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 66 1-time(s) 2.0sec												
└─ Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
└─ Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #199A: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 384x384 at 1064 1048, 100s cad With G-band and VLS closed												
Term	Pointing (x, y)				Comment							
07/06 16:31:30 - 07/06 17:59:54	Track (-277.4, -259.5) @ 07/06 16:28:30				AR 11785							
07/06 18:13:00 - 07/07 06:19:54	Track (-263.0, -259.9) @ 07/06 18:10:00				#AR 11785							
07/07 06:33:00 - 07/07 17:13:30	Track (-156.2, -262.3) @ 07/07 06:30:00				#AR 11785							
07/07 18:13:00 - 07/08 05:46:24	Track (-53.1, -263.9) @ 07/07 18:10:00				#AR 11785							
07/08 05:59:30 - 07/08 09:35:00	Track (51.7, -264.7) @ 07/08 05:56:30				#AR 11785							
07/08 16:03:00 - 07/08 17:35:54	Track (140.7, -264.8) @ 07/08 16:00:00				AR 11785							
PROG= 06 Inf.-time(s)												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 78 1-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└─ Seqn= 62 4-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Seqn= 63 40-time(s) 100.0sec												
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #199E: Synoptic Q95 2x2 - Al/mesh(12/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(33/362/1443) + G-b												
Term	Pointing (x, y)				Comment							
07/06 18:03:00 - 07/06 18:09:54	Fixed (0.0, 0.0)				synoptic							
07/07 06:23:00 - 07/07 06:29:54	Fixed (0.0, 0.0)				synoptic, shifted 20.0 min							
07/07 18:03:00 - 07/07 18:09:54	Fixed (0.0, 0.0)				synoptic							
07/08 05:49:30 - 07/08 05:56:24	Fixed (0.0, 0.0)				synoptic, shifted -13.5 min							
07/08 17:39:00 - 07/08 17:45:54	Fixed (0.0, 0.0)				synoptic, shifted -24.0 min							
07/09 06:03:00 - 07/09 06:52:00	Fixed (0.0, 0.0)				Extended synoptic/ SOT FG dark calibration							
PROG= 16 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 82 1-time(s) 4.0sec												
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5 1-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 83 1-time(s) 4.0sec												
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2	1-time(s)		2.0sec										
	Seqn= 79		1-time(s) 2.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3	30-time(s)		60.0sec										
	Seqn= 58		1-time(s) 2.0sec										
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Seqn= 60		1-time(s) 2.0sec										
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2	1-time(s)		2.0sec										
	Seqn= 79		1-time(s) 2.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3	30-time(s)		60.0sec										
	Seqn= 58		1-time(s) 2.0sec										
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Seqn= 60		1-time(s) 2.0sec										
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 4	24-time(s)		600.0sec										
	Seqn= 61		1-time(s) 2.0sec										
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

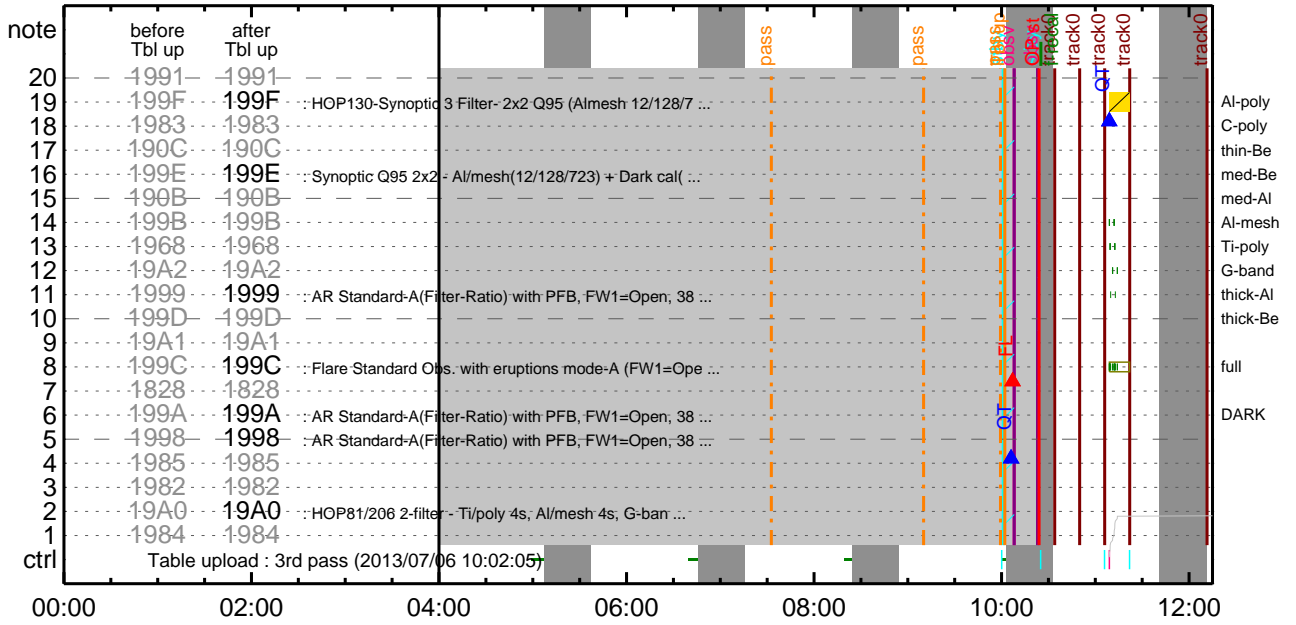
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Flare Detection

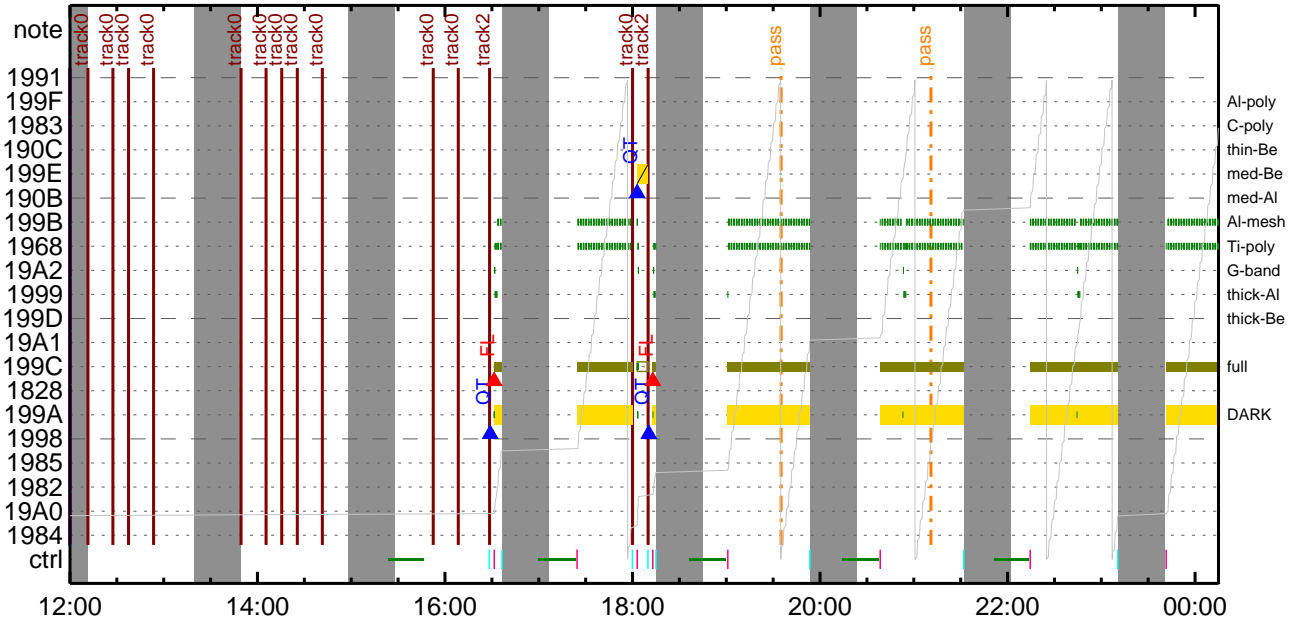
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FLD Patrol												
Term	Pointing (x, y)								Comment			
07/06 16:28:46 - 07/06 18:00:16	Track (-277.4,	-259.5)	[Ⓢ] 07/06 16:28:30	AR	11785						
07/06 18:10:16 - 07/07 06:20:16	Track (-263.0,	-259.9)	[Ⓢ] 07/06 18:10:00	#AR	11785						
07/07 06:30:16 - 07/07 18:00:16	Track (-156.2,	-262.3)	[Ⓢ] 07/07 06:30:00	#AR	11785						
07/07 18:10:16 - 07/08 05:46:46	Track (-53.1,	-263.9)	[Ⓢ] 07/07 18:10:00	#AR	11785						
07/08 05:56:46 - 07/08 17:36:16	Track (51.7,	-264.7)	[Ⓢ] 07/08 05:56:30	#AR	11785						
07/08 17:46:16 - 07/09 06:00:16	Track (156.2,	-264.7)	[Ⓢ] 07/08 17:46:00	#AR	11785						
07/09 09:10:16 - 07/11 10:12:00	Track (289.0,	-263.6)	[Ⓢ] 07/09 09:10:00	#AR	11785						
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

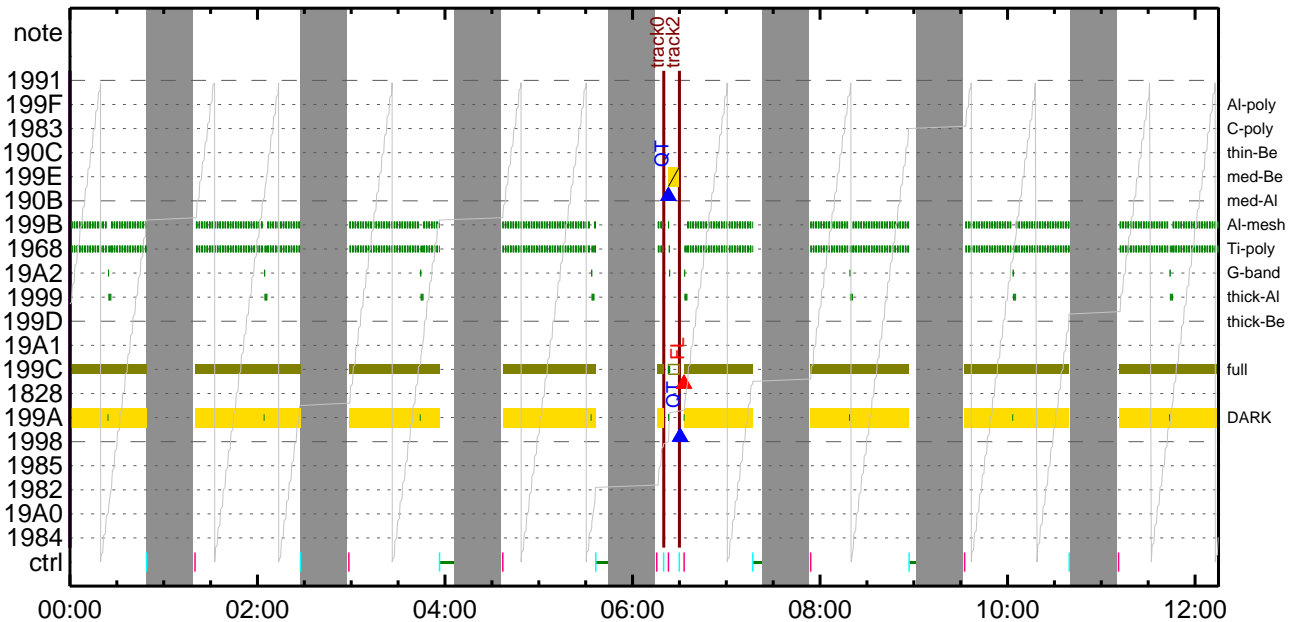
CMDI #0584 2013/07/06



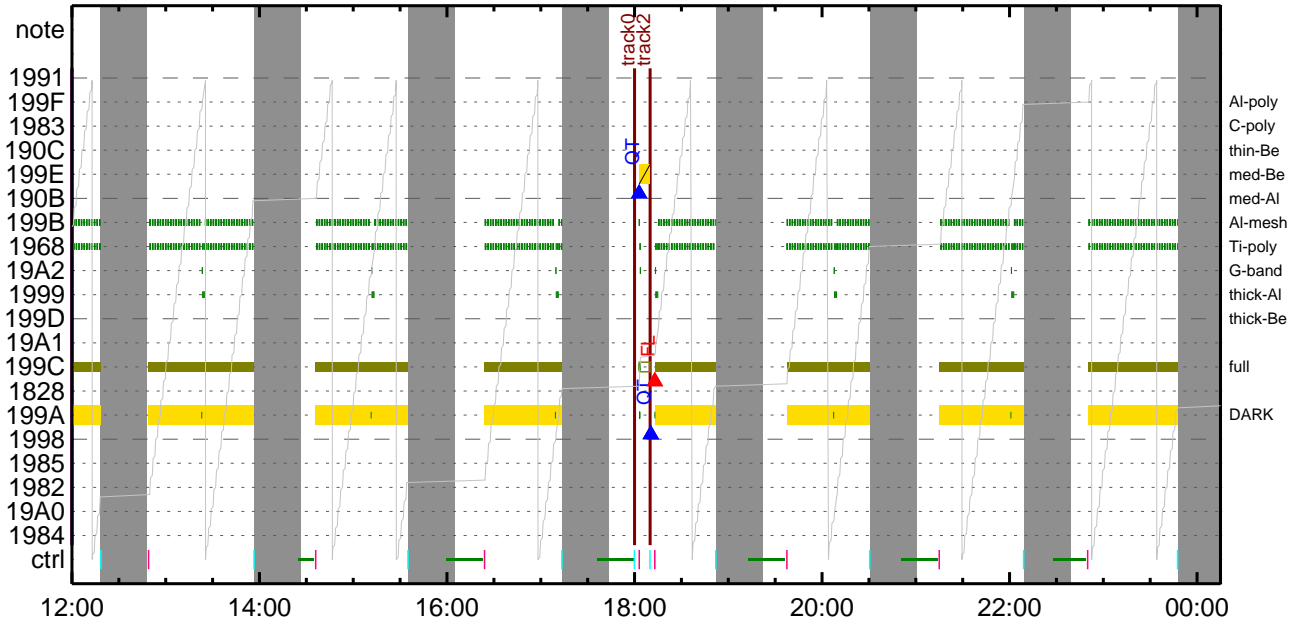
CMDI #0584 2013/07/06



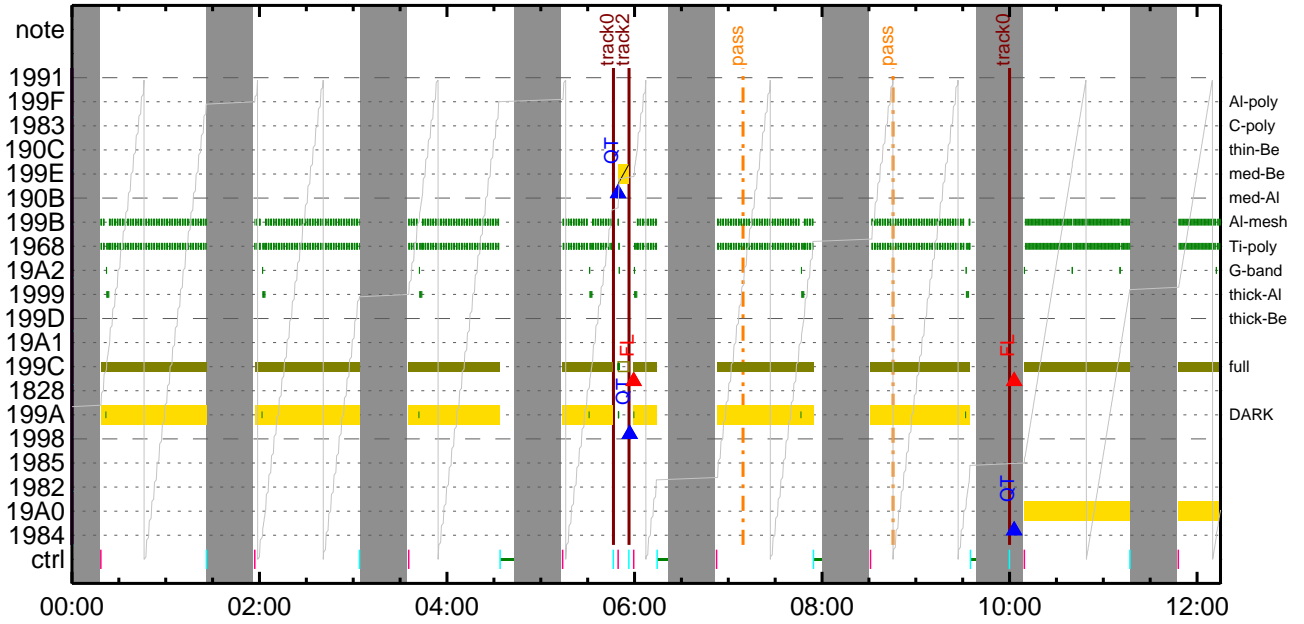
CMDI #0584 2013/07/07



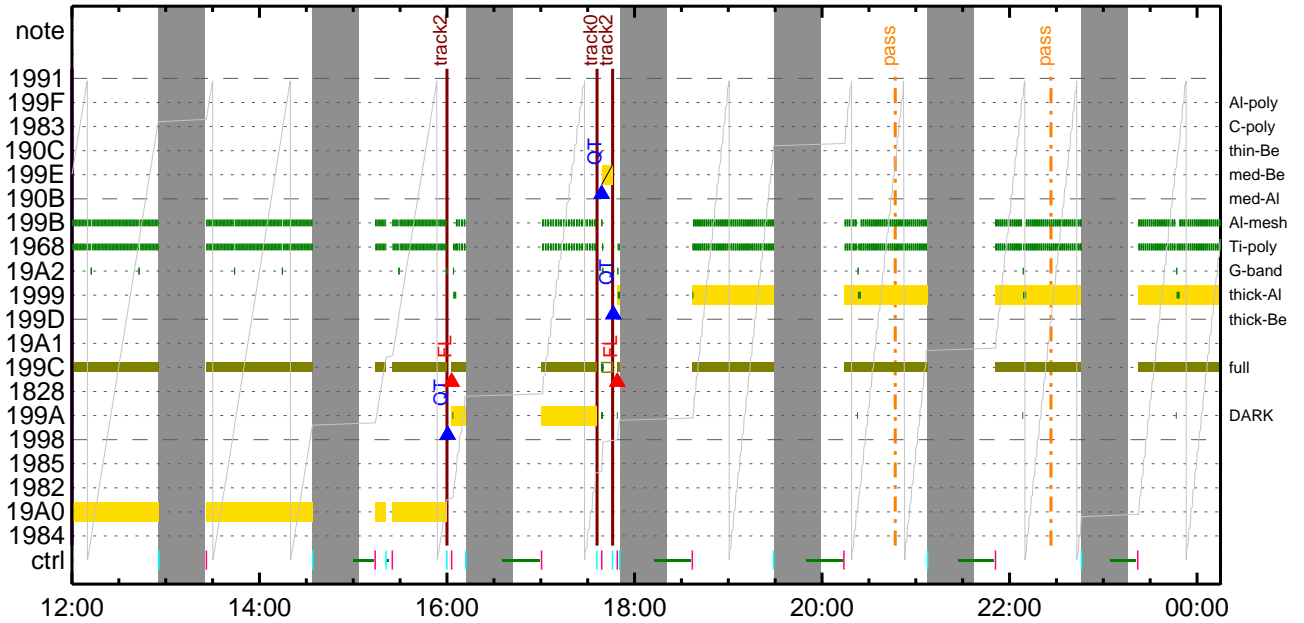
CMDI #0584 2013/07/07



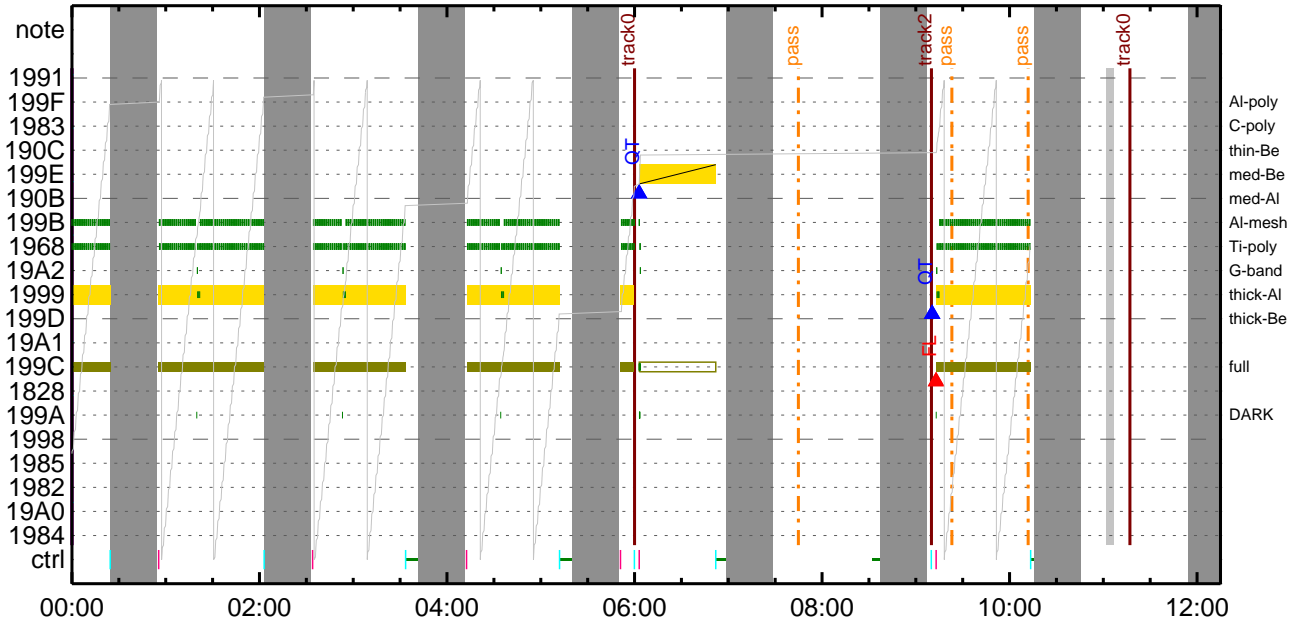
CMDI #0584 2013/07/08



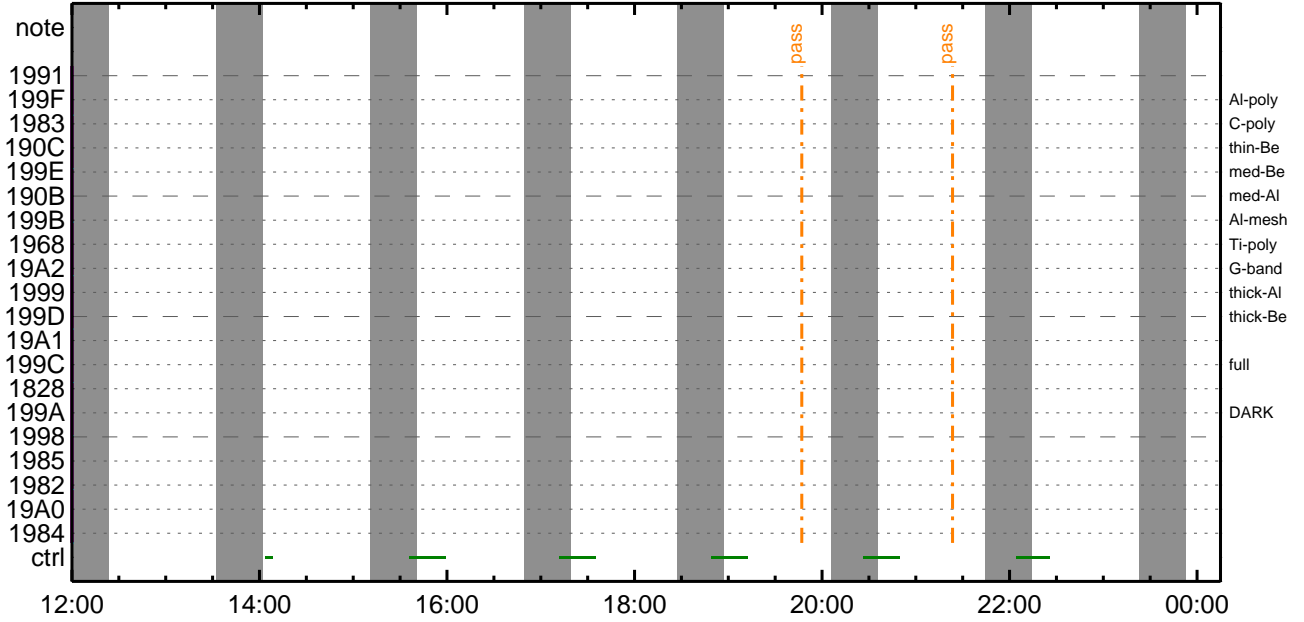
CMDI #0584 2013/07/08



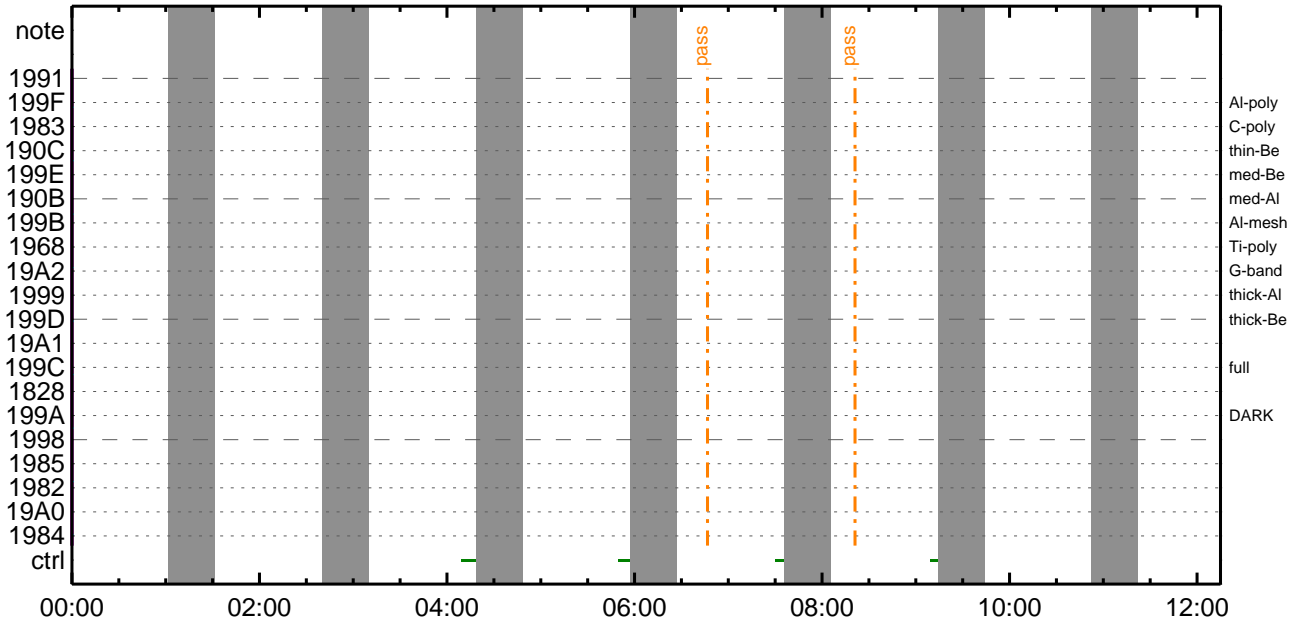
CMDI #0584 2013/07/09



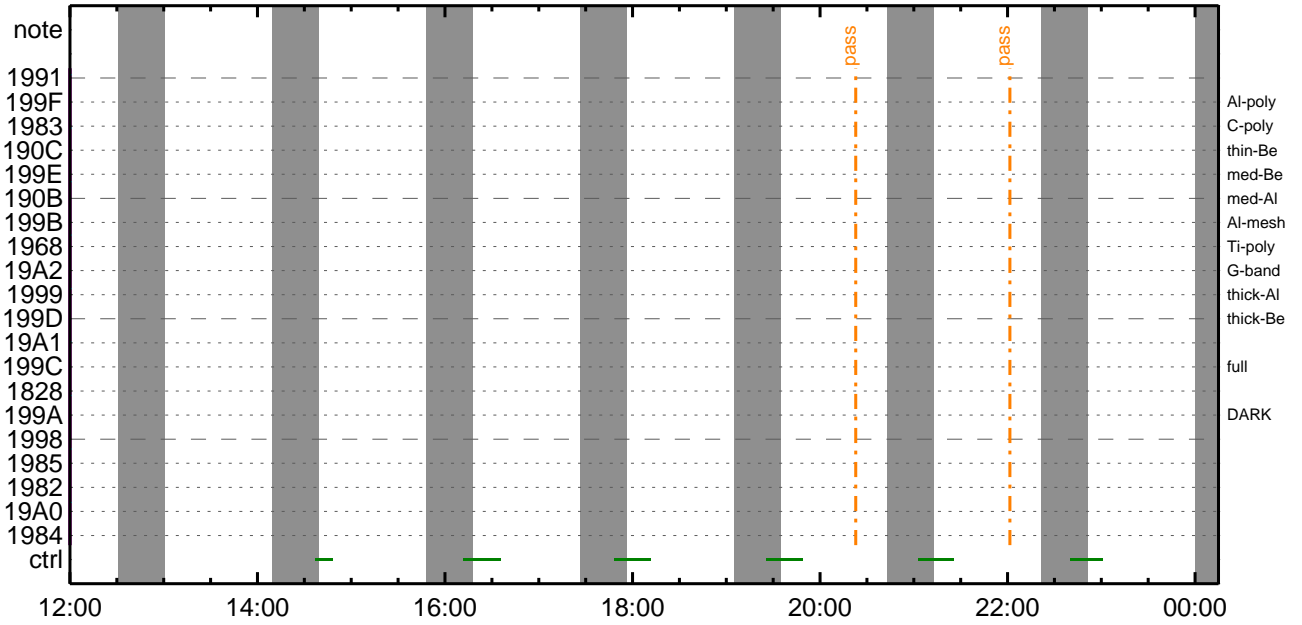
CMDI #0584 2013/07/09



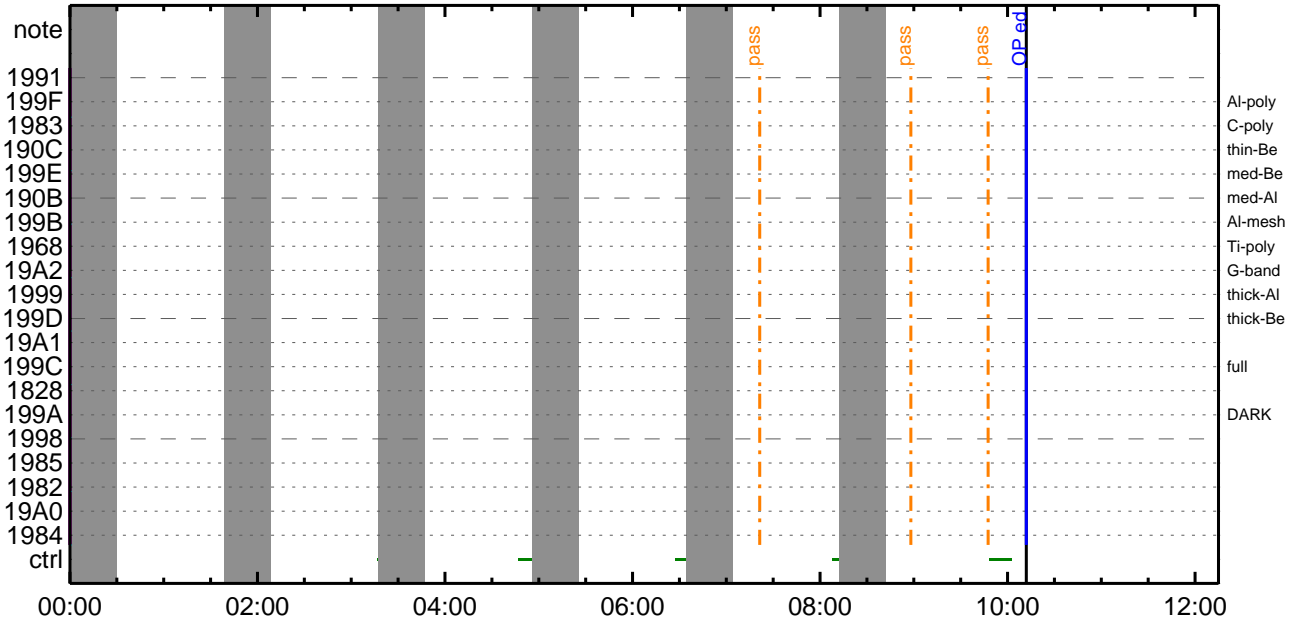
CMDI #0584 2013/07/10



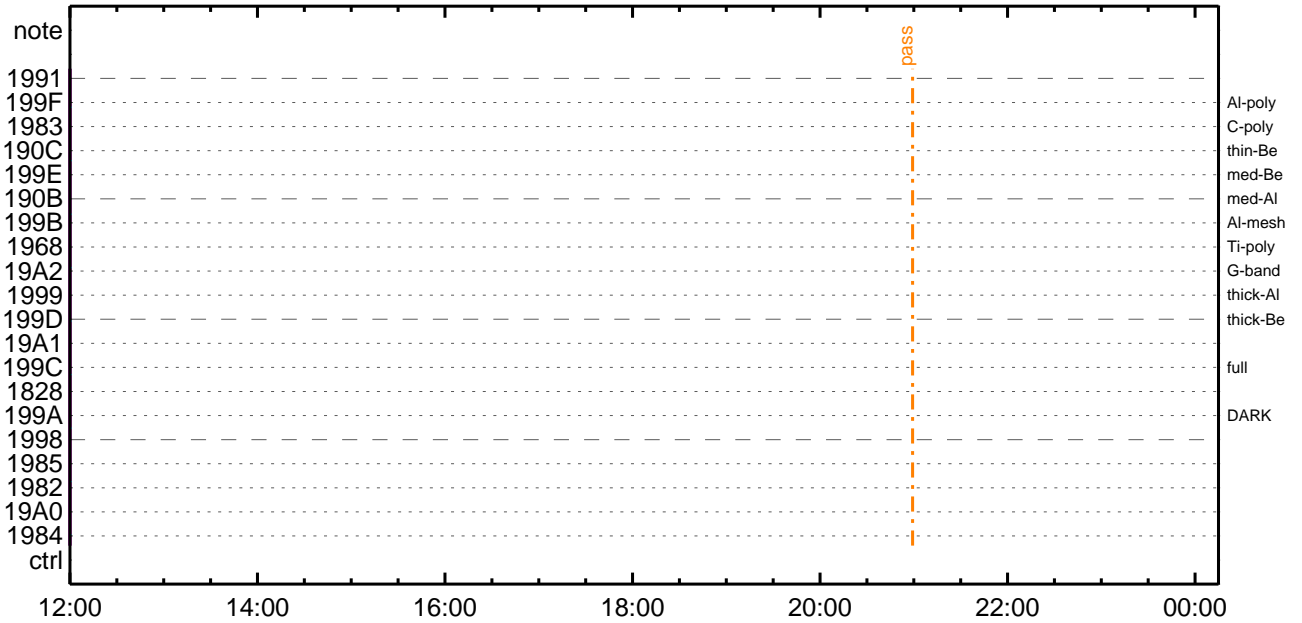
CMDI #0584 2013/07/10



CMDI #0584 2013/07/11



CMDI #0584 2013/07/11



(a) Spacecraft Operation Procedure (real-commands)

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main-761 2013-07-06 13:42:43 289 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÄY`¼Ä»Ü;ä
0005 C.
0006 C. YÄYß;¼Y³YÞYÓYÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;Ëð¿áÀ²·µ°Æ»Í×ÁÇðÍYÇYÁY×YÍ;¼YÉ;ËÈÈ¼µ·íÉ;ËðÈ¼°ÇÓ²·µ¿¼í¹ÇðÍ;ÇÀ®, ù²¹æ³ð³ÇÁ+¿®²·µÈæ³ðÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+¿µ;ON
0016 C. *****
0017 C. Ç` °ÆÀ, Í×ÈY²àLOS²³ÇðÍ»P` Ö²¹ÍÍ, µ.; ÇÉÖÍ×²ÈXÁÓON²¹Ö²È²¹æ³ðÈ;f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ[HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ[HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYÓYÉYÍYÄY`¾ÖÄÖ²·µÁ²æ³·µ¿²æ; Ç°È²¼²¹Ö²ÆÀ, ¼È¼Ç²³¼²¹Ö²¹æ;f
0030 C.
0031 . C. *****
0032 C. DR PT1 ÁÍ¼i°ÆÀ,
0033 C. *****
0034 C. Ç` RESTART;ÉPT1;Ë²·µ¿³¼¹ÇðÍ; Ç°È²¼²¹Ö²ÆÀ¹Ö²²º; ÇDCBC-150²³É²à;f
0035 C.
0036 . C. ;ãPT1°ÆÀ, ³»Í;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ü)
0043 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ü)
0044 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ü)
0045 C.
0046 . C. ;ãYÇYÓYÉYÄYÄÜÄ;ËÄ·Ä²³ÖÈ;Ë, á²¹°ÆÀ, °Æ³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ü)
0050 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ü)
0051 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ü)
0052 C.
0053 C.
0054 . C. PT1°ÆÀ, ²³¼»Æ°Äà»ß²·µ¿², á; Ç°È²¼²³¼¹Ö²¹æ;f
0055 C. YÇYÓYÉYÄYÄÜÄ;ËÄ·Ä²³ÖÈ²·µ¾á²³¼¹ÇðÍ` °Í²²º³³ÇÄÖ²Ä;f
0056 C.
0057 . C. *****
0058 C. DR PT2 ÁÍ¼i°ÆÀ,
0059 C. *****
0060 C. Ç` RESTART;ÉPT2;Ë²·µ¿³¼¹ÇðÍ; Ç°È²¼²¹Ö²ÆÀ¹Ö²²º; ÇDCBC-151²³É²à;f
0061 C.
0062 . C. ;ãPT2°ÆÀ, ³»Í;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ü)
0069 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ü)
0070 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ü)
0071 C.
0072 . C. ;ãYÇYÓYÉYÄYÄÜÄ;ËÄ·Ä²³ÖÈ;Ë, á²¹°ÆÀ, °Æ³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ü)
0076 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ü)
0077 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ü)
0078 C.
0079 . C. *****
0080 C. DR°ÆÀ, Äà»ß; ÇXÁ+¿µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÀ, Äà»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ[HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+¿µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ[HK1_XPA_ON/OFF] EQ OFF

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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ã
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-761:OP
0104 ( )
0105 S. OG og-761:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPîî°èYAYôYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYôYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î¼E¹ç•è²îOK²³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYôYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î¼E¹ç•è²îOK²³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYôYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î¼E¹ç•è²îOK²³îÇ§
0165 C.
0166 C. ***** °E²¼²î¼E¹ç•è²îOK²³îÇ§ *****
0167 C. DHUYâ;4YE;E½Y½;Yi;4YE;Eòîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷ç@NG²î¼E¹ç•è²¼²îTI-CMDÁ÷ç²î¼E¹Ô²•²E²³²E;f
0180 C. ²²²ç;çSET²E²DUMP²îE²±²îY²¹²ç¹Ô²|²³²E;f
0181 C.
0182 C. TIY³Y²YôYE²ò²ÁDîç(UT)
0183 +. TI 2013-07-06 10:19:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2013-07-06 10:19:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2013-07-06 10:19:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2013-07-06 10:23:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          ÷÷[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °ê²¼ɑïÄê%ïíñɑíŷÄŷ§ŷÄŷ-¹àü
0200 C.          ÷÷[HK1_TI_CMD_ENA/DIS]      EQ      ENA
0201 C.          ÷÷[HK1_TI_CMD_NUM]          EQ      4
0202 C.          ÷÷[HK1_NEXT_EXEC_PIM]       EQ      DHU
0203 C.          ÷÷[HK1_NEXT_EXEC_DC]       EQ      0xB3
0204 C.
0205 C. *****
0206 C. TÍíí°êŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          ÷÷[HK1_DMP_TOP_ADRS_1]     EQ      07
0213 C.          ÷÷[HK1_DMP_TOP_ADRS_0]     EQ      2B
0214 C.          ÷÷[HK1_DMP_BLOCK_NUM]      EQ      3
0215 C.          ÷÷[HK1_DMP_REPEAT_NUM]    EQ      0
0216 C.          ÷÷[HK1_DMA_DMP_PIM]       EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          ÷÷[HK1_PKT_FORM_NO]        EQ      7
0220 C.          ÷÷[HK1_PKT_GEN_TIME]       EQ      0.25 s
0221 C.          ÷÷[HK1_S_TLM_BIT_RATE]    EQ      32k
0222 C.          ÷÷[HK1_X_TLM_BIT_RATE]    EQ      4M
0223 C.          ÷÷[HK1_DMP_CHK_FLG]       EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªí»ɑð³íÇ§
0226 C.          ÷÷[HK1_DMP_CHK_FLG]       EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLɑí¼ê¹ç•ë²ìOKɑð³íÇ§
0229 C.
0230 C. DHUŷã;¼ŷÉ;ê¼ŷ¼. ŷì;¼ŷÉ;Ēɑðíãɑ¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          ÷÷[HK1_PKT_FORM_NO]        EQ      2
0234 C.          ÷÷[HK1_PKT_GEN_TIME]       EQ      0.5S
0235 C.          ÷÷[HK1_S_TLM_BIT_RATE]    EQ      32K
0236 C.          ÷÷[HK1_X_TLM_BIT_RATE]    EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2013-07-06 10:23:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2013-07-06 10:23:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2013-07-06 10:23:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2013-07-06 10:23:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´úÃíɑí»ö¼ŷɑĒÄðɑ¹ɑēDCBC•x²è *****
0276 C. (¼á°íŷÖŷÄŷÉŷŷŷÉŷáŷçŷēɑĒ¼ɑɑ¼Ä»Ūɑ¹ɑē)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷĐŷ¹•İ Daily±çİñɑĒĒ'Øɑ¹ɑēDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼Ä»Ū;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

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(a) Spacecraft Operation Procedure (real-commands)

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main-762 2013-07-06 13:42:43 166 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSÝÁÝSÝÄÝ-¼Ä»Û;ã
0005 C.
0006 C. ÝÀÝß;¼Ý³ÝPÝÓÝÉÀ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C.  Āĭ;Ĕα¿αĀα•μ°Ŕ»Ī×ĀÇαĪÝÇÝÄÝ×ÝĪ;¼ÝĒ;ĒĒÈ½μ•ĭĪĒ;ĒαĒ½°ÇÖα•α¿¼ĭ¹ÇαĪ;ÇĀ®, ùα¹αĒαPαÇĀ+¿®α•αĒααα³αĒ;Ē
0011 +. DC 02-8E AOCS_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C.  XĀ+¿μ;ON
0016 C. *****
0017 C.  °ĒĀ,Ī×ĒÝαĀĀLOSαPαÇαĪ»P'Öαð¹ĪĪ,α•;ÇĒÖĪ×αĒĒXĀÖONαĪ¹ÖαĒĪαĒααα³αĒ;Ē
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 +. DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 +. DC 03-95 TCIA_XMOD_QPSK
0024 C.      ÇÇ[HK1_XPA_ON/OFF]      EQ      ON
0025 C.      ÇÇ[HK1_XPA_PWR_HI/LO]   EQ      HI
0026 C.      ÇÇ[HK1_XMOD_ON/OFF]     EQ      ON
0027 C.      ÇÇ[HK1_XMOD_QPSK/PM]    EQ      QPSK
0028 C.
0029 . C.  XÝDÝÓÝÉÝĪÝÄÝ-¾ÖĀÖα-°ĀĀĒα•α¿μé;Ç°Ē²¼αĪ°ĒĀ,¼Ē½Çαð½Ā¹Öα¹αĒ;Ē
0030 C.
0031 . C. *****
0032 C.  DR PT1 ĀĪ¼ĭ°ĒĀ,
0033 C. *****
0034 C.  °ĒĀ RESTART;ĒPT1;Ēα•α¿αα¾ĭ¹ÇαĪ;Ç°Ē²¼αĪ°ĒĀ¹Öα»α°;ÇDCBC-150αØ¿ĒĒα;Ē
0035 C.
0036 . C. ;ãPT1°ĒĀ,³«»Ī;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 +. DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 +. DC 06-B3 DR_REP_START
0041 +. DC 01-32 DHU_X_VC4_ON
0042 C.      ÇÇ[HK1_REP_PT_1/2]      EQ      PT1 (¼Ā¹Ö,¼Ú)
0043 C.      ÇÇ[HK1_REP_STA/STP]    EQ      START (¼Ā¹Ö,¼Ú)
0044 C.      ÇÇ[HK1_X_VC4_ON/OFF]   EQ      ON (¼Ā¹Ö,¼Ú)
0045 C.
0046 . C. ;ãÝÇÝÓÝĒÝĒĒĀŦŦ;ĒĀ•Ā°²óĒ;Ē,ãαĪ°ĒĀ,°Ē³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 +. DC 01-32 DHU_X_VC4_ON
0049 C.      ÇÇ[HK1_REP_PT_1/2]      EQ      PT1 (¼Ā¹Ö,¼Ú)
0050 C.      ÇÇ[HK1_REP_STA/STP]    EQ      START (¼Ā¹Ö,¼Ú)
0051 C.      ÇÇ[HK1_X_VC4_ON/OFF]   EQ      ON (¼Ā¹Ö,¼Ú)
0052 C.
0053 C.
0054 . C.  PT1°ĒĀ,α-¼«Ē°ĀĀ»ßα•α¿,ã;Ç°Ē²¼αð½Ā¹Öα¹αĒ;Ē
0055 C.  ÝÇÝÓÝĒÝĒĒĀŦŦαĀĀ•Ā°²óĒðα-¼Āαα¾ĭ¹ÇαĪ'°Ī»α¹αĒαPαÇĀÖαĀ;Ē
0056 C.
0057 . C. *****
0058 C.  DR PT2 ĀĪ¼ĭ°ĒĀ,
0059 C. *****
0060 C.  °ĒĀ RESTART;ĒPT2;Ēα•α¿αα¾ĭ¹ÇαĪ;Ç°Ē²¼αĪ°ĒĀ¹Öα»α°;ÇDCBC-151αØ¿ĒĒα;Ē
0061 C.
0062 . C. ;ãPT2°ĒĀ,³«»Ī;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 +. DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 +. DC 06-B3 DR_REP_START
0067 +. DC 01-32 DHU_X_VC4_ON
0068 C.      ÇÇ[HK1_REP_PT_1/2]      EQ      PT2 (¼Ā¹Ö,¼Ú)
0069 C.      ÇÇ[HK1_REP_STA/STP]    EQ      START (¼Ā¹Ö,¼Ú)
0070 C.      ÇÇ[HK1_X_VC4_ON/OFF]   EQ      ON (¼Ā¹Ö,¼Ú)
0071 C.
0072 . C. ;ãÝÇÝÓÝĒÝĒĒĀŦŦ;ĒĀ•Ā°²óĒ;Ē,ãαĪ°ĒĀ,°Ē³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 +. DC 01-32 DHU_X_VC4_ON
0075 C.      ÇÇ[HK1_REP_PT_1/2]      EQ      PT2 (¼Ā¹Ö,¼Ú)
0076 C.      ÇÇ[HK1_REP_STA/STP]    EQ      START (¼Ā¹Ö,¼Ú)
0077 C.      ÇÇ[HK1_X_VC4_ON/OFF]   EQ      ON (¼Ā¹Ö,¼Ú)
0078 C.
0079 . C. *****
0080 C.  DR°ĒĀ,ĀĀ»ß;ÇXĀ+¿μ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ĒĀ,ĀĀ»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 +. DC 01-29 DHU_S/X_VC4_OFF
0086 C.      ÇÇ[HK1_REP_STA/STP]    EQ      STOP
0087 C.      ÇÇ[HK1_S_VC4_ON/OFF]   EQ      OFF
0088 C.      ÇÇ[HK1_X_VC4_ON/OFF]   EQ      OFF
0089 C.
0090 . C. ;ãXĀ+¿μ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 +. DC 03-B5 TCIA_XPA_OFF
0094 C.      ÇÇ[HK1_XMOD_ON/OFF]     EQ      OFF
0095 C.      ÇÇ[HK1_XPA_ON/OFF]     EQ      OFF

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(a) Spacecraft Operation Procedure (real-commands)

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main-763 2013-07-06 13:42:43 148 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÃY$YÅY~¼Ä»Ü;ä
0005 C.
0006 C. YÃY$;¼Y³YFÿóYÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Āí;Ë¿αΑβ•μ°Æ»Í×ΑÇαíYçYÅY×Yí;¼YÉ;ËÈ%μ•íÉ;ËαÈ¼°ÇÖα•α¿¼l¹çαÍ;çÀ®;ùα¹αÈαBαÇÁ+¿®α•αÈααα³αÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-266:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Stop SP table >
0036 +. DC 07-F0 MDP_SP_CTRL_MANU
0037 BC (61)
0038 C. -----
0039 C. MDP_SP_CTRL_MODE = MANU [ ]
0040 C. -----
0041 C.
0042 . C. <Upload SP Observation Table>
0043 . S. RAM ram-281:MDP_OBS_S
0044 ( )
0045 C.
0046 . C. < Dump RAMID=MDP_OBS_S >
0047 +. DC 07-F0 MDP_DUMP_SPTBL
0048 BC (83 07 00 00 00 38 b8)
0049 C. -----
0050 C. MDP_OBS_S verify = OK/NG [ ]
0051 C. -----
0052 C.
0053 C. *****
0054 C. SOT TI command set
0055 C. *****
0056 C. Execute, after the success of TBL upload.
0057 +. TI 2013-07-06 10:23:18.0
0058 DC 07-F0 MDP_SOT_MODE_OBSV
0059 BC (40)
0060 . C. -----
0061 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0062 C. -----
0063 C.
0064 C.
0065 C. ***** XRT START *****
0066 C.
0067 +. DC 07-F0 MDP_XRT_CTRL_MANU
0068 BC (c1)
0069 +. DC 07-F0 MDP_XRT_MODE_STBY
0070 BC (c3)
0071 . C. ----- Success Verify ? OK / NG_____
0072 C.
0073 C. XRT Obs. Table Upload
0074 . S. RAM ram-291:MDP_OBS_X
0075 ( )
0076 C.
0077 +. DC 07-F0 MDP_DUMP_XRTTBL
0078 BC (84 07 00 00 00 3a d4)
0079 . C. ----- Comparison Check ? OK / ERR _____
0080 C.
0081 C.
0082 +. DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 01 b1 b1 04 04)
0084 +. DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 02 b1 b1 08 08)
0086 +. DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 03 b1 b1 08 08)
0088 +. DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 04 b1 b1 06 06)
0090 +. DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 05 85 83 06 06)
0092 +. DC 07-F0 MDP_XRT_ROI_SET
0093 BC (cd 06 85 83 06 06)
0094 +. DC 07-F0 MDP_XRT_ROI_SET
0095 BC (cd 07 80 80 20 20)

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0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 08 80 80 20 08)
0098 + DC 07-F0 MDP_XRT_ROI_SET
0099 BC (cd 09 80 80 08 20)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101 BC (cd 0a 85 83 08 08)
0102 + DC 07-F0 MDP_XRT_ROI_SET
0103 BC (cd 0f 80 80 06 06)
0104 + DC 07-F0 MDP_XRT_ROI_SET
0105 BC (cd 10 80 80 08 08)
0106 + DC 07-F0 MDP_XRT_FLD_ENA
0107 BC (d8)
0108 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0109 BC (c8)
0110 + DC 07-F0 MDP_XRT_AEC_RESET
0111 BC (d0)
0112 + DC 07-F0 MDP_XRT_ARS_DIS
0113 BC (d5)
0114 + DC 07-F0 MDP_XRT_FLD_RESET
0115 BC (da)
0116 + DC 07-F0 MDP_XRT_QT_PROG_SET
0117 BC (c4 05)
0118 + DC 07-F0 MDP_XRT_FL_PROG_SET
0119 BC (c5 08)
0120 . C. ----- Success Verify ? OK / NG ____
0121 C.
0122 C.
0123 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0124 C.
0125 +. DC 07-F0 MDP_XRT_MODE_OBSV
0126 BC (c2)
0127 +. TI 2013-07-06 10:23:02.0
0128 DC 07-F0 MDP_XRT_MODE_OBSV
0129 BC (c2)
0130 . C. ----- Success Verify ? OK / NG ____
0131 C.
0132 C. ***** XRT END *****
0133 C.
0134 . C. ***** MDP 'úÃîñî»ö¼ýñëÄðñ¹ñëDCBC•x²è *****
0135 C. (%á°îÿÓÿÄÿËÿÏÿËÿáÿçÿëñ¼ññ¼Ä»Ûñ¹ñè)
0136 . S. DC-BC dcbc-402:DCBC
0137 (MDP_known_event)
0138 C.
0139 C.
0140 . C. ***** ÿÐÿ¹•Ï Daily±¿ÎññË´Øñ¹ñëDCBC•x²è *****
0141 . S. DC-BC dcbc-153:DCBC
0142 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0143 C.
0144 C.
0145 . C. ;ãLOSÿÄÿÏÿËÿËÿ¼Ä»Û;ä
0146 C.
0147 . C. ***** LOS *****
0148 C.

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*** OP Sequence for XRT ***

2013/07/06	10:25:00.5	XRT_CTRL_MANU_448_OG [0x1c0]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/06	10:25:10.5	XRT_FOCUS_RECALIBRATE_433_OG [0x1b1]							
		XRT_FOCUS_RECAL	2	07-F8	78 00				
2013/07/06	10:29:10.5	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/07/06	10:34:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 ac cd				
2013/07/06	10:50:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 d6 67				
2013/07/06	11:05:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/06	11:05:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/07/06	11:06:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2013/07/06	11:06:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/07/06	11:06:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/07/06	11:06:20.0	XRT_ARS_DIS_406_OG [0x196]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/07/06	11:08:58.0	XRT_QT_PROG_SET_409_OG [0x199]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2013/07/06	11:09:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/07/06	11:21:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/06	11:22:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 00 00 29 99				
2013/07/06	12:11:30.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00 00 00 53 33				
2013/07/06	12:27:30.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00 d6 36 b7 8e				
2013/07/06	12:37:30.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00 b4 b5 db 75				
2013/07/06	12:53:30.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00 ac 5b 00 00				
2013/07/06	13:49:30.0	AOCS_ORe-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00 b4 b5 24 8b				
2013/07/06	14:05:30.0	AOCS_ORe-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00 d6 36 48 72				
2013/07/06	14:15:30.0	AOCS_ORe-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00 29 ca b7 8e				
2013/07/06	14:25:30.0	AOCS_ORe-point_Start_12_OG [0x0a2]							
		AOCU_NM	5	02-76	00 4b 4b db 75				
2013/07/06	14:41:30.0	AOCS_ORe-point_Start_13_OG [0x0a3]							
		AOCU_NM	5	02-76	00 53 a5 00 00				
2013/07/06	15:52:30.0	AOCS_ORe-point_Start_14_OG [0x0a4]							
		AOCU_NM	5	02-76	00 4b 4b 24 8b				
2013/07/06	16:08:30.0	AOCS_ORe-point_Start_15_OG [0x0a5]							
		AOCU_NM	5	02-76	00 29 db 48 72				
2013/07/06	16:28:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/06	16:28:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/07/06	16:28:30.0	AOCS_ORe-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2013/07/06	16:28:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/07/06	16:28:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/07/06	16:28:50.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/07/06	16:28:52.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/07/06	16:28:54.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/07/06	16:28:56.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2013/07/06	16:31:28.0	XRT_FL_PROG_SET_431_OG [0x1af]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2013/07/06	16:31:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/07/06	16:36:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/06	16:36:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/07/06	16:36:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/07/06	16:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/07/06	17:23:30.0	XRT_Custom_430_OG [0x1ae]							
2013/07/06	17:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/07/06	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/06	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/07/06	18:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				

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2013/07/06	18:00:16.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/07/06	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/07/06	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/07/06	18:02:58.0	XRT_QT_PROG_SET_445_OG [0x1bd]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2013/07/06	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/07/06	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/06	18:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/07/06	18:10:00.0	AOCS_Ore-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2013/07/06	18:10:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/07/06	18:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/07/06	18:10:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/07/06	18:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/07/06	18:10:24.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/07/06	18:10:26.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2013/07/06	18:12:58.0	XRT_FL_PROG_SET_431_OG [0x1af]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2013/07/06	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/07/06	18:15:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/06	18:15:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/07/06	18:15:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/07/06	18:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/07/06	19:00:00.0	XRT_Custom_430_OG [0x1ae]							
2013/07/06	19:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/07/06	19:53:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/06	19:53:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/07/06	19:53:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/07/06	19:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/07/06	20:37:30.0	XRT_Custom_430_OG [0x1ae]							
2013/07/06	20:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/07/06	21:32:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/06	21:32:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/07/06	21:32:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/07/06	21:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/07/06	22:13:30.0	XRT_Custom_430_OG [0x1ae]							
2013/07/06	22:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/07/06	23:10:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/06	23:10:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/07/06	23:10:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/07/06	23:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/07/06	23:40:30.0	XRT_Custom_430_OG [0x1ae]							
2013/07/06	23:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/07/07	00:49:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/07	00:49:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/07/07	00:49:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/07/07	00:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/07/07	01:19:00.0	XRT_Custom_430_OG [0x1ae]							
2013/07/07	01:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/07/07	02:27:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/07	02:27:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/07/07	02:27:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				

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2013/07/07	02:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/07/07	02:57:30.0	XRT_Custom_430_OG [0x1ae]							
2013/07/07	02:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/07/07	03:56:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/07	03:56:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/07/07	03:56:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/07/07	03:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/07/07	04:36:00.0	XRT_Custom_430_OG [0x1ae]							
2013/07/07	04:37:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/07/07	05:36:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/07	05:36:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/07/07	05:36:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/07/07	05:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/07/07	06:14:30.0	XRT_Custom_430_OG [0x1ae]							
2013/07/07	06:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/07/07	06:19:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/07	06:19:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/07/07	06:20:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2013/07/07	06:20:16.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/07/07	06:22:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/07/07	06:22:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/07/07	06:22:58.0	XRT_QT_PROG_SET_445_OG [0x1bd]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2013/07/07	06:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/07/07	06:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/07	06:29:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/07/07	06:30:00.0	AOCS_Ore-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2013/07/07	06:30:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/07/07	06:30:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/07/07	06:30:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/07/07	06:30:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/07/07	06:30:24.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/07/07	06:30:26.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2013/07/07	06:32:58.0	XRT_FL_PROG_SET_431_OG [0x1af]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2013/07/07	06:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/07/07	07:17:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/07	07:17:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/07/07	07:17:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/07/07	07:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/07/07	07:53:00.0	XRT_Custom_430_OG [0x1ae]							
2013/07/07	07:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/07/07	08:57:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/07	08:57:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/07/07	08:57:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/07/07	09:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/07/07	09:31:30.0	XRT_Custom_430_OG [0x1ae]							
2013/07/07	09:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/07/07	10:39:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/07	10:39:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/07/07	10:39:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				

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2013/07/07	10:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/07/07	11:10:00.0	XRT_Custom_430_OG [0x1ae]								
2013/07/07	11:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/07/07	12:18:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/07/07	12:18:32.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2013/07/07	12:18:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/07/07	12:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/07/07	12:48:00.0	XRT_Custom_430_OG [0x1ae]								
2013/07/07	12:49:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/07/07	13:56:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/07/07	13:56:32.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2013/07/07	13:56:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/07/07	13:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/07/07	14:35:00.0	XRT_Custom_430_OG [0x1ae]								
2013/07/07	14:36:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/07/07	15:35:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/07/07	15:35:02.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2013/07/07	15:35:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/07/07	15:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/07/07	16:23:00.0	XRT_Custom_430_OG [0x1ae]								
2013/07/07	16:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/07/07	17:13:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/07/07	17:13:32.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2013/07/07	17:13:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/07/07	17:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/07/07	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/07/07	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]								
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00					
2013/07/07	18:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]								
		AOCU_NM	5	02-76	00 00 00 00 00					
2013/07/07	18:00:16.0	XRT_FLD_DIS_434_OG [0x1b2]								
		MDP_XRT_FLD_DIS	1	07-F0	d9					
2013/07/07	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]								
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2013/07/07	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2013/07/07	18:02:58.0	XRT_QT_PROG_SET_445_OG [0x1bd]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10					
2013/07/07	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/07/07	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/07/07	18:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]								
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00					
2013/07/07	18:10:00.0	AOCS_Ore-point_Start_16_OG [0x0a6]								
		AOCU_NM	5	02-76	02 00 00 00 00					
2013/07/07	18:10:16.0	XRT_FLD_ENA_411_OG [0x19b]								
		MDP_XRT_FLD_ENA	1	07-F0	d8					
2013/07/07	18:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]								
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2013/07/07	18:10:20.0	XRT_AEC_RESET_413_OG [0x19d]								
		MDP_XRT_AEC_RESET	1	07-F0	d0					
2013/07/07	18:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2013/07/07	18:10:24.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2013/07/07	18:10:26.0	XRT_QT_PROG_SET_401_OG [0x191]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06					
2013/07/07	18:12:58.0	XRT_FL_PROG_SET_431_OG [0x1af]								
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08					
2013/07/07	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/07/07	18:52:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/07/07	18:52:02.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2013/07/07	18:52:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/07/07	18:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/07/07	19:36:30.0	XRT_Custom_430_OG [0x1ae]								

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2013/07/07	19:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/07/07	20:30:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/07	20:30:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/07/07	20:30:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/07/07	20:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/07/07	21:14:00.0	XRT_Custom_430_OG [0x1ae]							
2013/07/07	21:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/07/07	22:09:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/07	22:09:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/07/07	22:09:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/07/07	22:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/07/07	22:49:00.0	XRT_Custom_430_OG [0x1ae]							
2013/07/07	22:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/07/07	23:47:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/07	23:47:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/07/07	23:47:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/07/07	23:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/07/08	00:17:30.0	XRT_Custom_430_OG [0x1ae]							
2013/07/08	00:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/07/08	01:26:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/08	01:26:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/07/08	01:26:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/07/08	01:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/07/08	01:56:00.0	XRT_Custom_430_OG [0x1ae]							
2013/07/08	01:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/07/08	03:04:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/08	03:04:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/07/08	03:04:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/07/08	03:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/07/08	03:34:30.0	XRT_Custom_430_OG [0x1ae]							
2013/07/08	03:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/07/08	04:34:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/08	04:34:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/07/08	04:34:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/07/08	04:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/07/08	05:13:00.0	XRT_Custom_430_OG [0x1ae]							
2013/07/08	05:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/07/08	05:46:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/08	05:46:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/07/08	05:46:30.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2013/07/08	05:46:46.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/07/08	05:49:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/07/08	05:49:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/07/08	05:49:28.0	XRT_QT_PROG_SET_445_OG [0x1bd]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2013/07/08	05:49:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/07/08	05:56:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/08	05:56:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/07/08	05:56:30.0	AOCS_Ore-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2013/07/08	05:56:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/07/08	05:56:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							

2013/07/08	05:56:50.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/07/08	05:56:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/07/08	05:56:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/08	05:56:56.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	06
2013/07/08	05:59:28.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08
2013/07/08	05:59:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/08	06:14:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/08	06:14:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/08	06:14:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/07/08	06:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/07/08	06:51:30.0	XRT_Custom_430_OG [0x1ae]					
2013/07/08	06:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/08	07:54:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/08	07:54:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/08	07:54:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/07/08	07:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/07/08	08:30:00.0	XRT_Custom_430_OG [0x1ae]					
2013/07/08	08:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/08	09:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/08	09:35:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/08	09:35:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/07/08	09:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/07/08	09:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/08	09:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2013/07/08	10:00:00.0	AOCS_Ore-point_Start_17_OG [0x0a7]	AOCU_NM	5	02-76	00 b0 32 00	b4
2013/07/08	10:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/07/08	10:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/07/08	10:00:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/07/08	10:00:22.0	XRT_ARS_DIS_417_OG [0x1a1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/07/08	10:02:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/08	10:02:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02
2013/07/08	10:02:58.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08
2013/07/08	10:08:30.0	XRT_Custom_430_OG [0x1ae]					
2013/07/08	10:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/08	11:17:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/08	11:17:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/08	11:17:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/07/08	11:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/07/08	11:47:00.0	XRT_Custom_430_OG [0x1ae]					
2013/07/08	11:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/08	12:55:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/08	12:55:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/08	12:55:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/07/08	12:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/07/08	13:25:00.0	XRT_Custom_430_OG [0x1ae]					
2013/07/08	13:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/08	14:34:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/08	14:34:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/08	14:34:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					

2013/07/08	14:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e8	
2013/07/08	15:13:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/07/08	15:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/08	15:21:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/08	15:21:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/08	15:21:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/07/08	15:24:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/07/08	15:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/07/08	15:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/08	15:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/08	15:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2013/07/08	16:00:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	02 00 00 00 00	
2013/07/08	16:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/07/08	16:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/07/08	16:00:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/07/08	16:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/07/08	16:00:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/08	16:00:26.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06	
2013/07/08	16:02:58.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08	
2013/07/08	16:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/08	16:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/08	16:12:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/08	16:12:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/07/08	16:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/07/08	16:59:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/07/08	17:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/08	17:35:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/08	17:35:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2013/07/08	17:36:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00	
2013/07/08	17:36:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/07/08	17:38:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/07/08	17:38:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/07/08	17:38:58.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10	
2013/07/08	17:39:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/08	17:45:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/08	17:45:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2013/07/08	17:46:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	02 00 00 00 00	
2013/07/08	17:46:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/07/08	17:46:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/07/08	17:46:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/07/08	17:46:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/07/08	17:46:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/08	17:46:26.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b	
2013/07/08	17:48:58.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08	
2013/07/08	17:49:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/08	17:50:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/08	17:50:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/08	17:50:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	

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2013/07/08	17:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/07/08	18:36:00.5	XRT_Custom_430_OG [0x1ae]						
2013/07/08	18:37:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/07/08	19:29:00.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/07/08	19:29:02.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/07/08	19:29:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/07/08	19:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/07/08	20:13:00.0	XRT_Custom_430_OG [0x1ae]						
2013/07/08	20:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/07/08	21:07:30.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/07/08	21:07:32.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/07/08	21:07:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/07/08	21:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/07/08	21:50:00.0	XRT_Custom_430_OG [0x1ae]						
2013/07/08	21:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/07/08	22:46:00.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/07/08	22:46:02.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/07/08	22:46:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/07/08	22:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/07/08	23:21:00.0	XRT_Custom_430_OG [0x1ae]						
2013/07/08	23:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/07/09	00:24:30.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/07/09	00:24:32.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/07/09	00:24:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/07/09	00:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/07/09	00:54:30.0	XRT_Custom_430_OG [0x1ae]						
2013/07/09	00:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/07/09	02:03:00.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/07/09	02:03:02.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/07/09	02:03:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/07/09	02:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/07/09	02:33:00.0	XRT_Custom_430_OG [0x1ae]						
2013/07/09	02:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/07/09	03:33:30.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/07/09	03:33:32.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/07/09	03:33:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/07/09	03:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/07/09	04:11:30.0	XRT_Custom_430_OG [0x1ae]						
2013/07/09	04:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/07/09	05:12:00.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/07/09	05:12:02.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/07/09	05:12:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/07/09	05:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/07/09	05:50:00.0	XRT_Custom_430_OG [0x1ae]						
2013/07/09	05:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/07/09	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/07/09	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]						
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2013/07/09	06:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]						
			AOCU_NM	5	02-76	00 00 00 00 00		
2013/07/09	06:00:16.0	XRT_FLD_DIS_434_OG [0x1b2]						
			MDP_XRT_FLD_DIS	1	07-F0	d9		
2013/07/09	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]						
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		

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2013/07/09	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/07/09	06:02:58.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	10
2013/07/09	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/09	06:52:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/09	06:52:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/09	06:52:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/07/09	06:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/07/09	09:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/09	09:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2013/07/09	09:10:00.0	AOCS_Ore-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	02	00 00 00 00
2013/07/09	09:10:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/07/09	09:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/07/09	09:10:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/07/09	09:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/07/09	09:10:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/09	09:10:26.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b
2013/07/09	09:12:58.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08
2013/07/09	09:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/09	10:13:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/09	10:13:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/09	10:13:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/07/09	10:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/07/09	11:17:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00 00 00 00