

# XRT Timeline to be uploaded on 2014/07/01

Period: 2014/07/01 09:29:00 - 2014/07/05 10:16:00

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**Normal mode**

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<b>XOB #1A1B: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 60s cadence, G-band - 384x384 33ms</b>													
Term	Pointing (x, y)							Comment					
07/01 09:42:00 - 07/01 15:42:00	Fixed ( -7.0, -976.0)							# OP start + 10min, HOP-240 at South Pole.					
<b>PROG= 04 Inf.-time(s)</b>													
┌ Subr= 1 1-time(s) 2.0sec													
└ Seqn= 8 2-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs 1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec	
┌ Subr= 2 1-time(s) 2.0sec													
└ Seqn= 48 1-time(s) 30.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs 1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec	
┌ Subr= 3 30-time(s) 2.0sec													
└ Seqn= 57 1-time(s) 60.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs 1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec	
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	5.66s	Obs 1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #19E3: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33ms)</b>													
Term	Pointing (x, y)							Comment					
07/01 16:30:30 - 07/01 17:20:30	Track ( -694.7, -296.1) <sup>Ⓢ 07/01 16:00:00</sup>							# AR 12104.					
07/01 18:19:00 - 07/02 05:52:54	Track ( -681.9, -297.0) <sup>Ⓢ 07/01 18:16:00</sup>							# AR cont.					
07/02 06:06:00 - 07/02 17:41:54	Track ( -610.4, -301.4) <sup>Ⓢ 07/02 06:03:00</sup>							# AR cont.					
07/02 17:55:00 - 07/03 06:01:54	Track ( -530.9, -305.6) <sup>Ⓢ 07/02 17:52:00</sup>							# AR cont.					
07/03 06:15:00 - 07/03 10:15:00	Track ( -440.5, -309.5) <sup>Ⓢ 07/03 06:12:00</sup>							# AR cont.					
<b>PROG= 20 Inf.-time(s)</b>													
┌ Subr= 1 1-time(s) 2.0sec													
└ Seqn= 8 2-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs 1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec	
┌ Subr= 2 2-time(s) 2.0sec													
└ Seqn= 24 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs 1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs 1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
└ Seqn= 42 4-time(s) 2.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs 1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec	
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
└ Seqn= 62 15-time(s) 120.0sec													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec	
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs 1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec	
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec	
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs 1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #1A10: Synoptic Q95 2x2 - Al/mesh(2/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(5/256/1443) + Thin...</b>													
Term	Pointing (x, y)							Comment					
07/01 18:09:00 - 07/01 18:15:54	Fixed ( 0.0, 0.0)							synoptic, shifted 6.0 min					
07/02 05:56:00 - 07/02 06:02:54	Fixed ( 0.0, 0.0)							synoptic, shifted -7.0 min					
07/02 17:45:00 - 07/02 17:51:54	Fixed ( 0.0, 0.0)							synoptic, shifted -18.0 min					
07/03 06:05:00 - 07/03 06:11:54	Fixed ( 0.0, 0.0)							synoptic, shifted 2.0 min					
<b>PROG= 14 1-time(s)</b>													
┌ Subr= 1 1-time(s) 12.0sec													
└ Seqn= 35 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└ Seqn= 5 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec	
└ Seqn= 72 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	5ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	250ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└ Seqn= 67 1-time(s) 2.0sec													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	16ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└ Seqn= 6 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs 2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec	

Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Flare mode

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#### XOB #19EC: Flare - high cad multifilter (Be/thin, Be/med, Al/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + Gband (4

Term	Pointing (x, y)	Comment
07/01 09:42:00 - 07/01 15:42:00	Fixed ( -7.0, -976.0)	# OP start + 10min, HOP-240 at South Pole.
07/01 16:30:30 - 07/01 17:20:30	Track ( -694.7, -296.1) <sup>Ⓜ 07/01 16:00:00</sup>	# AR 12104.
07/01 18:19:00 - 07/02 05:52:54	Track ( -681.9, -297.0) <sup>Ⓜ 07/01 18:16:00</sup>	# AR cont.
07/02 06:06:00 - 07/02 17:41:54	Track ( -610.4, -301.4) <sup>Ⓜ 07/02 06:03:00</sup>	# AR cont.
07/02 17:55:00 - 07/03 06:01:54	Track ( -530.9, -305.6) <sup>Ⓜ 07/02 17:52:00</sup>	# AR cont.
07/03 06:15:00 - 07/03 10:15:00	Track ( -440.5, -309.5) <sup>Ⓜ 07/03 06:12:00</sup>	# AR cont.

#### PROG= 01 30-time(s)

Subr= 1		30-time(s)	2.0sec										
Seqn= 26		1-time(s)	4.0sec										
thin-Be/Open	med-Be/Open	close	Safe	Norm	8ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
Subr= 2		1-time(s)	2.0sec										
Seqn= 10		1-time(s)	2.0sec										
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
Seqn= 11		1-time(s)	2.0sec										
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec	
Seqn= 15		1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

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### Active Region Search

\* \* \* \* \*

NOT USED

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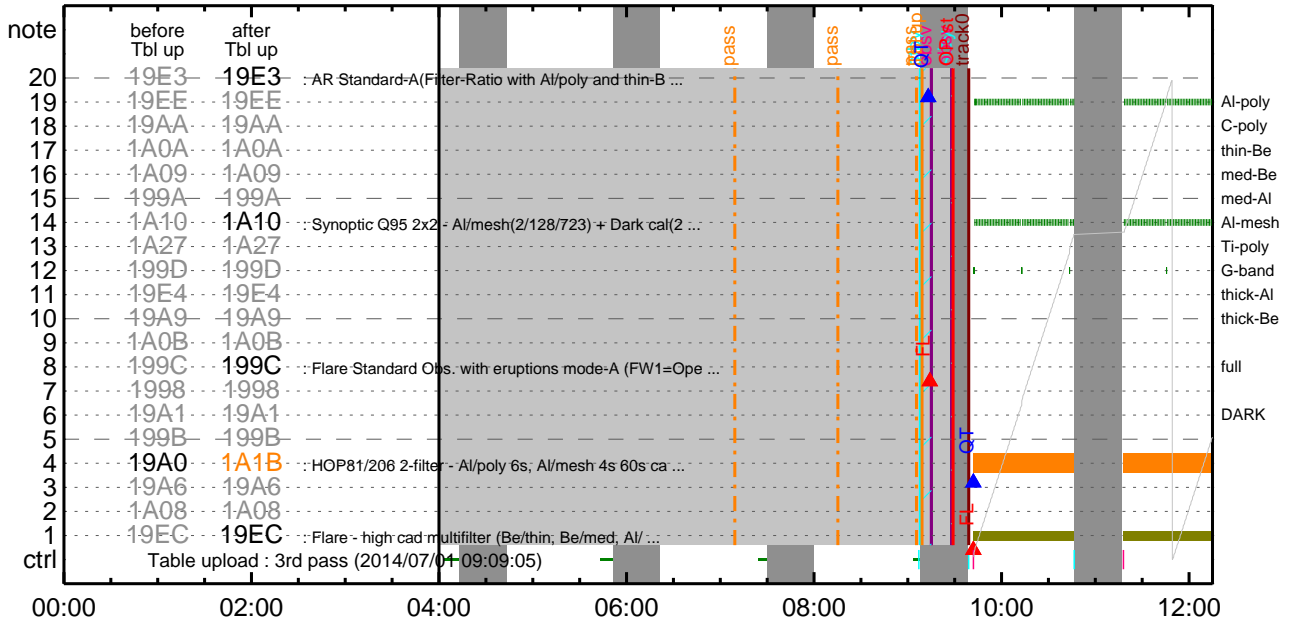
### Flare Detection

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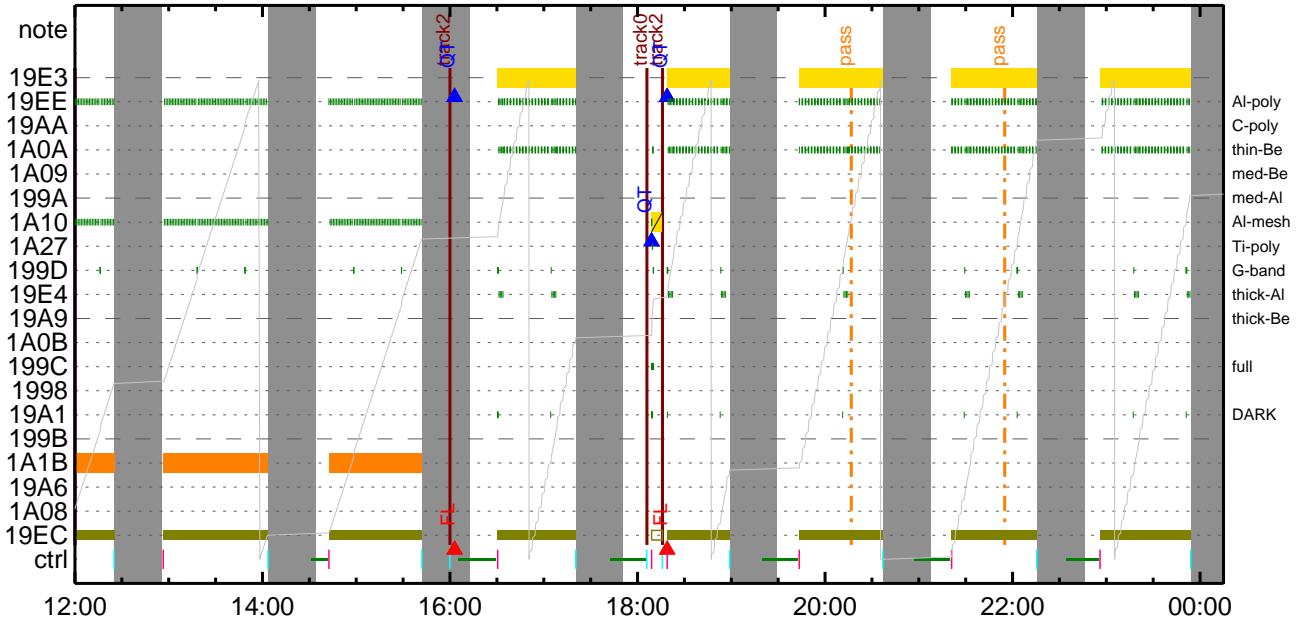
#### FLD Patrol

Term	Pointing (x, y)	Comment									
07/01 18:16:16 - 07/02 05:53:16	Track ( -681.9, -297.0) <sup>Ⓜ 07/01 18:16:00</sup>	# AR cont.									
07/02 06:03:16 - 07/02 17:42:16	Track ( -610.4, -301.4) <sup>Ⓜ 07/02 06:03:00</sup>	# AR cont.									
07/02 17:52:16 - 07/03 06:02:16	Track ( -530.9, -305.6) <sup>Ⓜ 07/02 17:52:00</sup>	# AR cont.									
07/03 06:12:16 - 07/05 10:16:00	Track ( -440.5, -309.5) <sup>Ⓜ 07/03 06:12:00</sup>	# AR cont.									
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

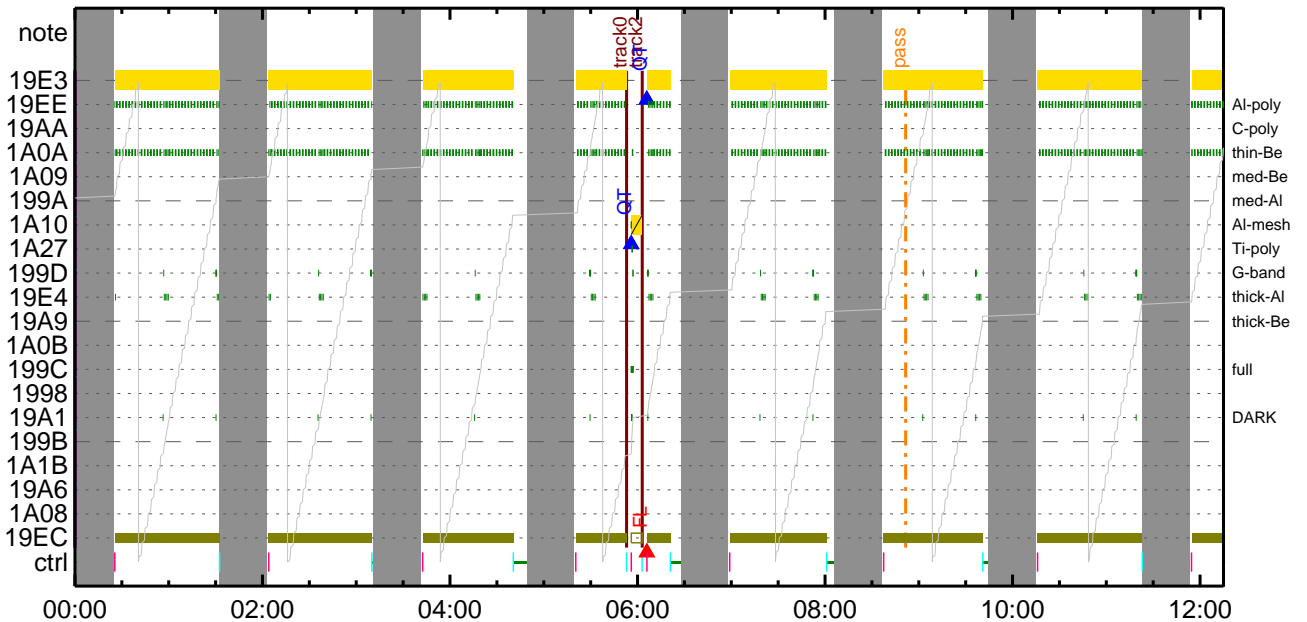
### CMDI #0378 2014/07/01



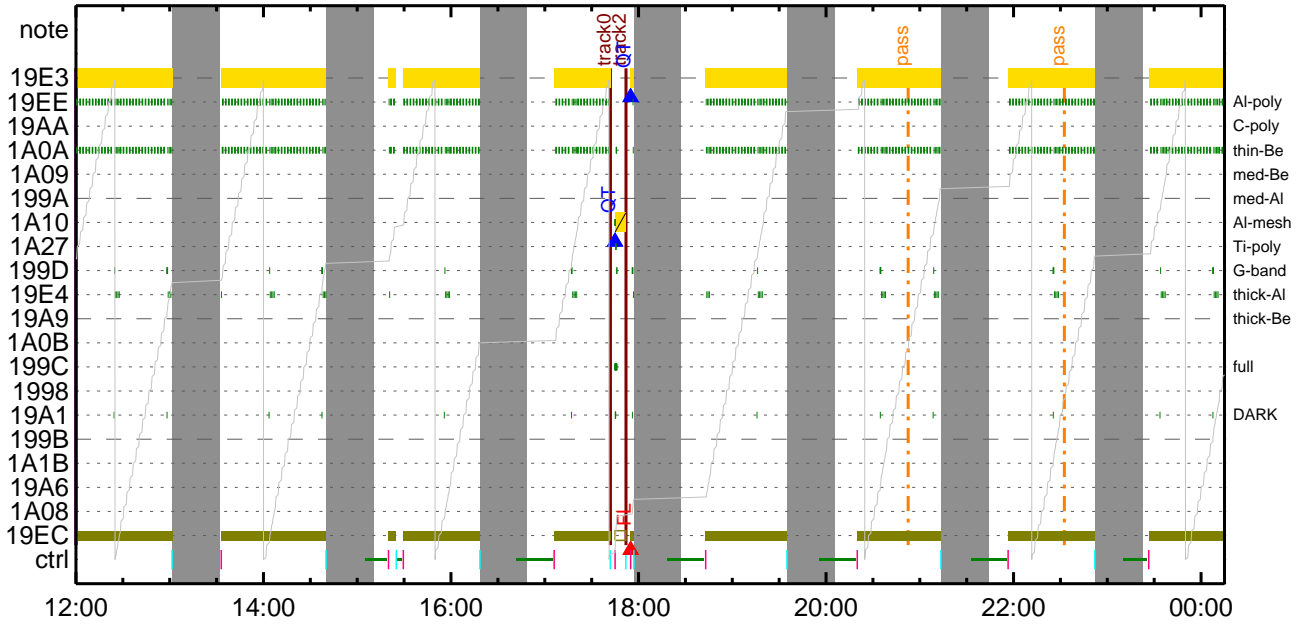
### CMDI #0378 2014/07/01



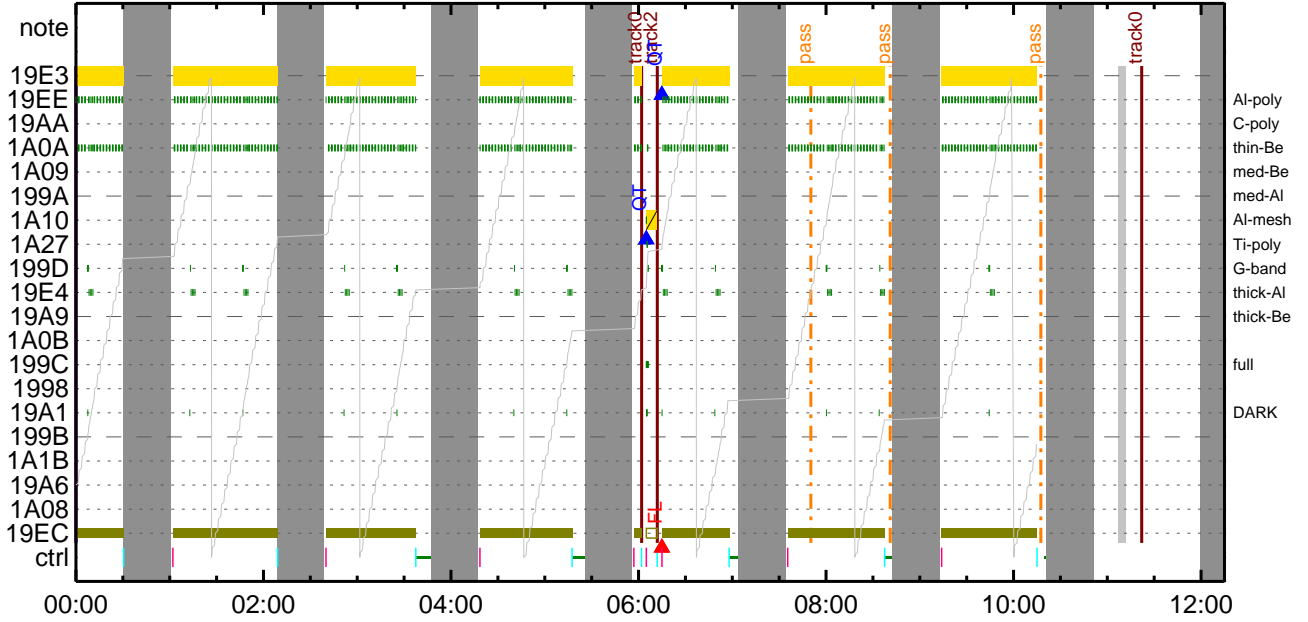
### CMDI #0378 2014/07/02



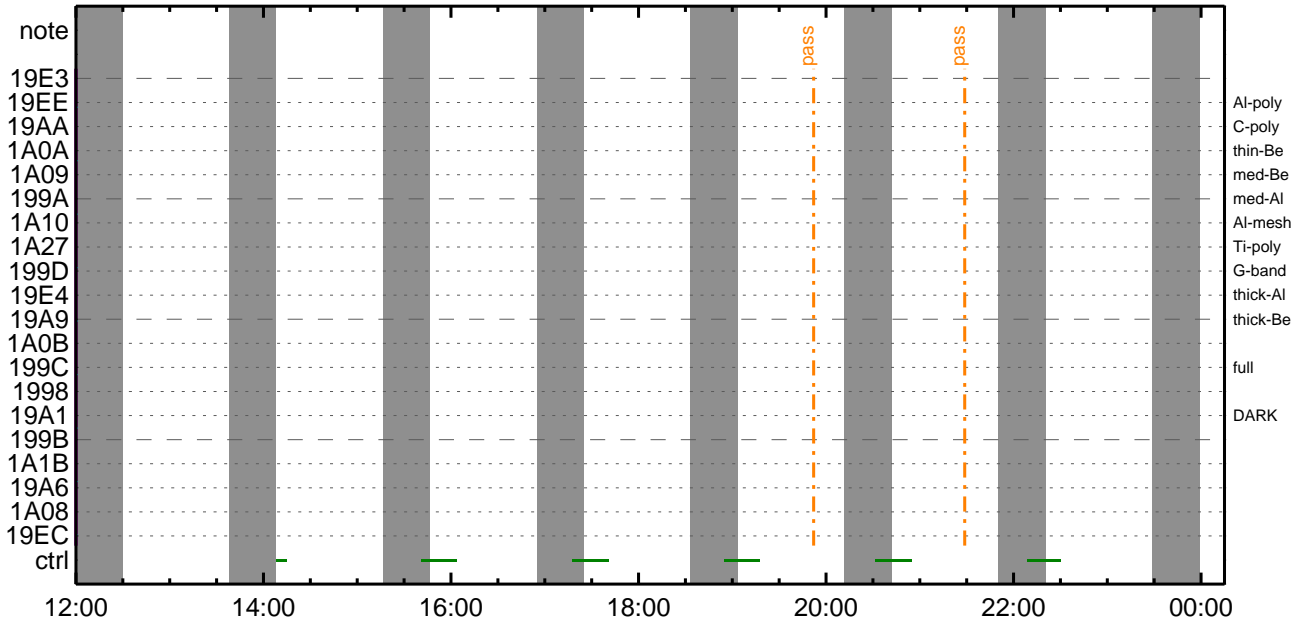
CMDI #0378 2014/07/02



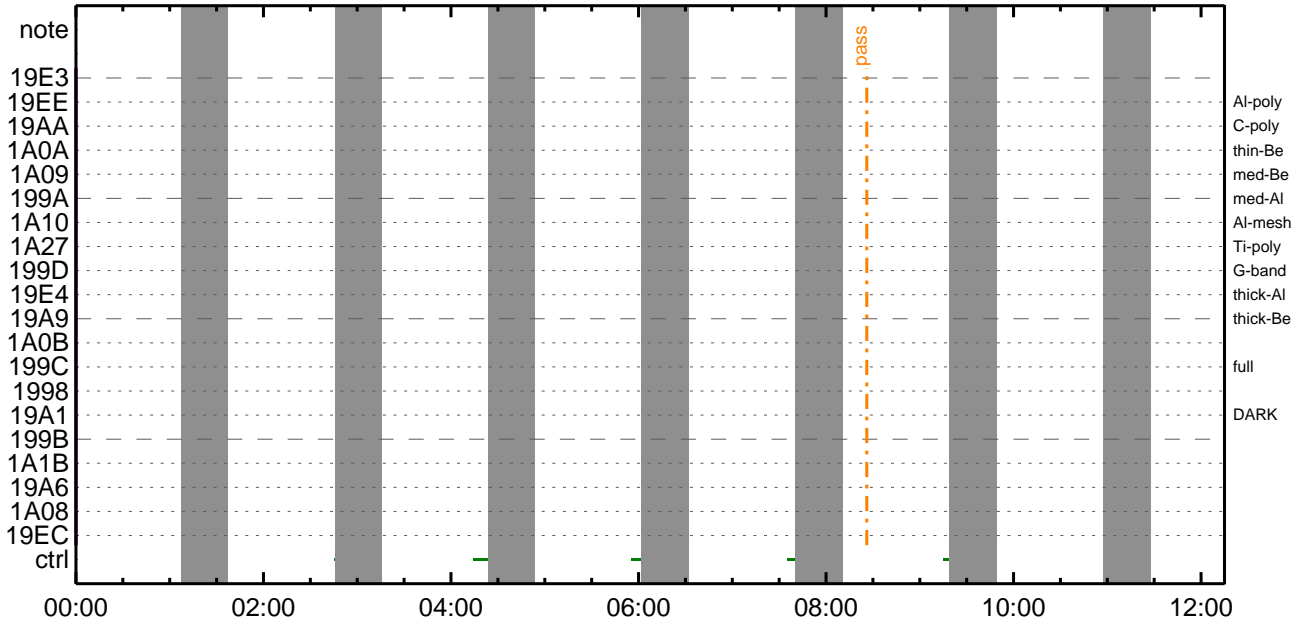
CMDI #0378 2014/07/03



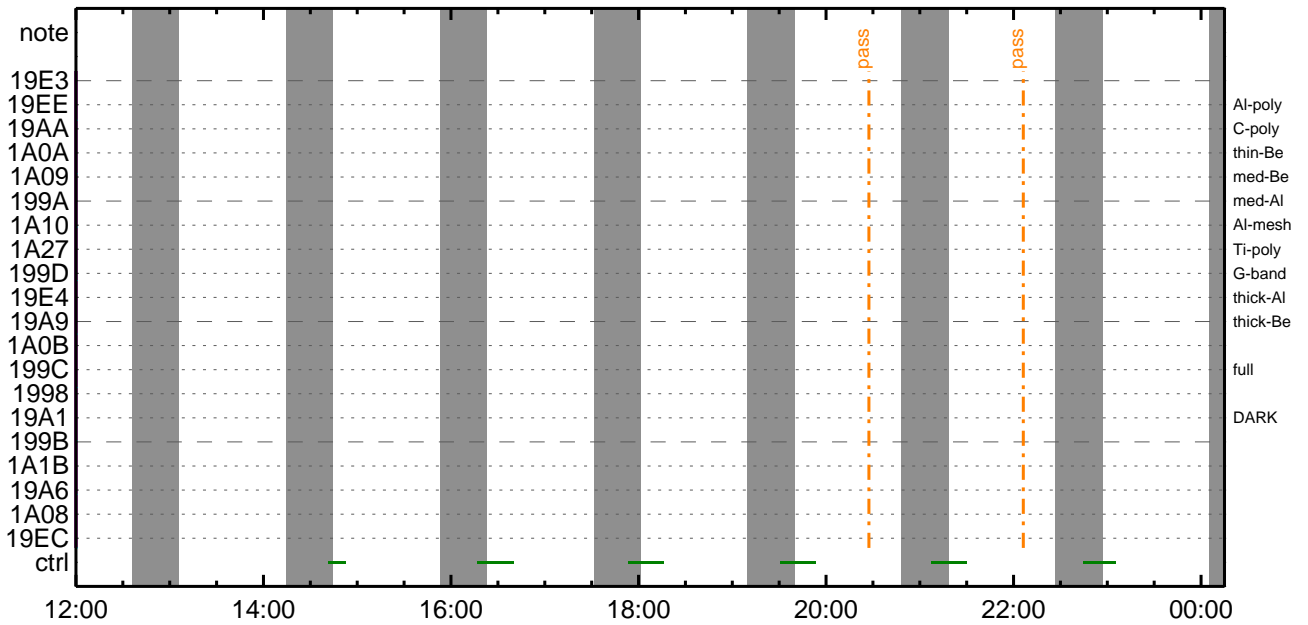
CMDI #0378 2014/07/03



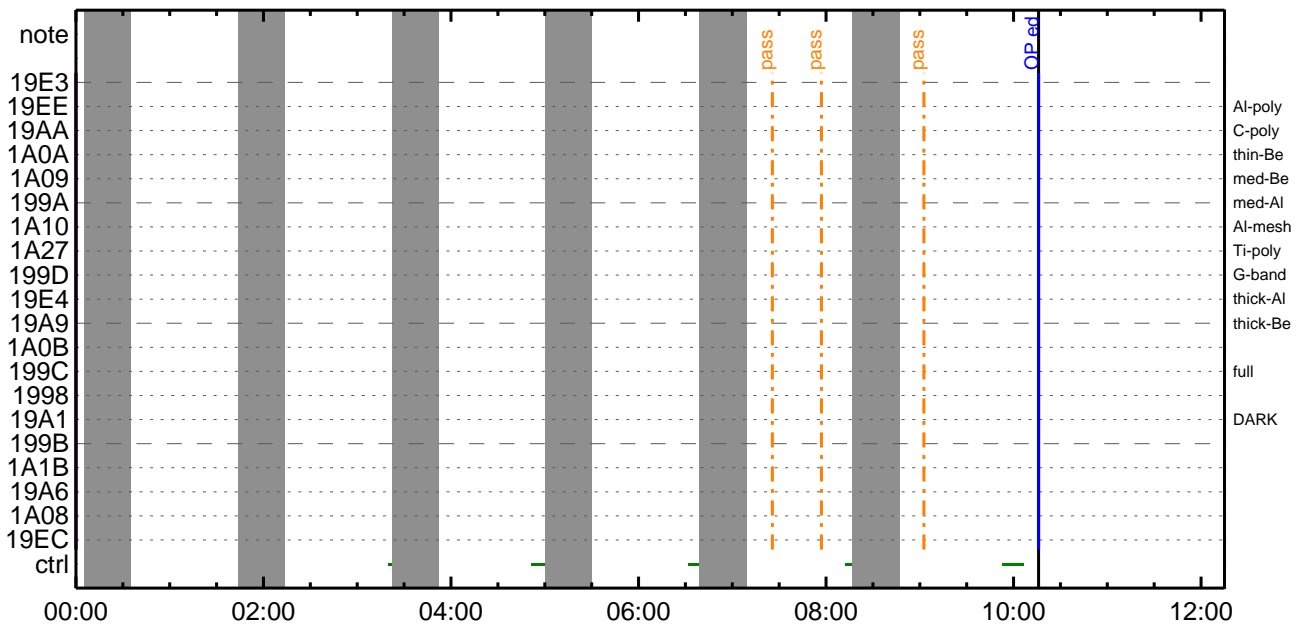
CMDI #0378 2014/07/04

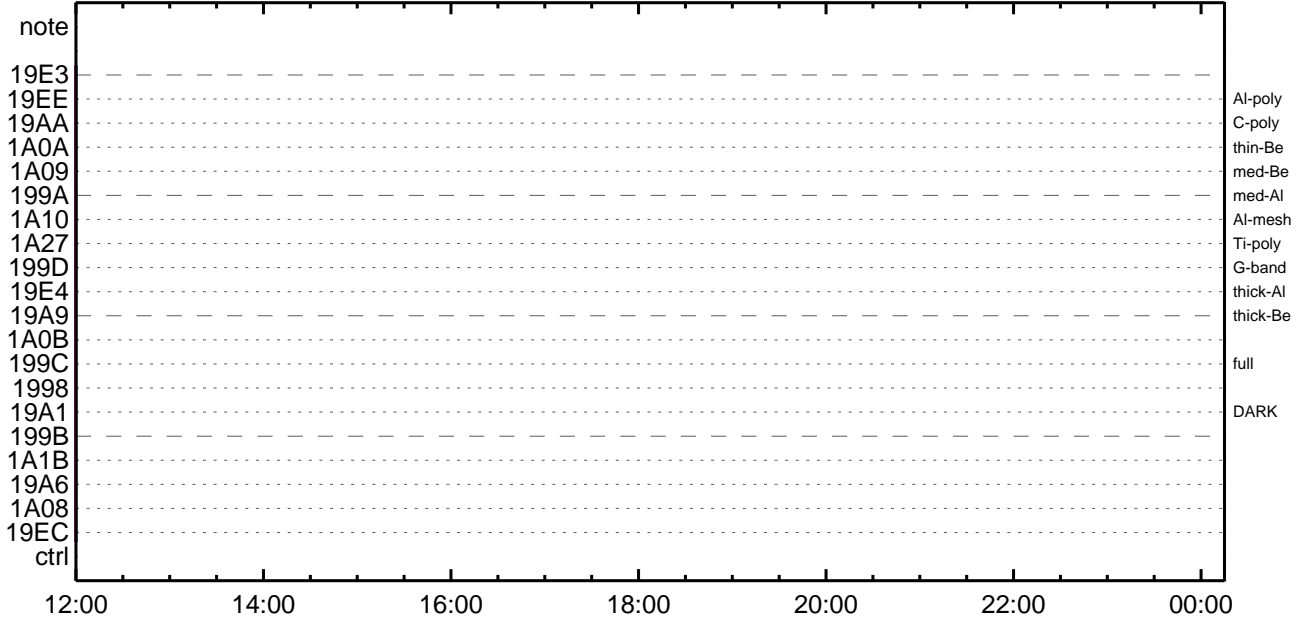


CMDI #0378 2014/07/04



CMDI #0378 2014/07/05











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0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥D¥!•İ Daily±;İÑøĒ'Øα¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
```





(a) Spacecraft Operation Procedure (real-commands)

```
main-481 2014-07-01 11:32:22 159 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿ãÀã•µ°È»Í×ÁÇãÍYçYÁY×Yí;¼YÉ;ÈÈèµ•íÉ;ÈÈÈ¼°ÇÔã•ã¿¼í¹çãÍ;çÀ®, ùã¹ãÈãÈãÇÁ+¿®ã•ãÈããã³ãÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop SP table >
0018 +. DC 07-F0 MDP_SP_CTRL_MANU
0019 BC (61)
0020 C. -----
0021 C. MDP_SP_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload SP Observation Table>
0025 . S. RAM ram-282:MDP_OBS_S
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_S >
0029 +. DC 07-F0 MDP_DUMP_SPTBL
0030 BC (83 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_S verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Upload DPL table >
0036 +. DC 07-F0 MDP_FG_CTRL_MANU
0037 BC (51)
0038 C. -----
0039 C. MDP_FG_CTRL_MODE = MANU [ ]
0040 C. -----
0041 C.
0042 C. YçYÁY×Yí;¼YÉãÎÁ°ãÈSTS_CHKãðOFFãÈã¹ãè
0043 C.
0044 . S. RAM ram-271:MDP_DPL
0045 ( )
0046 C.
0047 . C. < Dump RAMID=MDP_DPL >
0048 +. DC 07-F0 MDP_DUMP_FGTBL
0049 BC (82 07 00 38 b8 00 40)
0050 C. -----
0051 C. MDP_DPL verify = OK [ ]
0052 C. -----
0053 C.
0054 C. STS_CHKãðONãÈã¹ãè
0055 C.
0056 . C. < Update MDP DSC PAR1 >
0057 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0058 BC (4c)
0059 C. MDP_CMD_CODE = F04C0700[ ]
0060 C. MDP_CMD_CNT (count-up 1) [ ]
0061 C. -----
0062 C.
0063 C.
0064 C. *****
0065 C. SOT TI command set
0066 C. *****
0067 C. Execute, after the success of TBL upload.
0068 +. TI 2014-07-01 09:28:18.0
0069 DC 07-F0 MDP_SOT_MODE_OBSV
0070 BC (40)
0071 C. -----
0072 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0073 C. -----
0074 C.
0075 C.
0076 C. ***** XRT START *****
0077 C.
0078 +. DC 07-F0 MDP_XRT_CTRL_MANU
0079 BC (c1)
0080 +. DC 07-F0 MDP_XRT_MODE_STBY
0081 BC (c3)
0082 . C. ----- Success Verify ? OK / NG_____
0083 C.
0084 C. XRT Obs. Table Upload
0085 . S. RAM ram-291:MDP_OBS_X
0086 ( )
0087 C.
0088 +. DC 07-F0 MDP_DUMP_XRTTBL
0089 BC (84 07 00 00 00 3a d4)
0090 . C. ----- Comparison Check ? OK / ERR _____
0091 C.
0092 C.
0093 +. DC 07-F0 MDP_XRT_ROI_SET
0094 BC (cd 01 b1 b1 04 04)
0095 +. DC 07-F0 MDP_XRT_ROI_SET
```

```

0096 BC (cd 02 b1 b1 08 08)
0097 + DC 07-F0 MDP_XRT_ROI_SET
0098 BC (cd 03 b1 b1 08 08)
0099 + DC 07-F0 MDP_XRT_ROI_SET
0100 BC (cd 04 b1 b1 06 06)
0101 + DC 07-F0 MDP_XRT_ROI_SET
0102 BC (cd 05 85 83 06 06)
0103 + DC 07-F0 MDP_XRT_ROI_SET
0104 BC (cd 06 85 83 06 06)
0105 + DC 07-F0 MDP_XRT_ROI_SET
0106 BC (cd 07 85 83 08 08)
0107 + DC 07-F0 MDP_XRT_ROI_SET
0108 BC (cd 08 80 80 20 20)
0109 + DC 07-F0 MDP_XRT_ROI_SET
0110 BC (cd 09 80 80 20 08)
0111 + DC 07-F0 MDP_XRT_ROI_SET
0112 BC (cd 0a 80 80 08 20)
0113 + DC 07-F0 MDP_XRT_ROI_SET
0114 BC (cd 0f 80 80 06 06)
0115 + DC 07-F0 MDP_XRT_ROI_SET
0116 BC (cd 10 80 80 08 08)
0117 + DC 07-F0 MDP_XRT_FLD_ENA
0118 BC (d8)
0119 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0120 BC (c8)
0121 + DC 07-F0 MDP_XRT_AEC_RESET
0122 BC (d0)
0123 + DC 07-F0 MDP_XRT_ARS_DIS
0124 BC (d5)
0125 + DC 07-F0 MDP_XRT_FLD_RESET
0126 BC (da)
0127 + DC 07-F0 MDP_XRT_QT_PROG_SET
0128 BC (c4 14)
0129 + DC 07-F0 MDP_XRT_FL_PROG_SET
0130 BC (c5 08)
0131 . C. ----- Success Verify ? OK / NG ____
0132 C.
0133 C.
0134 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0135 C.
0136 + DC 07-F0 MDP_XRT_MODE_OBSV
0137 BC (c2)
0138 + TI 2014-07-01 09:28:02.0
0139 DC 07-F0 MDP_XRT_MODE_OBSV
0140 BC (c2)
0141 . C. ----- Success Verify ? OK / NG ____
0142 C.
0143 C. ***** XRT END *****
0144 C.
0145 . C. ***** MDP `ûÃîñî»ò%ÿñÊÃð¹ñèDCBC•x²è *****
0146 C. (%ã°îÿÓÿÃÿÈÿPÿÈÿÿãÿçÿèñE%¼ñ¼Ã»Ûñ¹ñè)
0147 . S. DC-BC dcbc-402:DCBC
0148 (MDP_known_event)
0149 C.
0150 C.
0151 . C. ***** ÿDÿ¹.Ï Daily±;îÑñÈ´Øñ¹ñèDCBC•x²è *****
0152 . S. DC-BC dcbc-153:DCBC
0153 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0154 C.
0155 C.
0156 . C. ;ãLOSÿÁÿSÿÿÿÿ¼Ã»Û;ã
0157 C.
0158 . C. ***** LOS *****
0159 C.

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Jul 01, 14 11:32

## XRT\_OGLIST\_0378.chk

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\*\*\* OP Sequence for XRT \*\*\*

2014/07/01	09:38:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/07/01	09:38:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2014/07/01	09:39:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 56 c0 00 9b		
2014/07/01	09:39:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/07/01	09:39:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/07/01	09:39:22.0	XRT_AEC_RESET_413_OG [0x19d]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/07/01	09:39:24.0	XRT_ARS_DIS_422_OG [0x1a6]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/07/01	09:41:54.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2014/07/01	09:41:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04		
2014/07/01	09:41:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01		
2014/07/01	09:42:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/07/01	10:46:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/07/01	10:46:32.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2014/07/01	10:46:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/07/01	10:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/07/01	11:17:00.0	XRT_Custom_430_OG [0x1ae]					
2014/07/01	11:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/07/01	12:25:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/07/01	12:25:02.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2014/07/01	12:25:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/07/01	12:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/07/01	12:55:30.0	XRT_Custom_430_OG [0x1ae]					
2014/07/01	12:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/07/01	14:03:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/07/01	14:03:32.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2014/07/01	14:03:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/07/01	14:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/07/01	14:41:30.0	XRT_Custom_430_OG [0x1ae]					
2014/07/01	14:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/07/01	15:42:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/07/01	15:42:02.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2014/07/01	15:42:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/07/01	15:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/07/01	15:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/07/01	15:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2014/07/01	16:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	02 00 00 00 00		
2014/07/01	16:00:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/07/01	16:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/07/01	16:00:20.0	XRT_AEC_RESET_413_OG [0x19d]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/07/01	16:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/07/01	16:00:24.0	XRT_FLD_RESET_431_OG [0x1af]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2014/07/01	16:02:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14		
2014/07/01	16:02:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01		
2014/07/01	16:29:30.0	XRT_Custom_430_OG [0x1ae]					
2014/07/01	16:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/07/01	17:20:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/07/01	17:20:32.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2014/07/01	17:20:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]					

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2014/07/01	17:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/07/01	18:05:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/07/01	18:05:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa 00
2014/07/01	18:06:00.5	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00	00 00 00
2014/07/01	18:06:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2014/07/01	18:08:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2014/07/01	18:08:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/07/01	18:08:58.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e	
2014/07/01	18:09:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/07/01	18:15:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/07/01	18:15:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97 00
2014/07/01	18:16:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	02	00	00 00 00
2014/07/01	18:16:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/07/01	18:16:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/07/01	18:16:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/07/01	18:16:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/07/01	18:16:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/07/01	18:18:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	14	
2014/07/01	18:18:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	01	
2014/07/01	18:19:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/07/01	18:59:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/07/01	18:59:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/07/01	18:59:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/07/01	19:02:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/07/01	19:42:30.0	XRT_Custom_430_OG [0x1ae]						
2014/07/01	19:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/07/01	20:37:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/07/01	20:37:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/07/01	20:37:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/07/01	20:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/07/01	21:20:00.0	XRT_Custom_430_OG [0x1ae]						
2014/07/01	21:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/07/01	22:15:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/07/01	22:15:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/07/01	22:15:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/07/01	22:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/07/01	22:55:00.0	XRT_Custom_430_OG [0x1ae]						
2014/07/01	22:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/07/01	23:54:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/07/01	23:54:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/07/01	23:54:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/07/01	23:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/07/02	00:24:30.0	XRT_Custom_430_OG [0x1ae]						
2014/07/02	00:25:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/07/02	01:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/07/02	01:32:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/07/02	01:32:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/07/02	01:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		

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2014/07/02	02:03:00.0	XRT_Custom_430_OG [0x1ae]								
2014/07/02	02:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2014/07/02	03:10:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2014/07/02	03:10:02.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2014/07/02	03:10:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2014/07/02	03:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2014/07/02	03:41:30.0	XRT_Custom_430_OG [0x1ae]								
2014/07/02	03:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2014/07/02	04:40:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2014/07/02	04:40:32.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2014/07/02	04:40:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2014/07/02	04:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2014/07/02	05:19:30.0	XRT_Custom_430_OG [0x1ae]								
2014/07/02	05:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2014/07/02	05:52:54.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2014/07/02	05:52:56.0	XRT_FOCUS_POSITION_403_OG [0x193]								
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00					
2014/07/02	05:53:00.0	AOCS_OrE-point_Start_3_OG [0x099]								
		AOCU_NM	5	02-76	00 00 00 00 00					
2014/07/02	05:53:16.0	XRT_FLD_DIS_434_OG [0x1b2]								
		MDP_XRT_FLD_DIS	1	07-F0	d9					
2014/07/02	05:55:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]								
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2014/07/02	05:55:56.0	XRT_ARS_DIS_423_OG [0x1a7]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2014/07/02	05:55:58.0	XRT_QT_PROG_SET_444_OG [0x1bc]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e					
2014/07/02	05:56:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2014/07/02	06:02:54.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2014/07/02	06:02:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]								
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00					
2014/07/02	06:03:00.0	AOCS_OrE-point_Start_2_OG [0x098]								
		AOCU_NM	5	02-76	02 00 00 00 00					
2014/07/02	06:03:16.0	XRT_FLD_ENA_411_OG [0x19b]								
		MDP_XRT_FLD_ENA	1	07-F0	d8					
2014/07/02	06:03:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]								
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2014/07/02	06:03:20.0	XRT_AEC_RESET_413_OG [0x19d]								
		MDP_XRT_AEC_RESET	1	07-F0	d0					
2014/07/02	06:03:22.0	XRT_ARS_DIS_423_OG [0x1a7]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2014/07/02	06:03:24.0	XRT_FLD_RESET_431_OG [0x1af]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2014/07/02	06:05:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14					
2014/07/02	06:05:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]								
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01					
2014/07/02	06:06:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2014/07/02	06:21:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2014/07/02	06:21:02.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2014/07/02	06:21:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2014/07/02	06:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2014/07/02	06:58:00.0	XRT_Custom_430_OG [0x1ae]								
2014/07/02	06:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2014/07/02	08:01:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2014/07/02	08:01:02.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2014/07/02	08:01:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2014/07/02	08:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2014/07/02	08:36:30.0	XRT_Custom_430_OG [0x1ae]								
2014/07/02	08:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2014/07/02	09:41:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2014/07/02	09:41:02.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2014/07/02	09:41:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2014/07/02	09:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					





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2014/07/02	17:55:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	01
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/07/02	17:57:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/02	17:57:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/07/02	17:57:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/07/02	18:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/07/02	18:42:00.5	XRT_Custom_430_OG [0x1ae]					
2014/07/02	18:43:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/07/02	19:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/02	19:35:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/07/02	19:35:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/07/02	19:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/07/02	20:19:00.0	XRT_Custom_430_OG [0x1ae]					
2014/07/02	20:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/07/02	21:13:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/02	21:13:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/07/02	21:13:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/07/02	21:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/07/02	21:55:30.0	XRT_Custom_430_OG [0x1ae]					
2014/07/02	21:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/07/02	22:52:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/02	22:52:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/07/02	22:52:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/07/02	22:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/07/02	23:25:30.0	XRT_Custom_430_OG [0x1ae]					
2014/07/02	23:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/07/03	00:30:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/03	00:30:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/07/03	00:30:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/07/03	00:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/07/03	01:01:00.0	XRT_Custom_430_OG [0x1ae]					
2014/07/03	01:02:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/07/03	02:09:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/03	02:09:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/07/03	02:09:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/07/03	02:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/07/03	02:39:00.0	XRT_Custom_430_OG [0x1ae]					
2014/07/03	02:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/07/03	03:37:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/03	03:37:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/07/03	03:37:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/07/03	03:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/07/03	04:17:30.0	XRT_Custom_430_OG [0x1ae]					
2014/07/03	04:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/07/03	05:17:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/03	05:17:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/07/03	05:17:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/07/03	05:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/07/03	05:56:00.0	XRT_Custom_430_OG [0x1ae]					
2014/07/03	05:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/07/03	06:01:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

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2014/07/03	06:01:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2014/07/03	06:02:00.0	AOCs_OrE-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	00	00
2014/07/03	06:02:16.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/07/03	06:04:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/07/03	06:04:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/07/03	06:04:58.0	XRT_QT_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e			
2014/07/03	06:05:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/07/03	06:11:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/03	06:11:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2014/07/03	06:12:00.0	AOCs_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	02	00	00	00	00
2014/07/03	06:12:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/07/03	06:12:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/07/03	06:12:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/07/03	06:12:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/07/03	06:12:24.0	XRT_FLD_RESET_431_OG [0x1af]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/07/03	06:14:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	14			
2014/07/03	06:14:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	01			
2014/07/03	06:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/07/03	06:58:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/03	06:58:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/07/03	06:58:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/07/03	07:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/07/03	07:34:30.0	XRT_Custom_430_OG [0x1ae]							
2014/07/03	07:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/07/03	08:37:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/03	08:37:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/07/03	08:37:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/07/03	08:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/07/03	09:13:00.0	XRT_Custom_430_OG [0x1ae]							
2014/07/03	09:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/07/03	10:15:00.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/03	10:15:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/07/03	11:22:00.0	AOCs_OrE-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	00	00