

XRT Timeline to be uploaded on 2014/07/15

Period: 2014/07/15 10:05:00 - 2014/07/19 10:34:00

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Normal mode

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XOB #1A08: CCD Monitor During Bakeout - G-Band 33ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms

Term	Pointing (x, y)	Comment
07/16 11:03:00 - 07/16 11:09:54	Fixed (-528.4, -528.4)	# XRT post-bakeout pointing Q1
PROG= 02 1-time(s)		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 88 1-time(s) 12.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 32ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 32ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 23 2-time(s) 2.0sec		
└─ Open/Al-mesh	Open/Ti-poly close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 12 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ Seqn= 14 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1A09: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms-2

Term	Pointing (x, y)	Comment
07/16 11:13:00 - 07/16 11:19:54	Fixed (528.4, -528.4)	# XRT post-bakeout pointing Q2
PROG= 07 1-time(s)		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 31 1-time(s) 12.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 32ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 32ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 23 2-time(s) 2.0sec		
└─ Open/Al-mesh	Open/Ti-poly close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 12 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ Seqn= 14 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1A0A: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms-2

Term	Pointing (x, y)	Comment
07/16 11:23:00 - 07/16 11:29:54	Fixed (528.4, 528.4)	# XRT post-bakeout pointing Q3
PROG= 17 1-time(s)		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 81 1-time(s) 12.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 32ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 32ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 23 2-time(s) 2.0sec		
└─ Open/Al-mesh	Open/Ti-poly close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 12 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ Seqn= 14 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1A0B: CCD Monitor During Bakeout - G-Band 33ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms

Term	Pointing (x, y)	Comment
07/16 11:33:00 - 07/16 11:41:00	Fixed (-528.4, 528.4)	# XRT post-bakeout pointing Q4
PROG= 09 1-time(s)		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 28 1-time(s) 12.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec

	Open/thick-Be	Open/thick-Be	close	Safe	Dark	32ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	32ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Subr= 2	1-time(s)		2.0sec										
	Seqn= 23		2-time(s) 2.0sec										
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 3	2-time(s)		2.0sec										
	Seqn= 12		1-time(s) 2.0sec										
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
	Seqn= 14		1-time(s) 2.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19E0: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33ms)

Term	Pointing (x, y)	Comment
07/16 12:14:00 - 07/16 17:59:54	Track (-830.0, -272.8) @ 07/16 11:40:00	# AR obs
07/16 22:18:00 - 07/17 06:18:24	Track (-788.6, -279.4) @ 07/16 22:00:00	#AR obs
07/17 06:31:30 - 07/17 07:59:54	Track (-748.8, -284.7) @ 07/17 06:28:30	#AR obs
07/17 08:03:00 - 07/17 08:56:00	Fixed (810.0, 395.0)	# HOP 255

PROG= 19 Inf.-time(s)

Subr= 1	1-time(s)		2.0sec										
	Seqn= 8		2-time(s) 2.0sec										
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2	2-time(s)		2.0sec										
	Seqn= 24		1-time(s) 2.0sec										
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Seqn= 42		4-time(s) 2.0sec										
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Seqn= 62		36-time(s) 50.0sec										
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A10: Synoptic Q95 2x2 - Al/mesh(2/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(5/256/1443) + Thin

Term	Pointing (x, y)	Comment
07/16 18:03:00 - 07/16 18:14:30	Fixed (0.0, 0.0)	synoptic and SOT flat field
07/17 06:21:30 - 07/17 06:28:24	Fixed (0.0, 0.0)	synoptic, shifted 18.5 min

PROG= 14 1-time(s)

Subr= 1	1-time(s)		12.0sec										
	Seqn= 35		1-time(s) 4.0sec										
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Seqn= 5		1-time(s) 2.0sec										
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
	Seqn= 72		1-time(s) 4.0sec										
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Seqn= 67		1-time(s) 2.0sec										
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Seqn= 6		1-time(s) 2.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A1C: CME watch - 4x4 - AEC 2 - Al-poly - G-band (2x2,8ms) - Leak (33ms) - 100s cad

Term	Pointing (x, y)	Comment
07/16 19:08:00 - 07/16 21:31:30	Fixed (0.0, 0.0)	synoptic and SOT flat field

PROG= 05 Inf.-time(s)

Subr= 1	60-time(s)		100.0sec										
	Seqn= 78		1-time(s) 4.0sec										
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Subr= 2	1-time(s)		2.0sec										

Seqn= 6		1-time(s)		2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)		Q=90	0	0	2.0sec		
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval			

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Flare mode

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XOB #19EC: Flare - high cad multifilter (Be/thin, Be/med, Al/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + Gband (4

Term	Pointing (x, y)	Comment
07/16 12:14:00 - 07/16 17:59:54	Track (-830.0, -272.8) ^{Ⓢ 07/16 11:40:00}	# AR obs
07/16 19:08:00 - 07/16 21:31:30	Fixed (0.0, 0.0)	synoptic and SOT flat field
07/16 22:18:00 - 07/17 06:18:24	Track (-788.6, -279.4) ^{Ⓢ 07/16 22:00:00}	#AR obs
07/17 06:31:30 - 07/17 07:59:54	Track (-748.8, -284.7) ^{Ⓢ 07/17 06:28:30}	#AR obs
07/17 08:03:00 - 07/17 08:56:00	Fixed (810.0, 395.0)	# HOP 255

PROG= 01 30-time(s)

Subr= 1		30-time(s)		2.0sec											
Seqn= 26		1-time(s)		4.0sec											
thin-Be/Open	med-Be/Open	close	Safe	Norm	8ms	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec		
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec		
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec		
Subr= 2		1-time(s)		2.0sec											
Seqn= 10		1-time(s)		2.0sec											
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec		
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec		
Seqn= 11		1-time(s)		2.0sec											
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)		Q=95	2	0	2.0sec		
Seqn= 15		1-time(s)		2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec		
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec		
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)		Q=98	0	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval			

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Active Region Search

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NOT USED

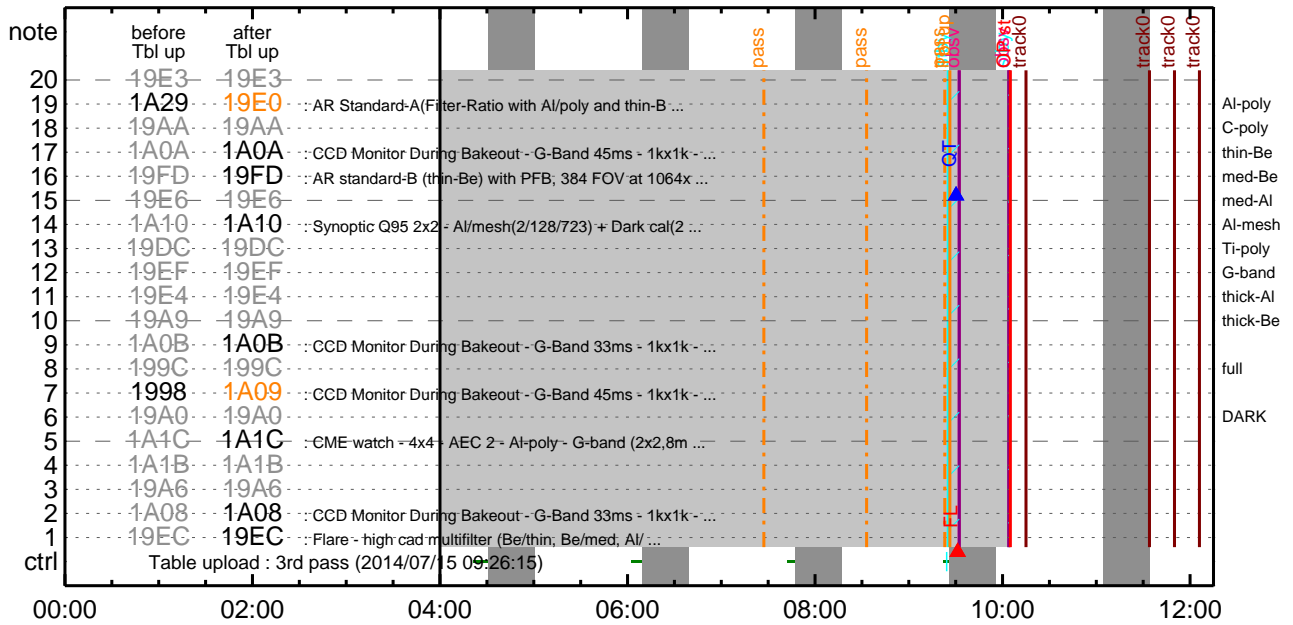
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Flare Detection

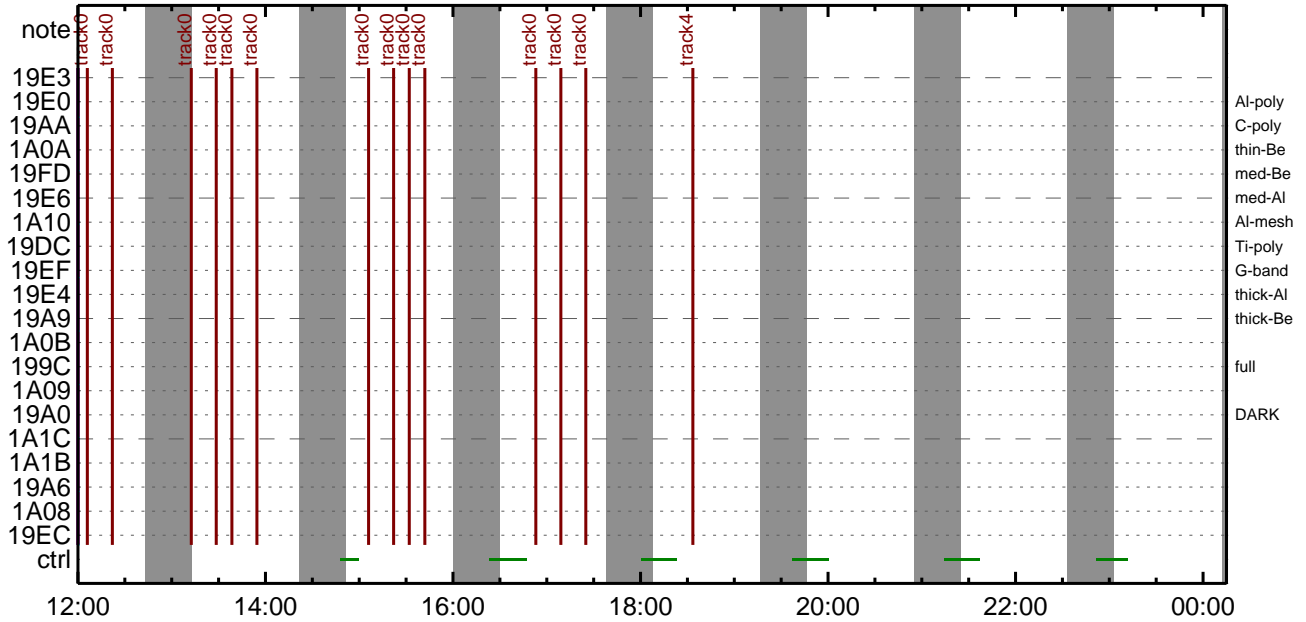
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FLD Patrol													
Term	Pointing (x, y)	Comment											
07/16 12:11:16 - 07/16 18:00:16	Track (-830.0, -272.8) ^{Ⓢ 07/16 11:40:00}	# AR obs											
07/16 19:05:16 - 07/17 06:18:46	Fixed (0.0, 0.0)	synoptic and SOT flat field											
07/17 06:28:46 - 07/19 10:34:00	Track (-748.8, -284.7) ^{Ⓢ 07/17 06:28:30}	#AR obs											
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8			Q=50	30sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

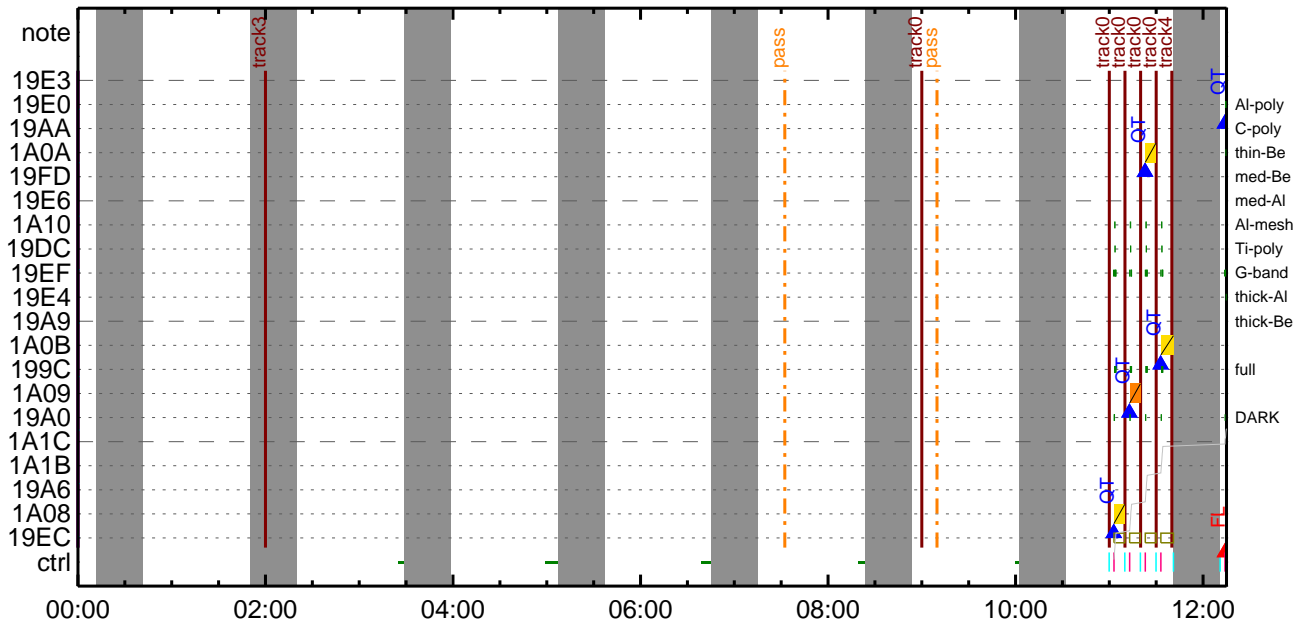
CMDI #0407 2014/07/15



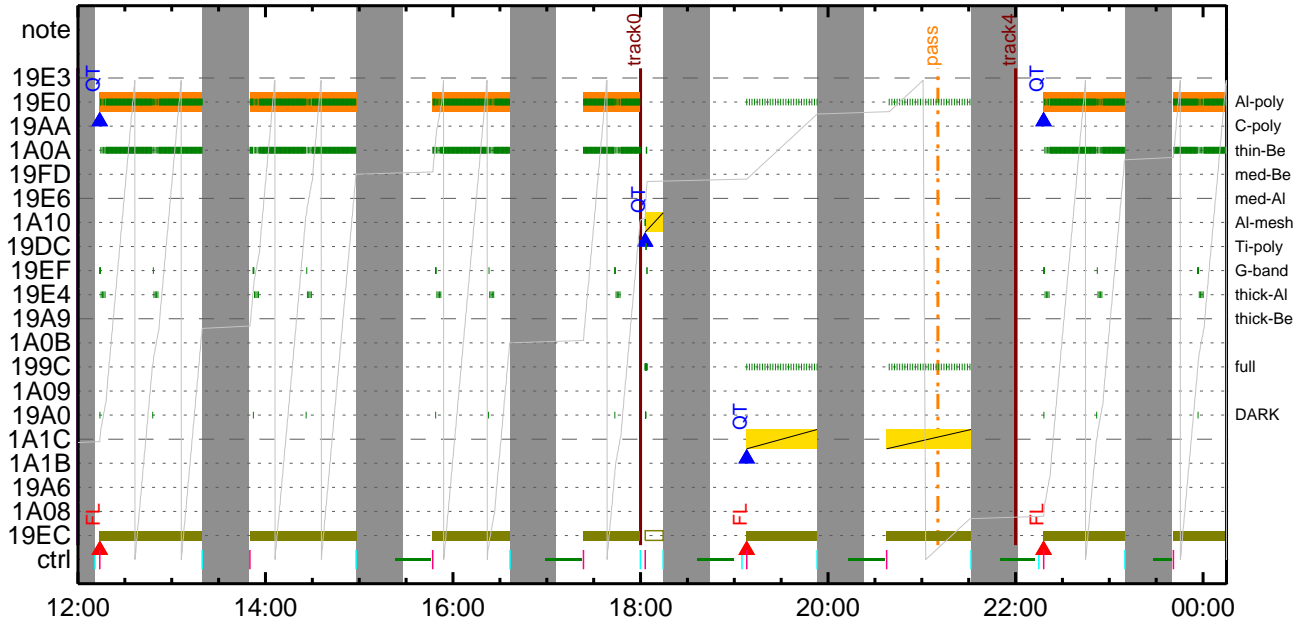
CMDI #0407 2014/07/15



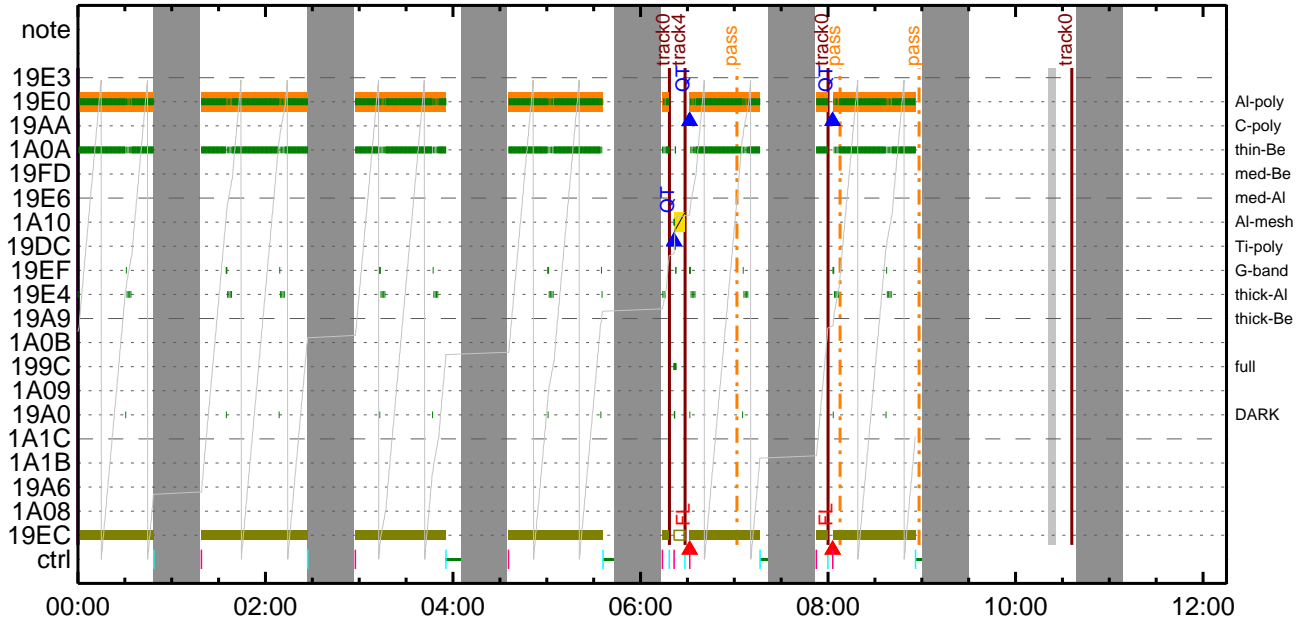
CMDI #0407 2014/07/16



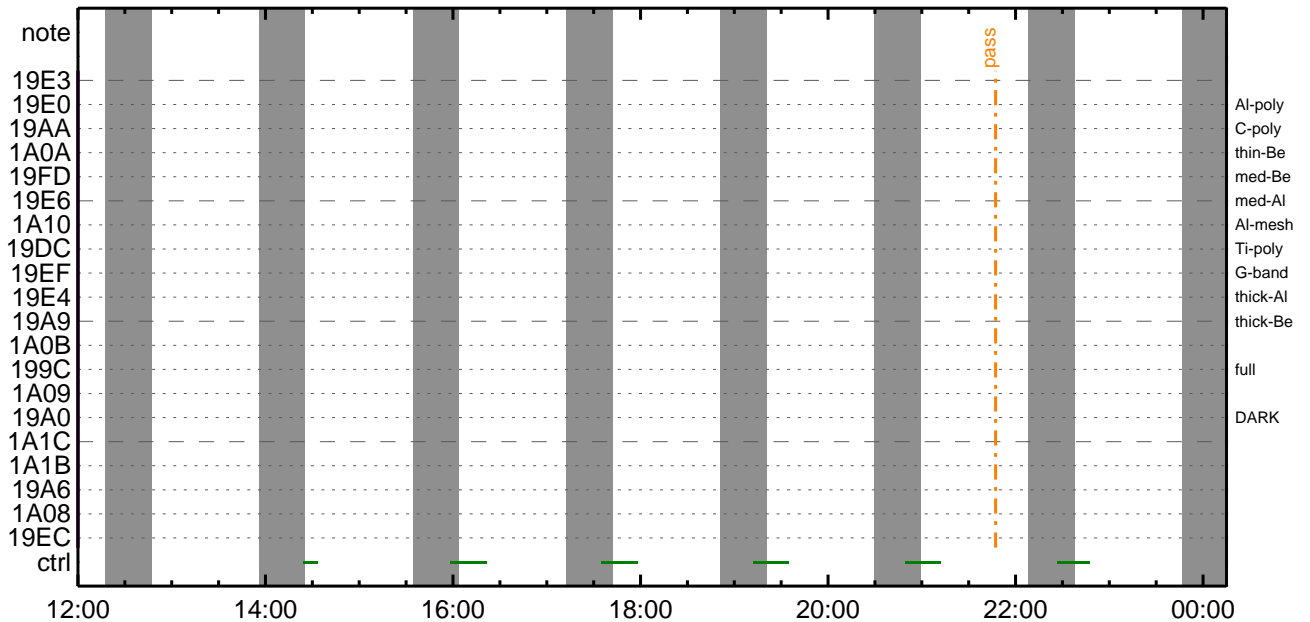
CMDI #0407 2014/07/16



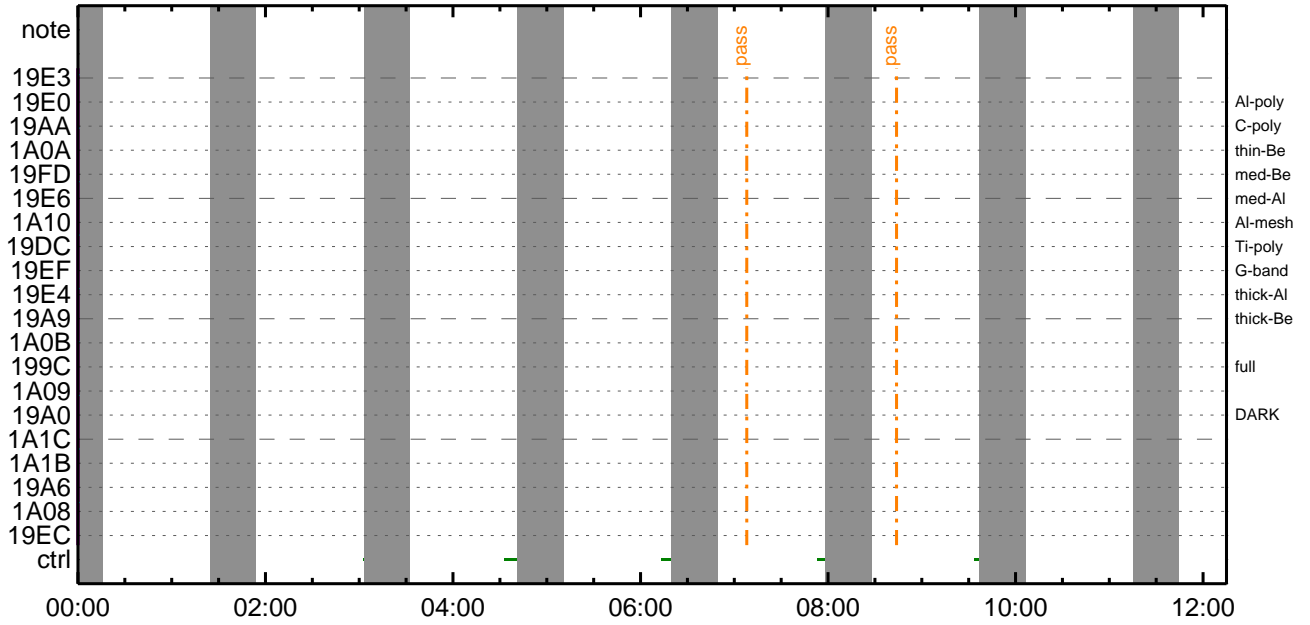
CMDI #0407 2014/07/17



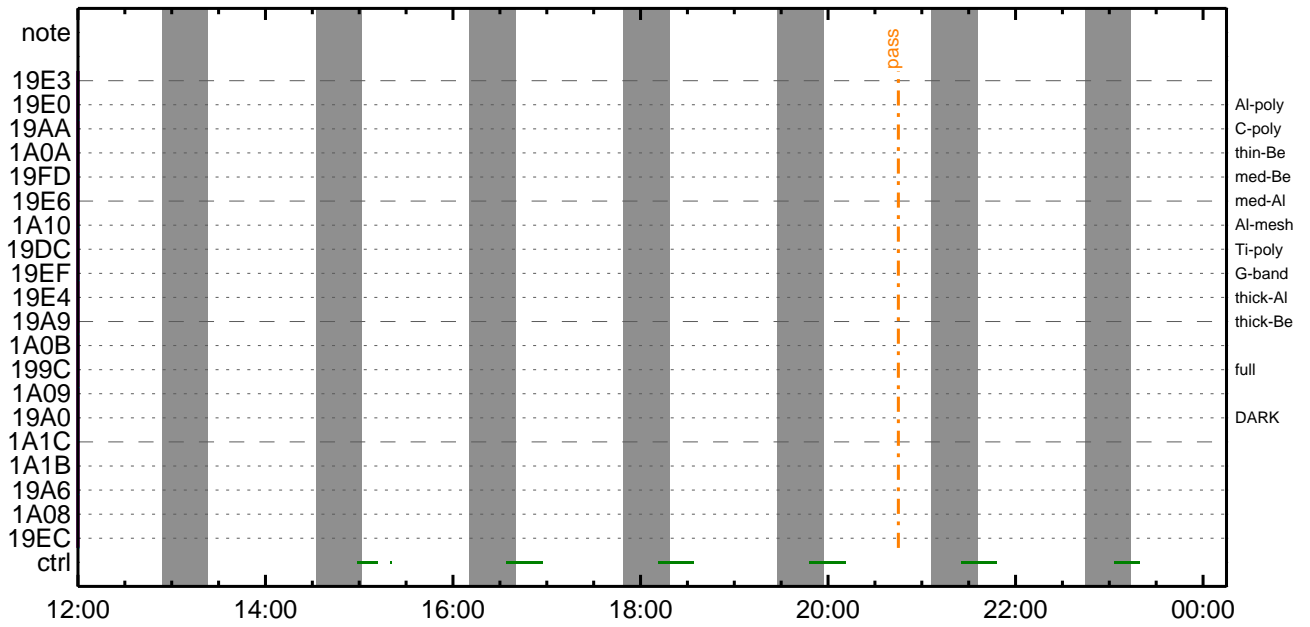
CMDI #0407 2014/07/17



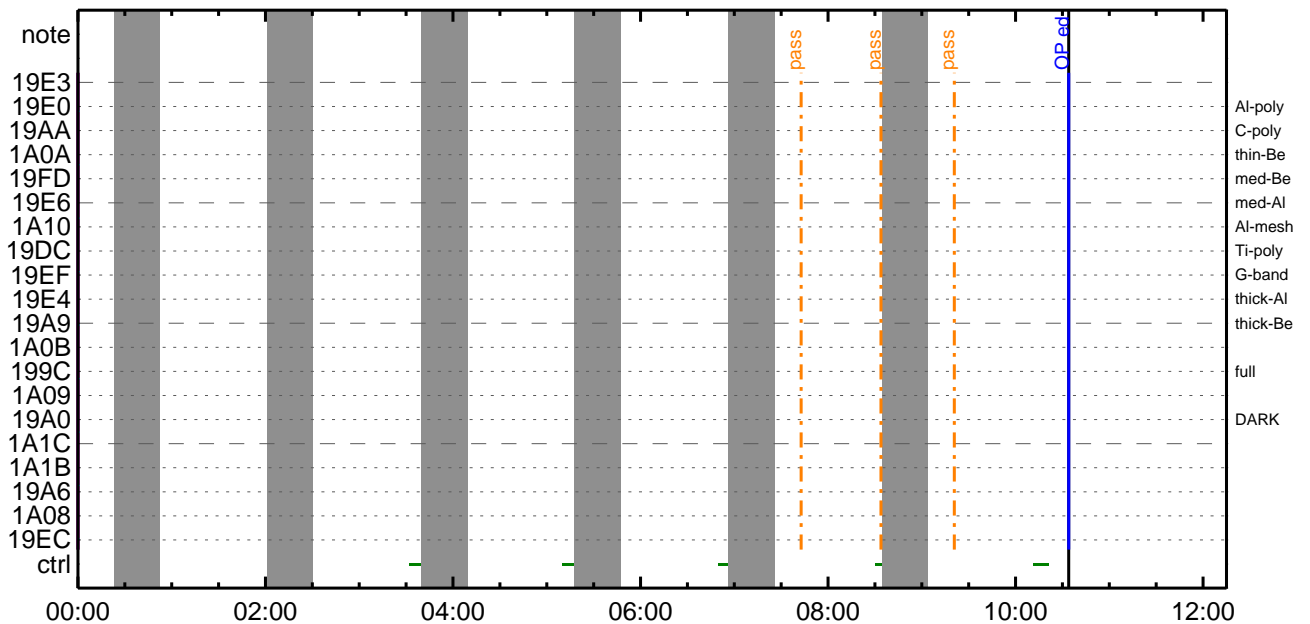
CMDI #0407 2014/07/18



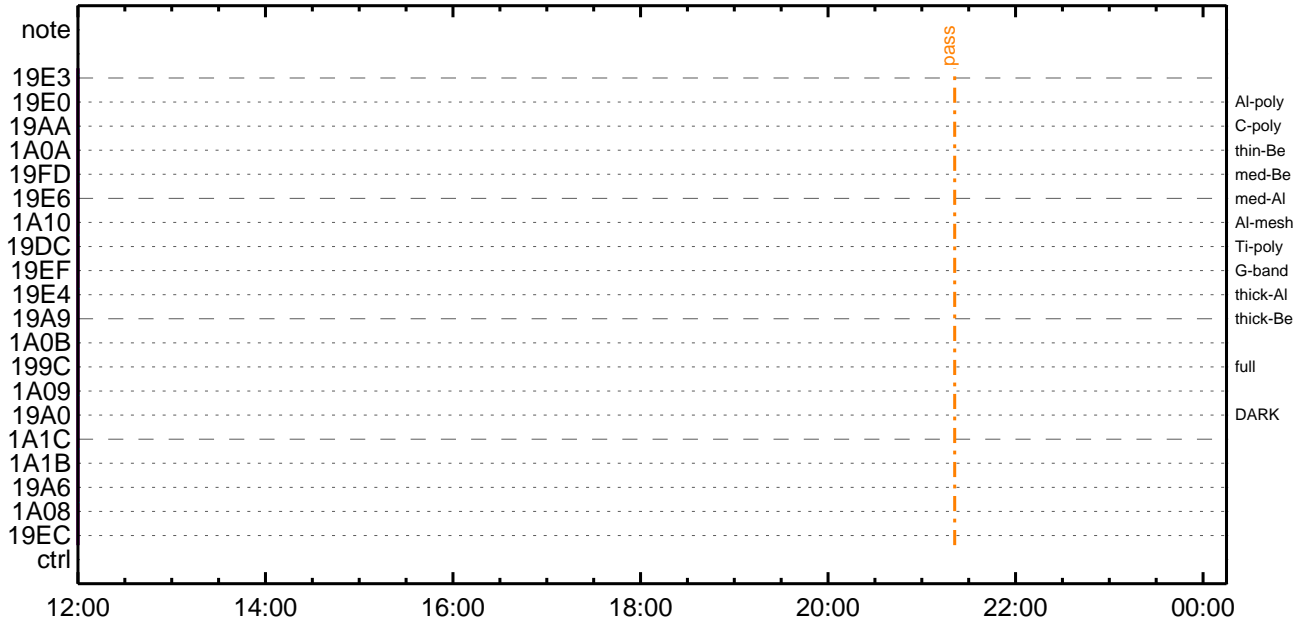
CMDI #0407 2014/07/18



CMDI #0407 2014/07/19



CMDI #0407 2014/07/19



(a) Spacecraft Operation Procedure (real-commands)

```
main-506 2014-07-15 15:57:43 205 33 SOLAR-B MAIN //
0001 . C.
0002 . C. ***** AOS *****
0003 . C.
0004 . C. ;ãAOSYÃY$YÃY-¼Ä»Û;ã
0005 . C.
0006 . C. YÃY$;¼Y³YFÿÖYÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 . C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 . C. Äí;È¿¿òÄò•µ°È»Í×ÁÇ¿íYçYÃY×Yí;¼YÉ;ÈÈè¼µ•ííÉ;ÈòÈ¼°ÇÔò•ò¿¼í¹ç¿í;çÄ®, ùò¹òÈòòçÄ+¿®ò•òÈòòò³òÈ; Æ
0011 +. DC 02-8E AOCU_ORB_UPD
0012 . C.
0013 . C.
0014 . C. *****
0015 . C. OP/OGYí;¼YÉ;|YÃYÖY×
0016 . C. *****
0017 . C.
0018 . C. ;ãOP/OGYí;¼YÉ;ã
0019 . S. OP op-506:OP
0020 . C.
0021 . S. OG og-506:OG
0022 . C.
0023 . C.
0024 . C. ;ãNMOG&OPí°èYÃYÖY×;ã
0025 . C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 . C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 . C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 . C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 . C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 . C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 . C. çç[HK1_PKT_FORM_NO] EQ 7
0036 . C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 . C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 . C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 . C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. YÃYÖY×¼ªª î»òð³ îÇ§
0041 . C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGòî¼È¹ç•è² îOKòð³ îÇ§
0043 . C.
0044 . C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 . C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 . C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 . C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 . C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 . C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 . C. çç[HK1_PKT_FORM_NO] EQ 7
0055 . C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 . C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 . C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 . C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. YÃYÖY×¼ªª î»òð³ îÇ§
0060 . C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGòî¼È¹ç•è² îOKòð³ îÇ§
0062 . C.
0063 . C. NMOG(0x210000-0x210FFF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 . C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 . C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 . C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 . C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 . C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 . C. çç[HK1_PKT_FORM_NO] EQ 7
0074 . C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 . C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 . C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 . C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. YÃYÖY×¼ªª î»òð³ îÇ§
0079 . C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG, RAM ID=OPòî¼È¹ç•è² îOKòð³ îÇ§
0081 . C.
0082 . C. ***** òÈ²¼òî¼Ä´¶í°òÈÈ-òòÄ+¿® (¼âµ-YÃYÖY×¼½çòðÄÖÃæçªªò"òÈ¼í¹çòçòâ) *****
0083 . C. DHUã;¼YÉ;È¼Y¼; Yí;¼YÉ;Èòòíãò¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 . C. çç[HK1_PKT_FORM_NO] EQ 2
0087 . C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 . C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0089 . C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 . C.
0091 . C. *****
0092 . C. TI-CMD SET (OPOG STOP/COPY/START)
0093 . C. *****
0094 . C.
0095 . C. NOTICE |§ OPOG UPLOADò-Ä+¿®NGòî¼í¹ç; ç°È²¼òî¼TI-CMDÄ+¿®òî¼Ä¹Ôò•òÈòòò³òÈ; Æ
```



```

0096 C.                0B02; 0SET0EDUMPAIÆ±°iYNY¹qÇ¹Òa|a³aE; f
0097 C.
0098 . C.    TIY³YBYÖYÉ0dÅDi (UT)
0099 +. TI 2014-07-15 10:00:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                00[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0102 C.
0103 +. TI 2014-07-15 10:00:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                00[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0106 C.
0107 +. TI 2014-07-15 10:00:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                00[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0110 C.
0111 +. TI 2014-07-15 10:04:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                00[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0114 C.
0115 C.    °E²¼0IÄæ%ííN0IYÁY§YÁY-¹àÛ
0116 C.                00[HK1_TI_CMD_ENA/DIS]                EQ        ENA
0117 C.                00[HK1_TI_CMD_NUM]                EQ        4
0118 C.                00[HK1_NEXT_EXEC_PIM]                EQ        DHU
0119 C.                00[HK1_NEXT_EXEC_DC]                EQ        0xB3
0120 C.
0121 . C.    *****
0122 C.    TIÏÎ°èYAYÖYX
0123 C.    *****
0124 C.
0125 C.    TI_TBL(0x03AB00-0x03AEFF; § 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC    (03 ab 03 01 02)
0128 C.                00[HK1_DMP_TOP_ADRS_1]                EQ        07
0129 C.                00[HK1_DMP_TOP_ADRS_0]                EQ        2B
0130 C.                00[HK1_DMP_BLOCK_NUM]                EQ        3
0131 C.                00[HK1_DMP_REPEAT_NUM]                EQ        0
0132 C.                00[HK1_DMA_DMP_PIM]                EQ        DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC    (07 0b f8)
0135 C.                00[HK1_PKT_FORM_NO]                EQ        7
0136 C.                00[HK1_PKT_GEN_TIME]                EQ        0.25 s
0137 C.                00[HK1_S_TLM_BIT_RATE]                EQ        32k
0138 C.                00[HK1_X_TLM_BIT_RATE]                EQ        4M
0139 C.                00[HK1_DMP_CHK_FLG]                EQ        EXEC
0140 C.
0141 . C.    YAYÖYX½ªI»0d³IÇ§
0142 C.                00[HK1_DMP_CHK_FLG]                EQ        NON
0143 C.
0144 . C.    RAM ID=TI_TBL0I%È¹Ç•è²I0K0d³IÇ§
0145 C.
0146 . C.    DHU¥â;¼YÉ;È¼Y½,¥i;¼YÈ;È0dÏá0¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC    (02 0a f8)
0149 C.                00[HK1_PKT_FORM_NO]                EQ        2
0150 C.                00[HK1_PKT_GEN_TIME]                EQ        0.5S
0151 C.                00[HK1_S_TLM_BIT_RATE]                EQ        32K
0152 C.                00[HK1_X_TLM_BIT_RATE]                EQ        4M
0153 C.
0154 C.    *****
0155 C.    SOT TI command set
0156 C.    *****
0157 C.    Execute, after the success of OP upload.
0158 +. TI 2014-07-15 10:04:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC    (41)
0161 . C.    -----
0162 C.    HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0163 C.    -----
0164 C.    ***** SOT END *****
0165 C.
0166 C.    ***** XRT START *****
0167 C.    Execute, after the success of OP upload.
0168 +. TI 2014-07-15 10:04:00.0
0169 DC 07-F0 MDP_XRT_MODE_STBY
0170 BC    (c3)
0171 . C.                [ ] [HK1_TI_CMD_NUM]                EQ        1COUNTUP
0172 C.
0173 C.    ***** XRT END *****
0174 . C.    Stop EIS observation and temporarily disable EIS mode changes
0175 C.
0176 C.
0177 C.    ***** Start EIS operation (TI set) *****
0178 C.    Execute, after the success of OP upload.
0179 C.    Set EIS TI-commands
0180 +. TI 2014-07-15 10:04:30.0
0181 DC 07-FC EIS_MODE_MANU
0182 BC    (21 02)
0183 +. TI 2014-07-15 10:04:40.0
0184 DC 07-FC EIS_MODE_CHG_DIS
0185 BC    (22)
0186 . C.                [ ] [HK1_TI_CMD_NUM]                EQ        2 COUNTUP
0187 C.    ***** End EIS operation (TI set) *****
0188 C.
0189 C.
0190 C.
0191 . C.    ***** MDP `ÃÏ0I»ö¼Y0ÈÄ0¹0eDCBC•x²è *****
0192 C.    (%ã°iYÖYAYÉYpBYÉYáYÇYè0E¼¼0¼¼Ä»Û0¹0é)
0193 . S.    DC-BC dcbc-402:DCBC

```

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
```



```
0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. ***** AOCs Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 + DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 2727980.6 +- 1.0 (s) [ ]
0136 C.
0137 . C.
0138 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139 +. DC 07-FC EIS_MODE_CHG_ENA
0140 BC (20)
0141 . C. Verify EIS_MODE_CHG_FLG is ENA
0142 +. DC 07-FC EIS_MODE_MANU
0143 BC (21 02)
0144 . C. Verify EIS in MANUAL mode
0145 . C. Estimated OBSTBL upload time is 41s
0146 C. *****
0147 C. EIS START OBSTBL LOAD
0148 C. *****
0149 . S. RAM ram-821:EIS_OBSTBL
0150 ( )
0151 +. DC 07-FC EIS_DUMP_OBSTBL
0152 BC (07 07 07 00 00 70 00)
0153 C.
0154 C. Execute, after the success of OBSTBL upload.
0155 C. Set EIS TI-commands
0156 +. TI 2014-07-15 10:04:50.0
0157 DC 07-FC EIS_MODE_CHG_ENA
0158 BC (20)
0159 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0160 C. *****
0161 C. EIS END OBSTBL LOAD
0162 C. *****
0163 C.
0164 . C. ***** MDP `úÃîðÎ»ö¼ÝðÈÁð¹ñèDCBC•x²è *****
0165 C. (%ã°îÝÓÝÁÝÈÝÞÝÈÝÁÝÇÝÈðÈ%¼ñ¼Á»Û¹ñé)
0166 . S. DC-BC dcbc-402:DCBC
0167 (MDP_known_event)
0168 C.
0169 C.
0170 . C. ***** ÝDY¹•İ Daily±;İÑðÈ´Ø¹ñèDCBC•x²è *****
0171 . S. DC-BC dcbc-153:DCBC
0172 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0173 C.
0174 C.
0175 . C. ;ãLOSÝÁÝSÝÁÝ-¼Á»Û;ã
0176 C.
0177 . C. ***** LOS *****
0178 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```

main-508 2014-07-15 15:57:43 162 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;äAOSYÄYSYÄY-¼Ä»Û;ä
0005 C.
0006 C. YÄYB;¼Y³YFÏóYÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCSS : Reload orbital element (send every contact) *****
0010 C. Äí;Éç¿ÄÇµ°Æ»Í×ÁÇçíYçYÄY×Yí;¼YÉ;ÉÈèµ•ííÉ;ÉÇÈ¼°ÇÖÇµ•ÇÏ¼l¹çµÍ;çÄ®, ùñ¹ñèñÇÄ+¿®µ•ñÉñññ³ñÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-263:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Upload DPL table >
0036 C.
0037 C. YçYÄY×Yí;¼YÉçîÄ°ñÉSTS_CHKçðOFFçÈñ¹ñè
0038 C.
0039 . S. RAM ram-271:MDP_DPL
0040 ( )
0041 C.
0042 . C. < Dump RAMID=MDP_DPL >
0043 +. DC 07-F0 MDP_DUMP_FGTBL
0044 BC (82 07 00 38 b8 00 40)
0045 C. -----
0046 C. MDP_DPL verify = OK [ ]
0047 C. -----
0048 C.
0049 C. STS_CHKçðONçÈñ¹ñè
0050 C.
0051 . C. < Update MDP DSC PAR1 >
0052 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0053 BC (4c)
0054 C. MDP_CMD_CODE = F04C0700 [ ]
0055 C. MDP_CMD_CNT (count-up 1) [ ]
0056 C. -----
0057 C.
0058 C.
0059 C. *****
0060 C. SOT TI command set
0061 C. *****
0062 C. Execute, after the success of TBL upload.
0063 +. TI 2014-07-15 10:04:18.0
0064 DC 07-F0 MDP_SOT_MODE_OBSV
0065 BC (40)
0066 C. -----
0067 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0068 C. -----
0069 C.
0070 C.
0071 C. ***** XRT START *****
0072 C.
0073 +. DC 07-F0 MDP_XRT_CTRL_MANU
0074 BC (c1)
0075 + DC 07-F0 MDP_XRT_MODE_STBY
0076 BC (c3)
0077 . C. ----- Success Verify ? OK / NG____
0078 C.
0079 C. XRT Obs. Table Upload
0080 . S. RAM ram-291:MDP_OBS_X
0081 ( )
0082 C.
0083 +. DC 07-F0 MDP_DUMP_XRTTBL
0084 BC (84 07 00 00 00 3a d4)
0085 . C. ----- Comparison Check ? OK / ERR _____
0086 C.
0087 C.
0088 +. DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 01 b1 b1 04 04)
0090 + DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 02 b1 b1 08 08)
0092 + DC 07-F0 MDP_XRT_ROI_SET
0093 BC (cd 03 b1 b1 08 08)
0094 + DC 07-F0 MDP_XRT_ROI_SET
0095 BC (cd 04 b1 b1 06 06)

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0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 05 85 83 06 06)
0098 + DC 07-F0 MDP_XRT_ROI_SET
0099 BC (cd 06 85 83 06 06)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101 BC (cd 07 85 83 08 08)
0102 + DC 07-F0 MDP_XRT_ROI_SET
0103 BC (cd 08 c0 c0 10 10)
0104 + DC 07-F0 MDP_XRT_ROI_SET
0105 BC (cd 09 80 80 20 20)
0106 + DC 07-F0 MDP_XRT_ROI_SET
0107 BC (cd 0a 40 c0 10 10)
0108 + DC 07-F0 MDP_XRT_ROI_SET
0109 BC (cd 0b 40 40 10 10)
0110 + DC 07-F0 MDP_XRT_ROI_SET
0111 BC (cd 0c c0 40 10 10)
0112 + DC 07-F0 MDP_XRT_ROI_SET
0113 BC (cd 0d 80 80 20 08)
0114 + DC 07-F0 MDP_XRT_ROI_SET
0115 BC (cd 0e 80 80 08 20)
0116 + DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 0f 80 80 06 06)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 10 80 80 08 08)
0120 + DC 07-F0 MDP_XRT_FLD_ENA
0121 BC (d8)
0122 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0123 BC (c8)
0124 + DC 07-F0 MDP_XRT_AEC_RESET
0125 BC (d0)
0126 + DC 07-F0 MDP_XRT_ARS_DIS
0127 BC (d5)
0128 + DC 07-F0 MDP_XRT_FLD_RESET
0129 BC (da)
0130 + DC 07-F0 MDP_XRT_QT_PROG_SET
0131 BC (c4 10)
0132 + DC 07-F0 MDP_XRT_FL_PROG_SET
0133 BC (c5 01)
0134 . C. ----- Success Verify ? OK / NG ____
0135 C.
0136 C.
0137 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0138 C.
0139 + DC 07-F0 MDP_XRT_MODE_OBSV
0140 BC (c2)
0141 + TI 2014-07-15 10:04:02.0
0142 DC 07-F0 MDP_XRT_MODE_OBSV
0143 BC (c2)
0144 . C. ----- Success Verify ? OK / NG ____
0145 C.
0146 C. ***** XRT END *****
0147 C.
0148 . C. ***** MDP `uAiuu»o%YnaEAdo¹aèDCBC•x²è *****
0149 C. (%â°iYÖYAYEYÈYËYäYçYèaE%¼aa¼A»Üa¹aè)
0150 . S. DC-BC dcbc-402:DCBC
0151 (MDP_known_event)
0152 C.
0153 C.
0154 . C. ***** YBY¹.İ Daily±;iNnaE`0a¹aèDCBC•x²è *****
0155 . S. DC-BC dcbc-153:DCBC
0156 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0157 C.
0158 C.
0159 . C. jãLOSŸÁYŠYÄY~¼A»Ü;ã
0160 C.
0161 . C. ***** LOS *****
0162 C.

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*** OP Sequence for XRT ***

2014/07/15	10:15:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00	ea	f2	b1	cb
2014/07/15	11:34:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	ac	cd
2014/07/15	11:50:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	d6	67
2014/07/15	12:06:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	00	00	00	00
2014/07/15	12:22:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	00	00	29	99
2014/07/15	13:12:30.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	00	00	53	33
2014/07/15	13:28:30.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	d6	36	b7	8e
2014/07/15	13:38:30.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00	b4	b5	db	75
2014/07/15	13:54:30.0	AOCS_ORe-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00	ac	5b	00	00
2014/07/15	15:06:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00	b4	b5	24	8b
2014/07/15	15:22:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00	d6	36	48	72
2014/07/15	15:32:00.0	AOCS_ORe-point_Start_12_OG [0x0a2]							
		AOCU_NM	5	02-76	00	29	ca	b7	8e
2014/07/15	15:42:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]							
		AOCU_NM	5	02-76	00	4b	4b	db	75
2014/07/15	16:53:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]							
		AOCU_NM	5	02-76	00	53	a5	00	00
2014/07/15	17:09:00.0	AOCS_ORe-point_Start_15_OG [0x0a5]							
		AOCU_NM	5	02-76	00	4b	4b	24	8b
2014/07/15	17:25:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	00	29	db	48	72
2014/07/15	18:33:30.0	AOCS_ORe-point_Start_17_OG [0x0a7]							
		AOCU_NM	5	02-76	04	00	00	00	00
2014/07/16	02:00:00.0	AOCS_ORe-point_Start_18_OG [0x0a8]							
		AOCU_NM	5	02-76	03	00	00	00	00
2014/07/16	04:45:00.0	XRT_TCIB_XRT_S_HTR_A_DIS_417_OG [0x1a1]							
		TCIB_XRT_S_HTR_A_DIS	0	04-C0					
2014/07/16	09:00:00.0	AOCS_ORe-point_Start_19_OG [0x0a9]							
		AOCU_NM	5	02-76	00	12	35	b0	00
2014/07/16	10:59:54.0	XRT_CTRL_MANU_401_OG [0x191]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2014/07/16	11:00:00.0	AOCS_ORe-point_Start_20_OG [0x0aa]							
		AOCU_NM	5	02-76	00	2e	f9	2e	f9
2014/07/16	11:02:32.0	XRT_FOCUS_POSITION_404_OG [0x194]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2014/07/16	11:02:52.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0		c4	02		
2014/07/16	11:02:54.0	XRT_FLD_DIS_428_OG [0x1ac]							
		MDP_XRT_FLD_DIS	1	07-F0		d9			
2014/07/16	11:02:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0		c9			
2014/07/16	11:02:58.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0		d5			
2014/07/16	11:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2014/07/16	11:09:54.0	XRT_CTRL_MANU_401_OG [0x191]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2014/07/16	11:10:00.0	AOCS_ORe-point_Start_21_OG [0x0ab]							
		AOCU_NM	5	02-76	00	2e	f9	d1	07
2014/07/16	11:12:32.0	XRT_FOCUS_POSITION_404_OG [0x194]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2014/07/16	11:12:52.0	XRT_QT_PROG_SET_429_OG [0x1ad]							
		MDP_XRT_QT_PROG_SET	2	07-F0		c4	07		
2014/07/16	11:12:54.0	XRT_FLD_DIS_428_OG [0x1ac]							
		MDP_XRT_FLD_DIS	1	07-F0		d9			
2014/07/16	11:12:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0		c9			
2014/07/16	11:12:58.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0		d5			
2014/07/16	11:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2014/07/16	11:19:54.0	XRT_CTRL_MANU_401_OG [0x191]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2014/07/16	11:20:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]							
		AOCU_NM	5	02-76	00	d1	07	d1	07
2014/07/16	11:22:32.0	XRT_FOCUS_POSITION_404_OG [0x194]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2014/07/16	11:22:52.0	XRT_QT_PROG_SET_437_OG [0x1b5]							
		MDP_XRT_QT_PROG_SET	2	07-F0		c4	11		
2014/07/16	11:22:54.0	XRT_FLD_DIS_428_OG [0x1ac]							
		MDP_XRT_FLD_DIS	1	07-F0		d9			
2014/07/16	11:22:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0		c9			
2014/07/16	11:22:58.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0		d5			
2014/07/16	11:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2014/07/16	11:29:54.0	XRT_CTRL_MANU_401_OG [0x191]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2014/07/16	11:30:00.0	AOCS_ORe-point_Start_23_OG [0x0ad]							

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2014/07/16	11:32:32.0	XRT_FOCUS_POSITION_404_OG [0x194]	5	02-76	00	d1	07	2e	f9
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2014/07/16	11:32:52.0	XRT_QT_PROG_SET_440_OG [0x1b8]	2	07-F0	c4	09			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	09			
2014/07/16	11:32:54.0	XRT_FLD_DIS_428_OG [0x1ac]	1	07-F0	d9				
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/07/16	11:32:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]	1	07-F0	c9				
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/07/16	11:32:58.0	XRT_ARS_DIS_423_OG [0x1a7]	1	07-F0	d5				
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/07/16	11:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/07/16	11:40:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]	5	02-76	04	00	00	00	00
		AOCU_NM	5	02-76	04	00	00	00	00
2014/07/16	11:41:00.5	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1				
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/16	11:41:02.5	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da				
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/07/16	11:41:04.5	XRT_PREFLR_STRT_418_OG [0x1a2]	1	07-F0	e8				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/07/16	11:44:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/07/16	12:10:54.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1				
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/16	12:10:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	4	07-F8	22	fe	97	00	
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2014/07/16	12:11:16.0	XRT_FLD_ENA_411_OG [0x19b]	1	07-F0	d8				
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/07/16	12:11:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	1	07-F0	c8				
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/07/16	12:11:20.0	XRT_AEC_RESET_413_OG [0x19d]	1	07-F0	d0				
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/07/16	12:11:22.0	XRT_ARS_DIS_423_OG [0x1a7]	1	07-F0	d5				
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/07/16	12:11:24.0	XRT_FLD_RESET_431_OG [0x1af]	1	07-F0	da				
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/07/16	12:13:56.0	XRT_QT_PROG_SET_406_OG [0x196]	2	07-F0	c4	13			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	13			
2014/07/16	12:13:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]	2	07-F0	c5	01			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	01			
2014/07/16	12:14:00.0	XRT_CTRL_AUTO_408_OG [0x198]	1	07-F0	c0				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/07/16	13:19:30.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1				
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/16	13:19:32.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da				
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/07/16	13:19:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	1	07-F0	e8				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/07/16	13:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/07/16	13:49:00.0	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/07/16	13:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/07/16	14:58:00.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1				
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/16	14:58:02.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da				
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/07/16	14:58:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	1	07-F0	e8				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/07/16	15:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/07/16	15:46:00.0	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/07/16	15:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/07/16	16:36:30.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1				
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/16	16:36:32.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da				
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/07/16	16:36:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	1	07-F0	e8				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/07/16	16:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/07/16	17:22:30.0	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/07/16	17:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/07/16	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1				
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/16	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	4	07-F8	22	ff	aa	00	
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2014/07/16	18:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	5	02-76	00	00	00	00	00
		AOCU_NM	5	02-76	00	00	00	00	00
2014/07/16	18:00:16.0	XRT_FLD_DIS_434_OG [0x1b2]	1	07-F0	d9				
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/07/16	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	1	07-F0	c9				
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/07/16	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	1	07-F0	d5				
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/07/16	18:02:58.0	XRT_QT_PROG_SET_444_OG [0x1bc]	2	07-F0	c4	0e			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e			
2014/07/16	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	1	07-F0	c0				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/07/16	18:14:30.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1				

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2014/07/16	18:14:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/16	18:14:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/07/16	18:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/07/16	19:04:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/07/16	19:04:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/16	19:05:16.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2014/07/16	19:05:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/07/16	19:05:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/07/16	19:05:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/07/16	19:05:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/07/16	19:07:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/07/16	19:07:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05	
2014/07/16	19:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 01	
2014/07/16	19:53:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/07/16	19:53:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/16	19:53:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/07/16	19:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/07/16	20:36:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/07/16	20:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2014/07/16	21:31:30.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]				
2014/07/16	21:31:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/07/16	21:31:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/16	21:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/07/16	22:00:00.0	AOCs_OrE-point_Start_17_OG [0x0a7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/07/16	22:14:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/07/16	22:14:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	AOCU_NM	5	02-76	04 00 00 00 00	
2014/07/16	22:15:16.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2014/07/16	22:15:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/07/16	22:15:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/07/16	22:15:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/07/16	22:15:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/07/16	22:17:56.0	XRT_QT_PROG_SET_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/07/16	22:17:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13	
2014/07/16	22:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 01	
2014/07/16	23:10:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/07/16	23:10:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/16	23:10:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/07/16	23:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/07/16	23:40:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/07/16	23:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2014/07/17	00:48:30.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]				
2014/07/17	00:48:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/07/17	00:48:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/17	00:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/07/17	01:18:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/07/17	01:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/07/17	02:27:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]				
2014/07/17	02:27:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
			MDP_XRT_CTRL_MANU	1	07-F0	c1	

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2014/07/17	02:27:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/07/17	02:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/07/17	02:56:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/07/17	02:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/07/17	03:55:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/17	03:55:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/07/17	03:55:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/07/17	03:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/07/17	04:34:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/07/17	04:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/17	05:36:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/17	05:36:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/07/17	05:36:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/07/17	05:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/07/17	06:13:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/07/17	06:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/17	06:18:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/17	06:18:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2014/07/17	06:18:30.0	AOCS_Ore-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 00 00 00 00
2014/07/17	06:18:46.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9
2014/07/17	06:21:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2014/07/17	06:21:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2014/07/17	06:21:28.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2014/07/17	06:21:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/07/17	06:28:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/17	06:28:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/07/17	06:28:30.0	AOCS_Ore-point_Start_17_OG [0x0a7]	AOCU_NM	5	02-76	04 00 00 00 00
2014/07/17	06:28:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2014/07/17	06:28:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/07/17	06:28:50.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0
2014/07/17	06:28:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2014/07/17	06:28:54.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/07/17	06:31:26.0	XRT_QT_PROG_SET_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2014/07/17	06:31:28.0	XRT_FL_PROG_SET_438_OG [0x1b6]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 01
2014/07/17	06:31:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/07/17	07:16:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/17	07:16:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/07/17	07:16:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/07/17	07:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/07/17	07:51:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/07/17	07:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/17	07:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/17	07:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/07/17	08:00:00.0	AOCS_Ore-point_Start_24_OG [0x0ae]	AOCU_NM	5	02-76	00 dc e6 b8 00
2014/07/17	08:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2014/07/17	08:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/07/17	08:00:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0
2014/07/17	08:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5

2014/07/17	08:00:24.0	XRT_FLD_RESET_431_OG [0x1af]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/07/17	08:02:56.0	XRT_QT_PROG_SET_406_OG [0x196]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2014/07/17	08:02:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01				
2014/07/17	08:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/07/17	08:56:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/17	08:56:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/07/17	08:56:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/07/17	08:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/07/17	10:36:00.0	AOCs_OrE-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 00 00 00 00				